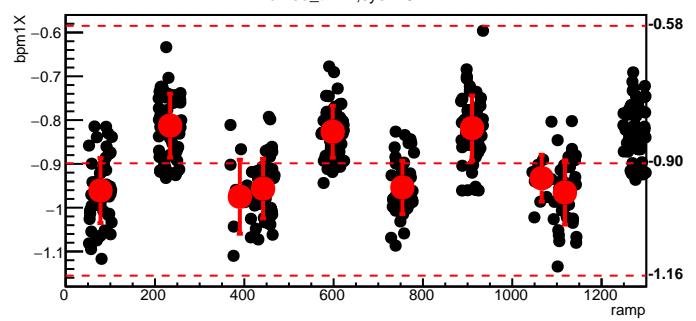
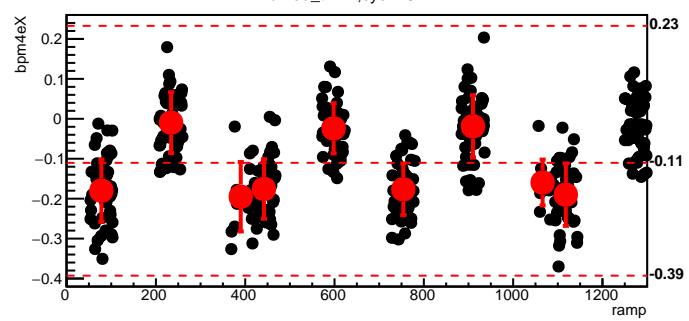


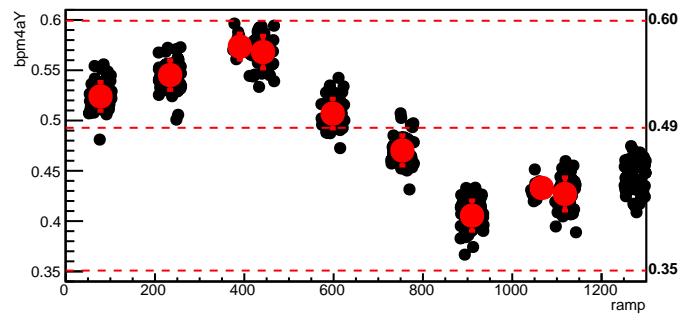
bmod Trim2,cyc=10124



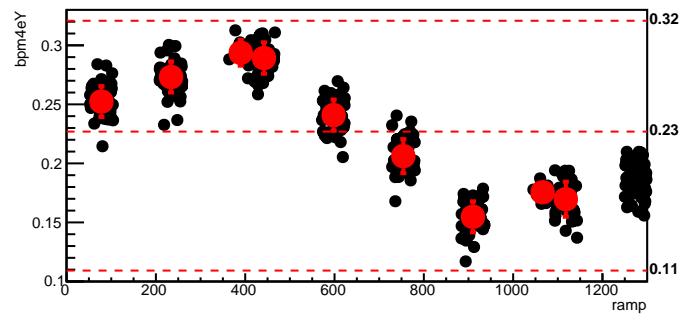
bmod Trim2,cyc=10124



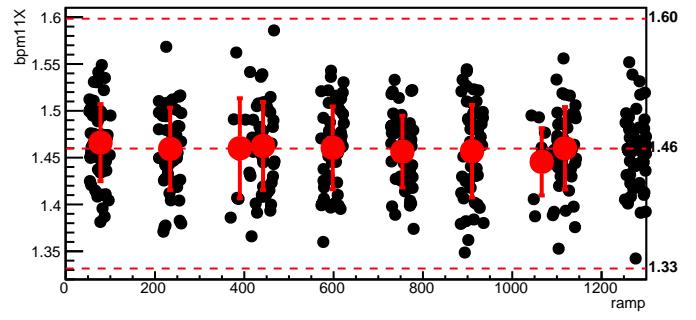
bmod Trim2,cyc=10124



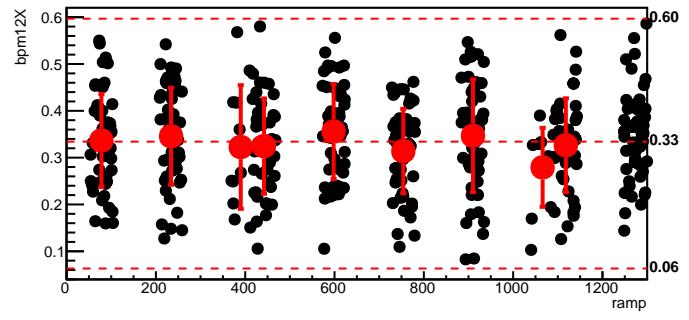
bmod Trim2,cyc=10124



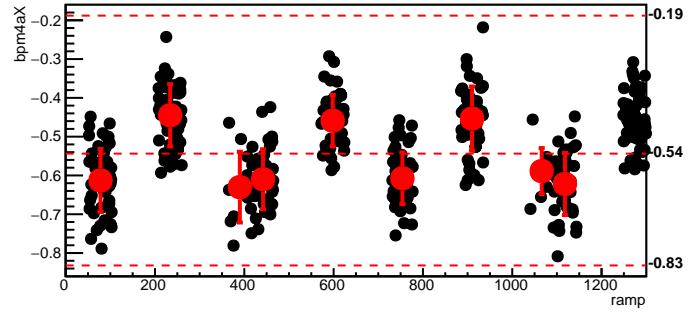
bmod Trim2,cyc=10124



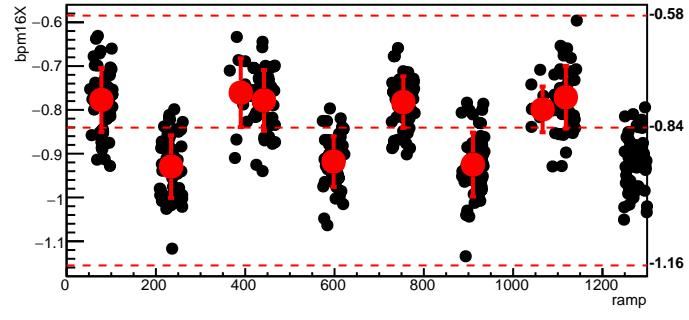
bmod Trim2,cyc=10124



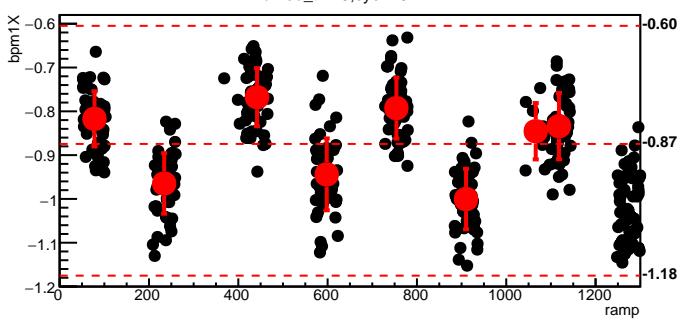
bmod Trim2,cyc=10124



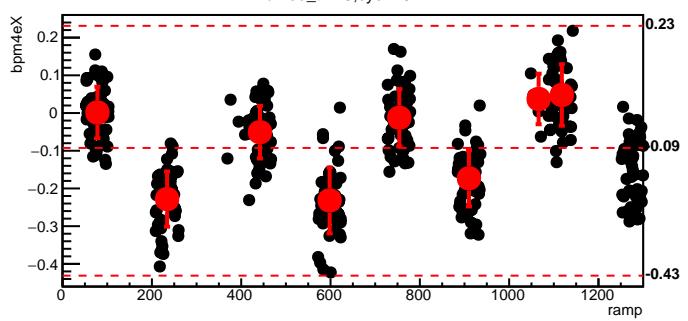
bmod Trim2,cyc=10124



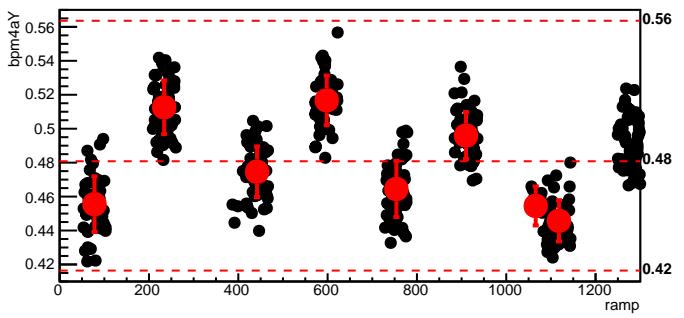
bmod Trim3,cyc=10124



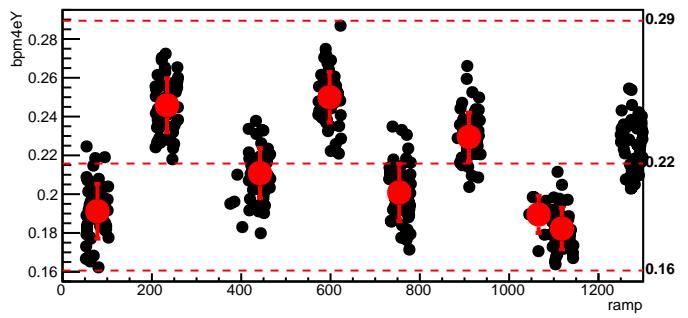
bmod Trim3,cyc=10124



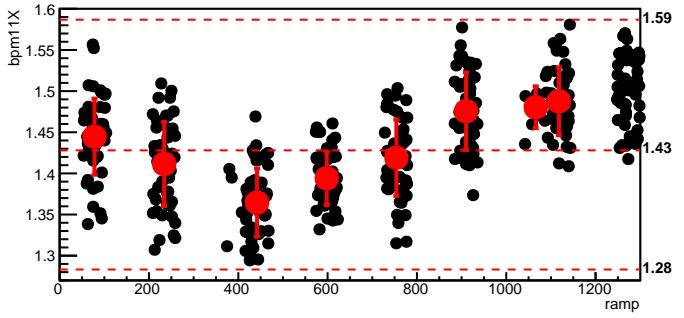
bmod Trim3,cyc=10124



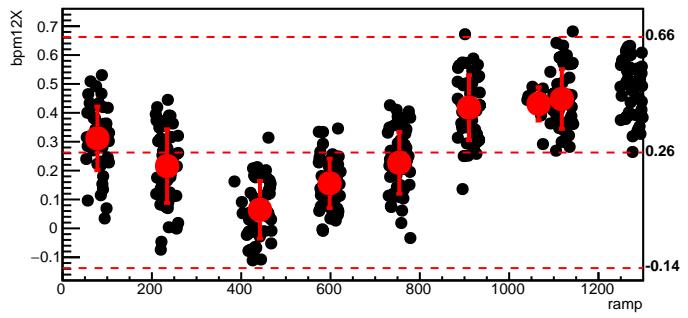
bmod Trim3,cyc=10124



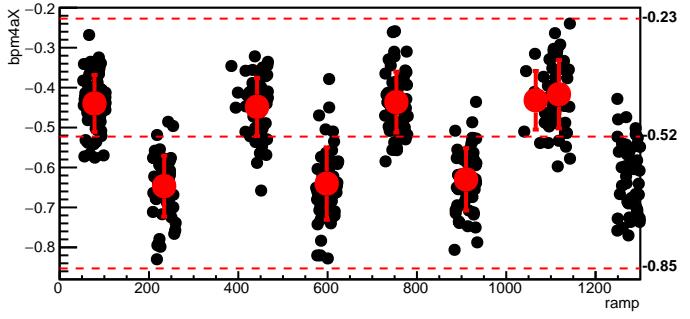
bmod Trim3,cyc=10124



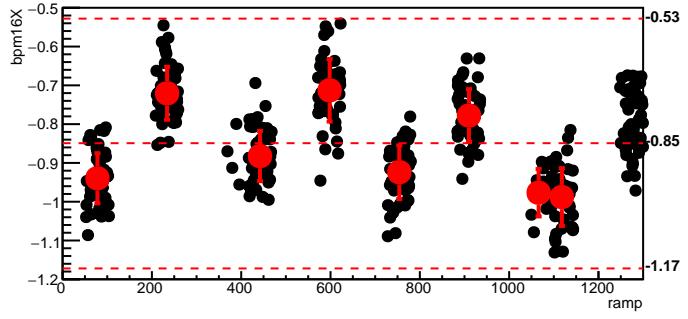
bmod Trim3,cyc=10124



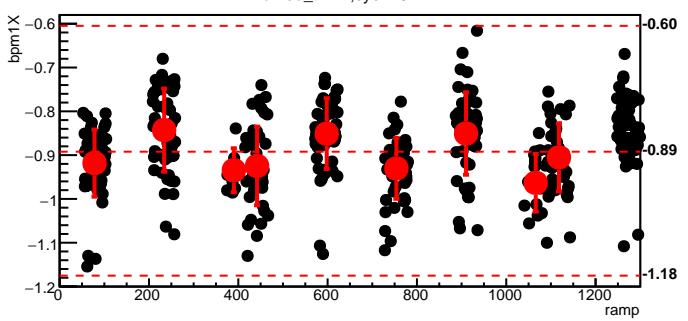
bmod Trim3,cyc=10124



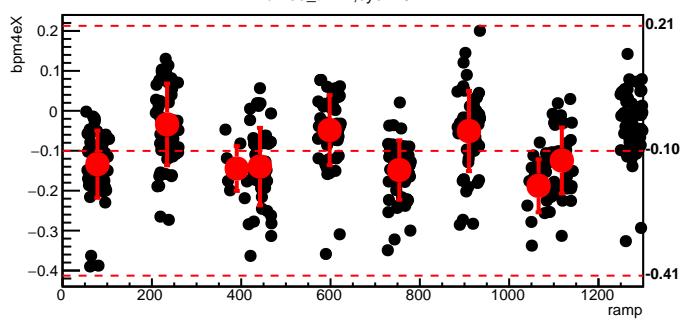
bmod Trim3,cyc=10124



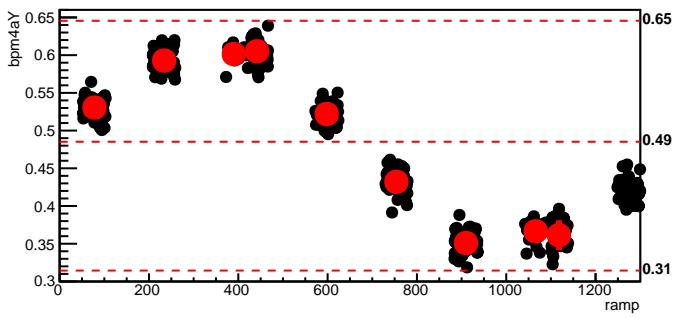
bmod Trim4,cyc=10124



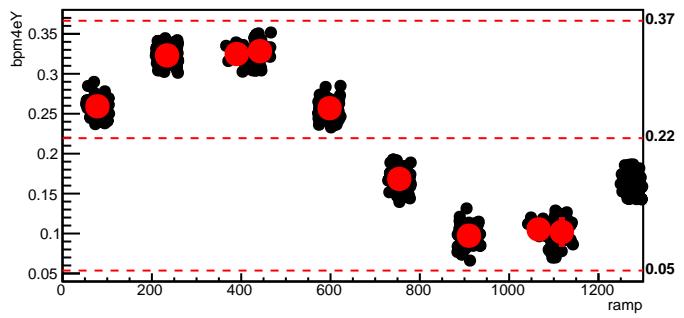
bmod Trim4,cyc=10124



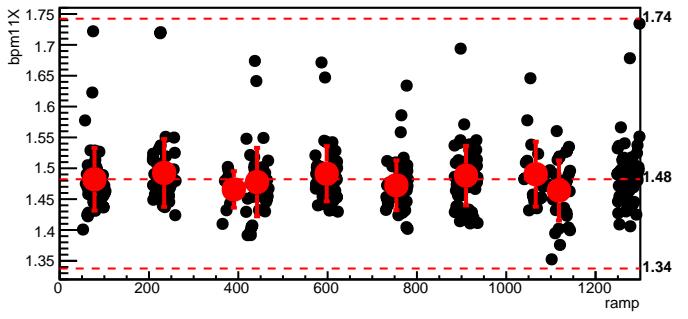
bmod Trim4,cyc=10124



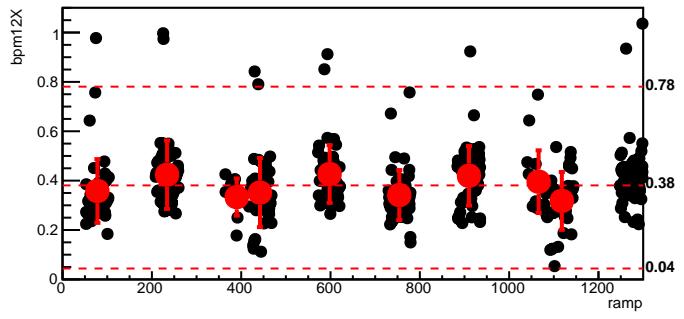
bmod Trim4,cyc=10124



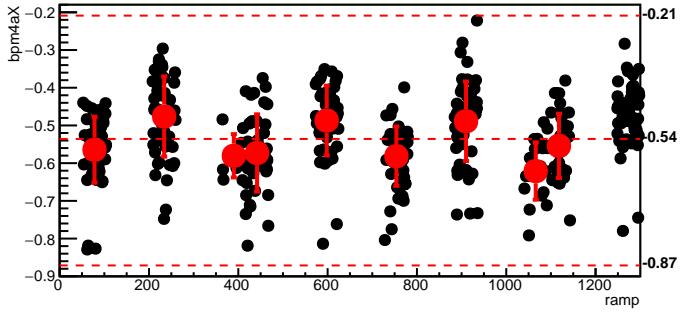
bmod Trim4,cyc=10124



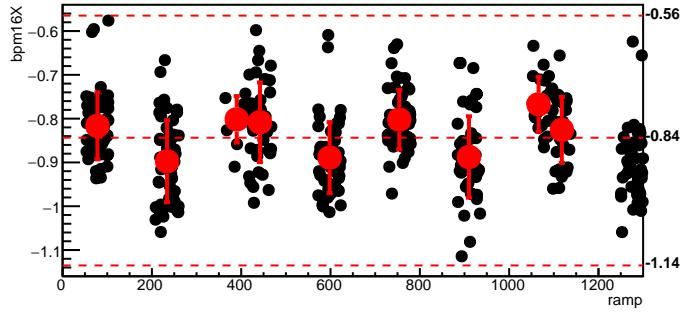
bmod Trim4,cyc=10124



bmod Trim4,cyc=10124

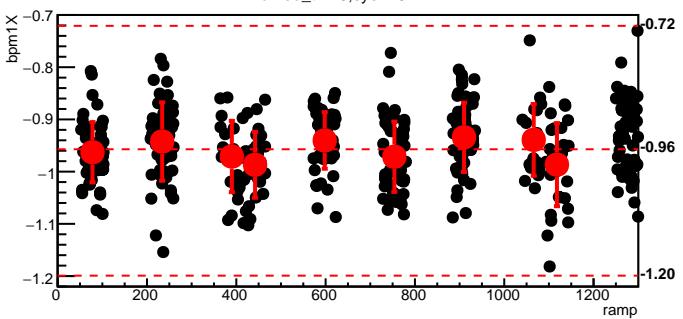


bmod Trim4,cyc=10124

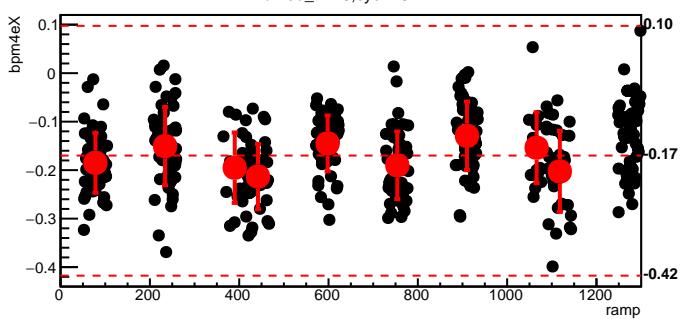




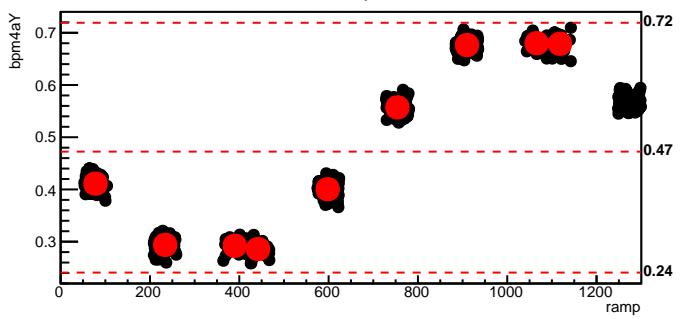
bmod Trim6,cyc=10124



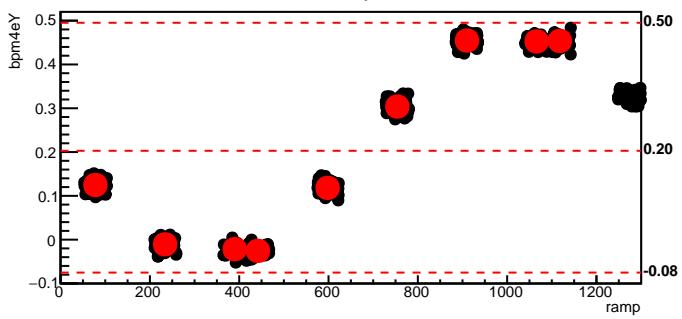
bmod Trim6,cyc=10124



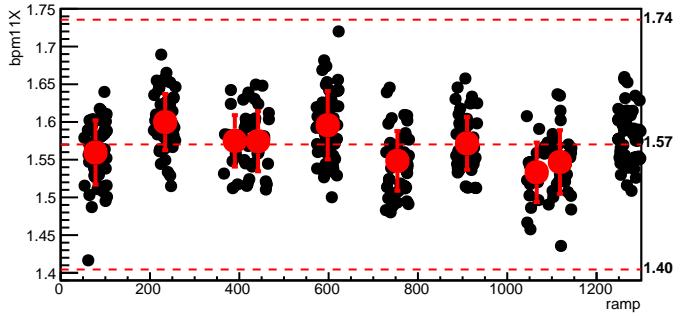
bmod Trim6,cyc=10124



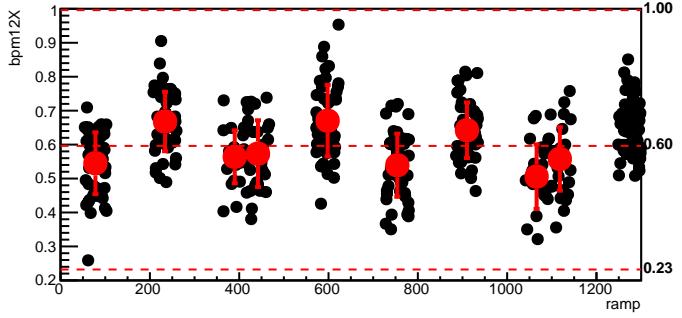
bmod Trim6,cyc=10124



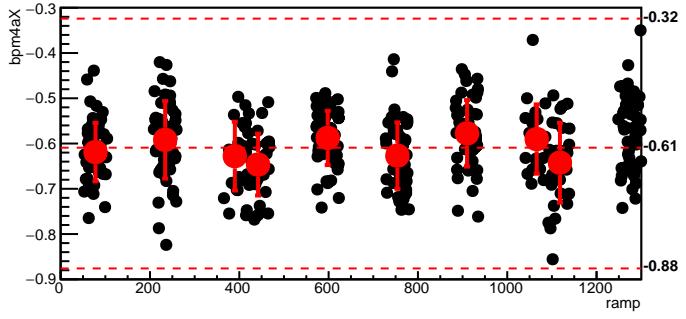
bmod Trim6,cyc=10124



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