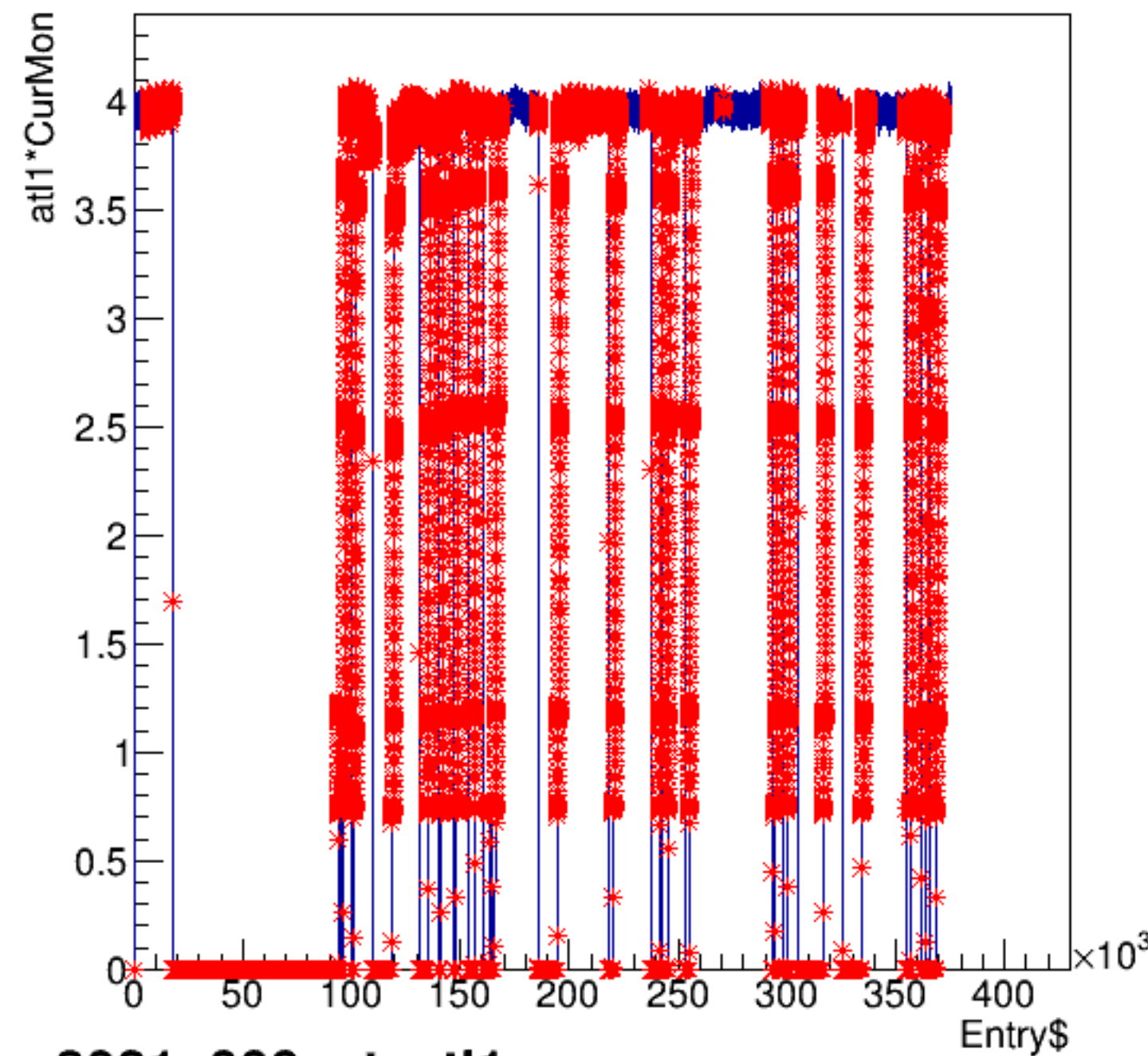
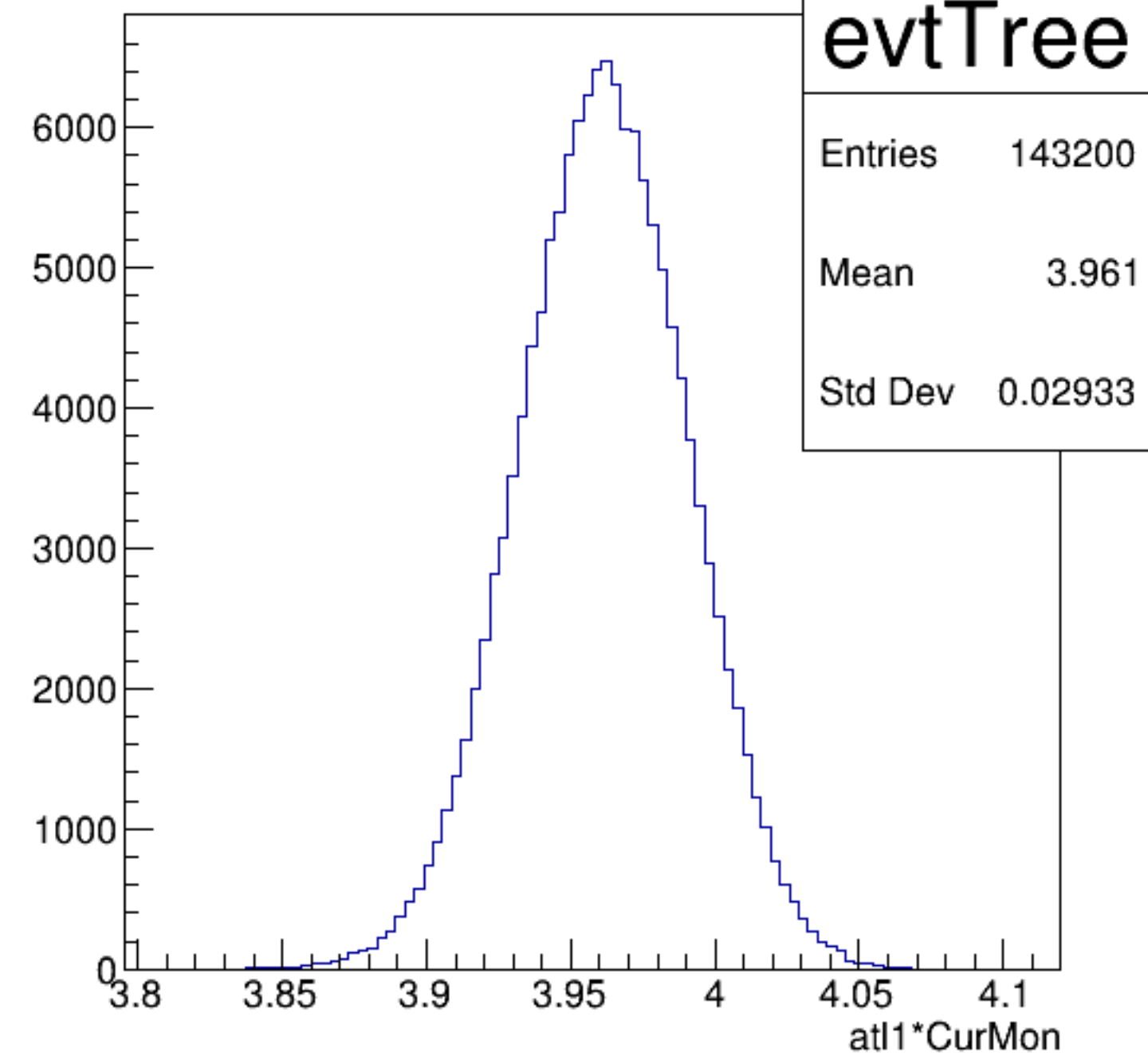


atl1*CurMon:Entry\$



run8021_000_at_atl1.png

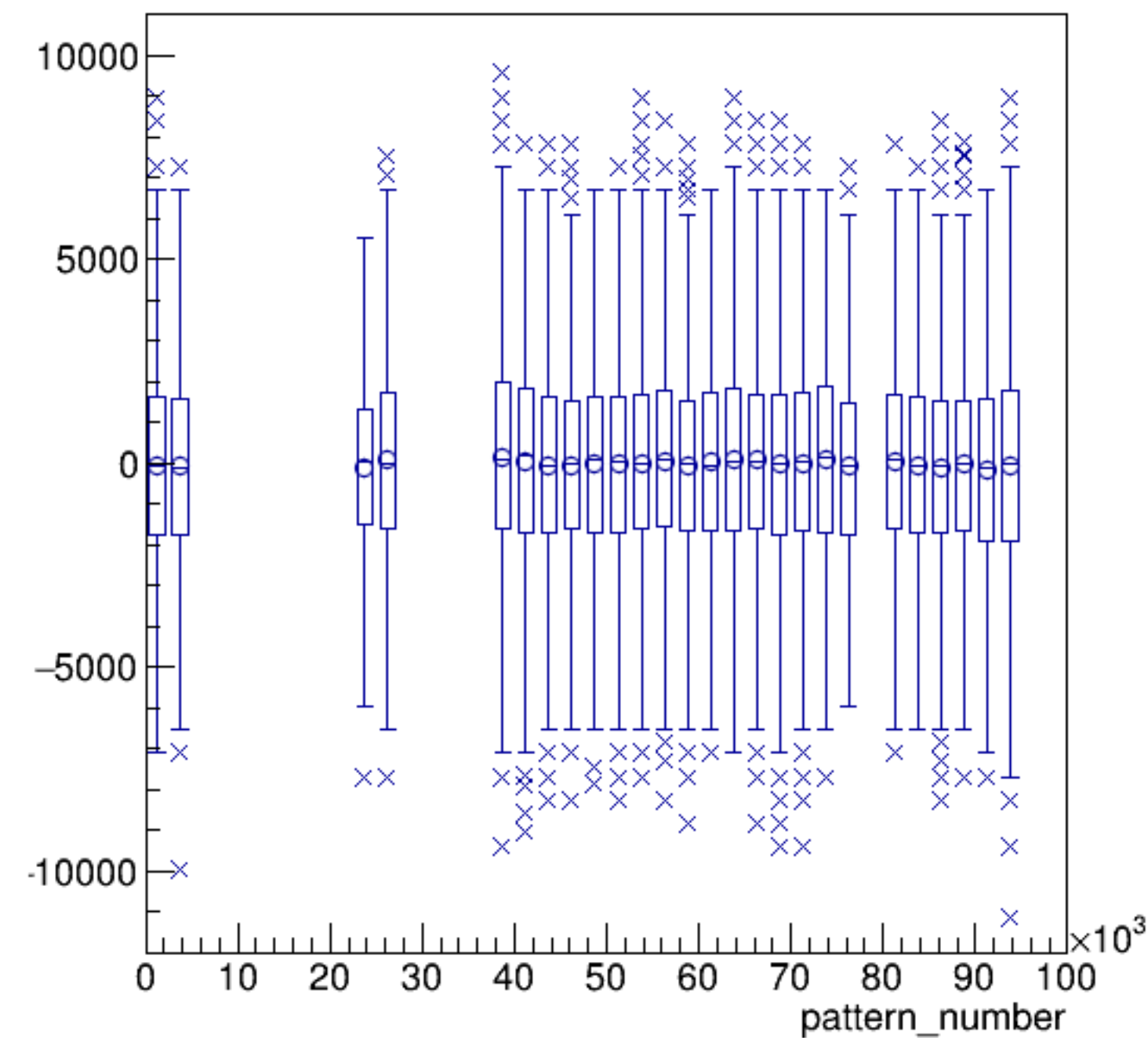
atl1*CurMon {ErrorFlag==0}



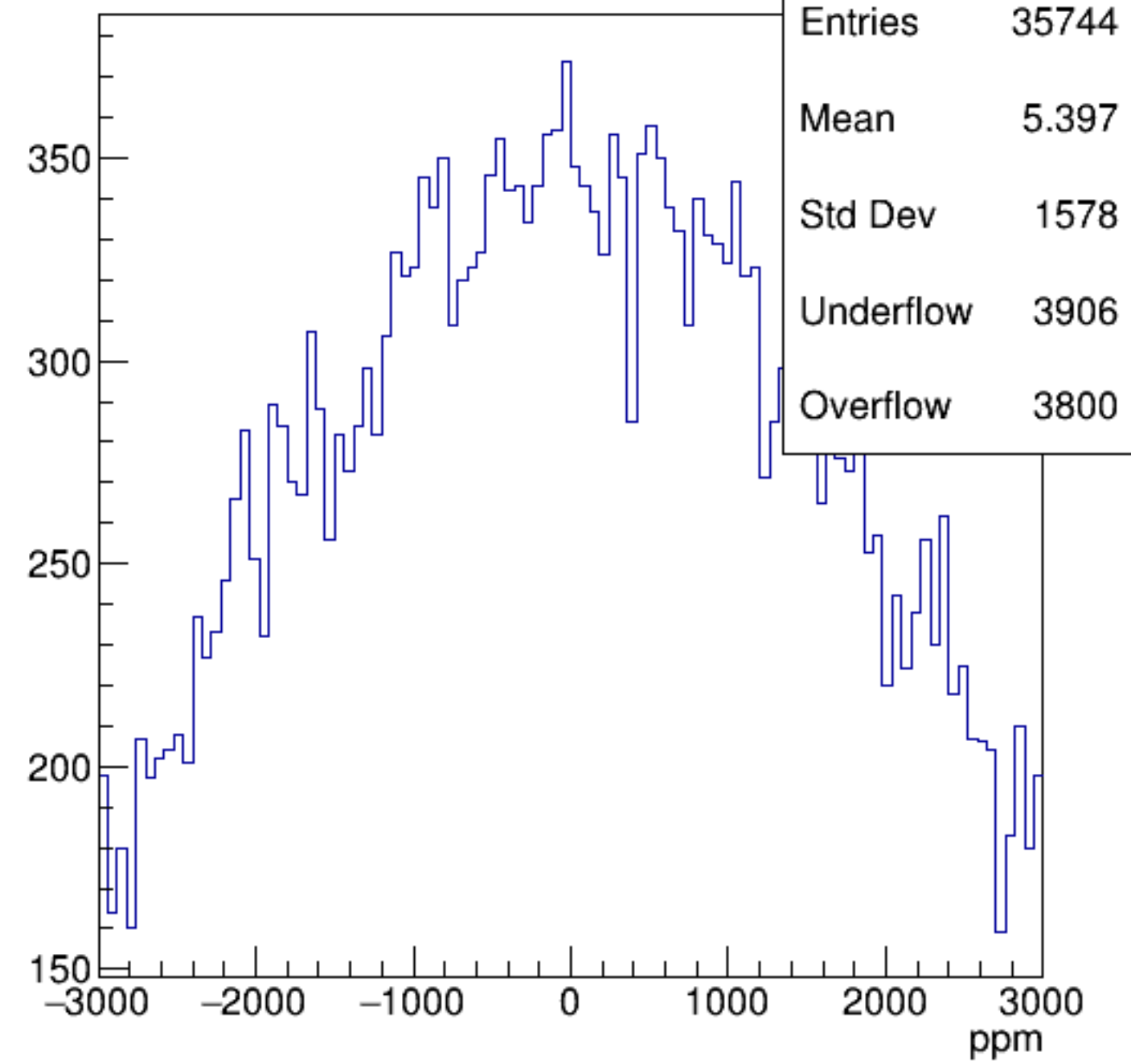
evtTree

Entries	143200
Mean	3.961
Std Dev	0.02933

asym_atl1/ppm:pattern_number {ErrorFlag==0}

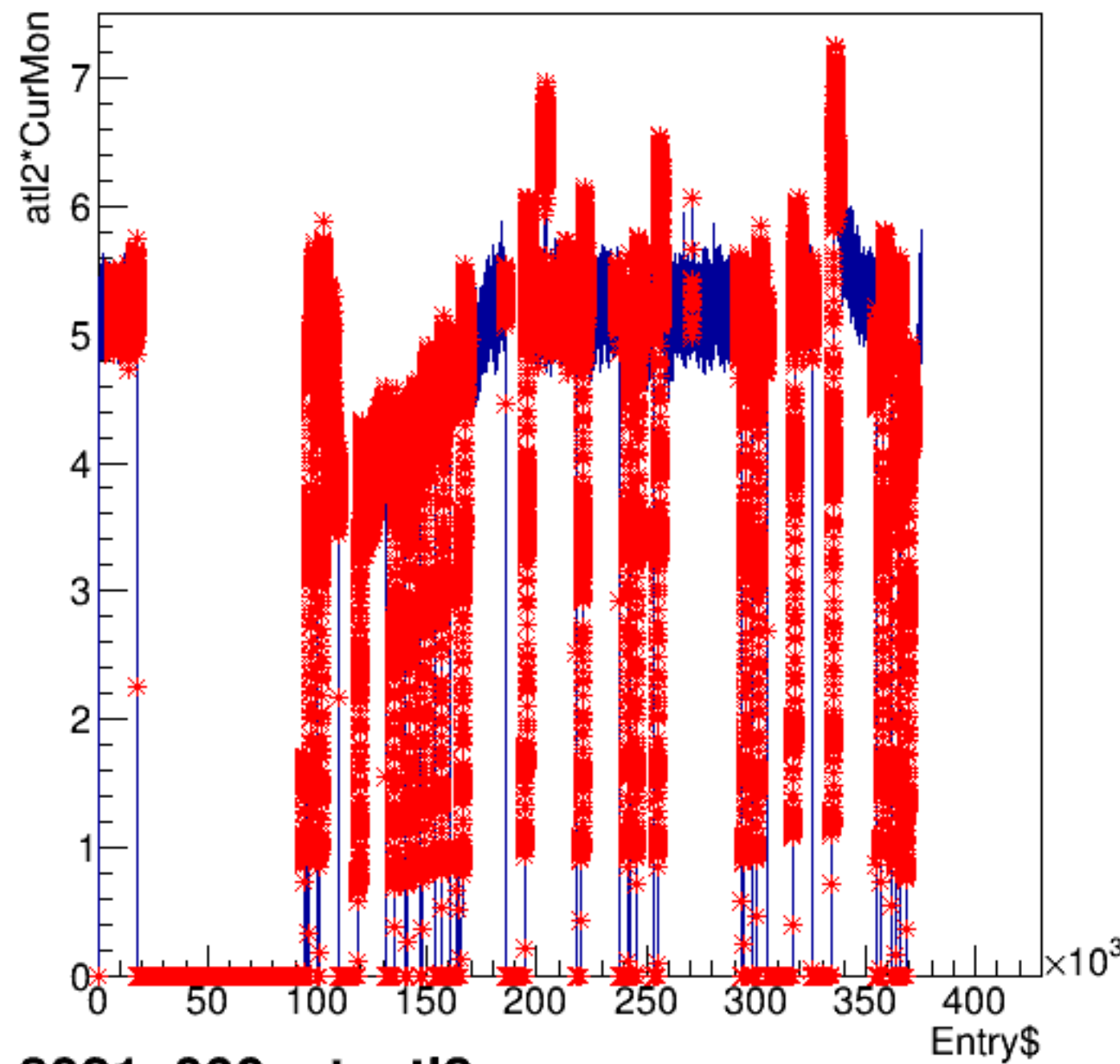


asym_atl1



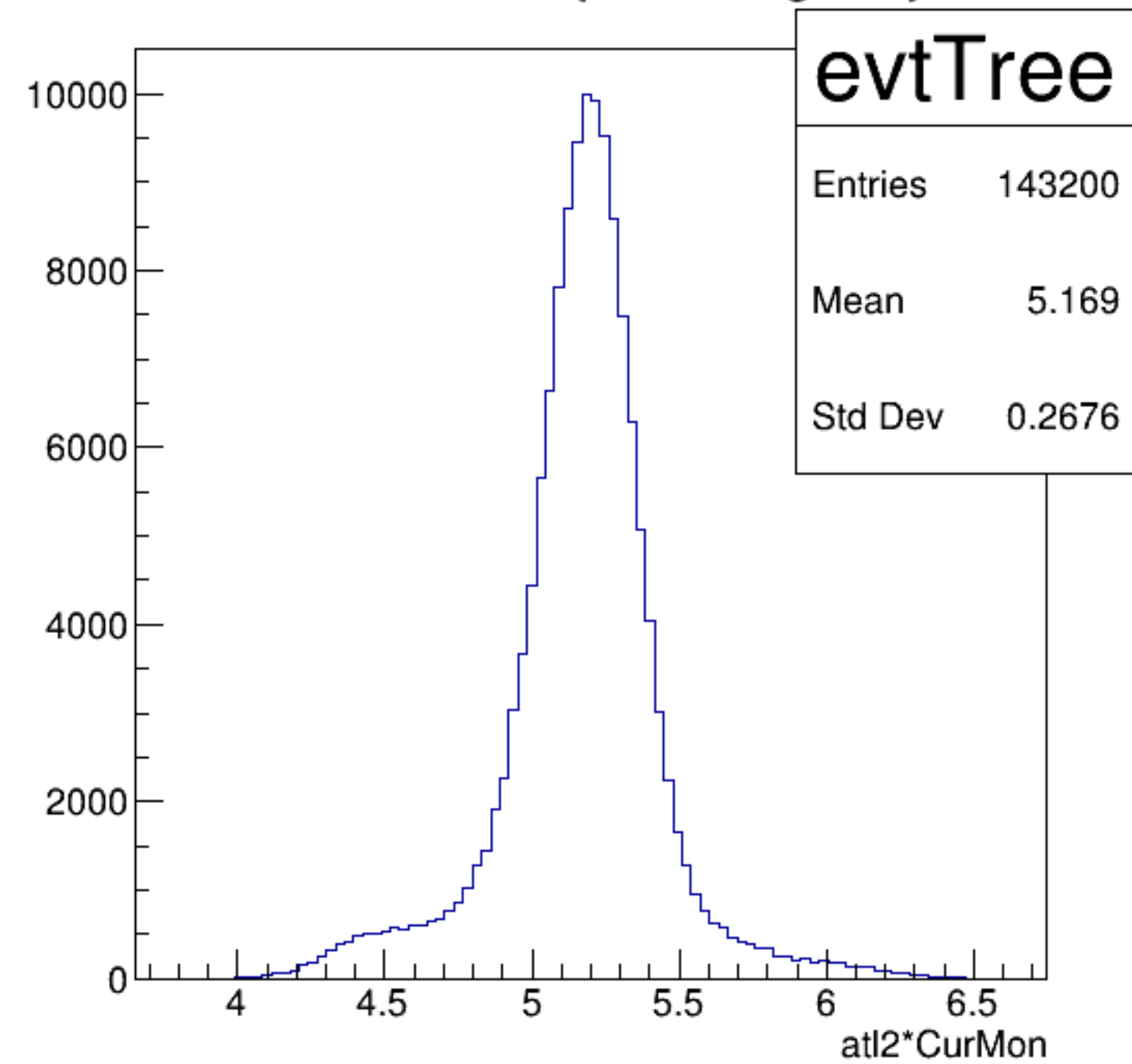
Entries	35744
Mean	5.397
Std Dev	1578
Underflow	3906
Overflow	3800

atl2*CurMon:Entry\$

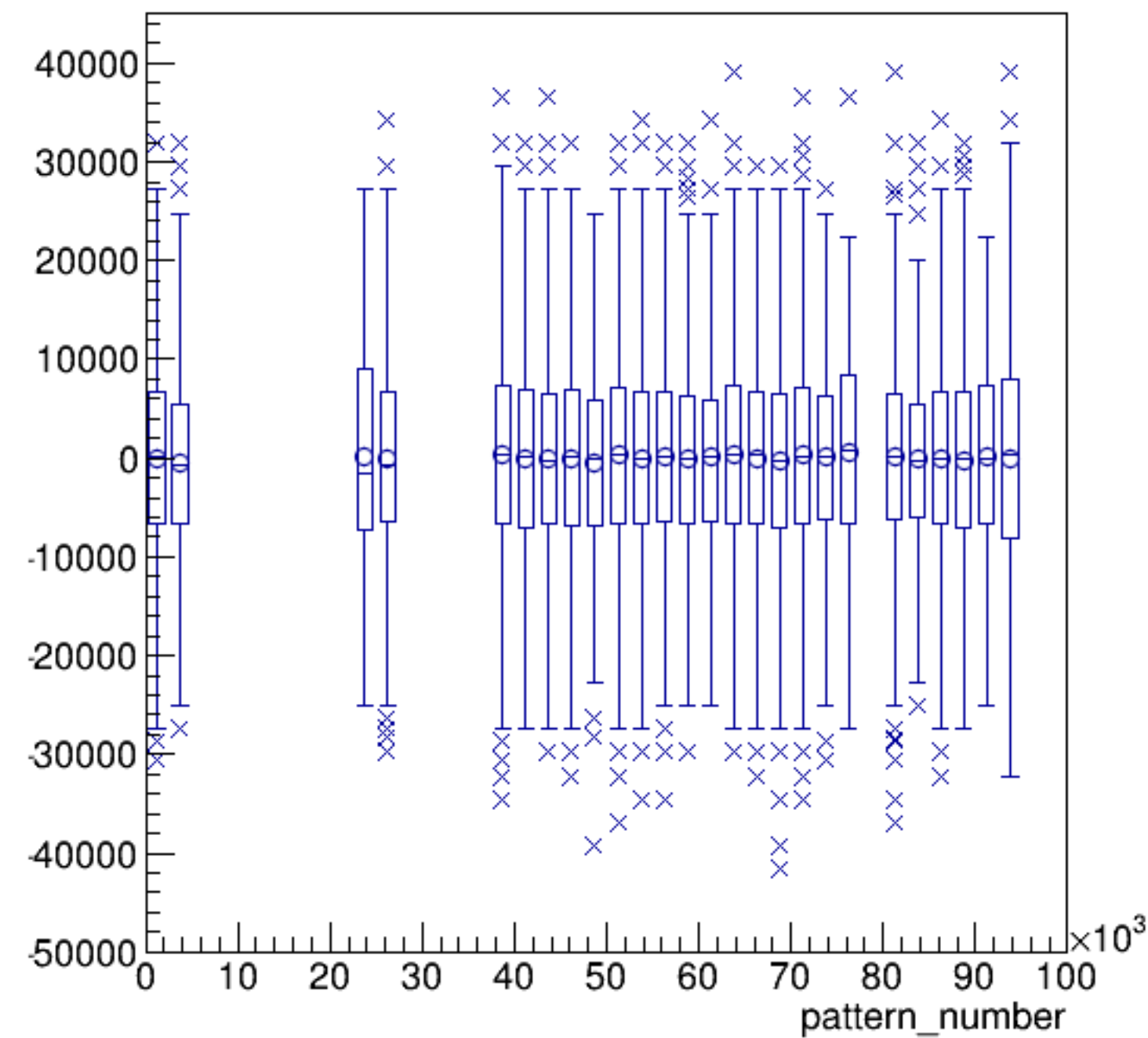


run8021_000_at_atl2.png

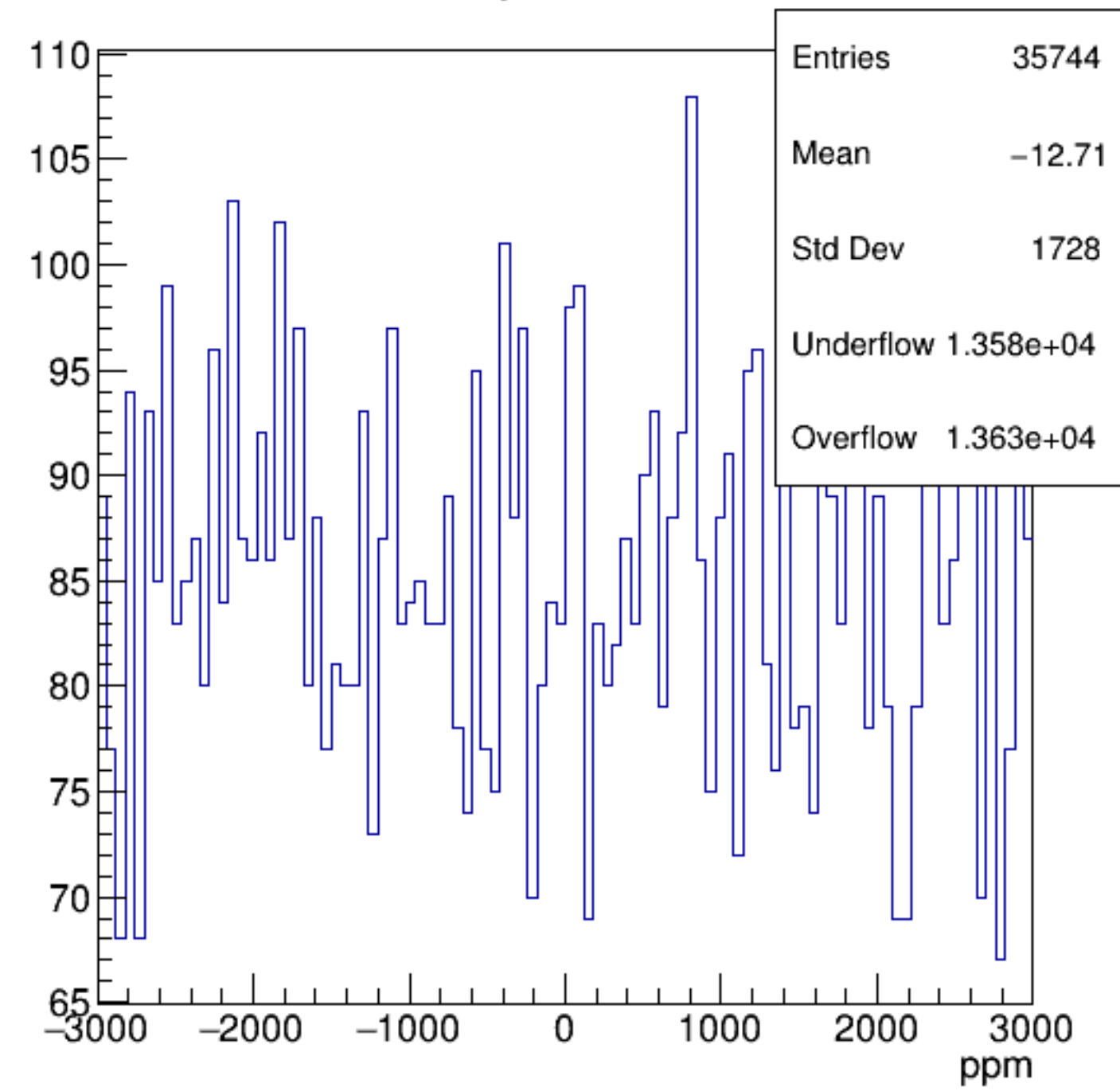
atl2*CurMon {ErrorFlag==0}



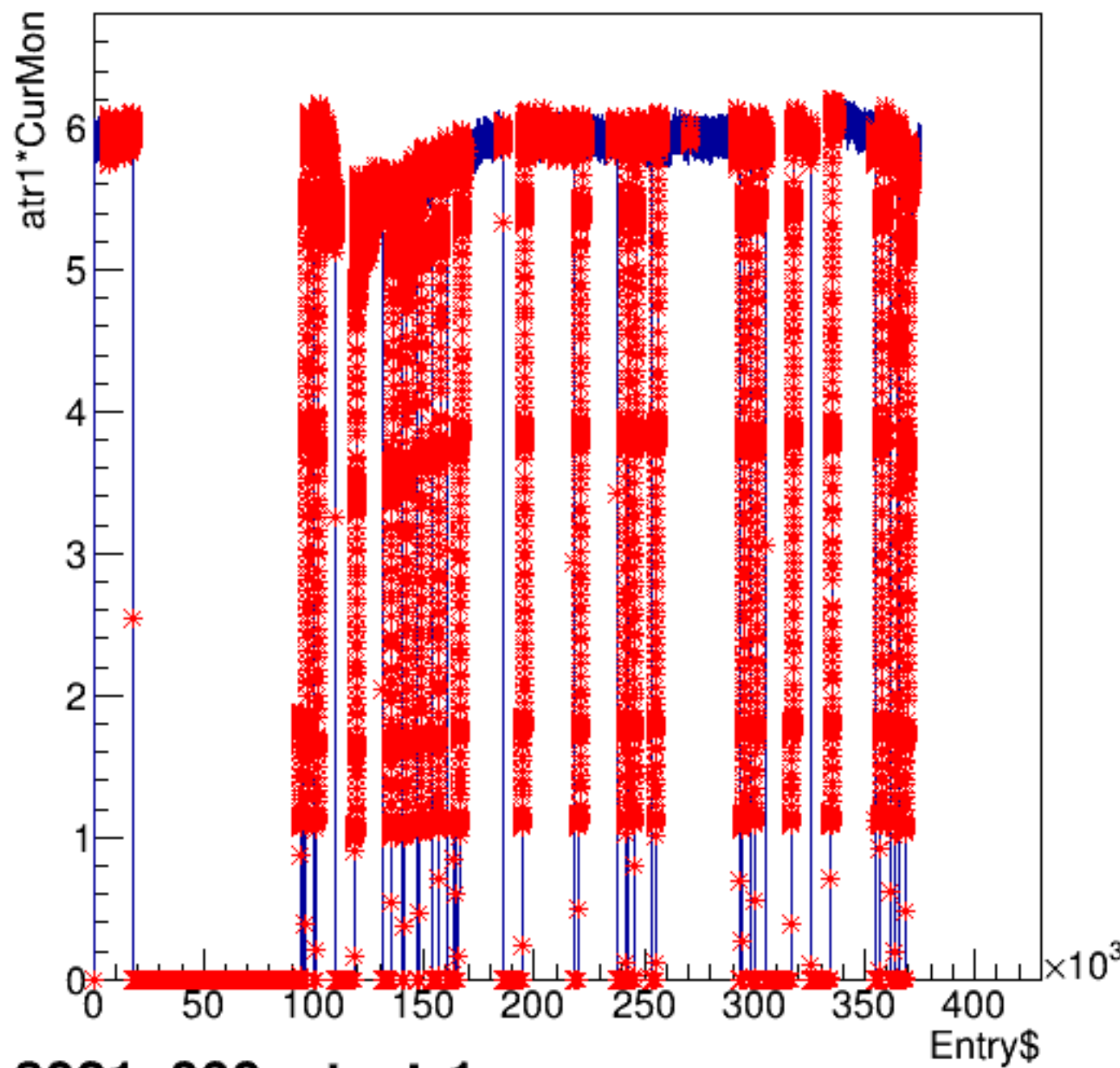
asym_atl2/ppm:pattern_number {ErrorFlag==0}



asym_atl2

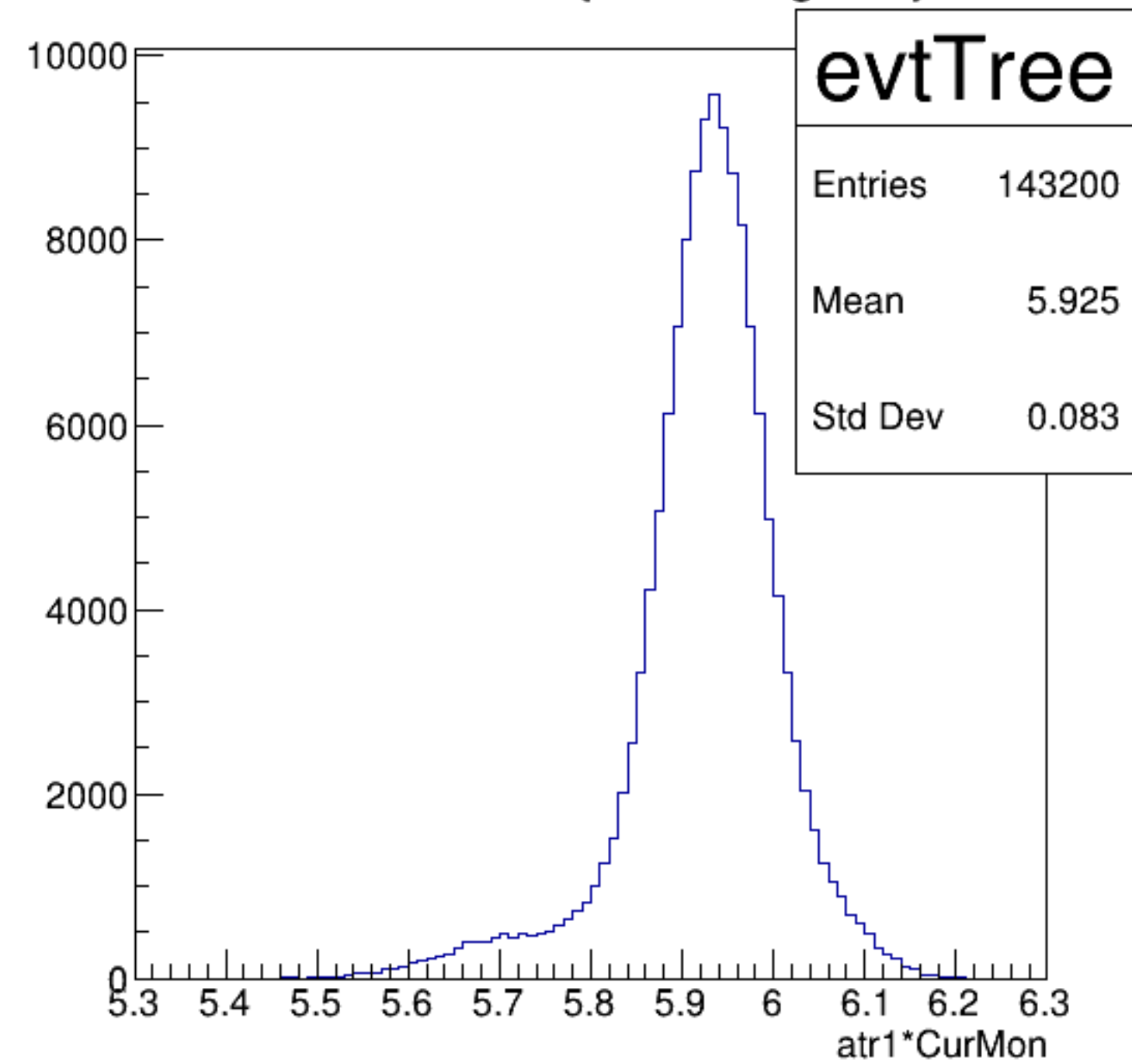


atr1*CurMon:Entry\$

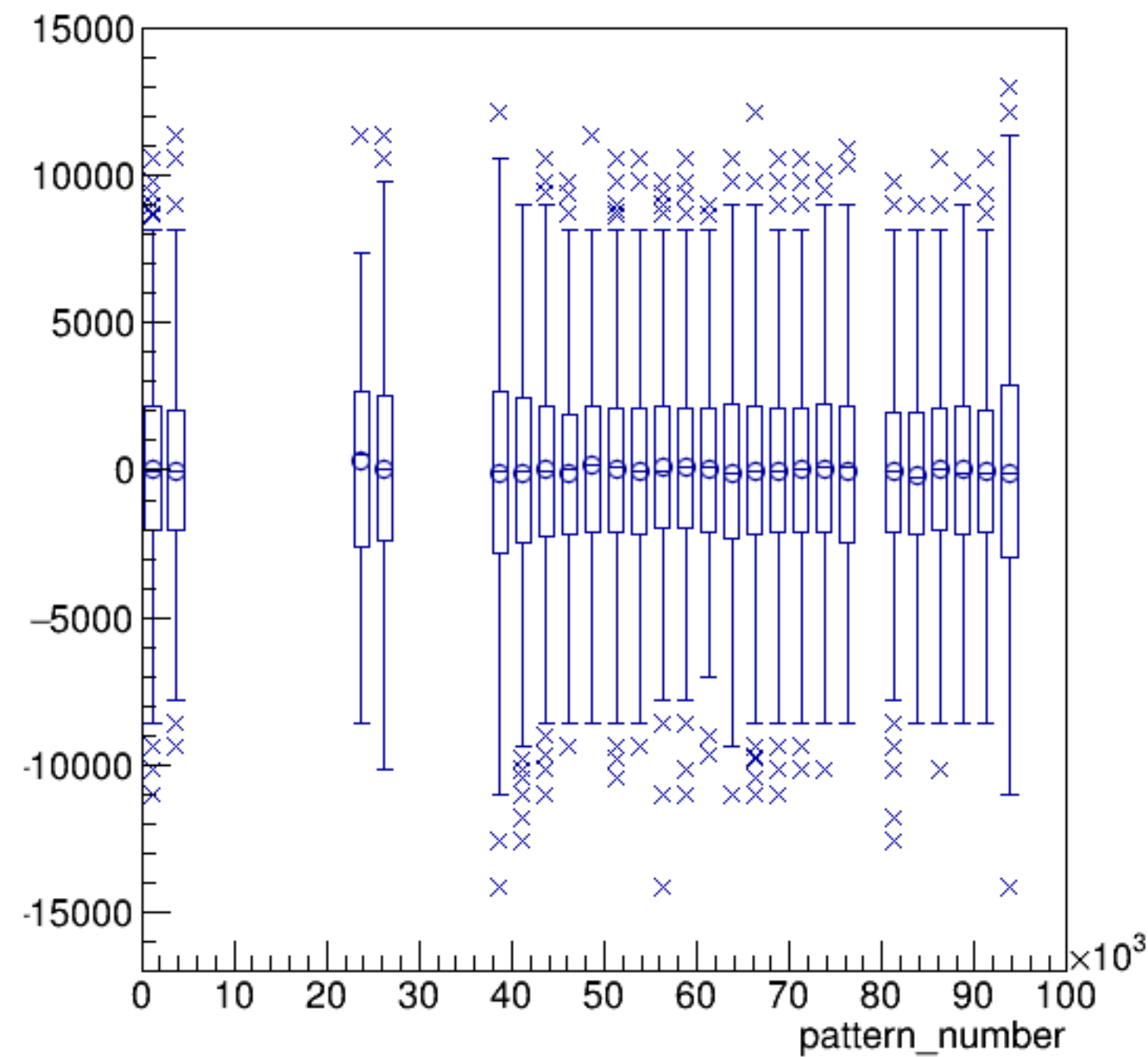


run8021_000_at_atr1.png

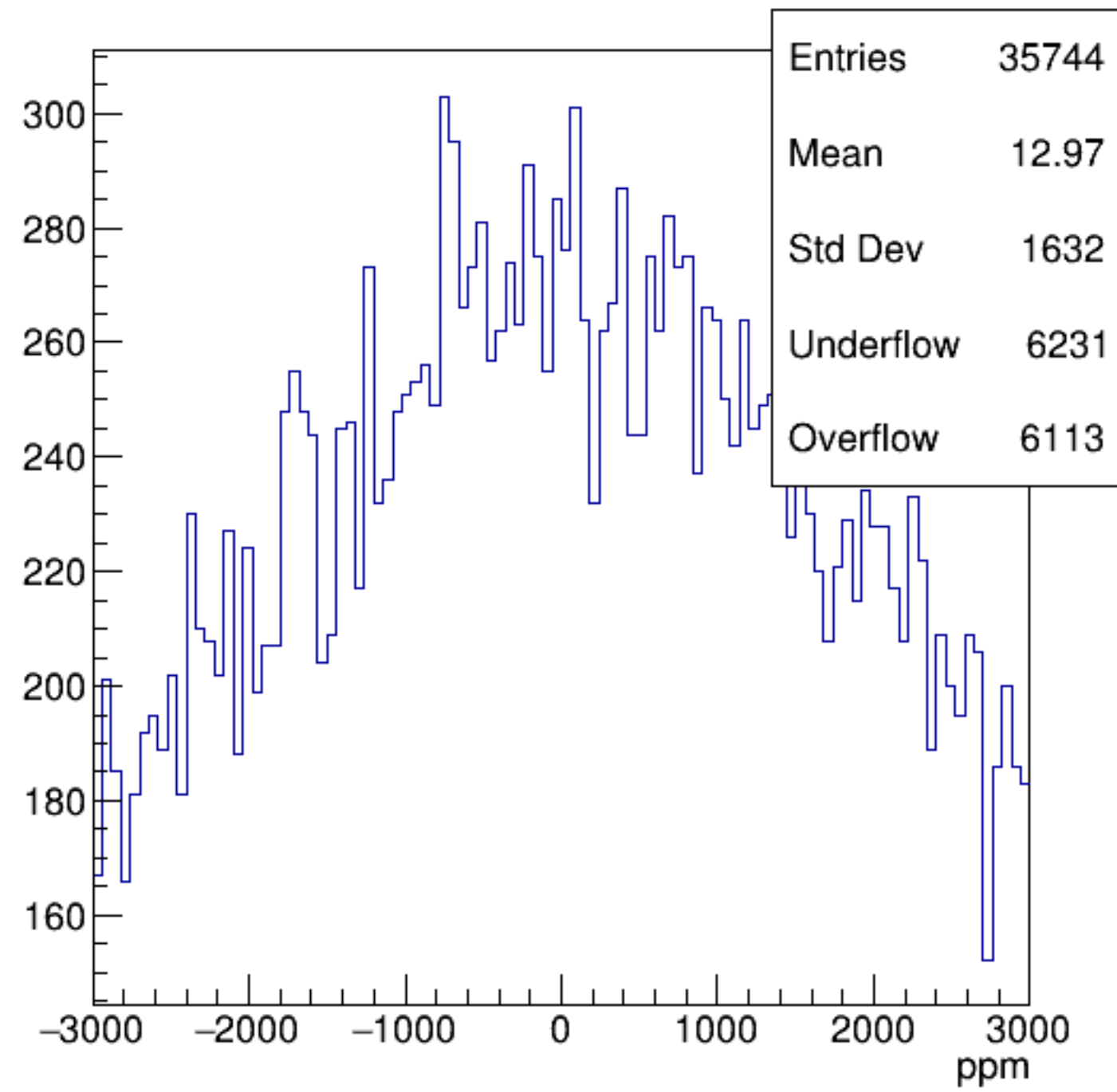
atr1*CurMon {ErrorFlag==0}



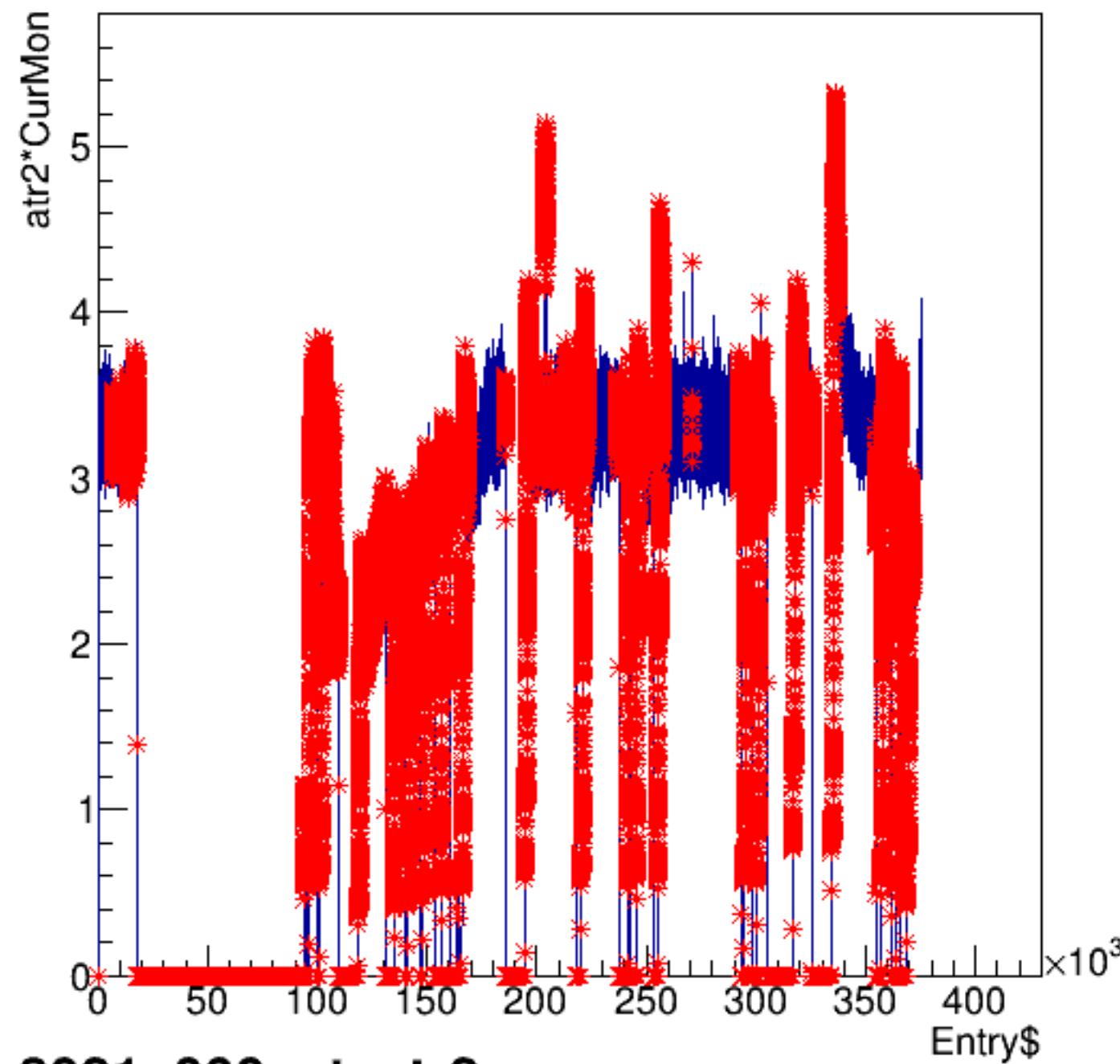
asym_atr1/ppm:pattern_number {ErrorFlag==0}



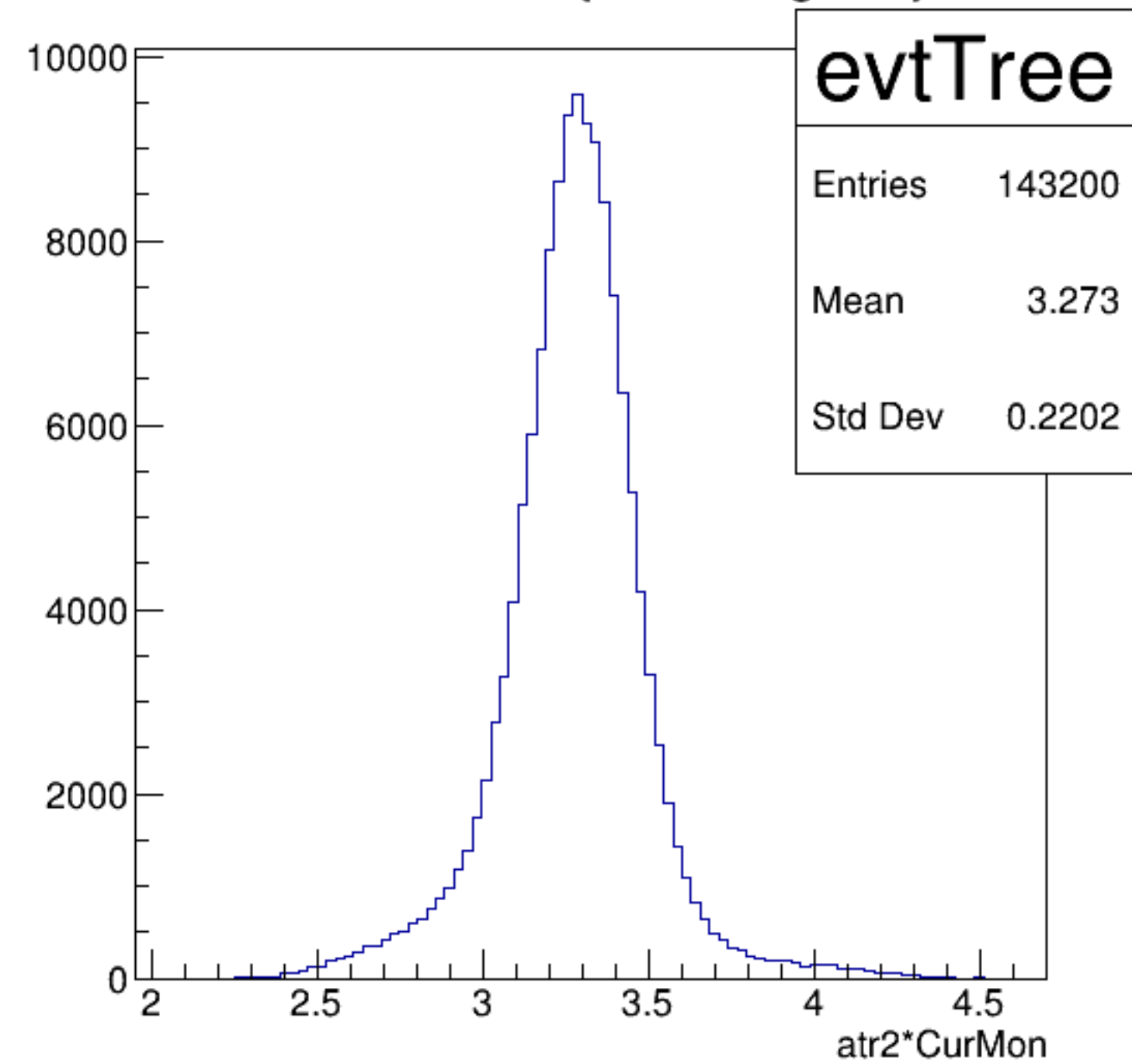
asym_atr1



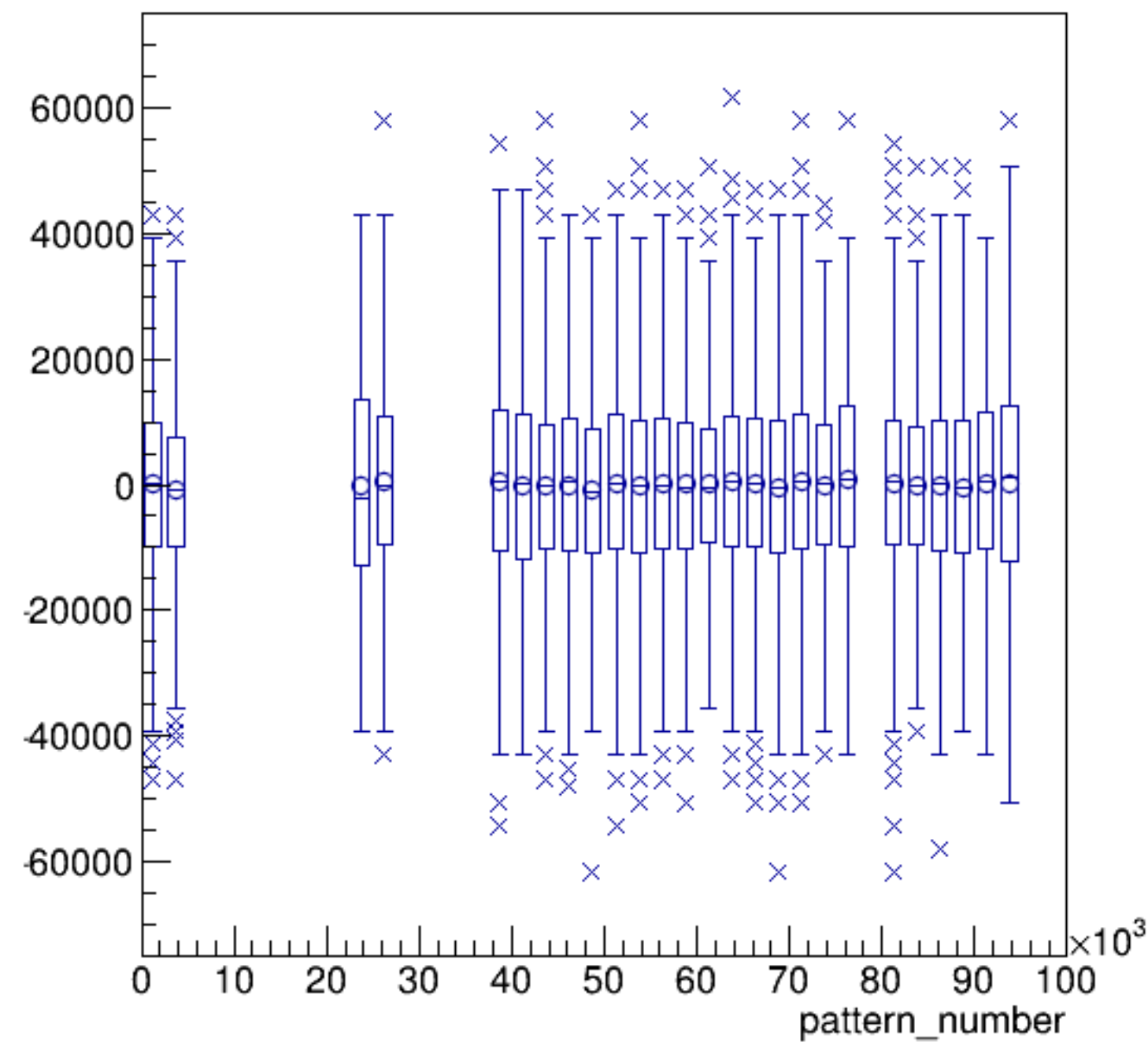
atr2*CurMon:Entry\$



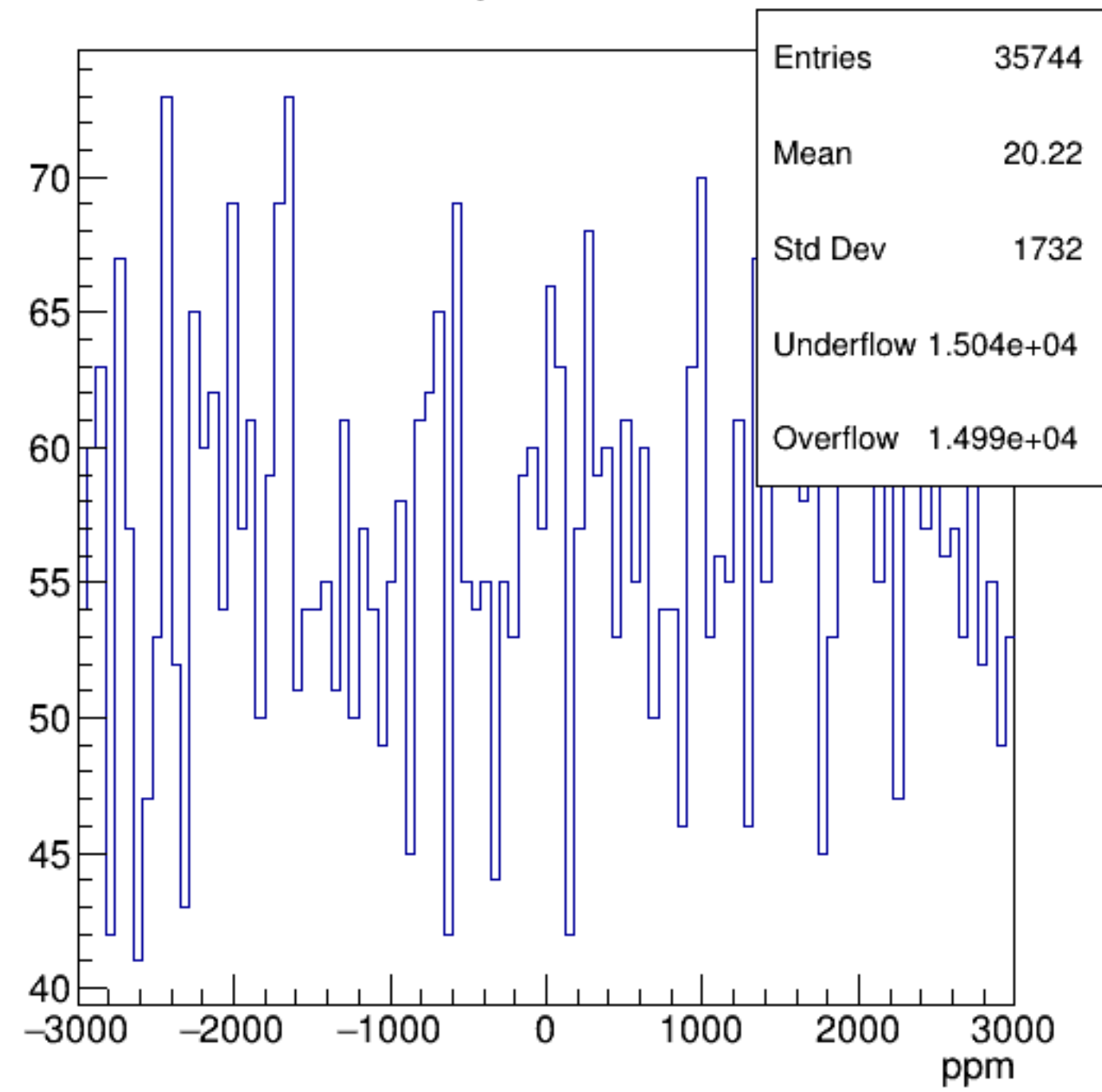
atr2*CurMon {ErrorFlag==0}



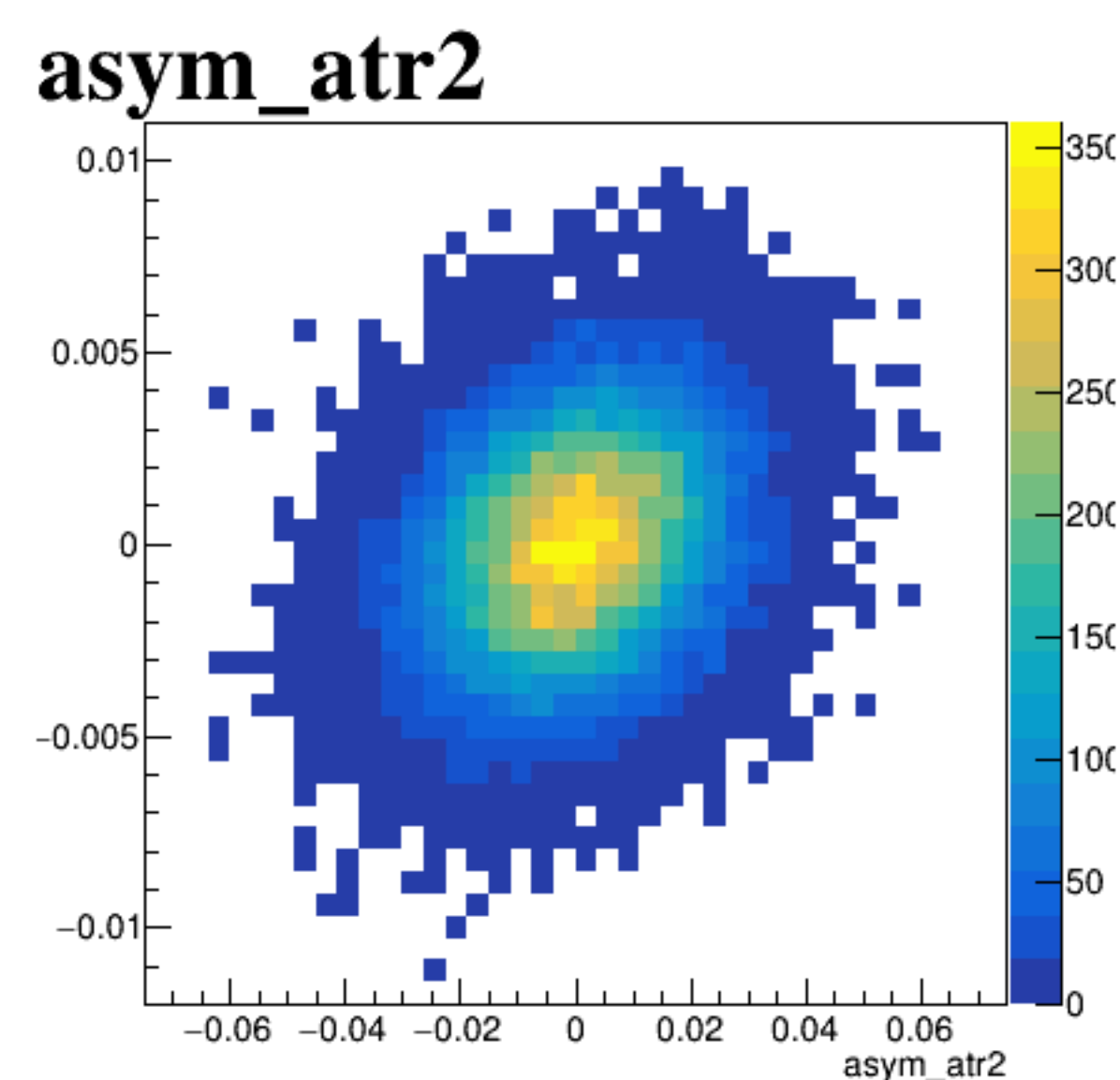
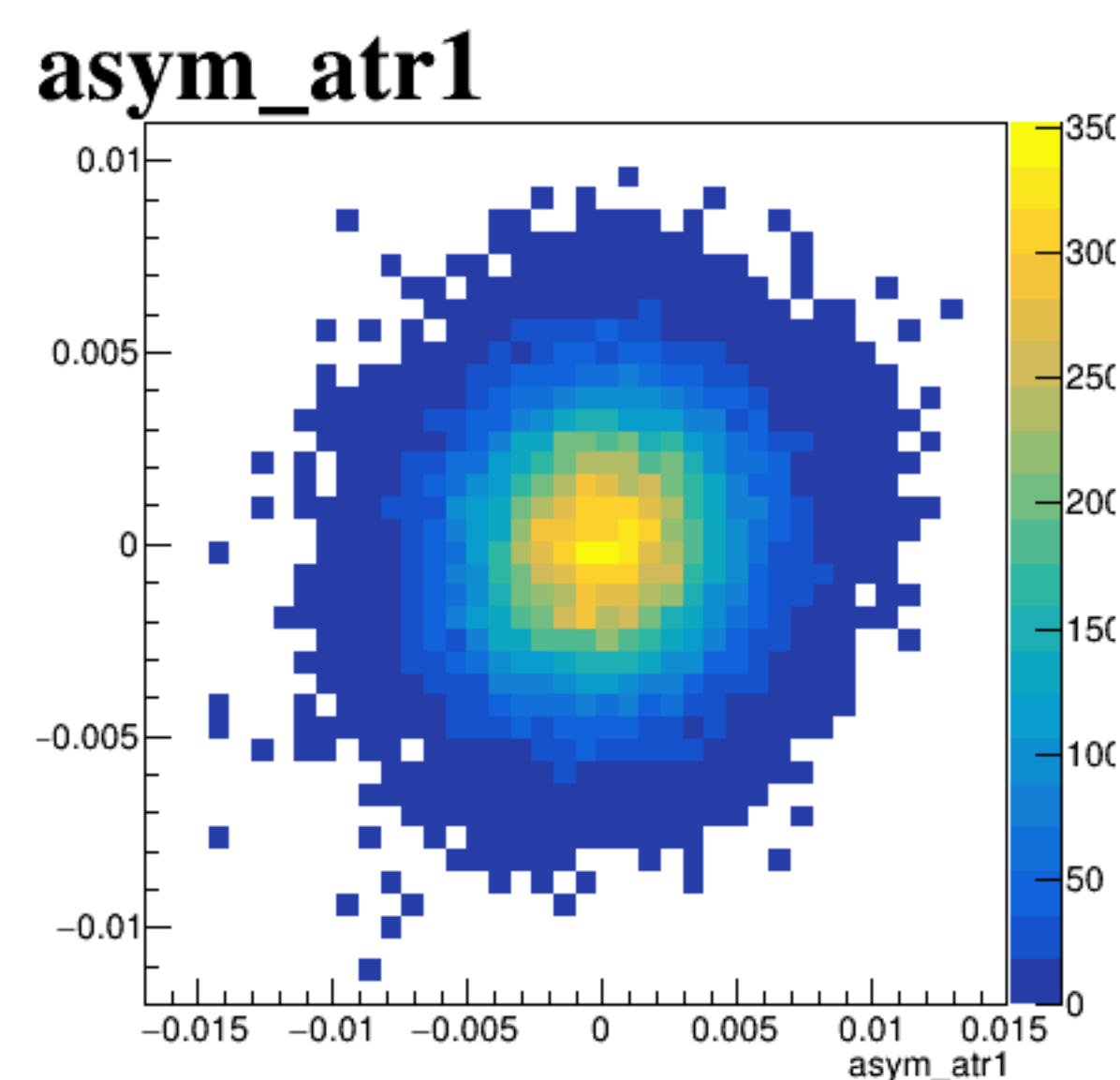
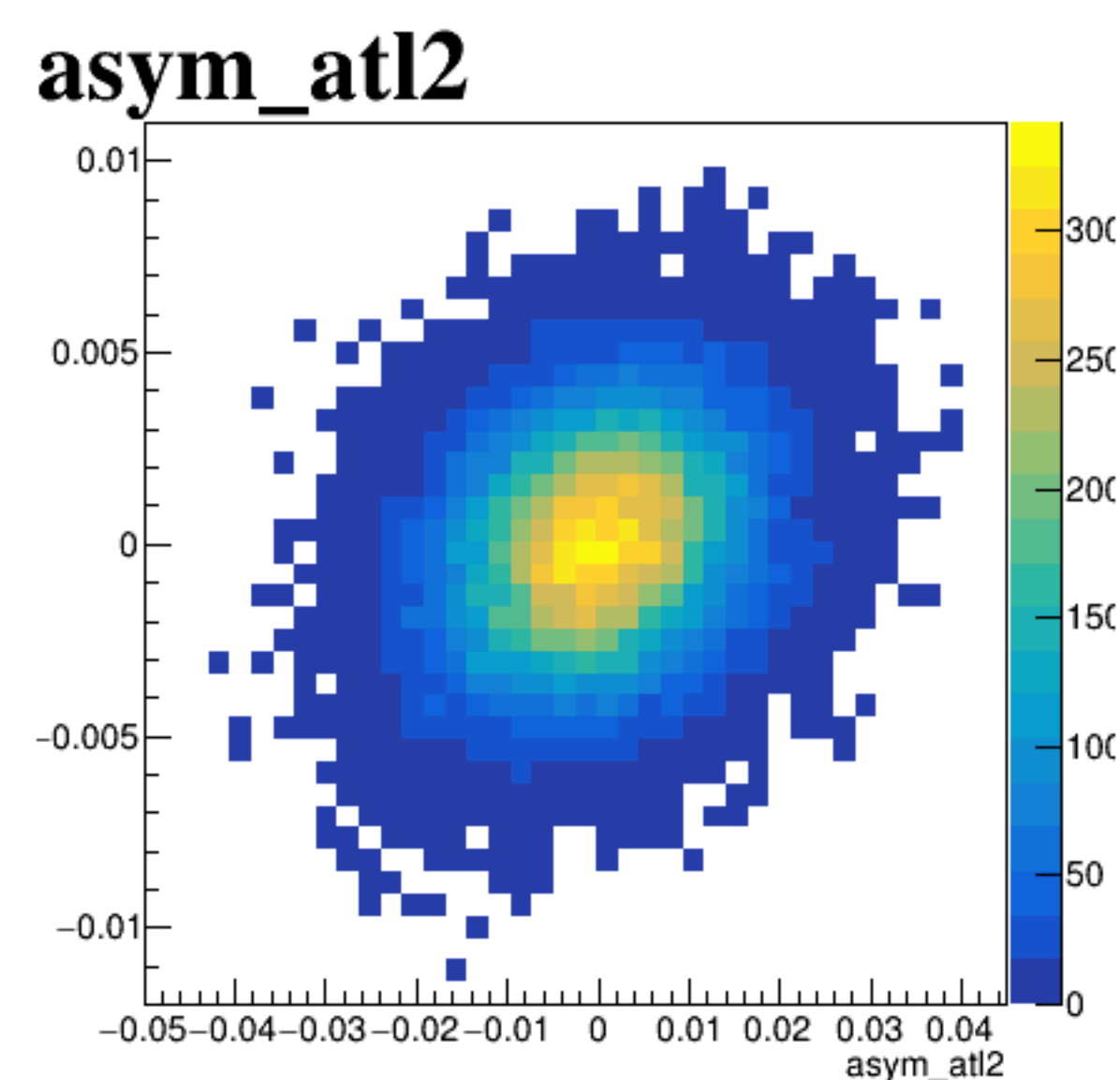
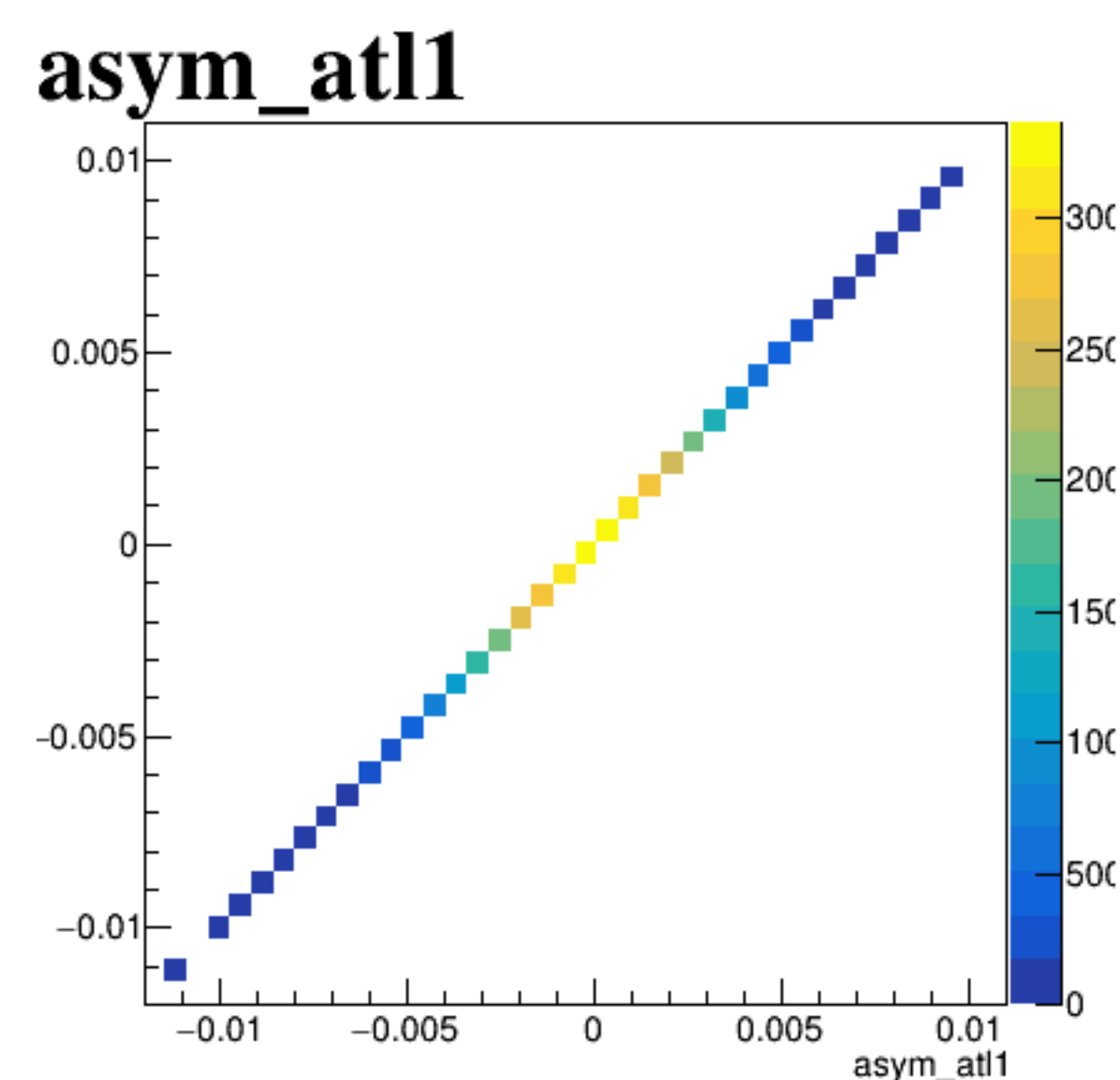
asym_atr2/ppm:pattern_number {ErrorFlag==0}



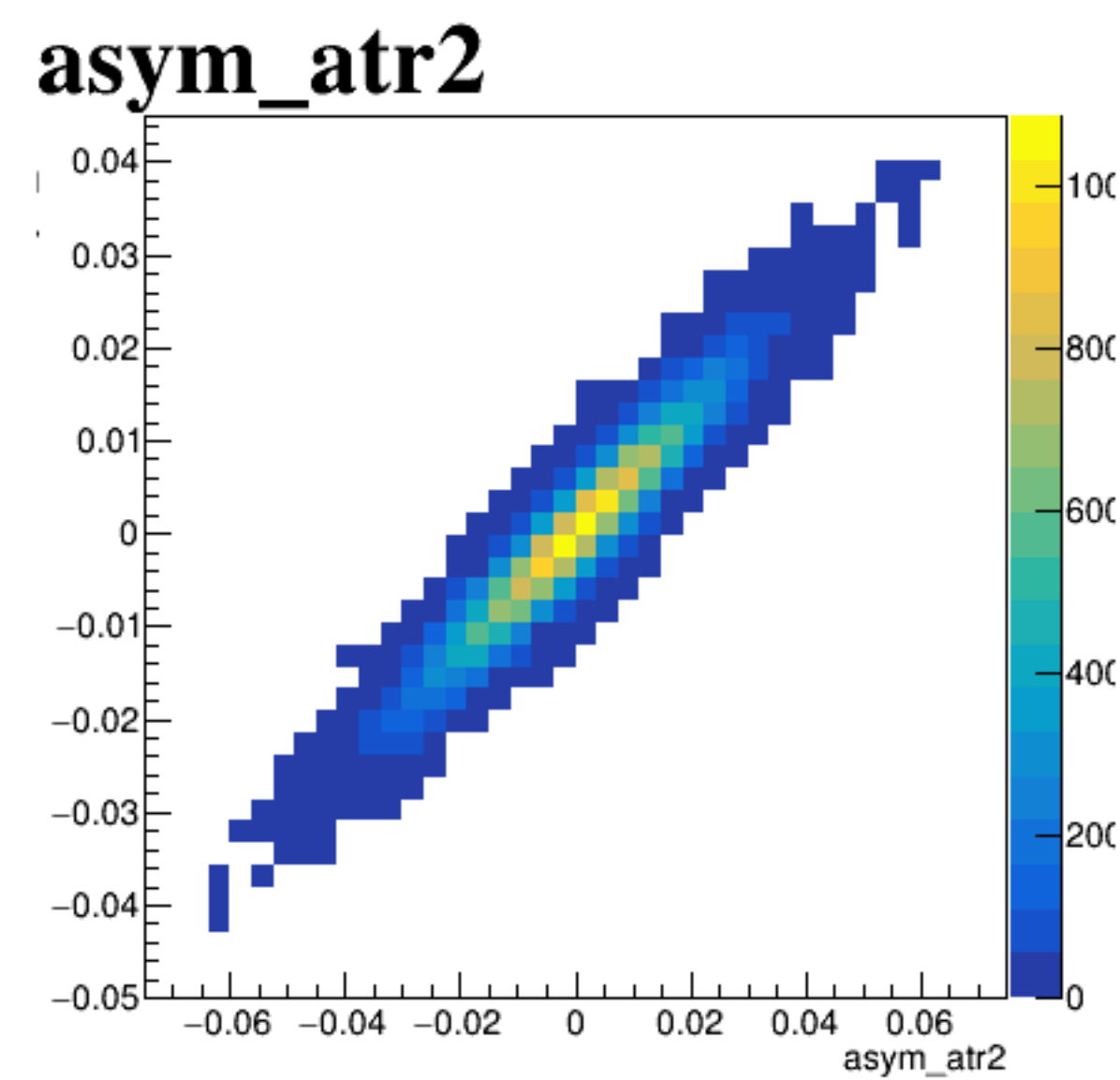
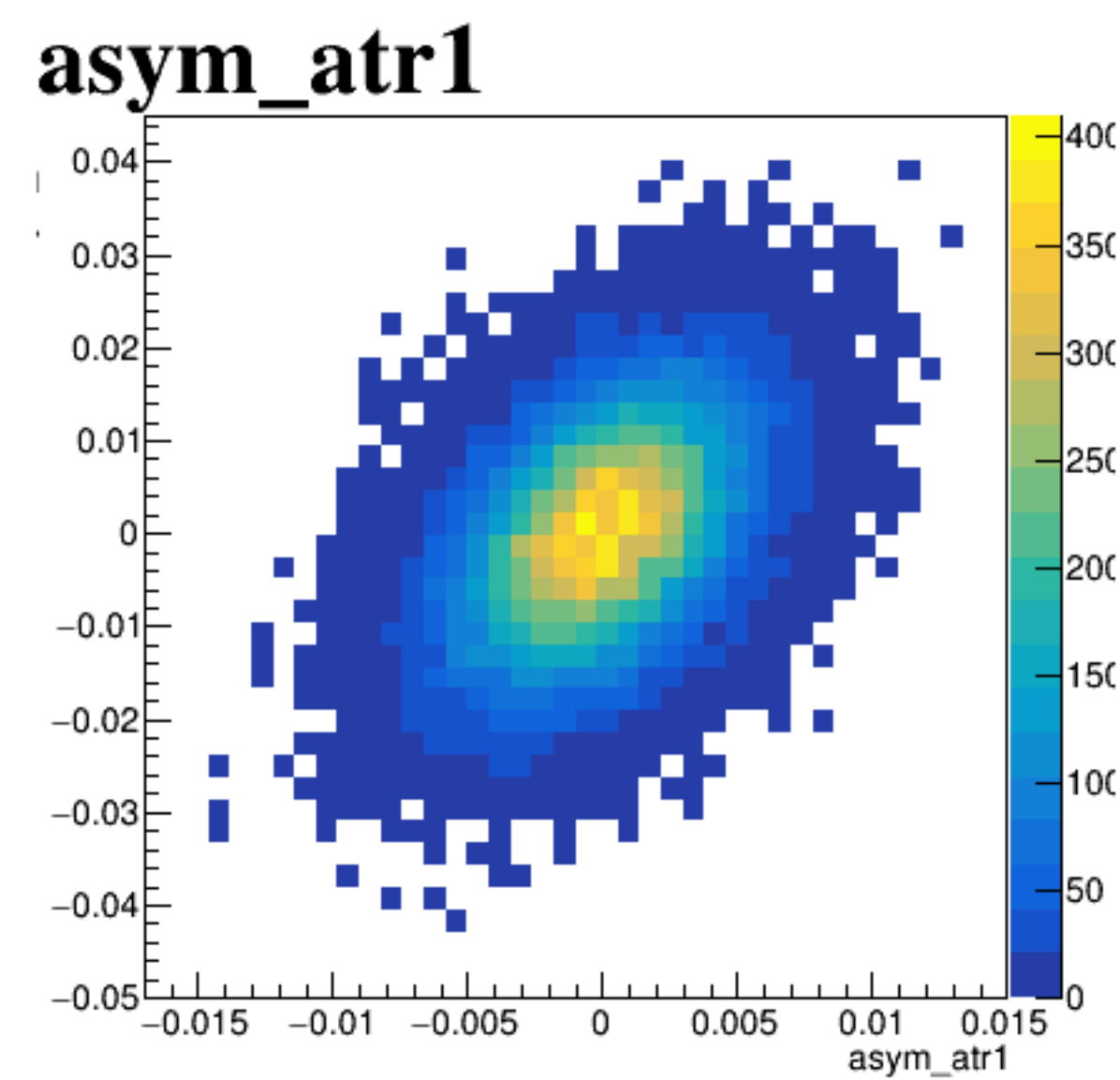
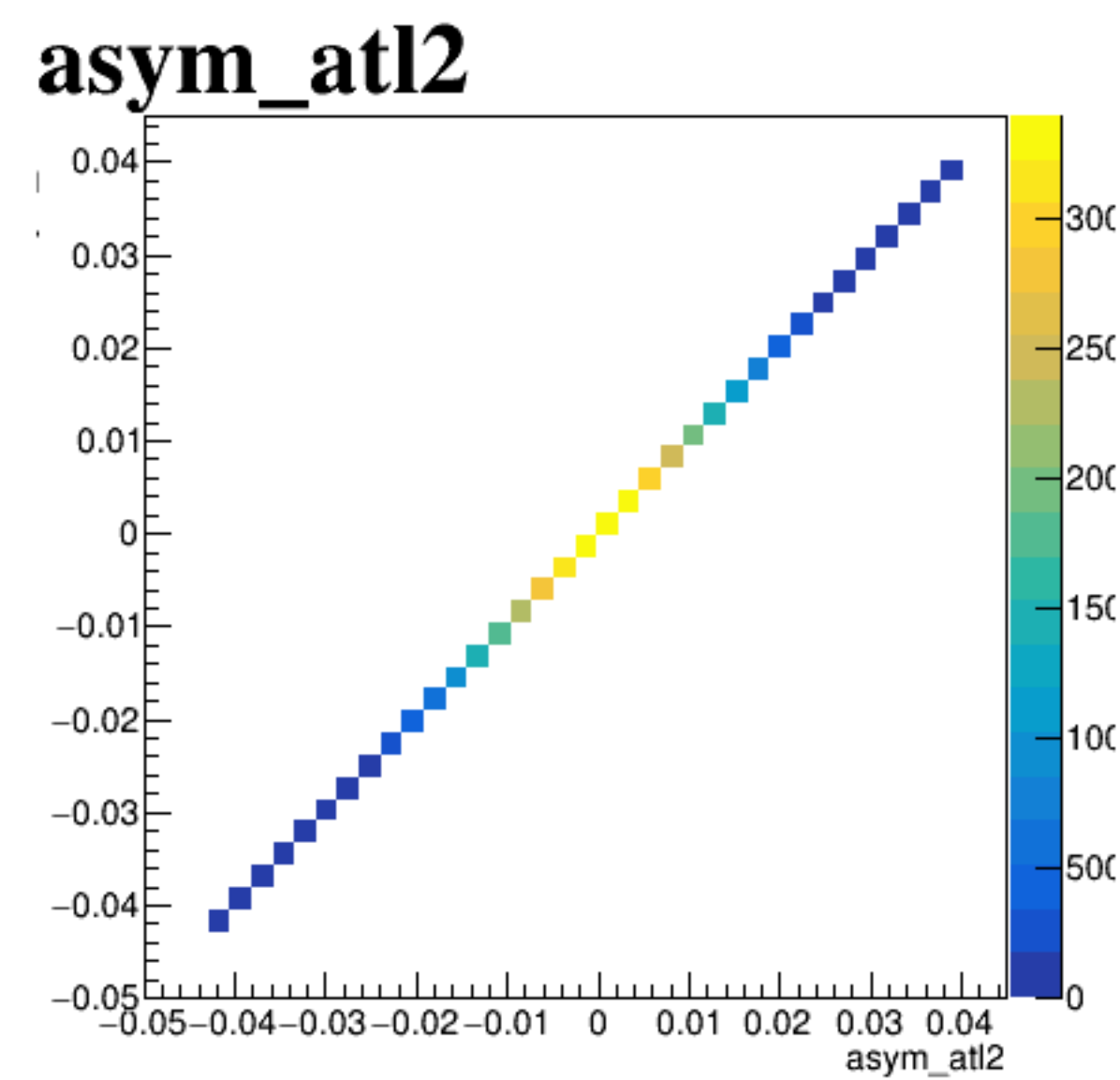
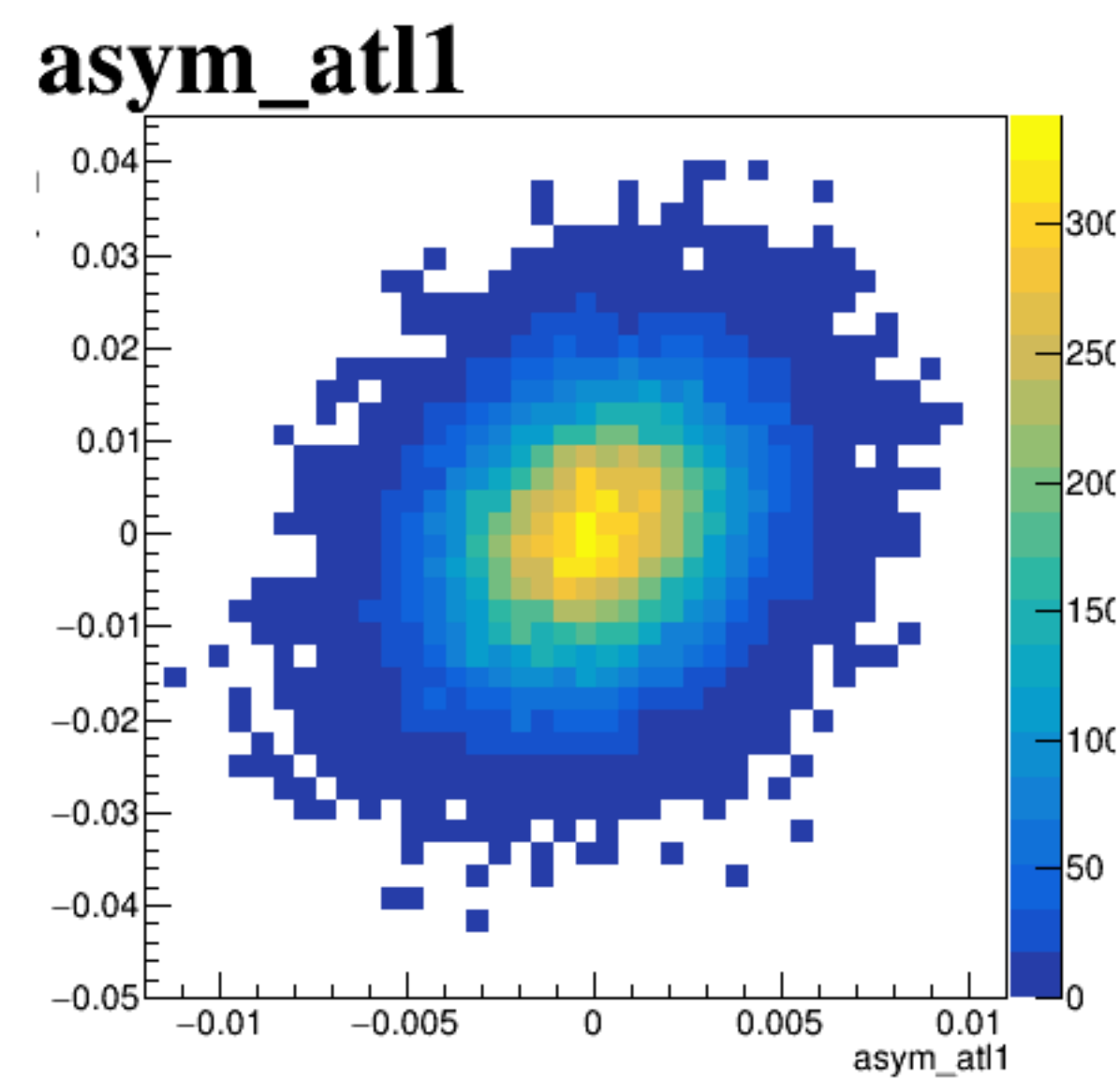
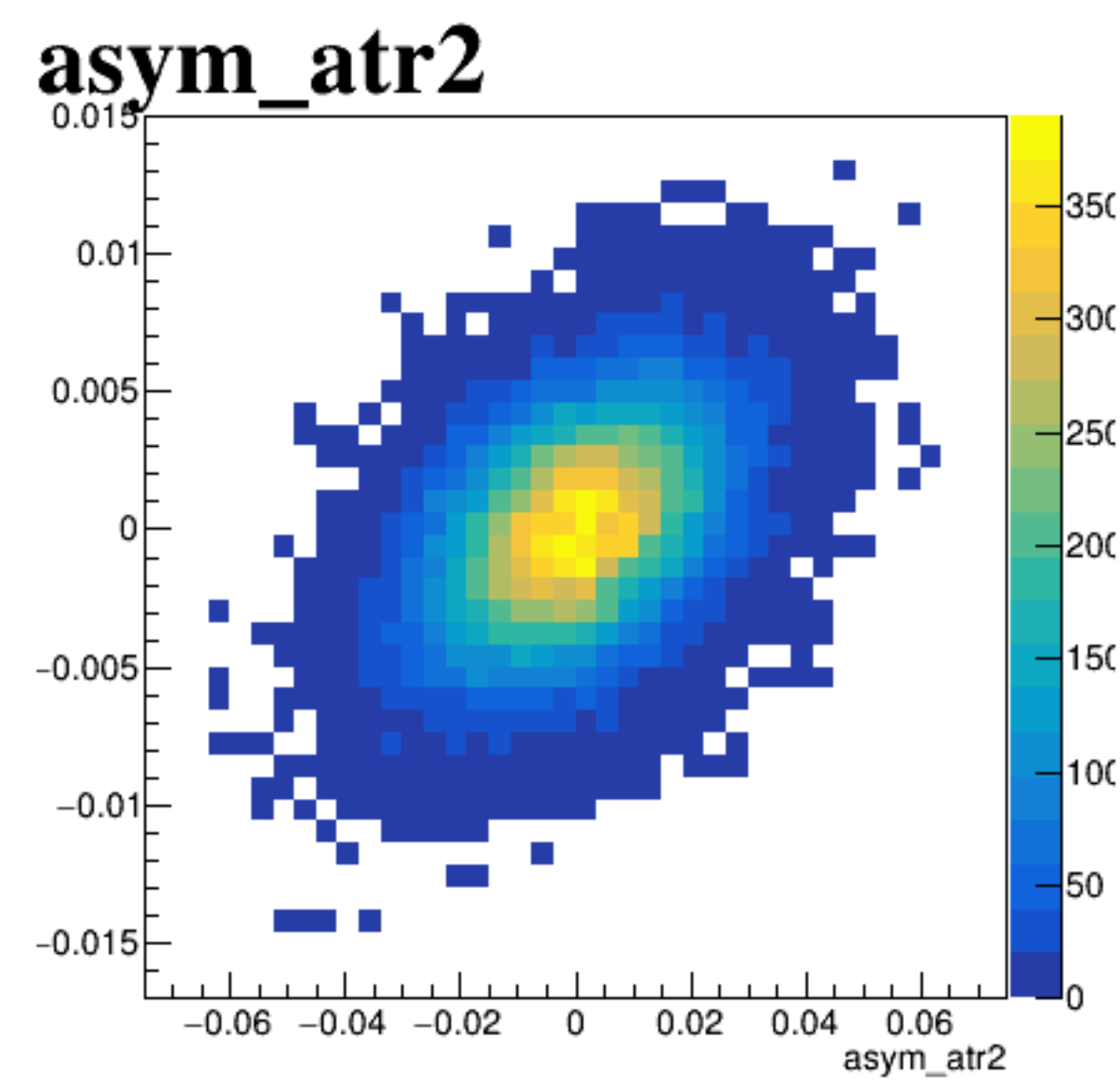
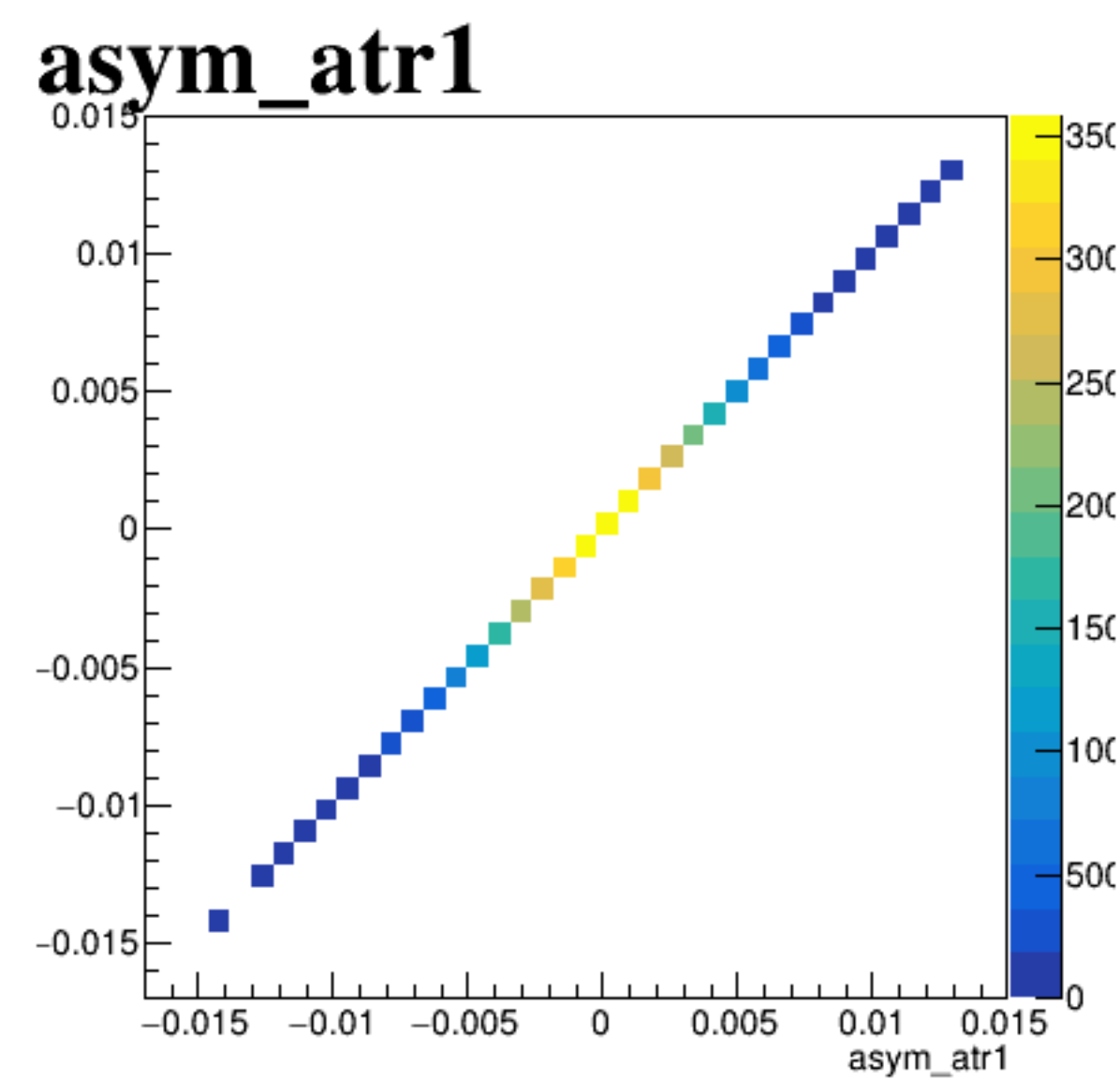
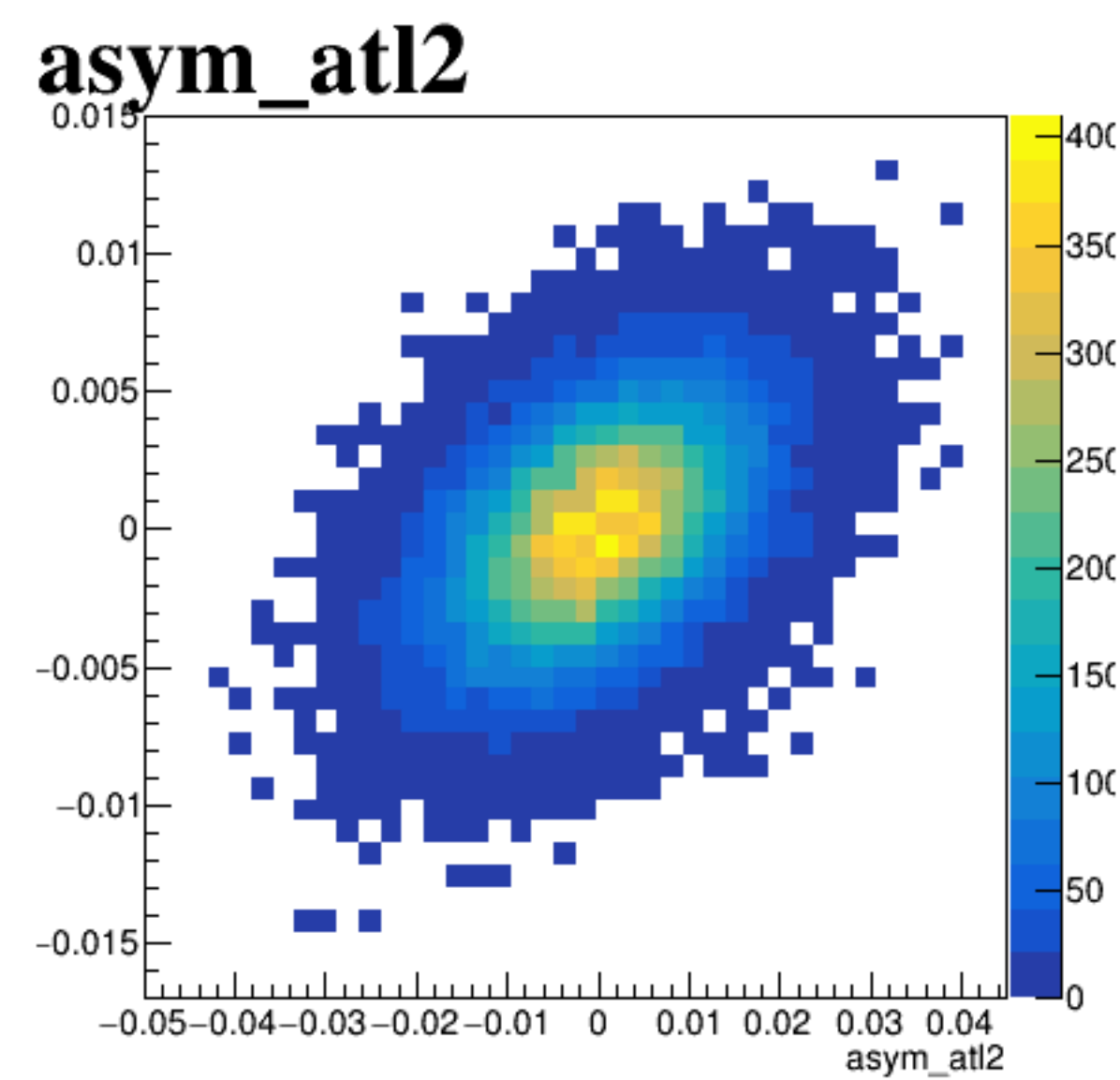
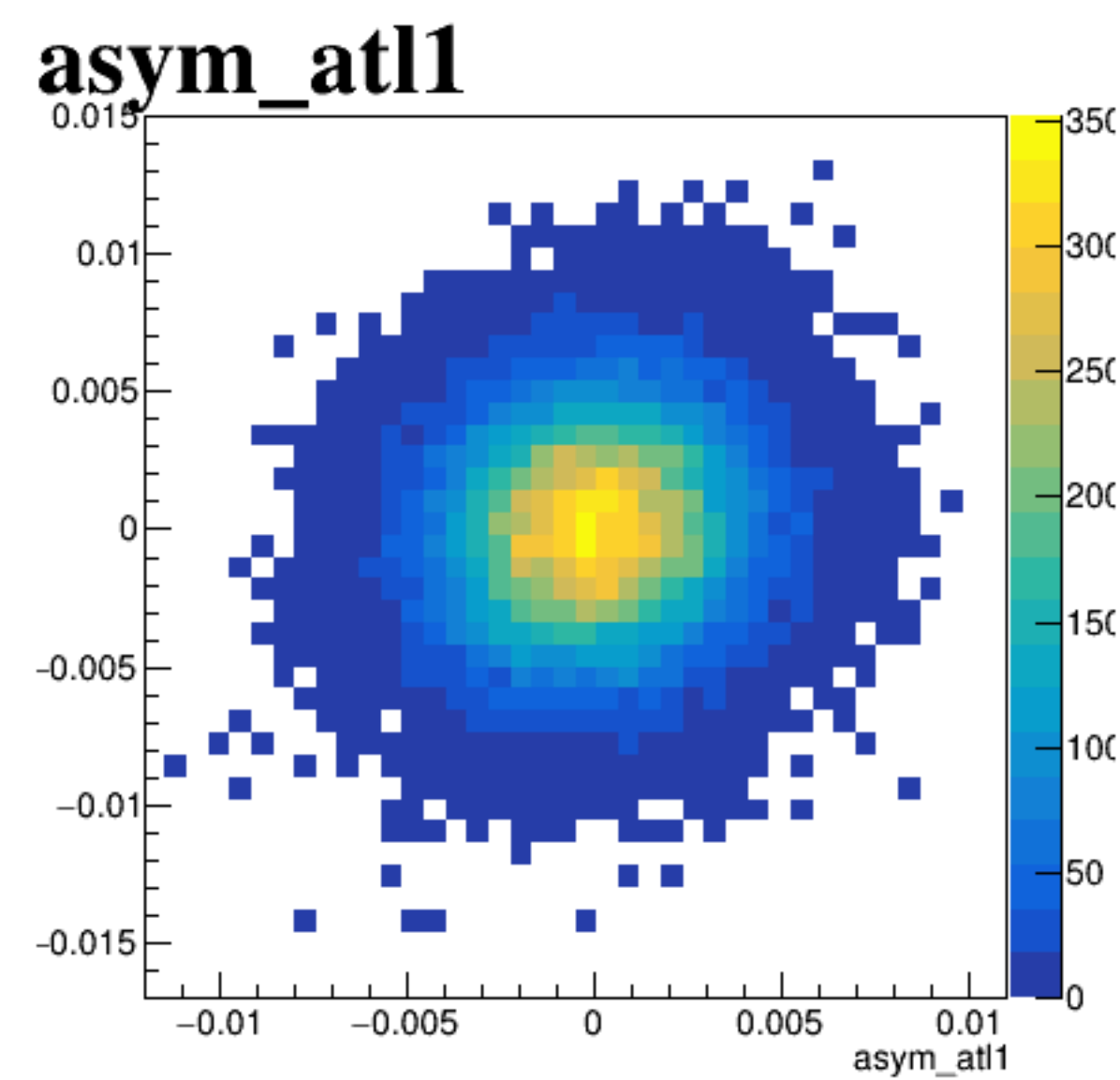
asym_atr2



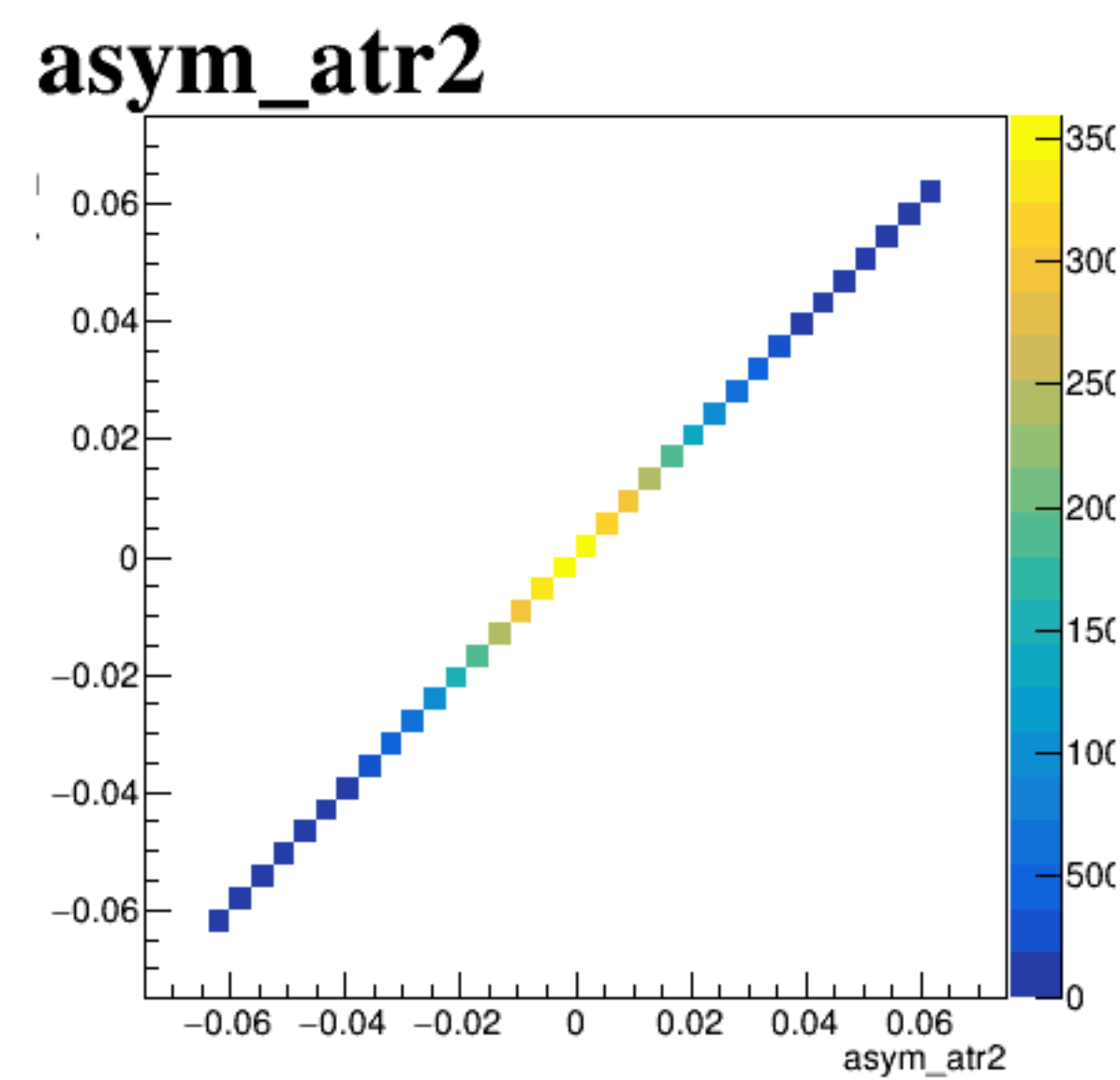
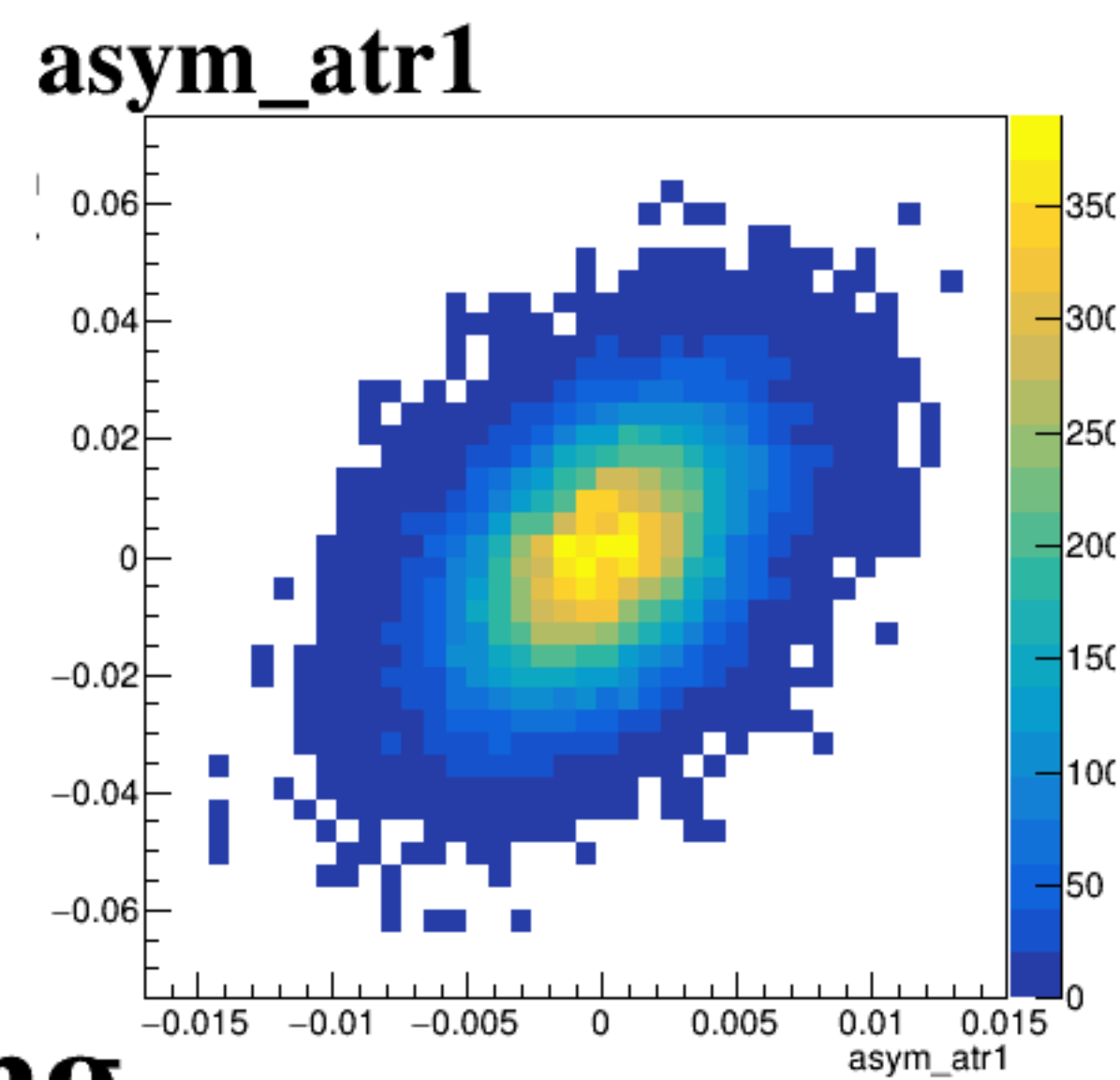
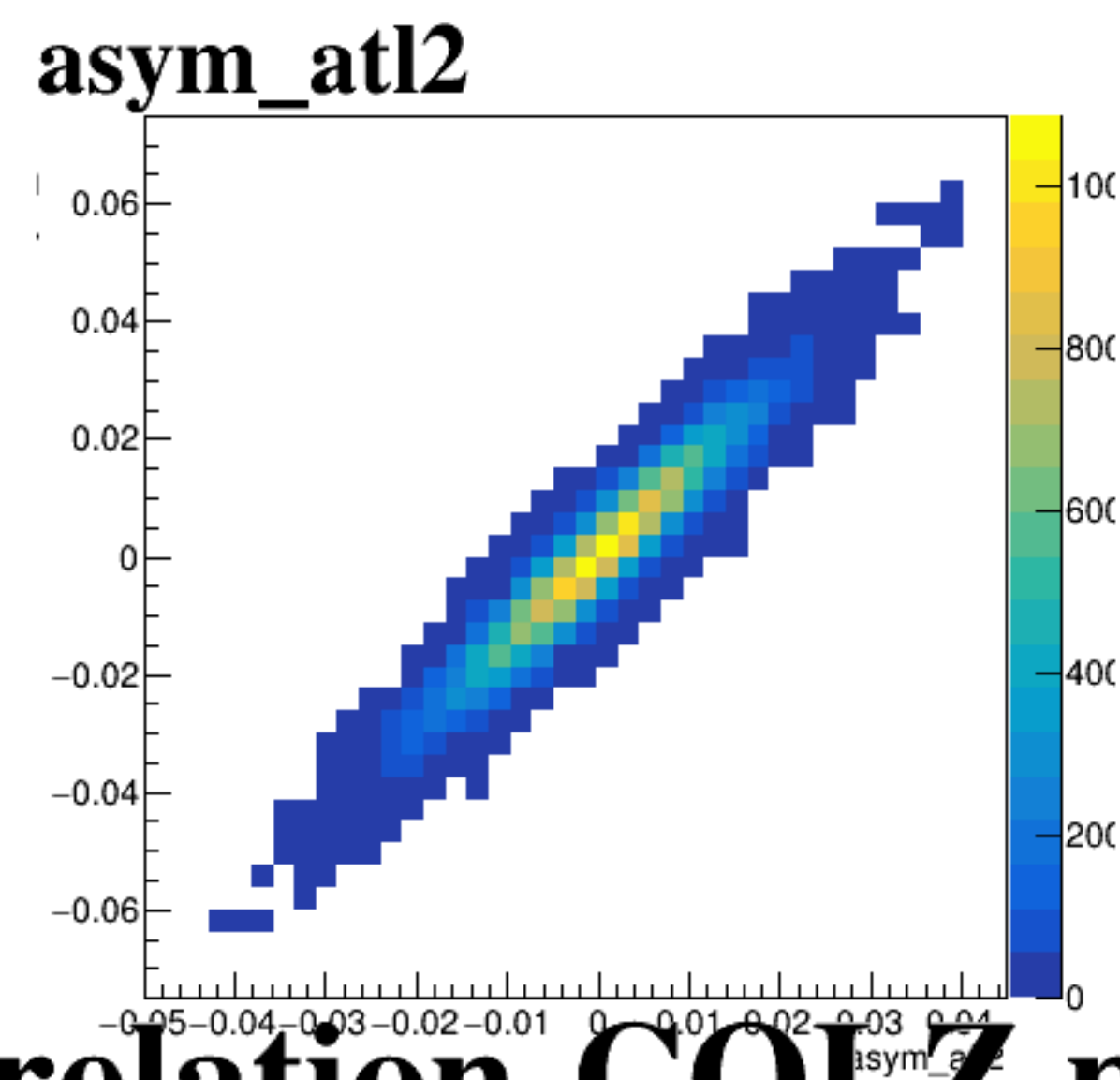
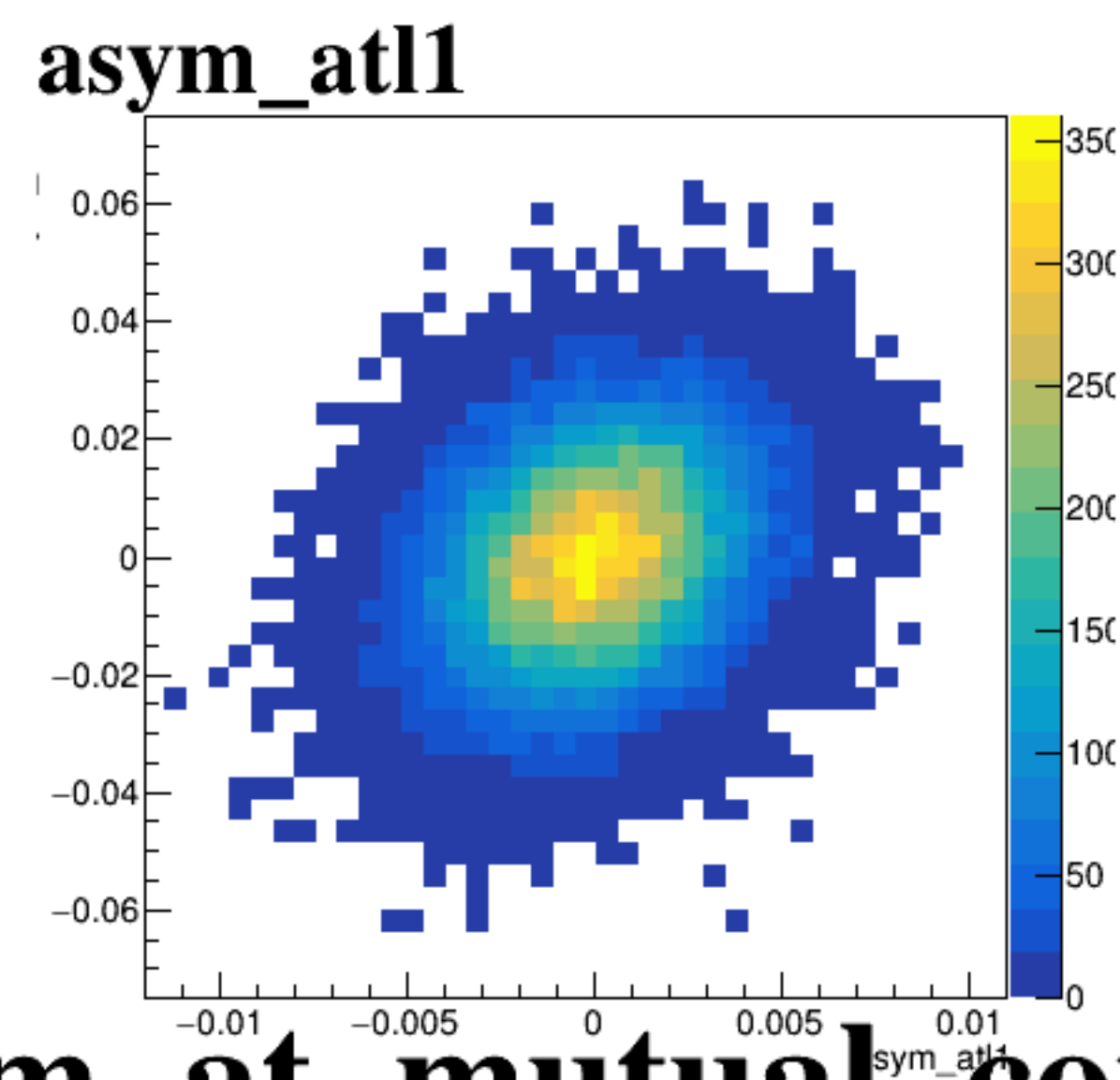
asym_atl1



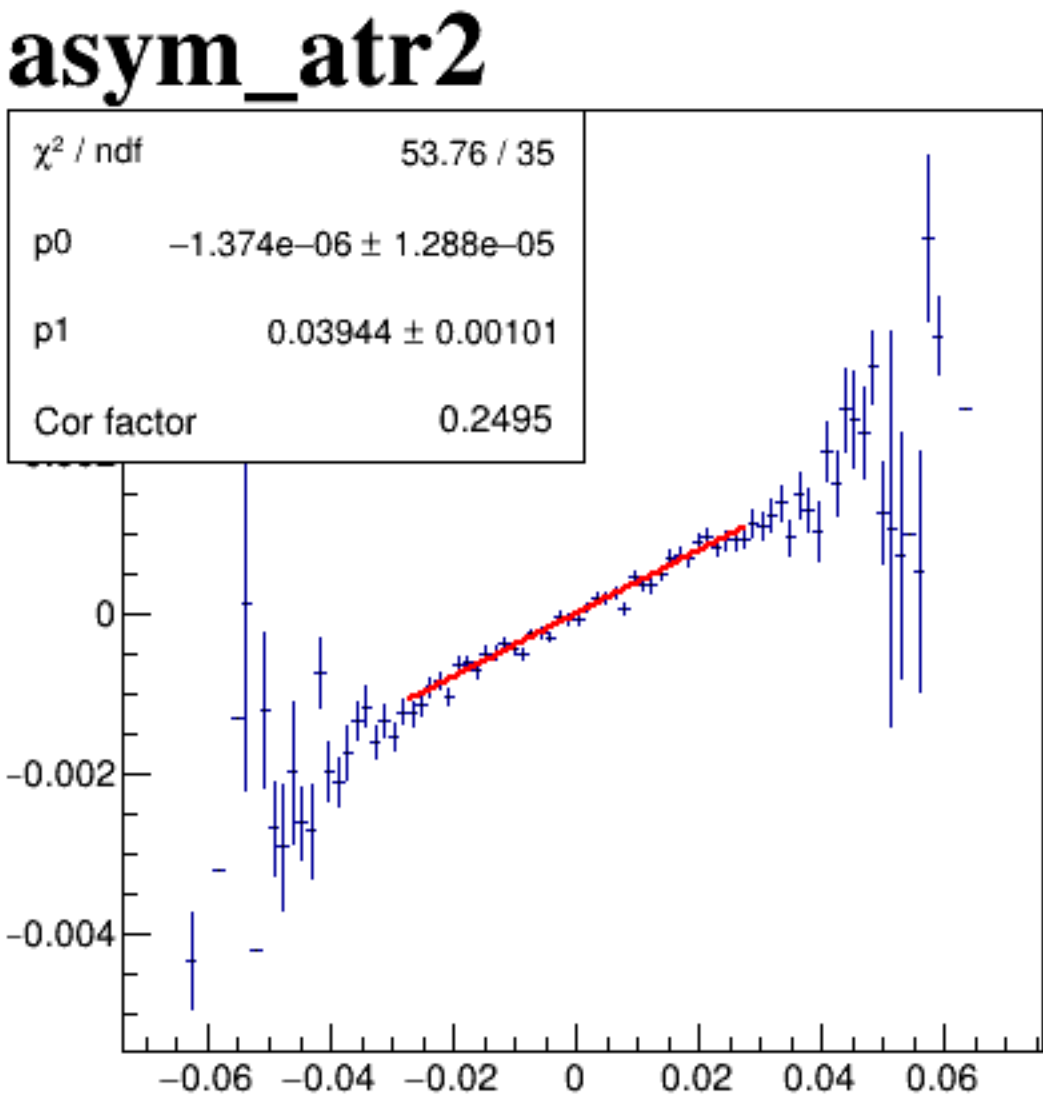
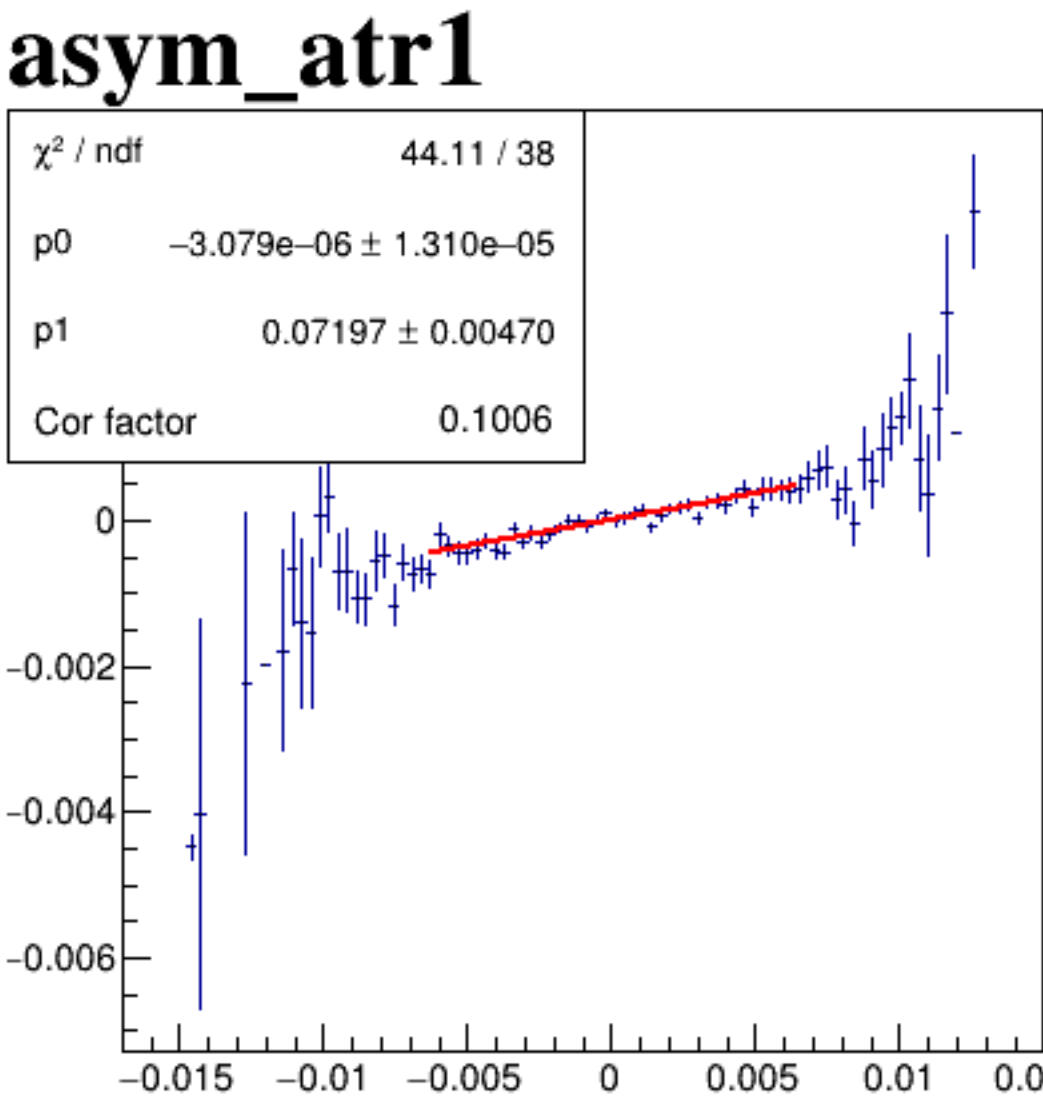
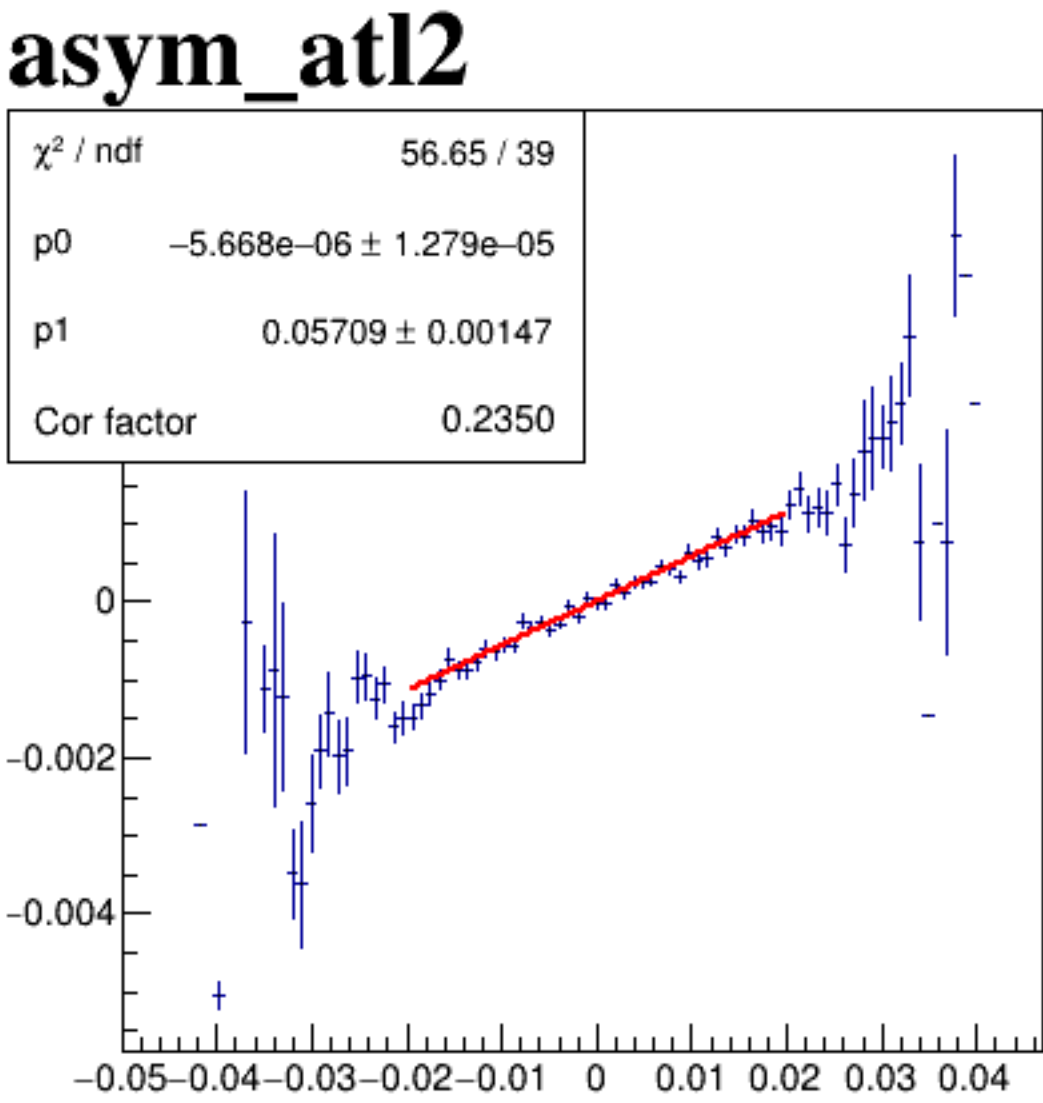
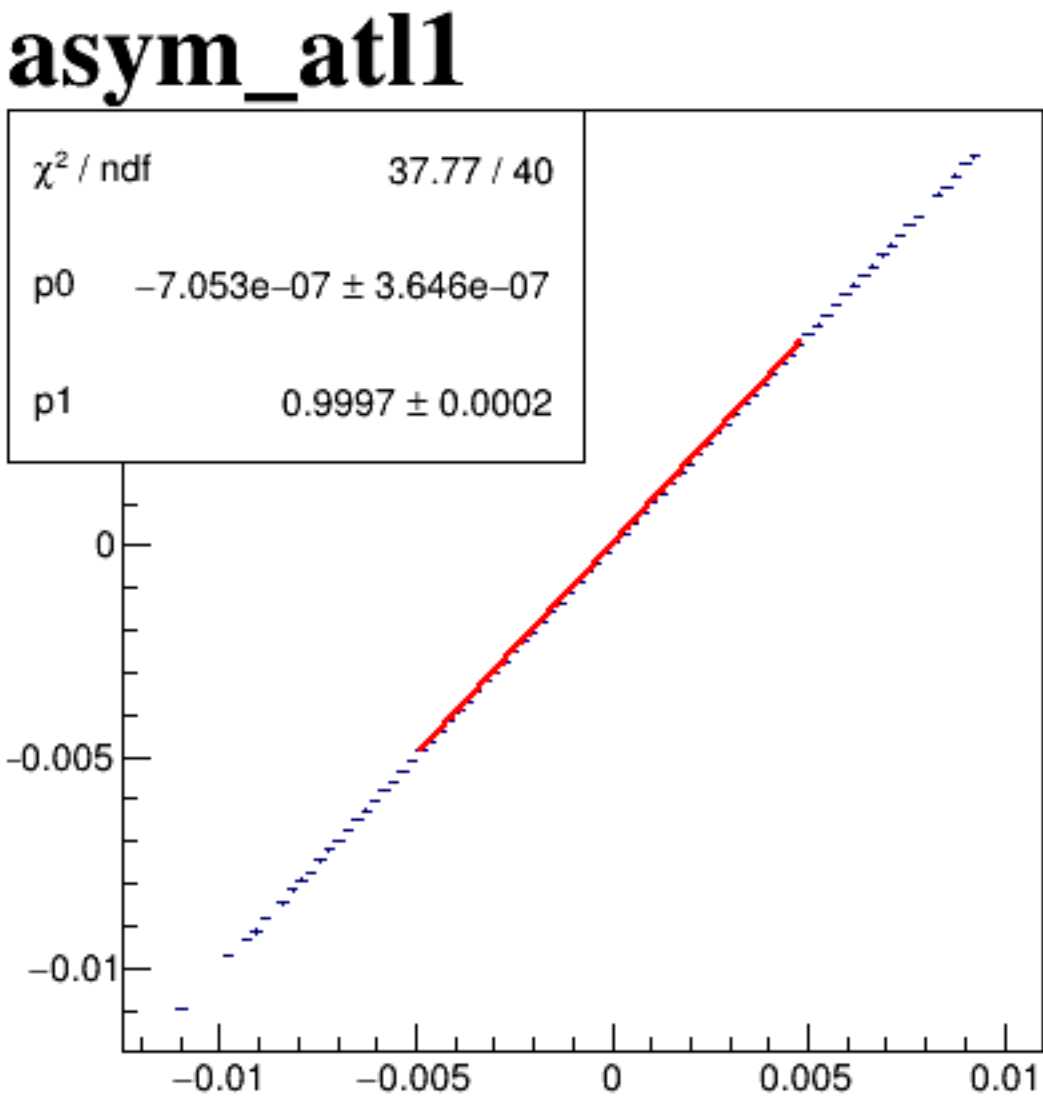
asym_atl2

**asym_atr1**

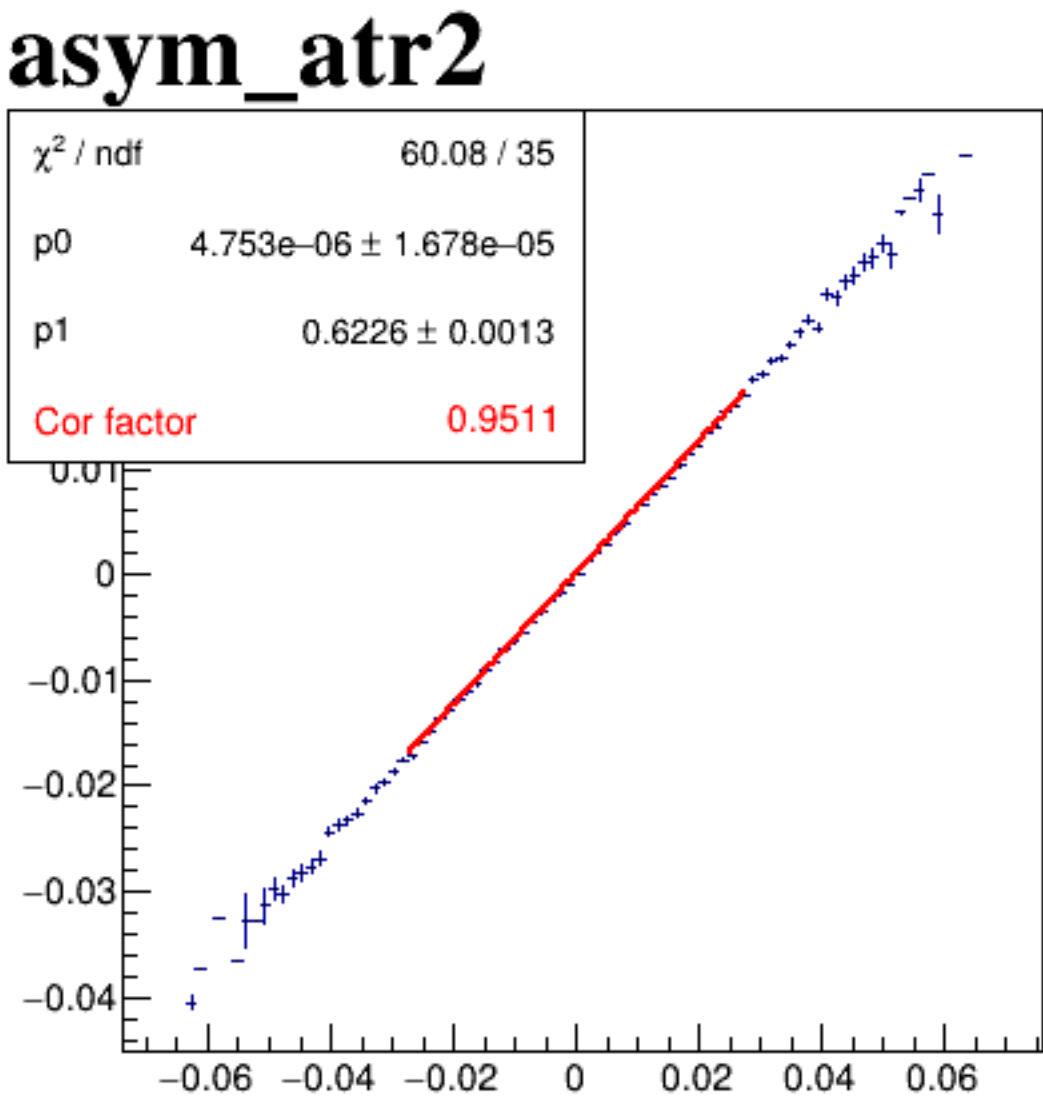
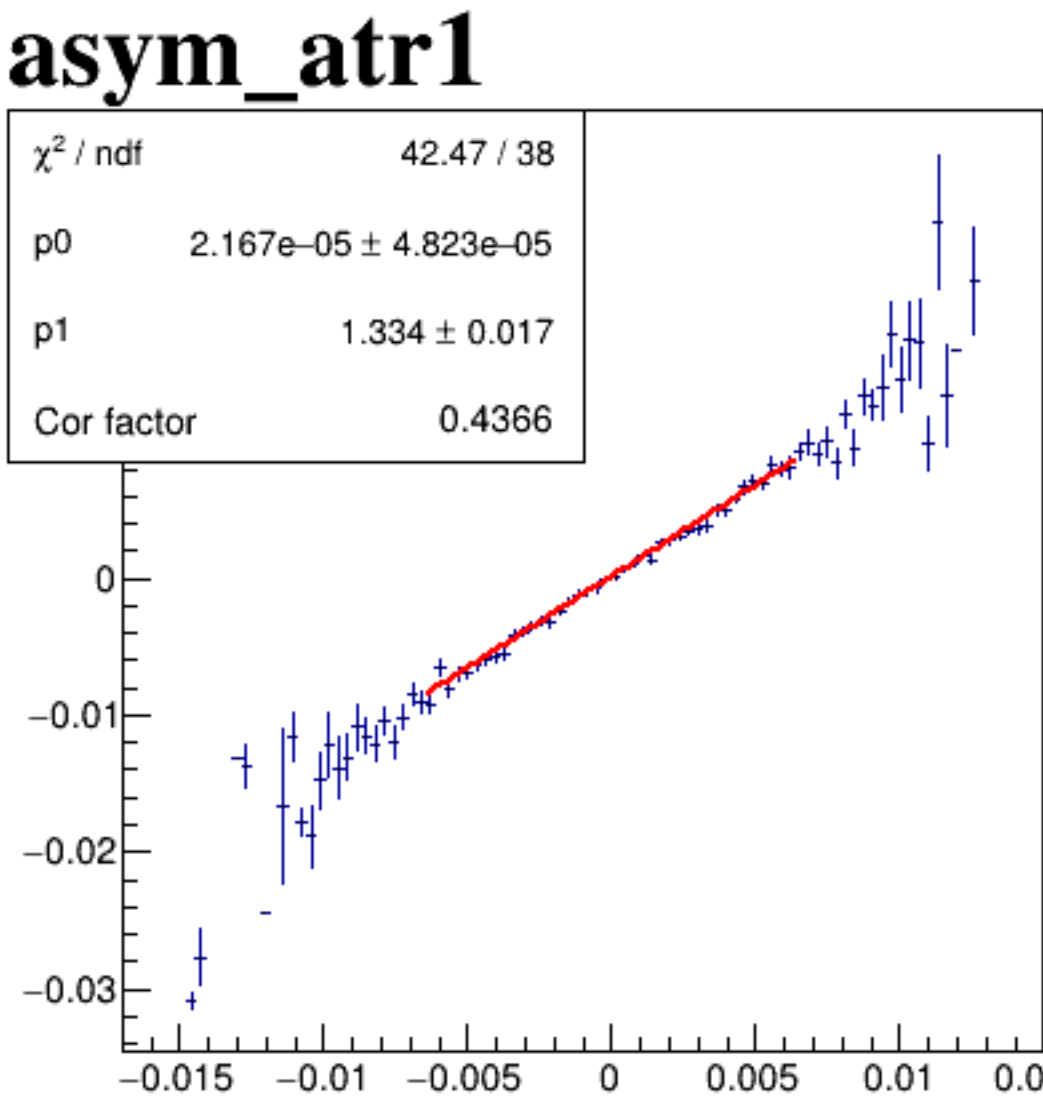
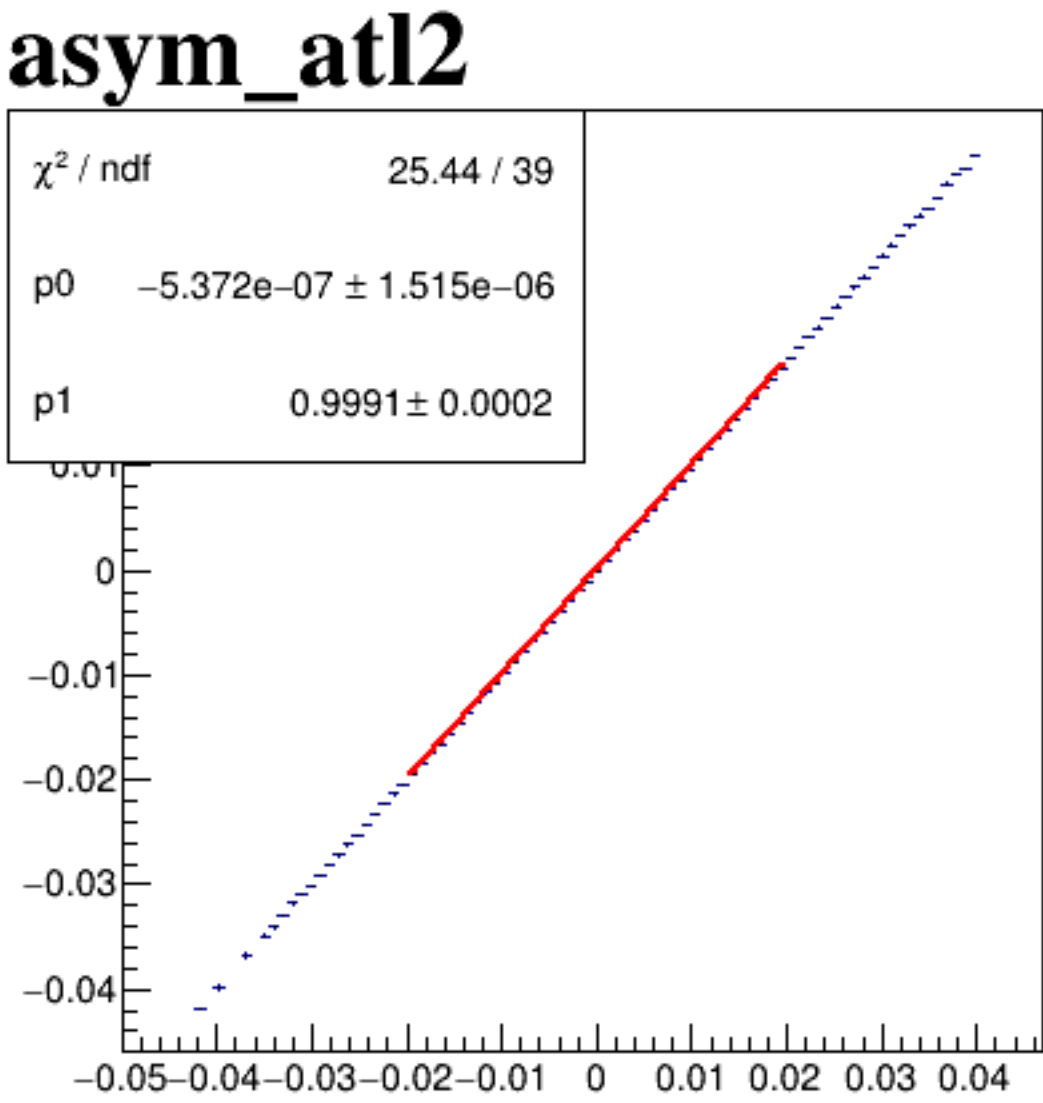
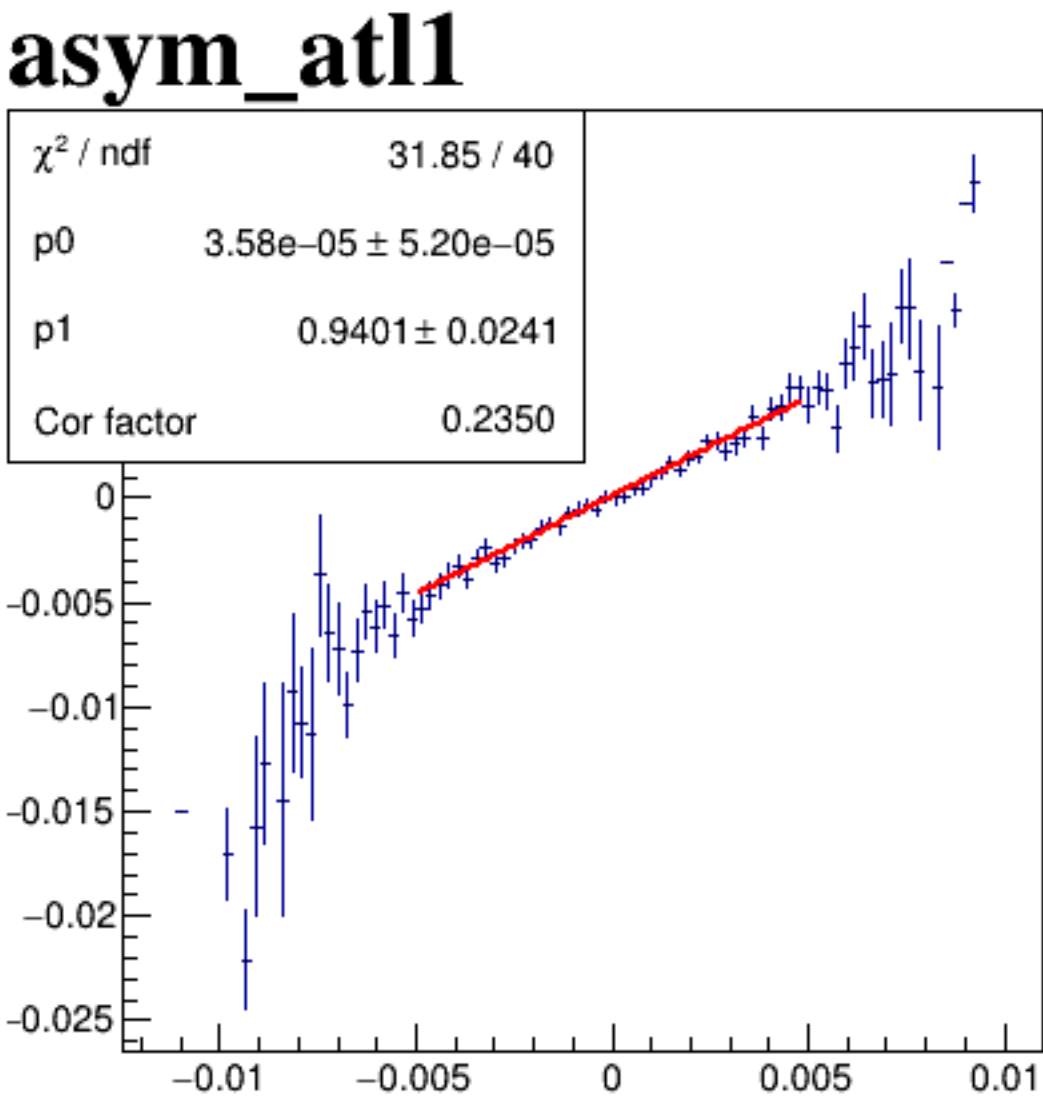
asym_atr2



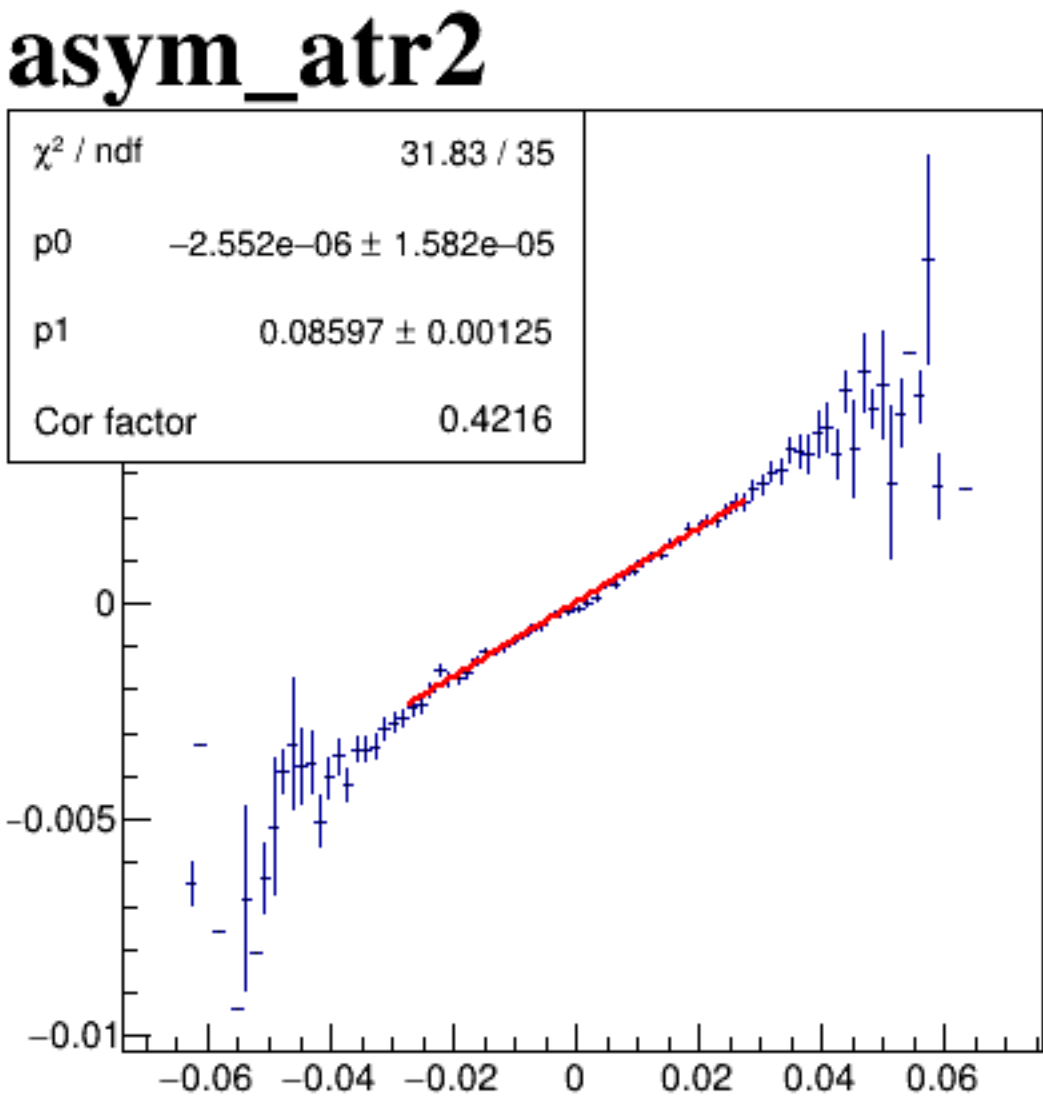
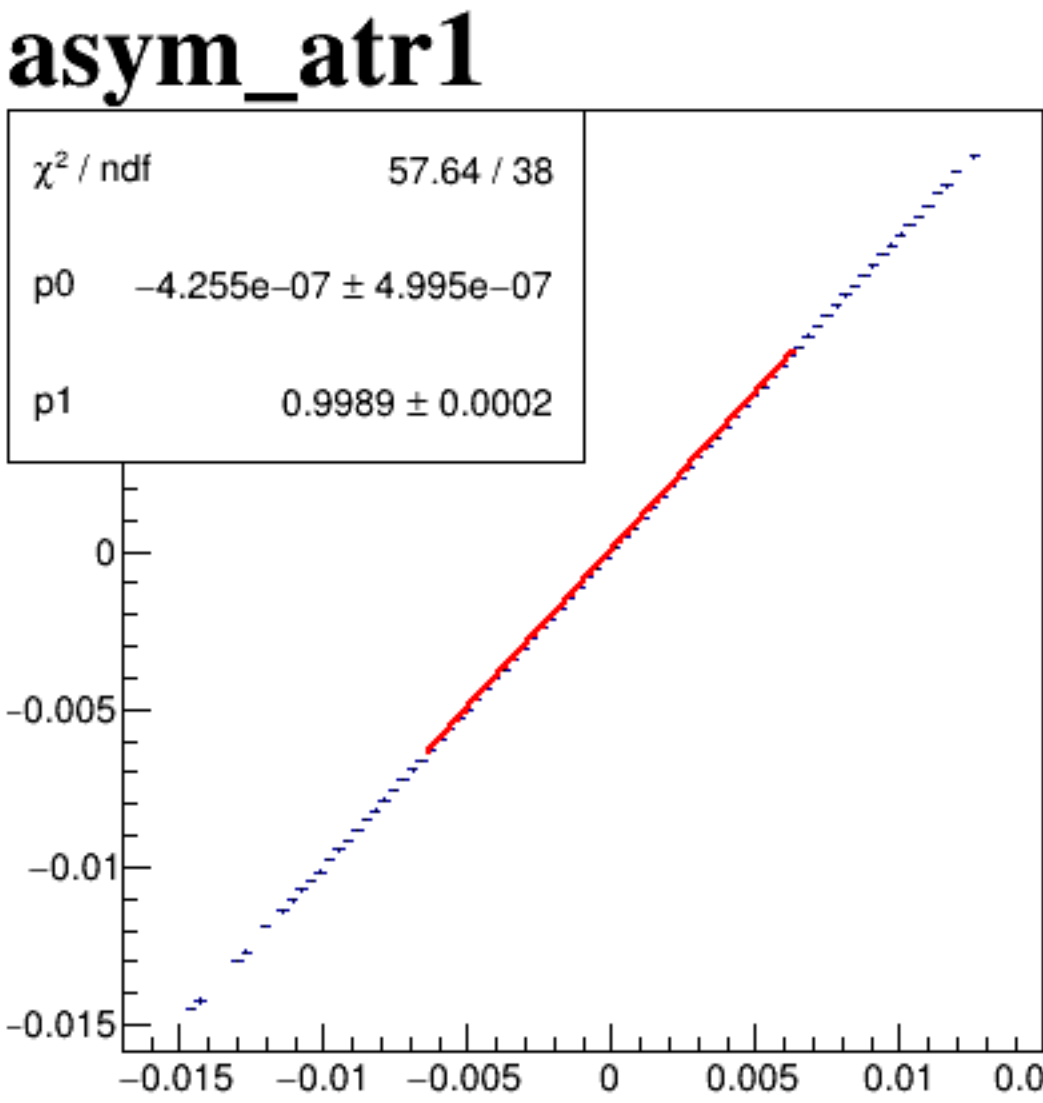
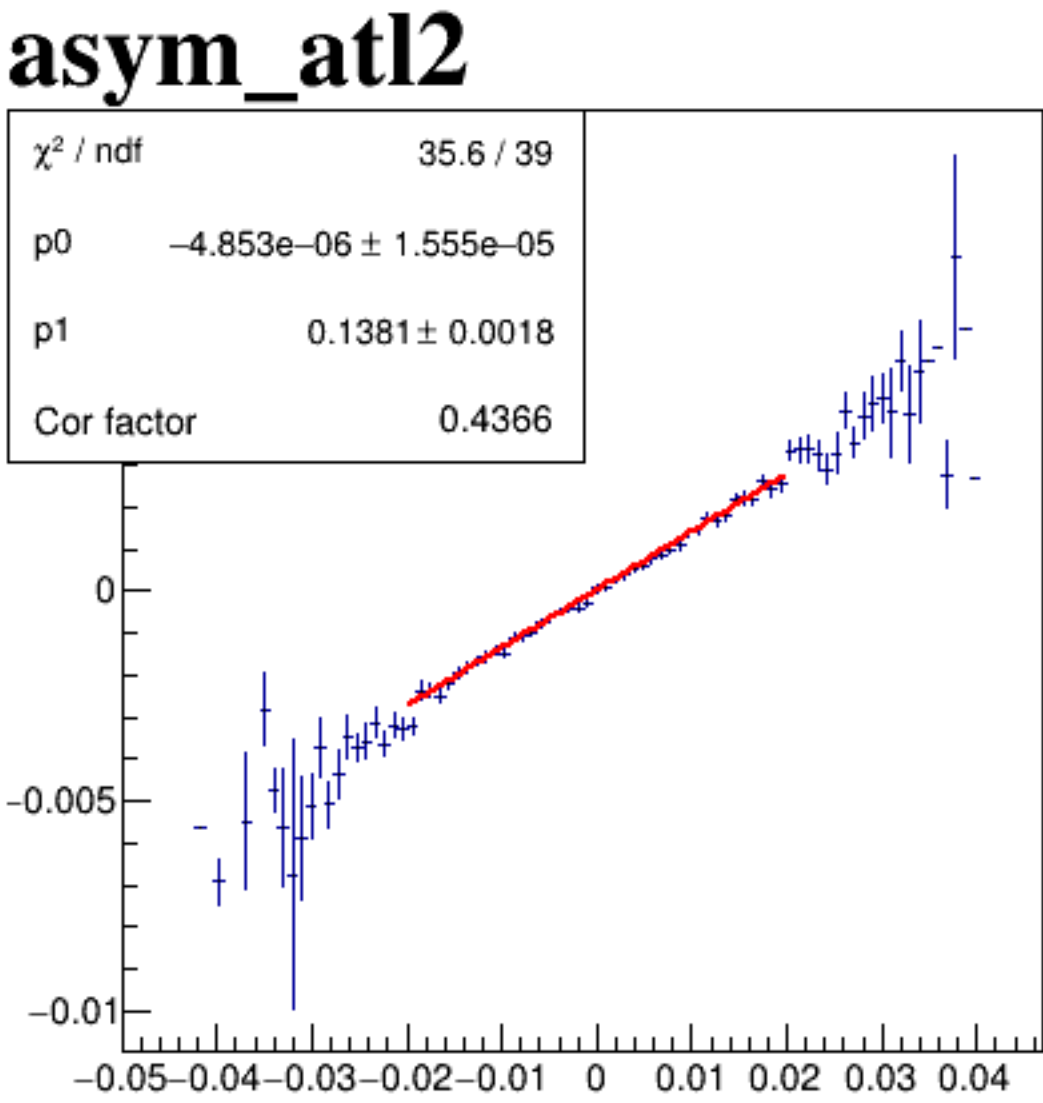
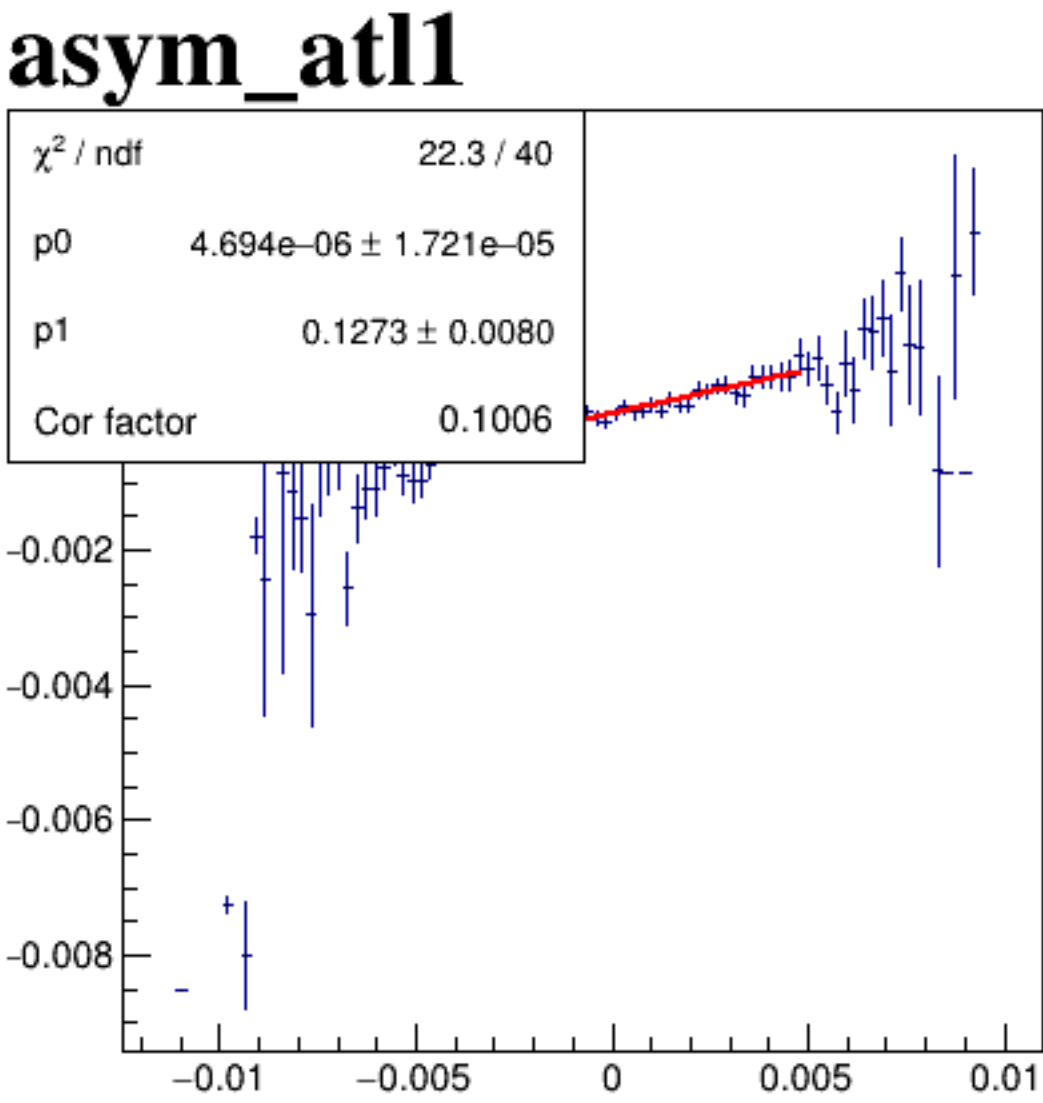
asym_atl1



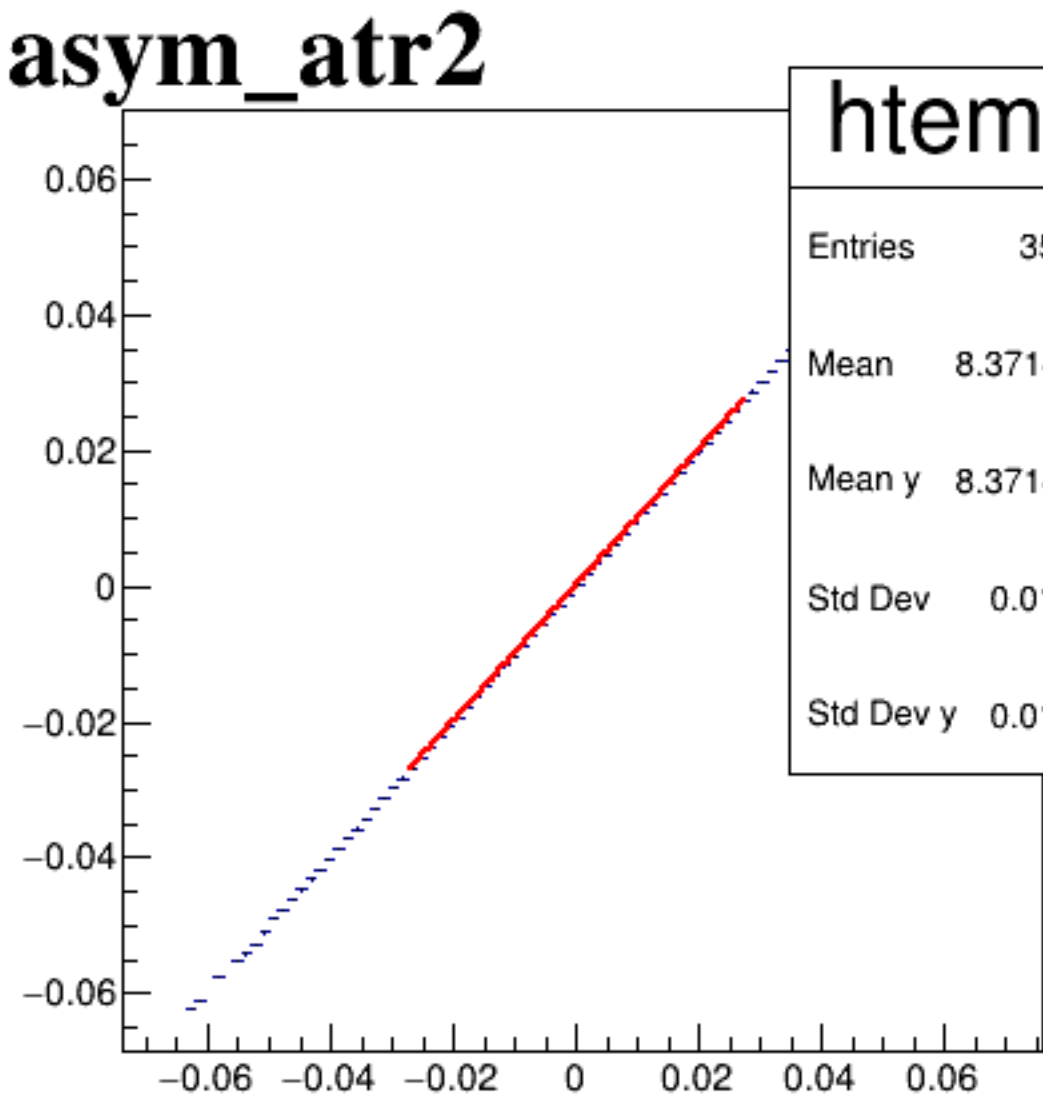
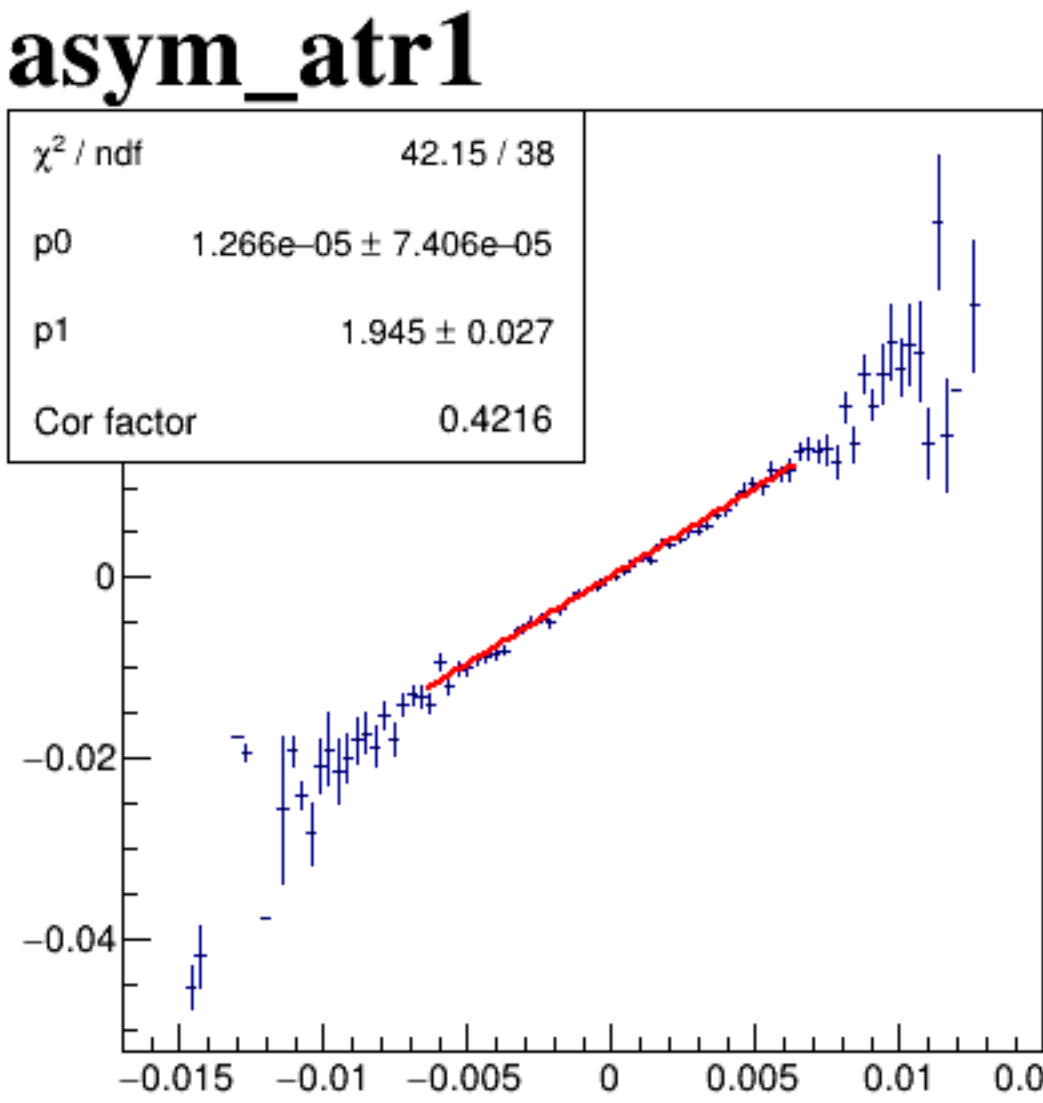
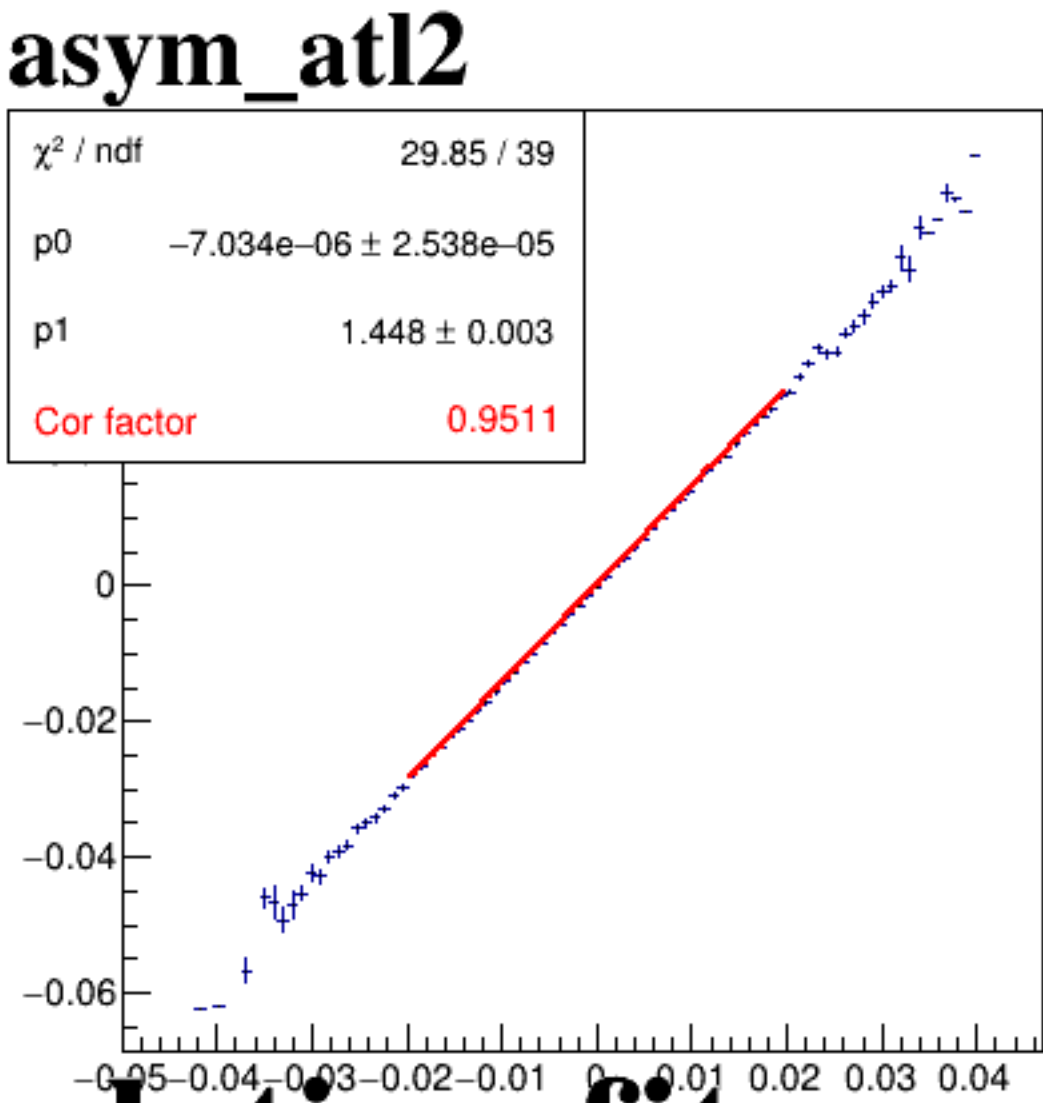
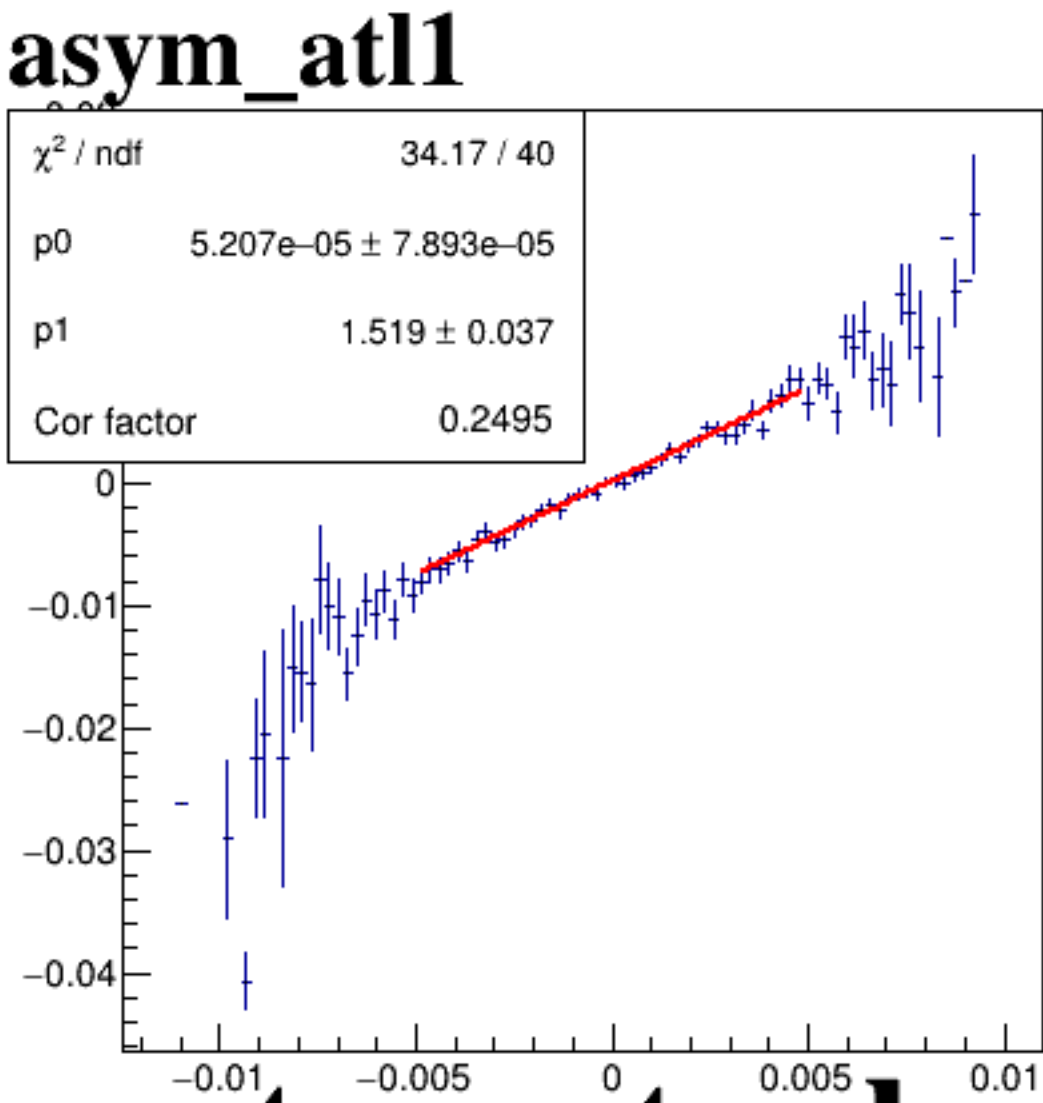
asym_atl2



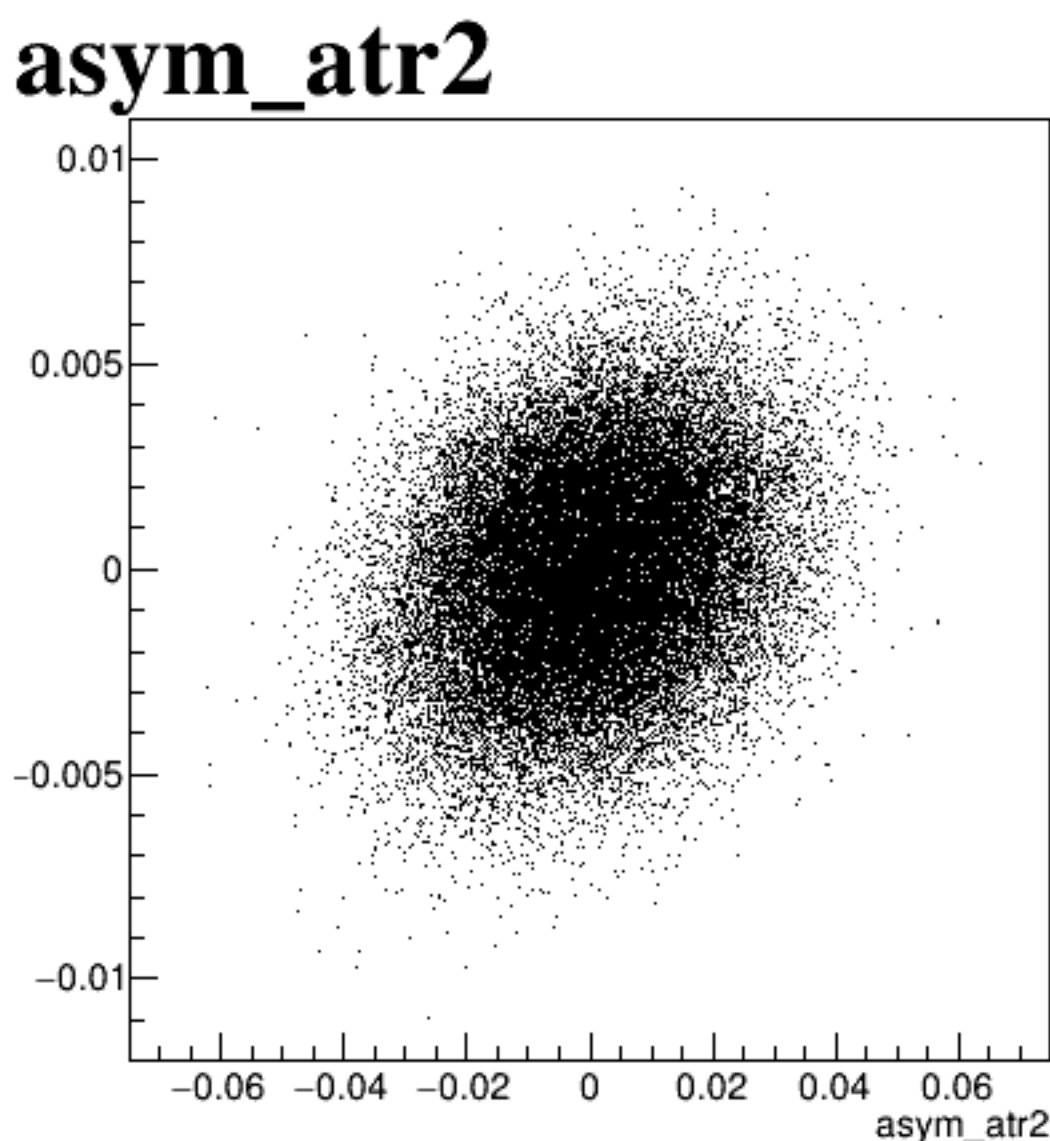
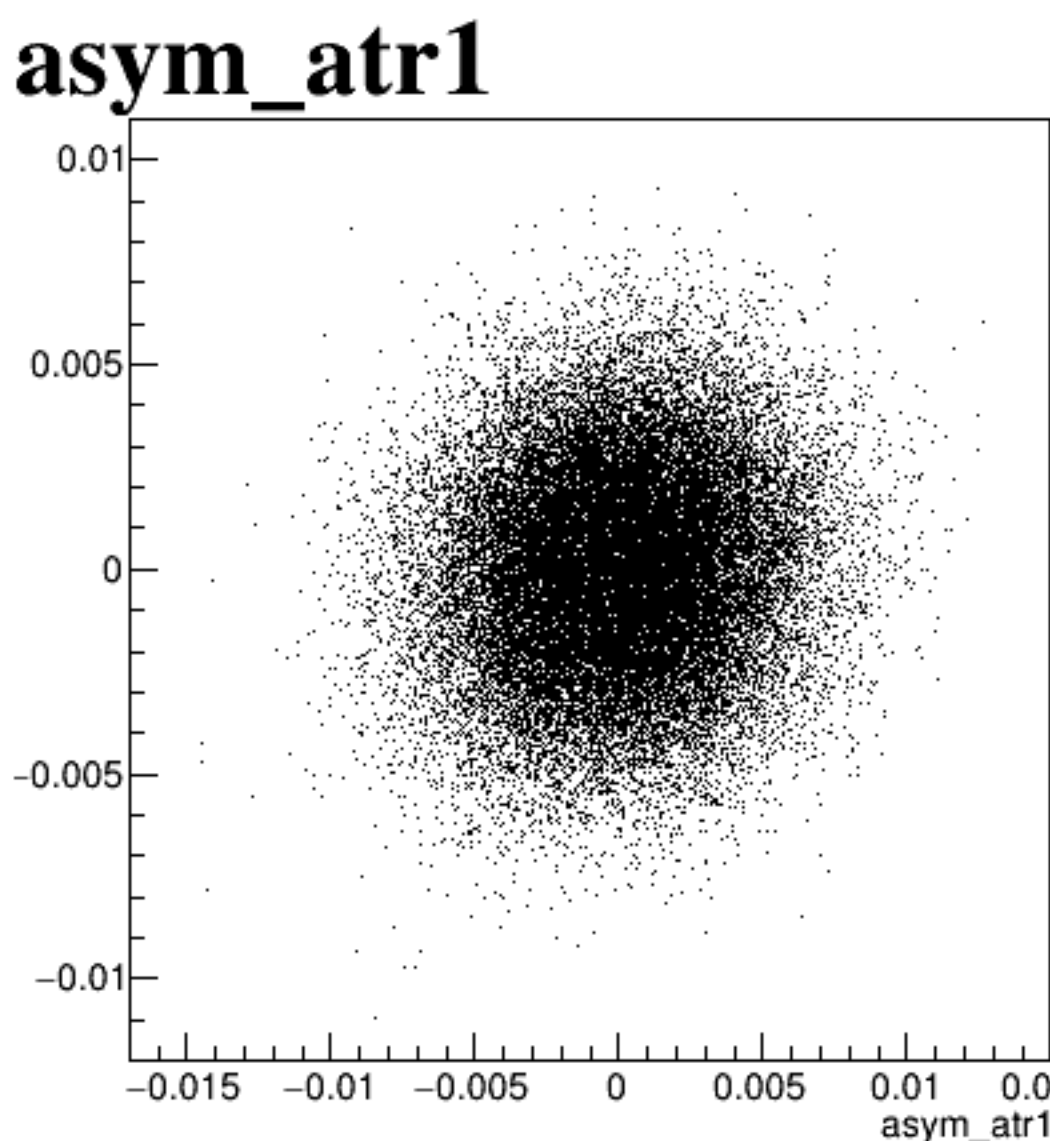
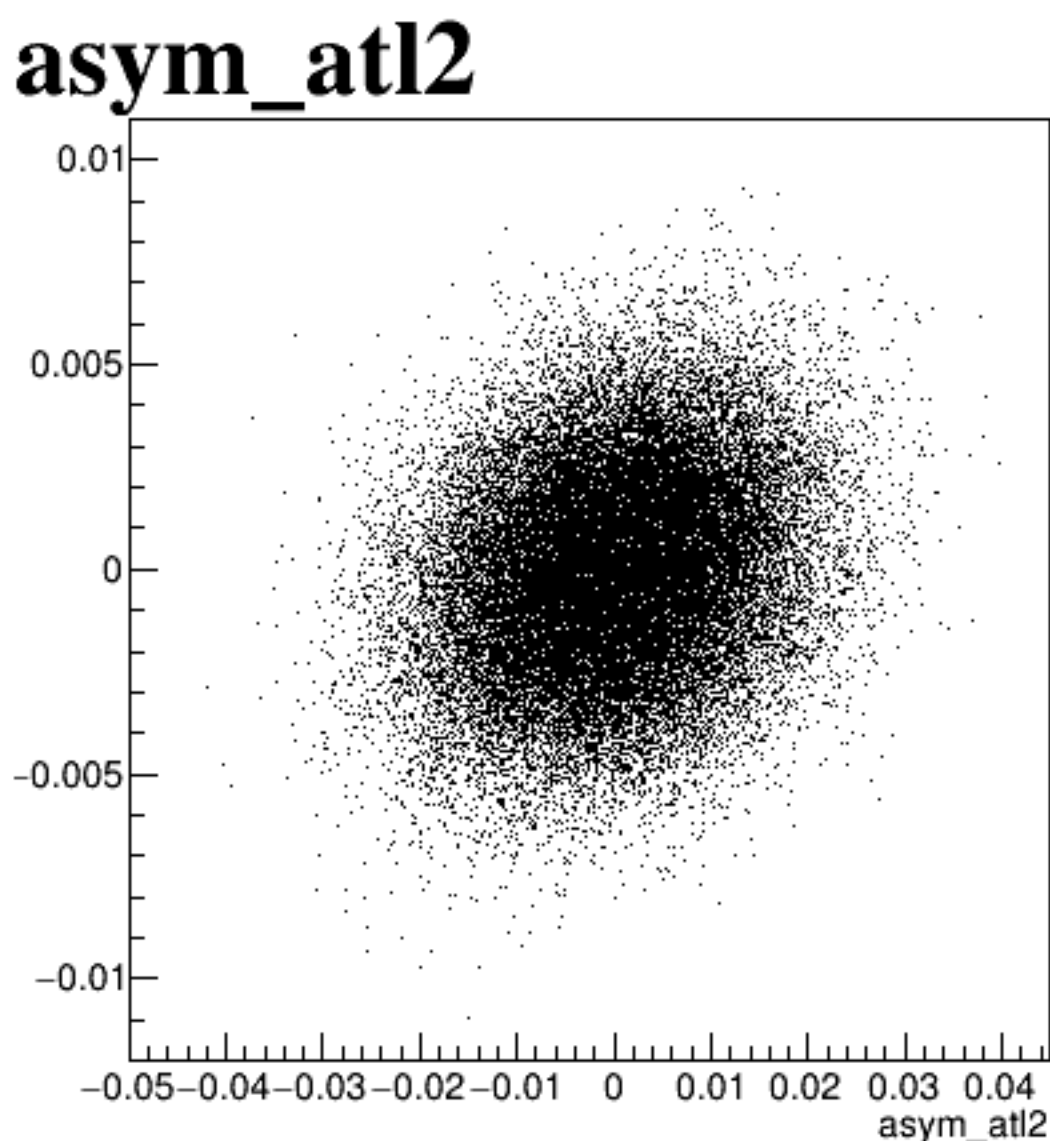
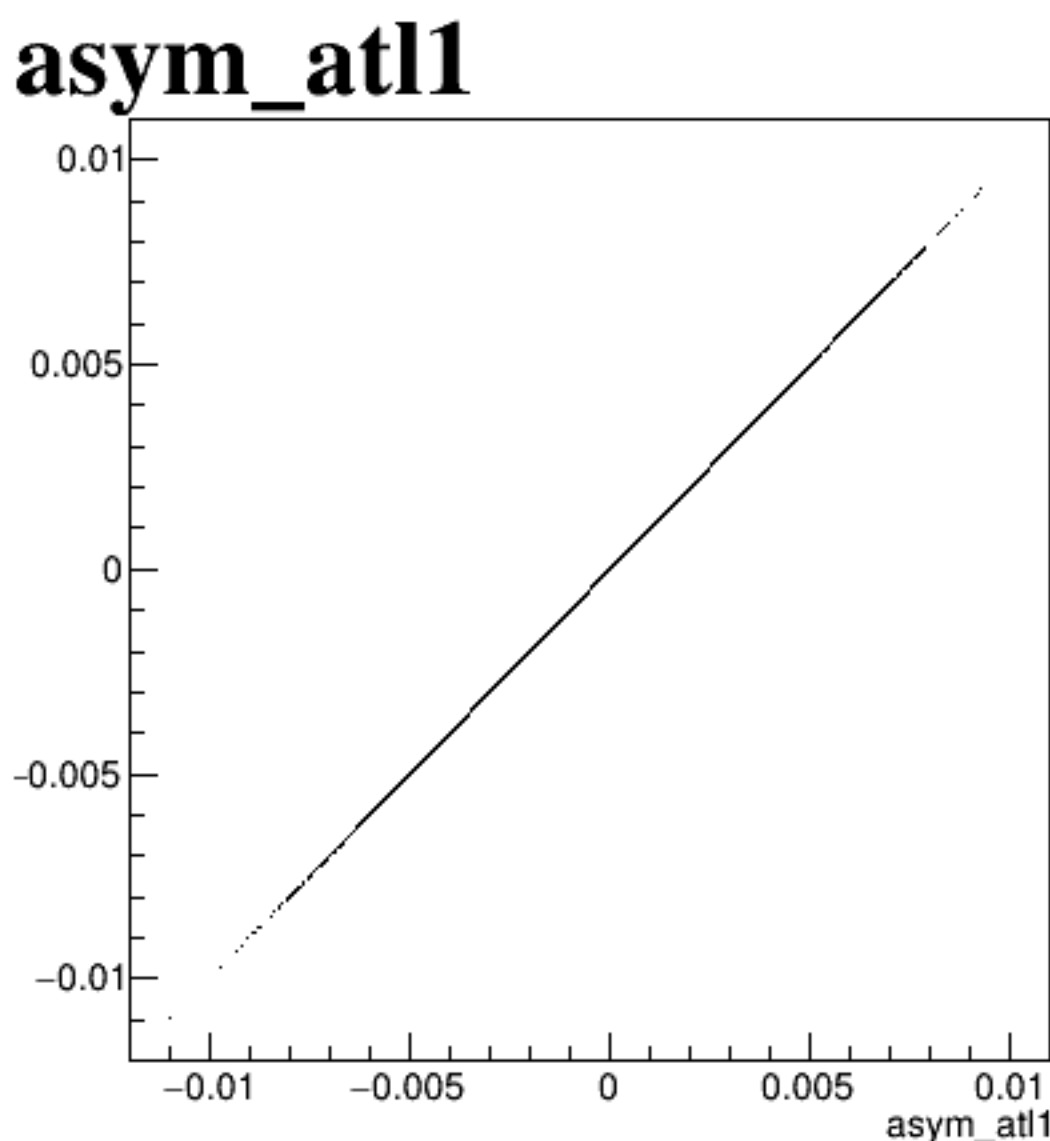
asym_atr1



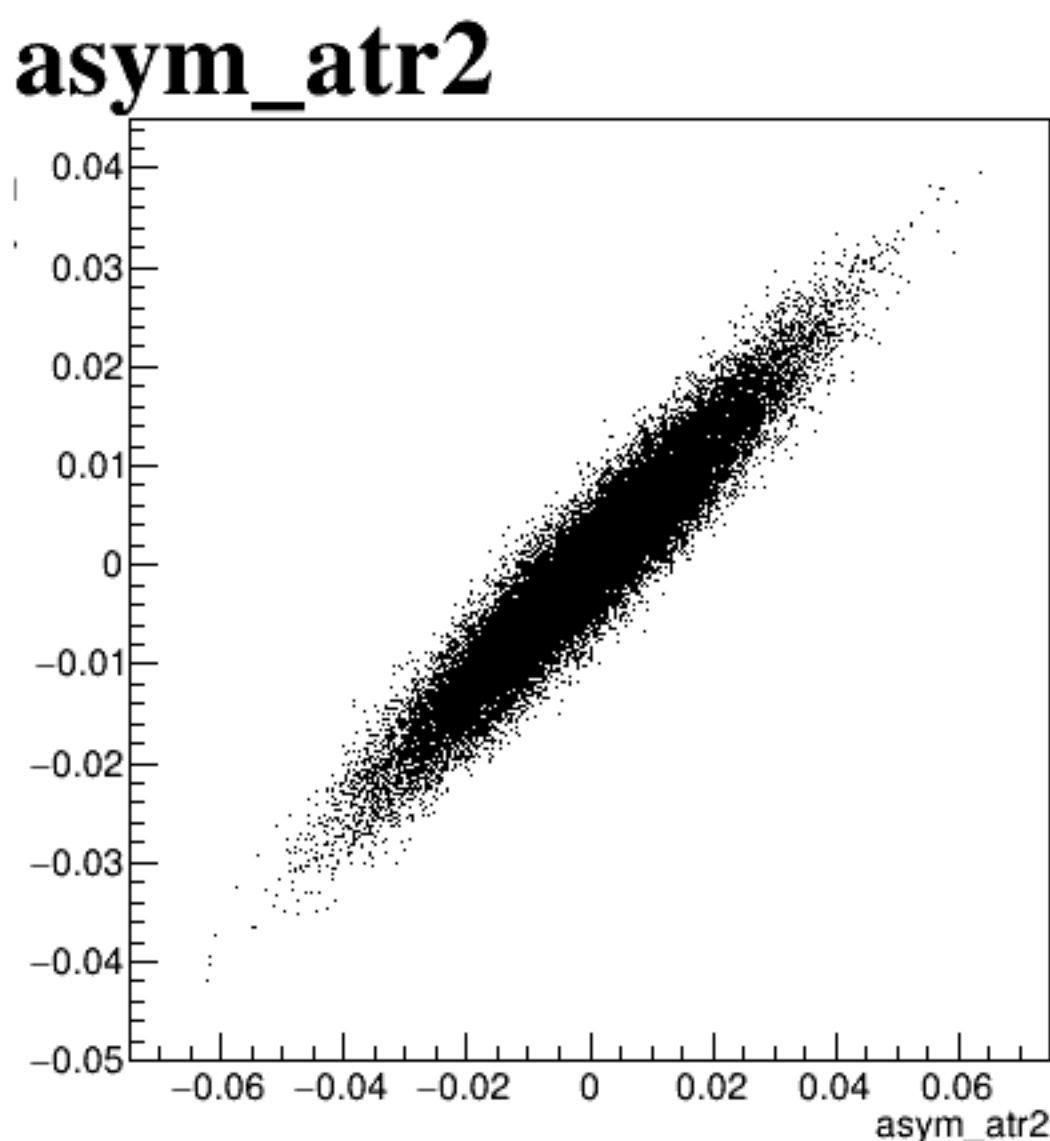
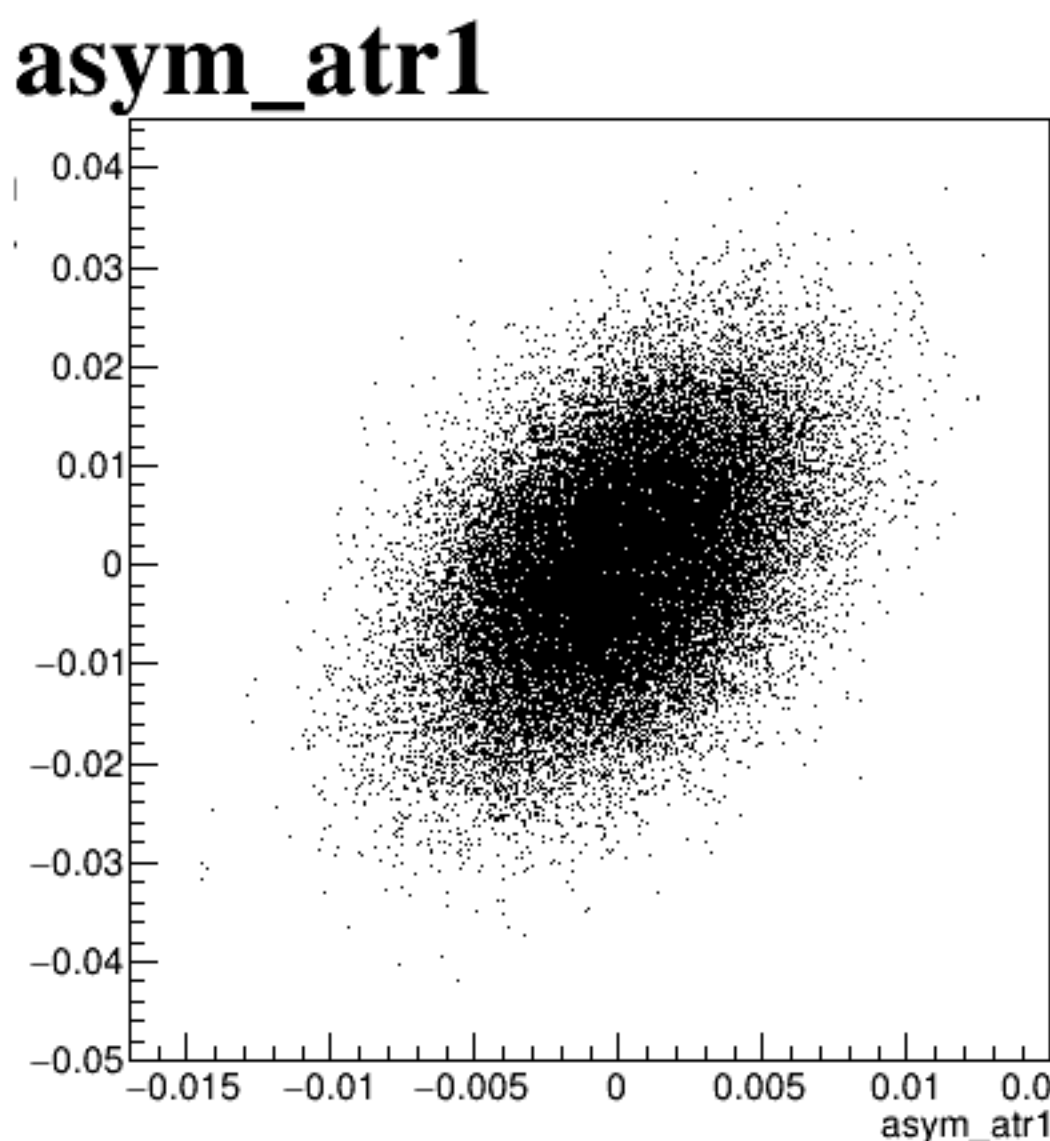
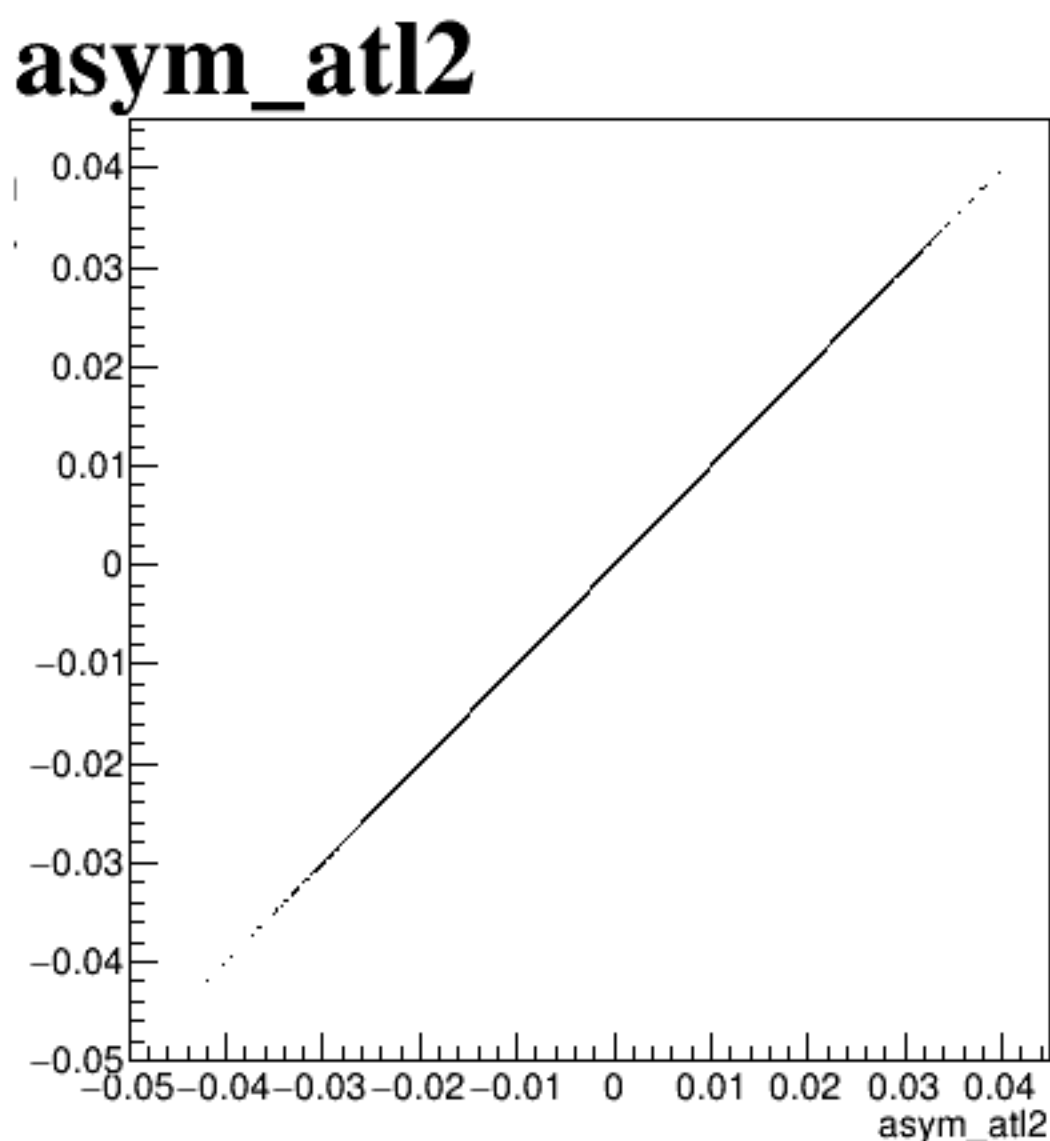
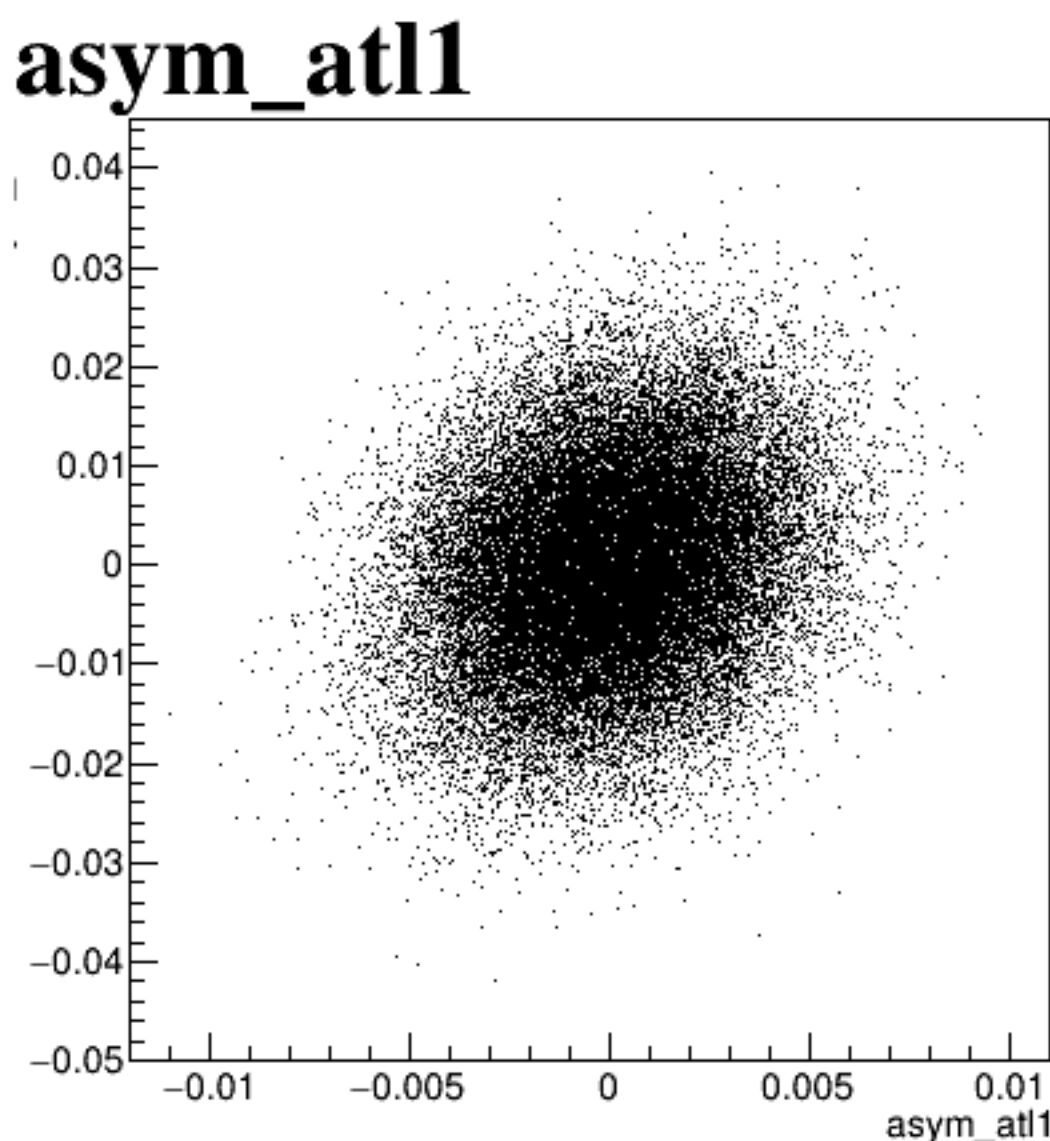
asym_atr2



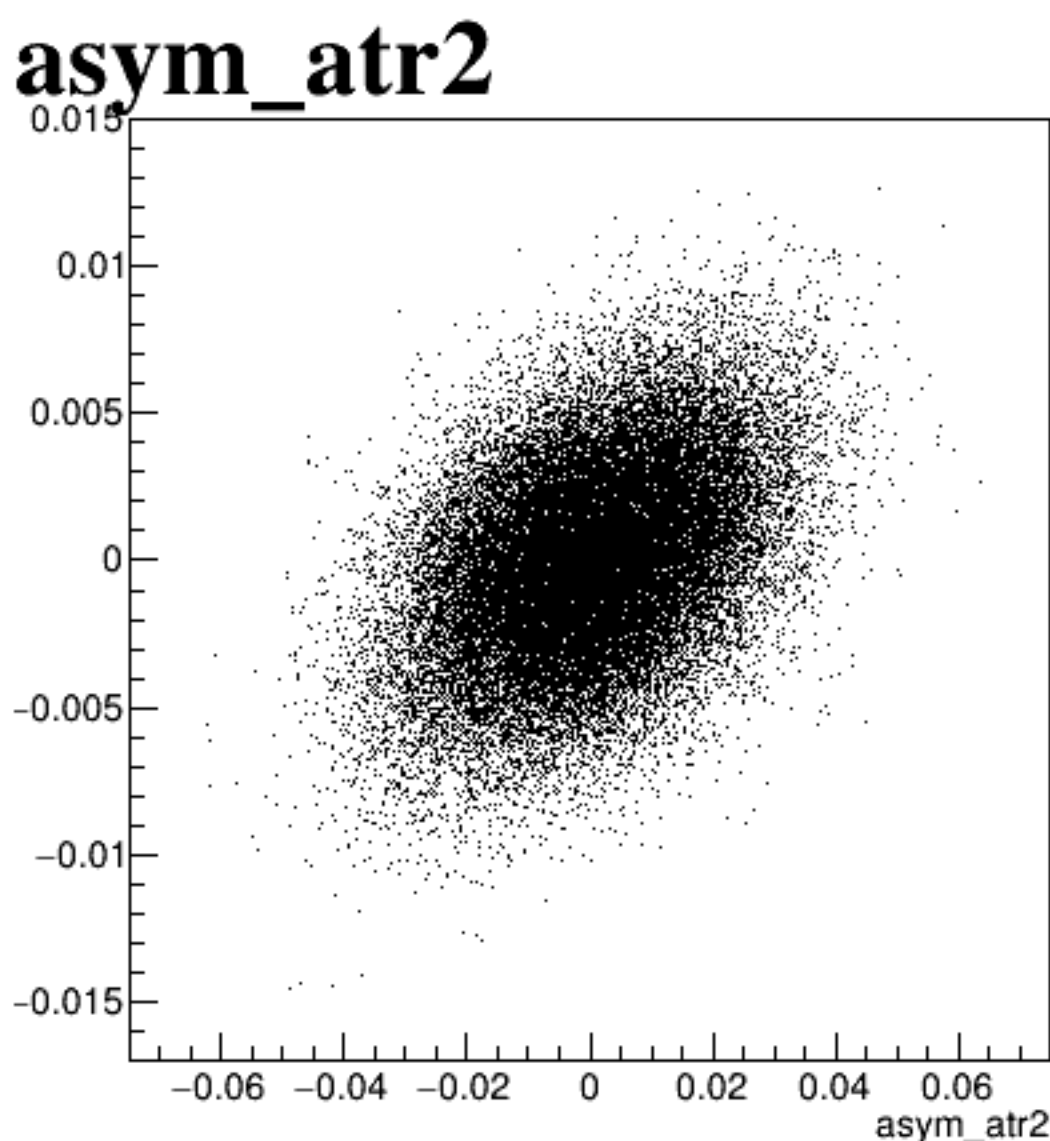
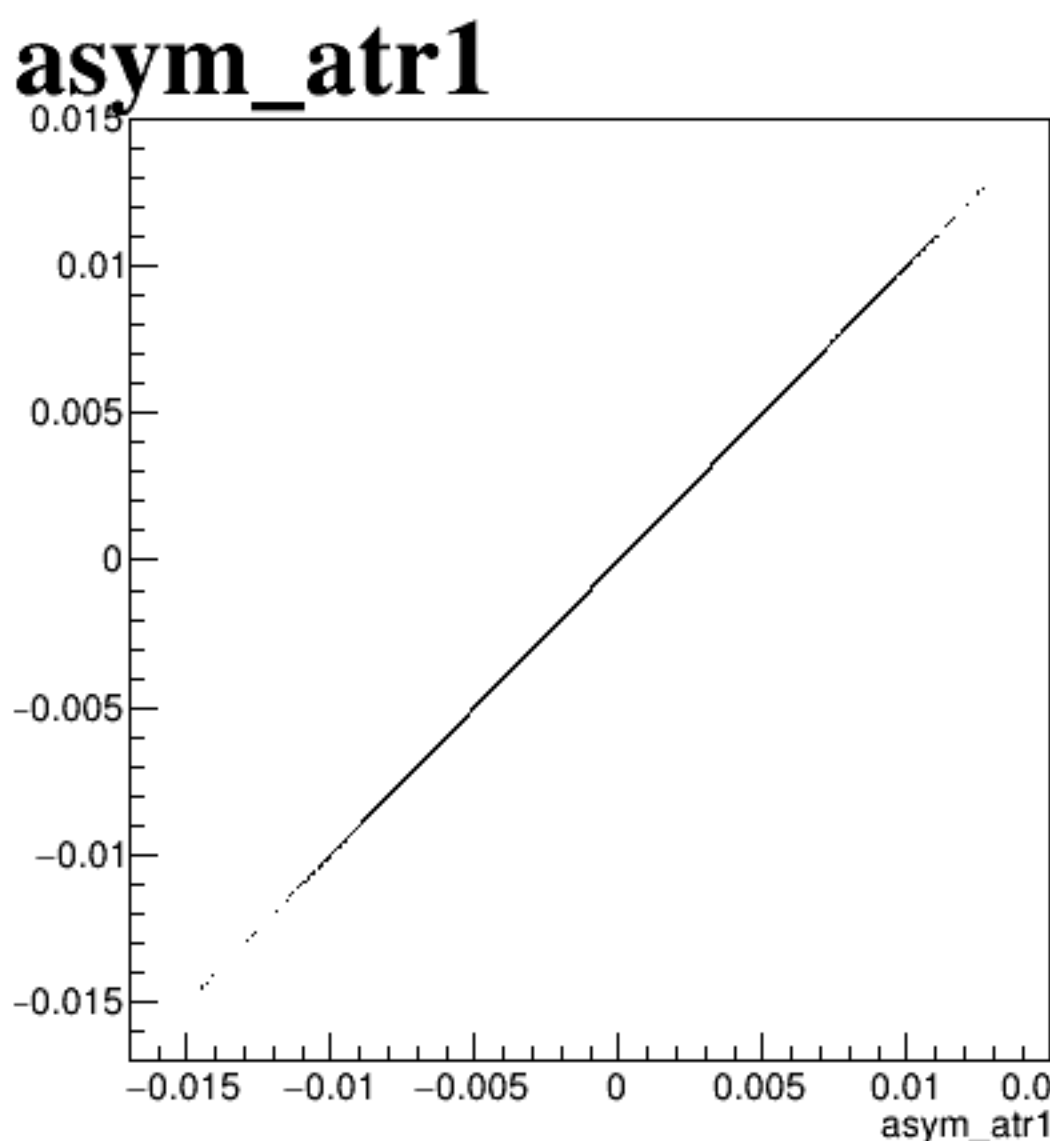
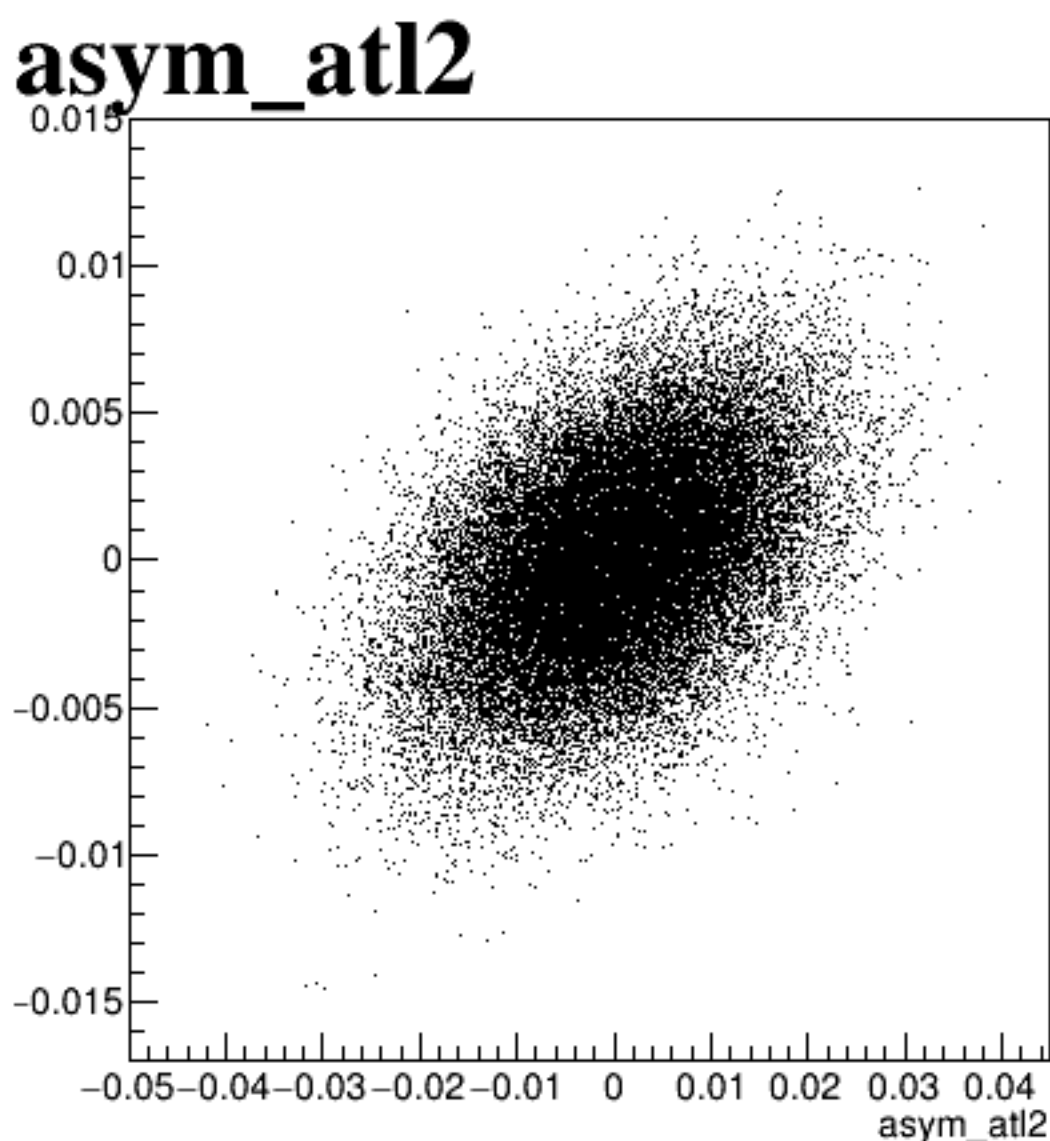
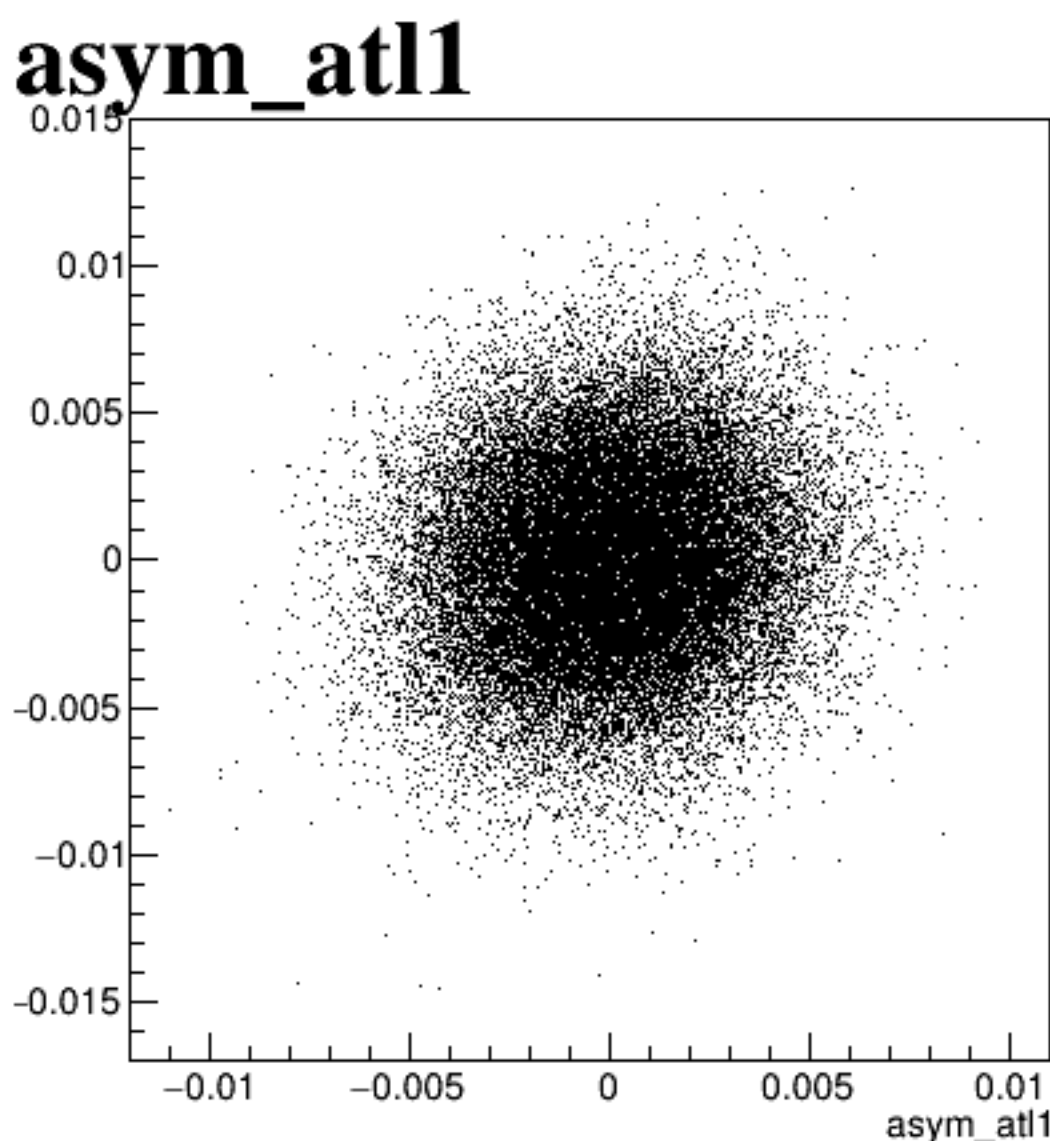
asym_atl1



asym_atl2



asym_atr1



asym_atr2

