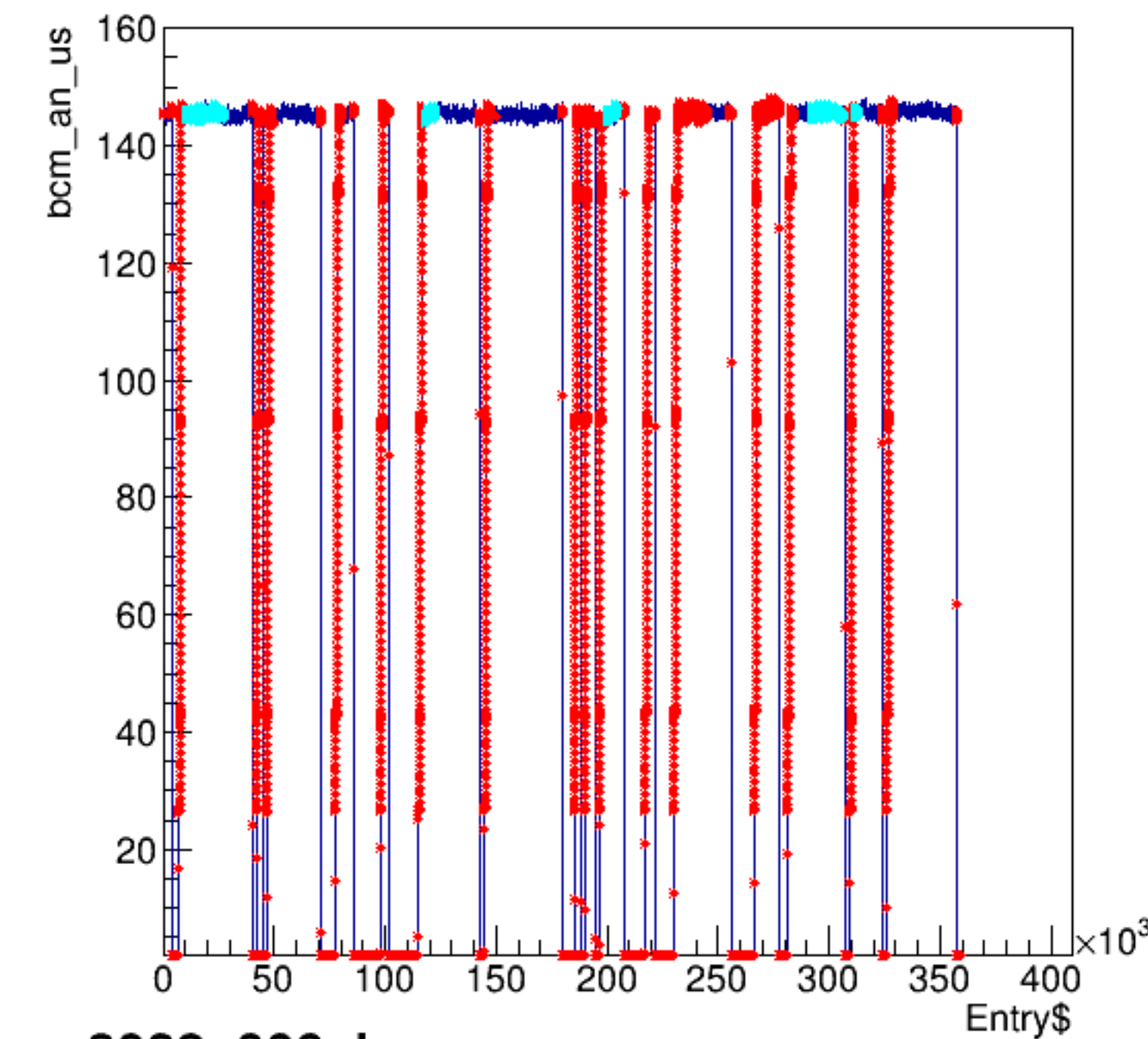
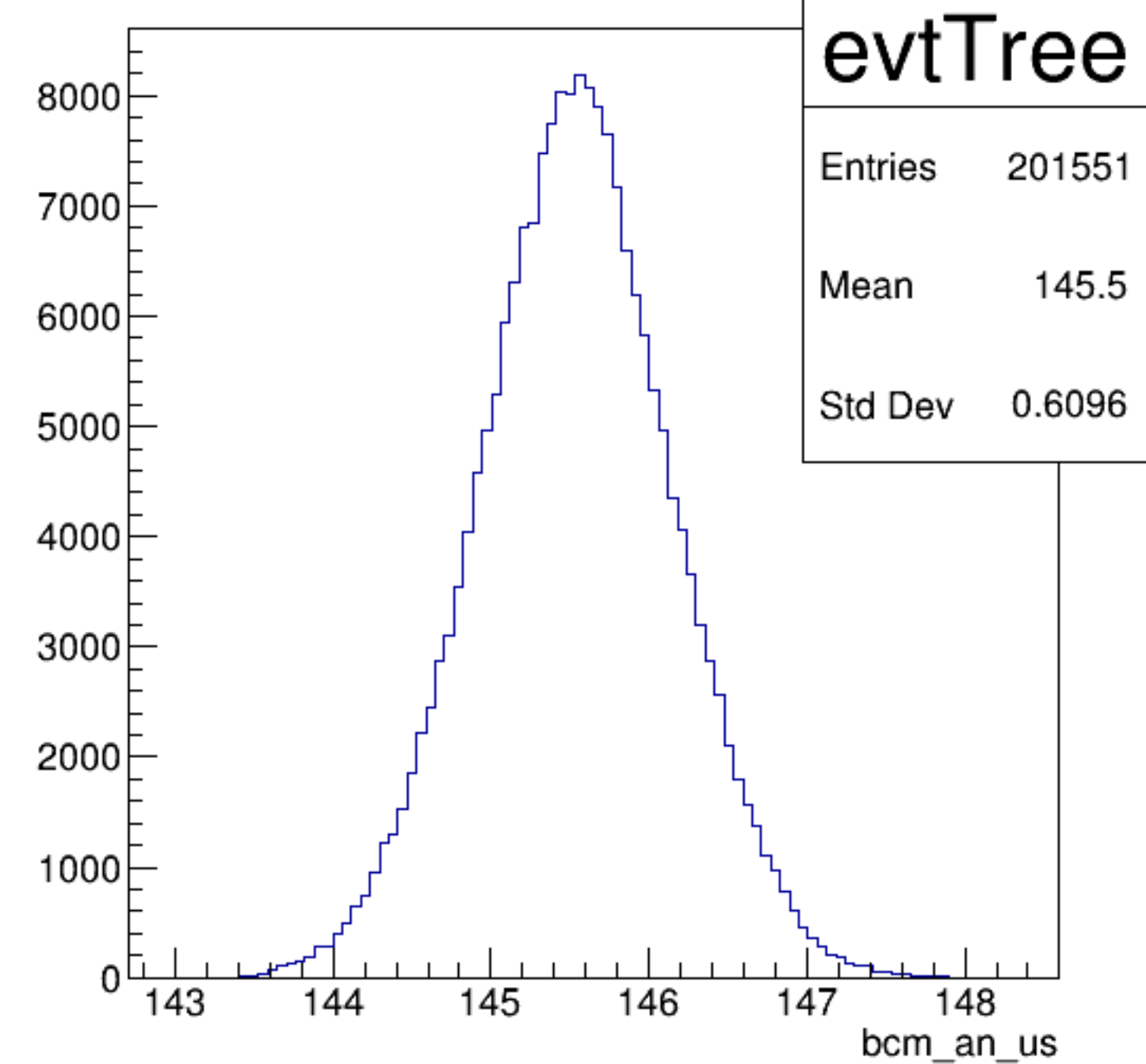


bcm\_an\_us:Entry\$

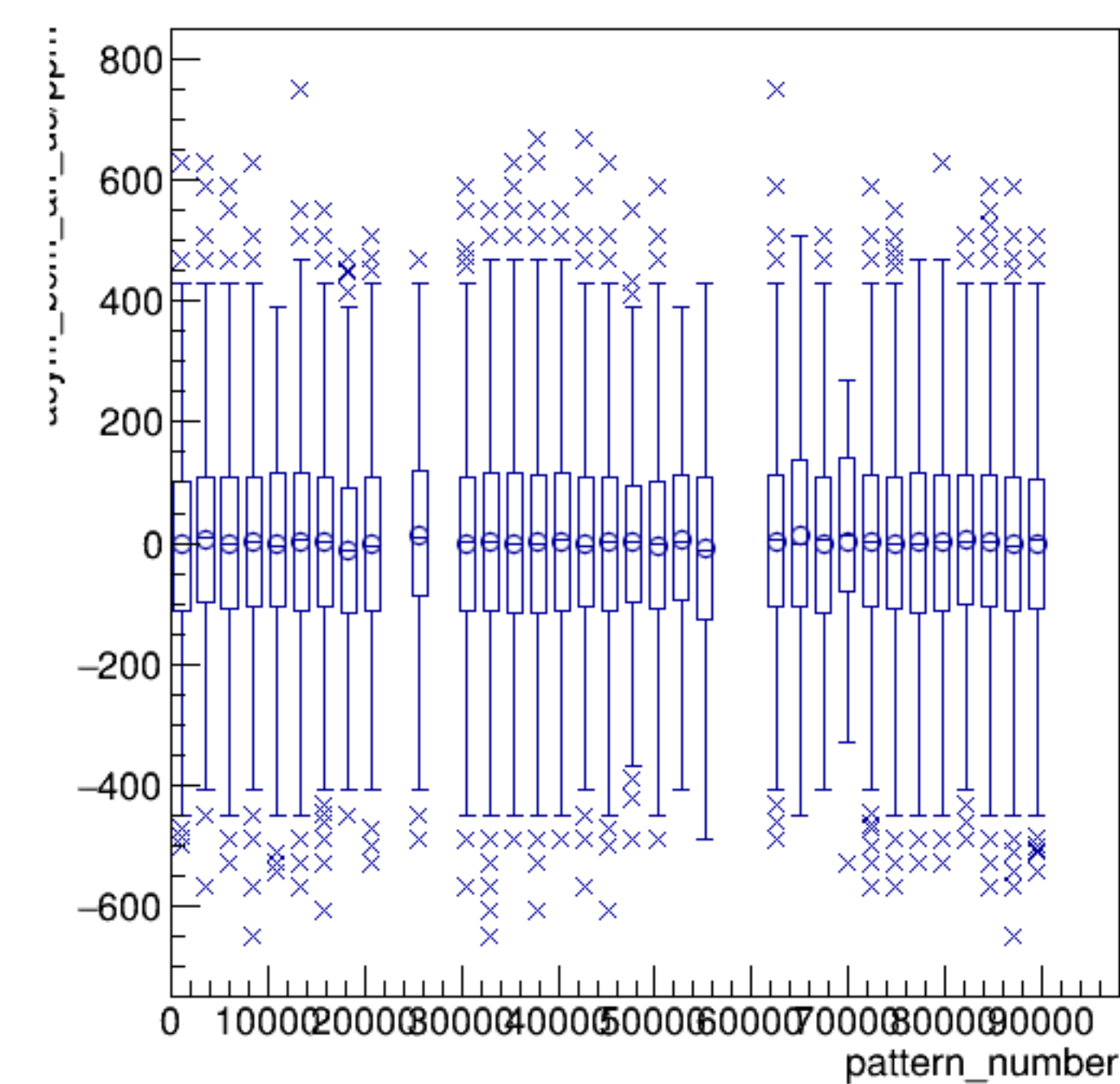


run8022\_000\_bcm\_an\_us.png

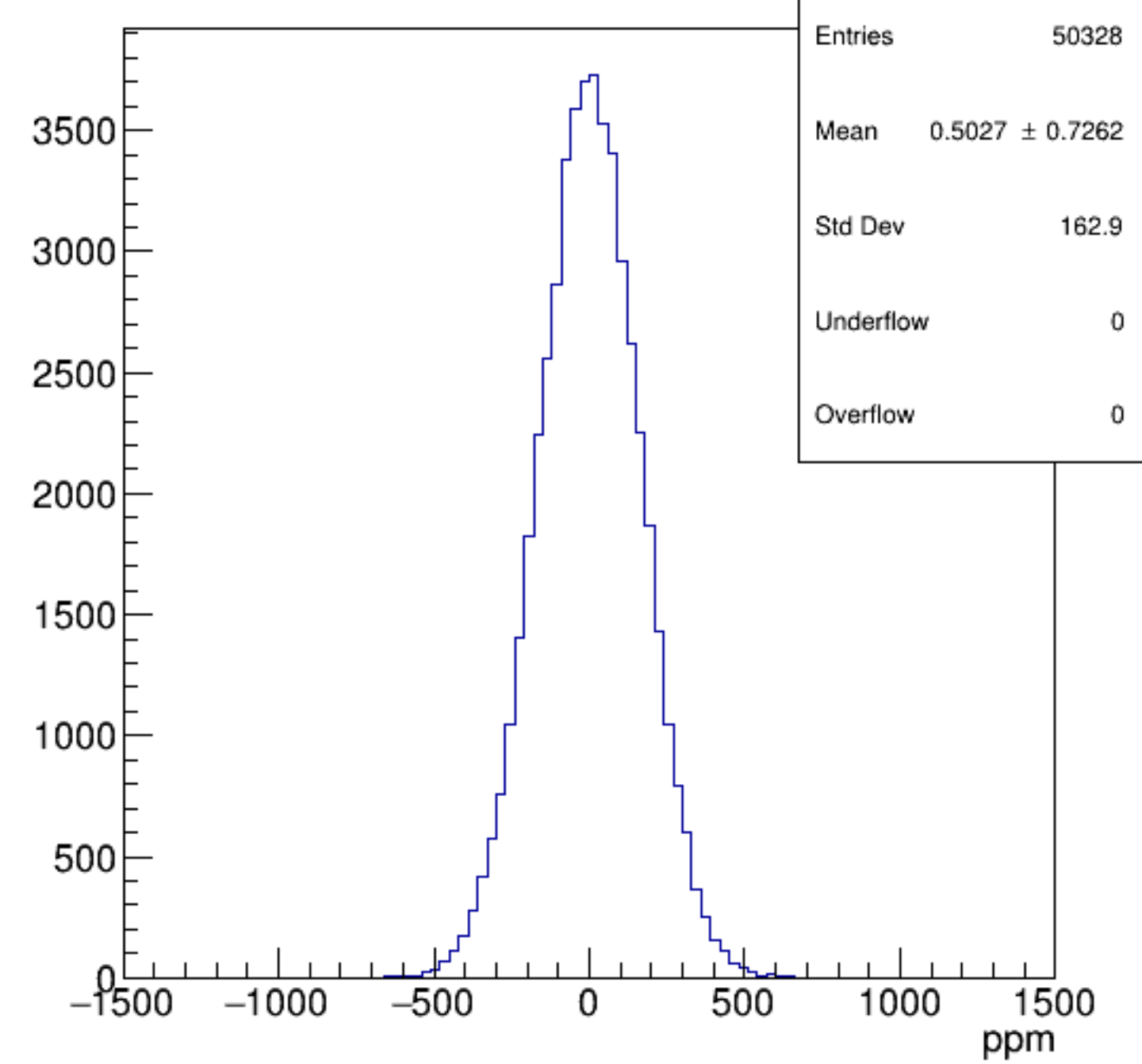
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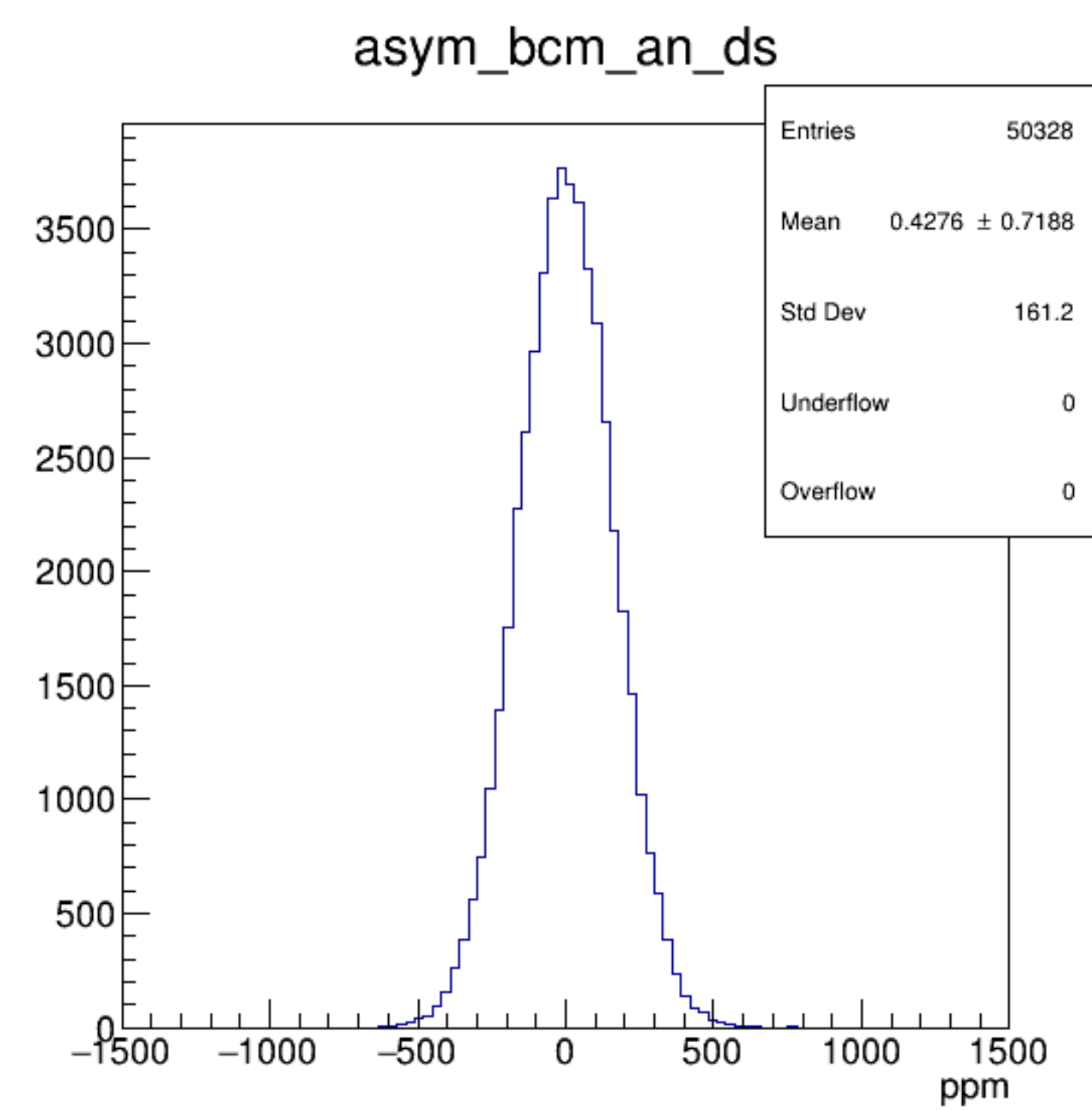
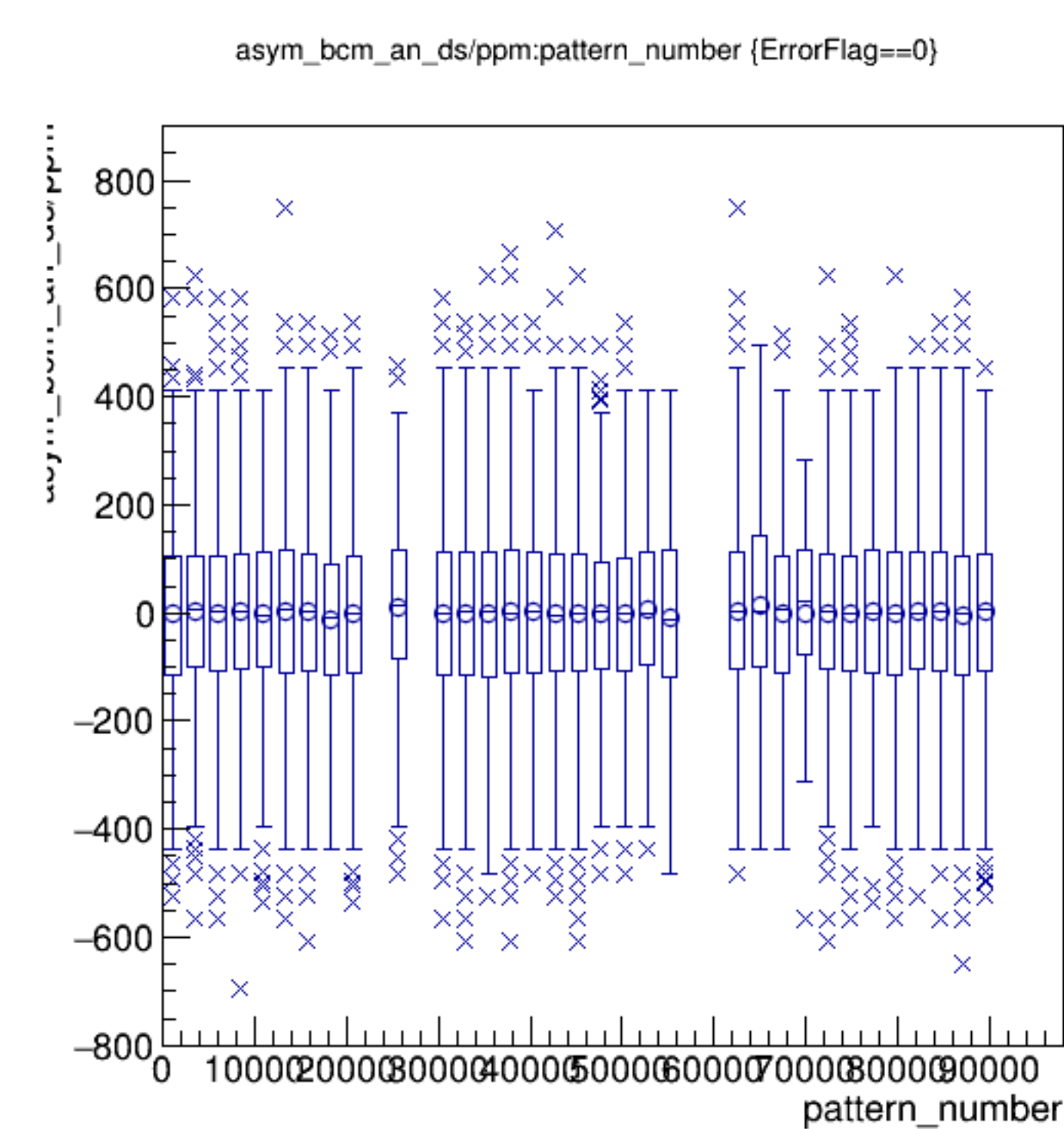
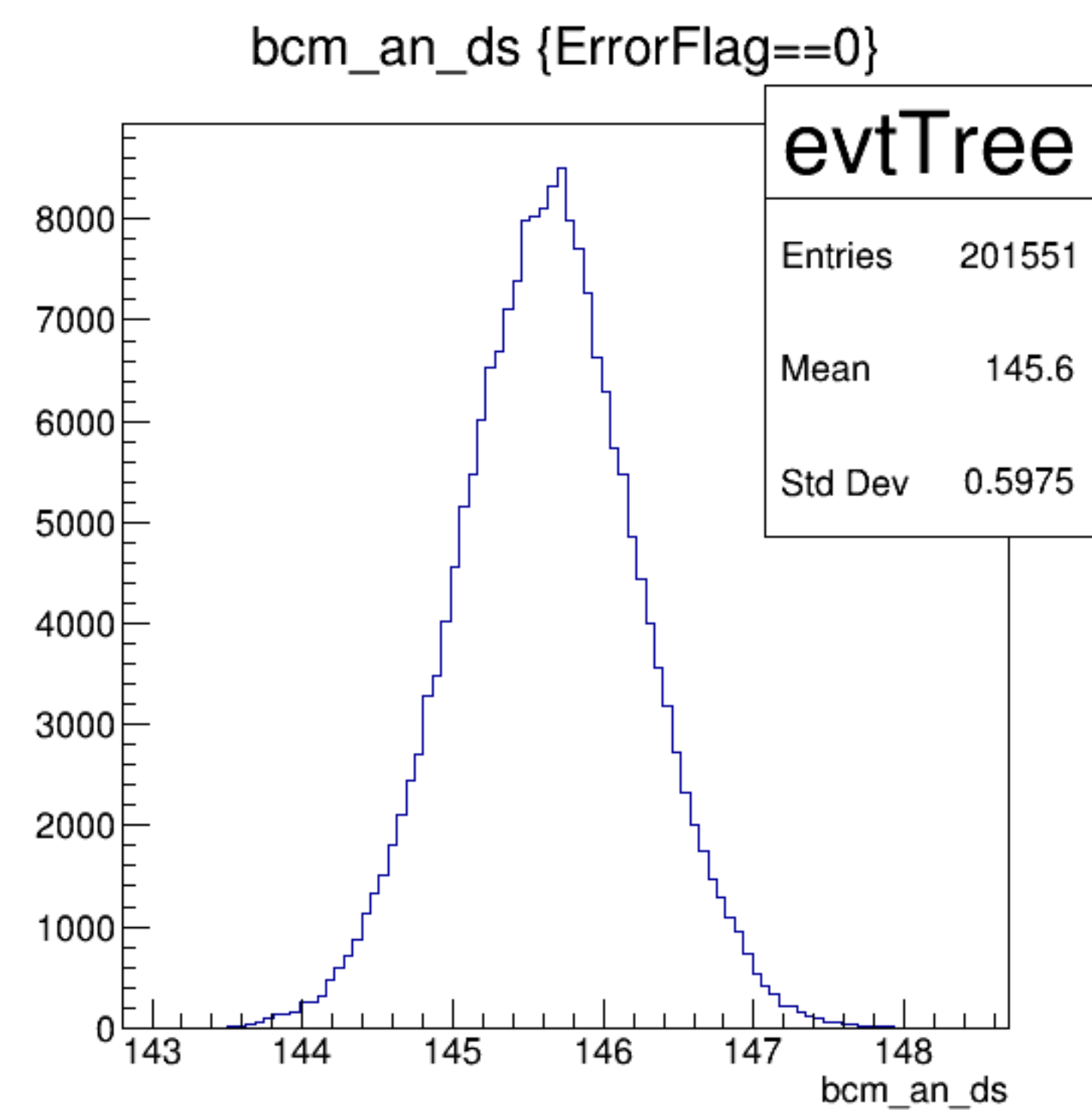
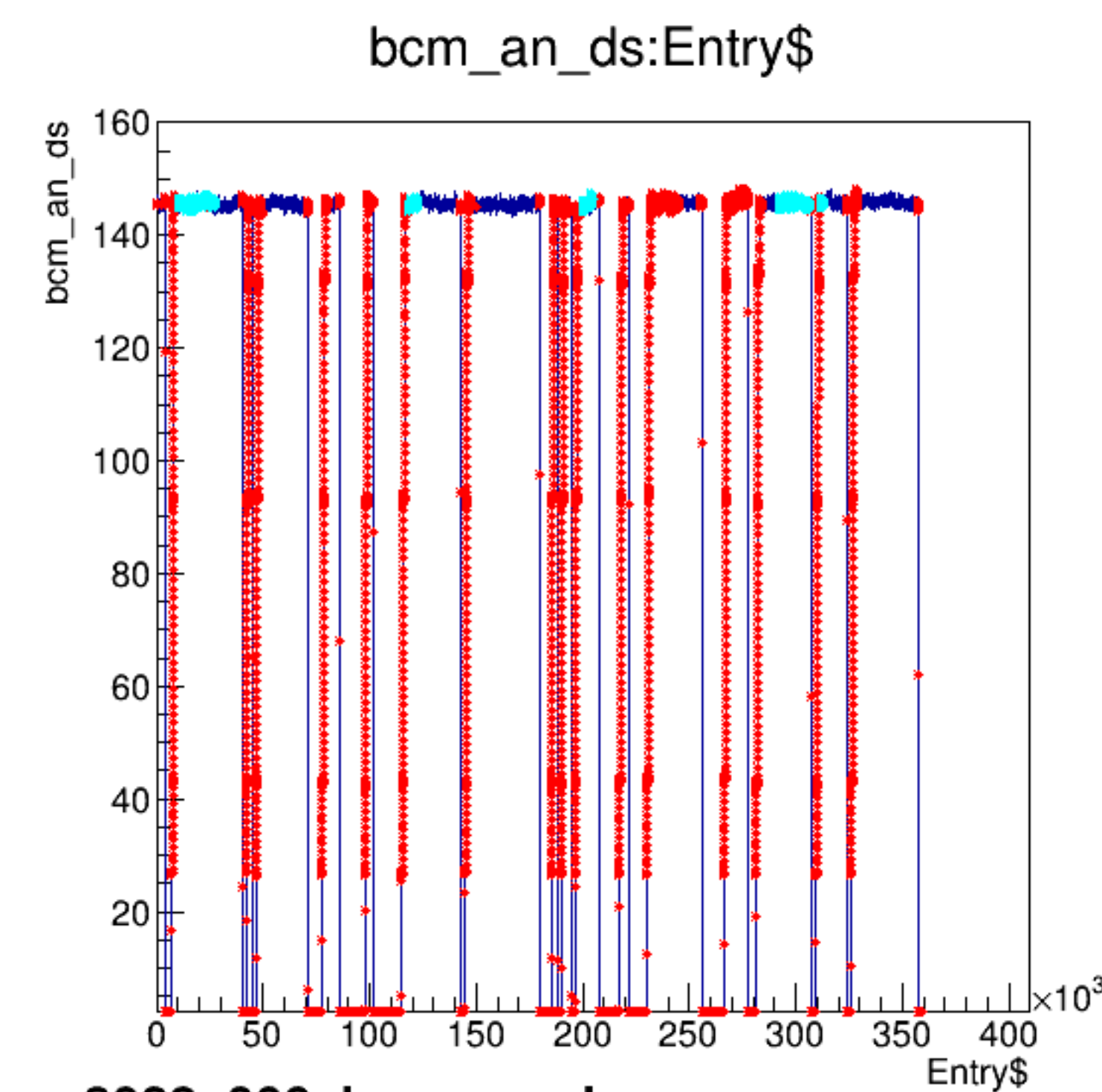


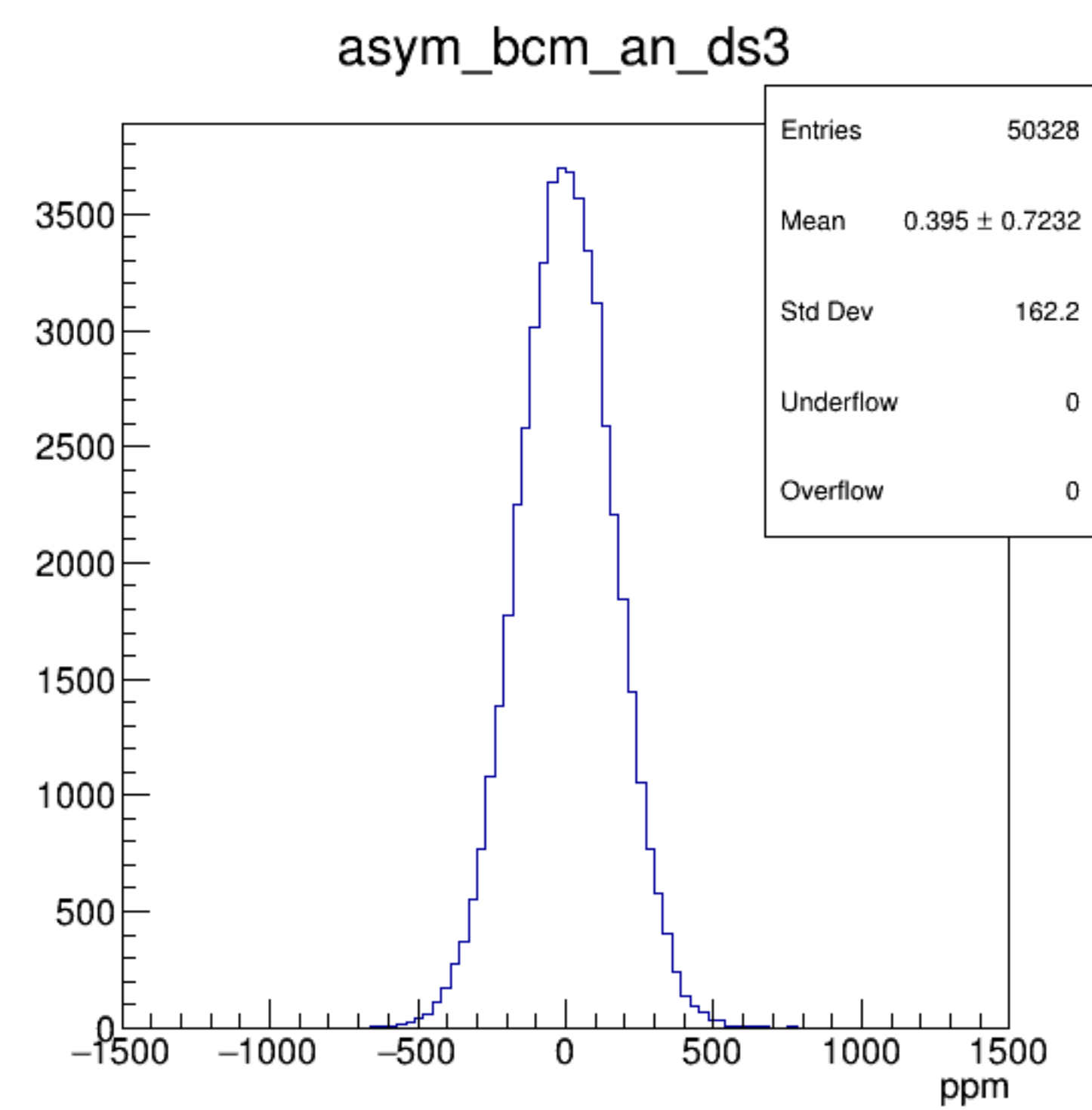
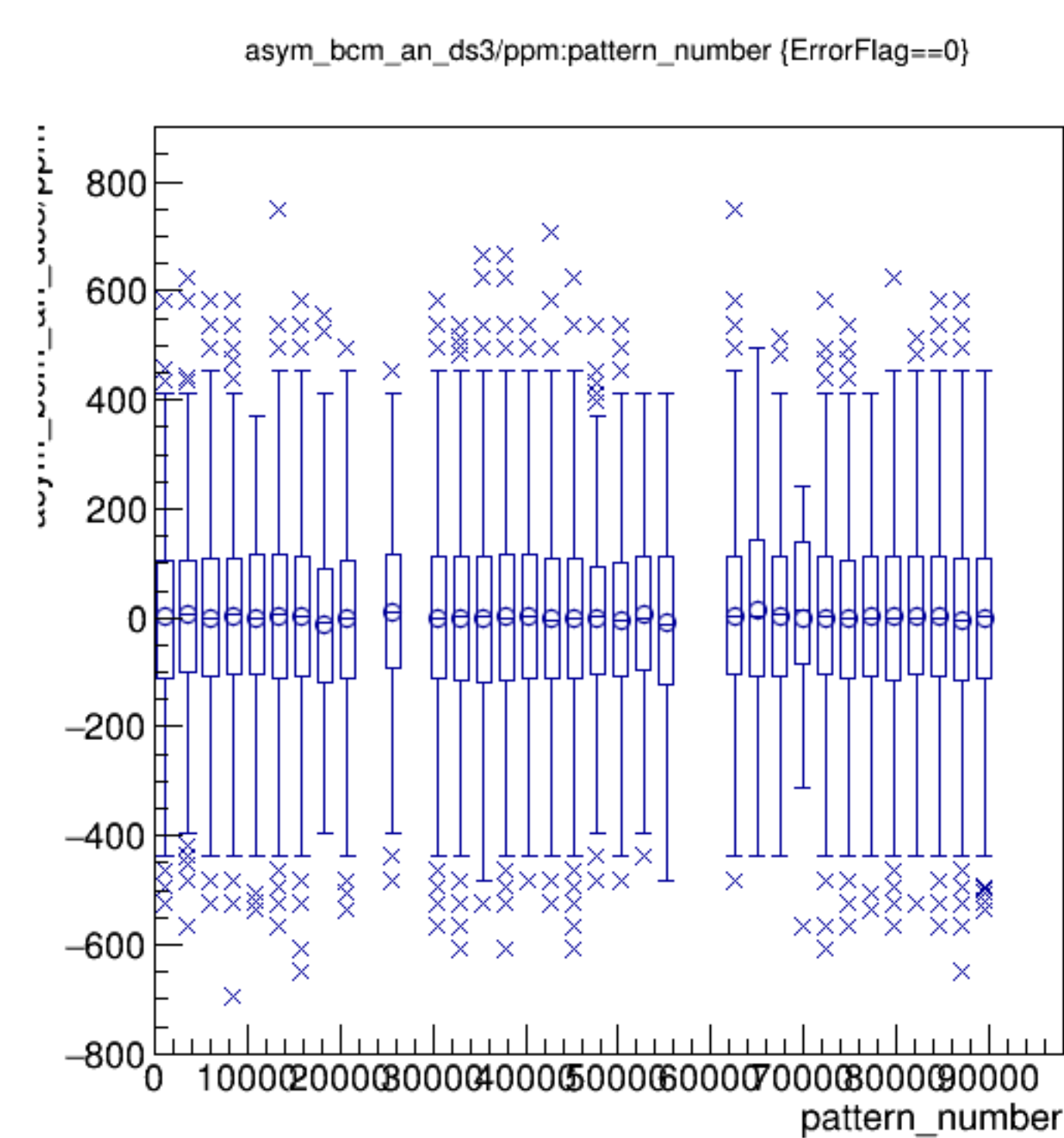
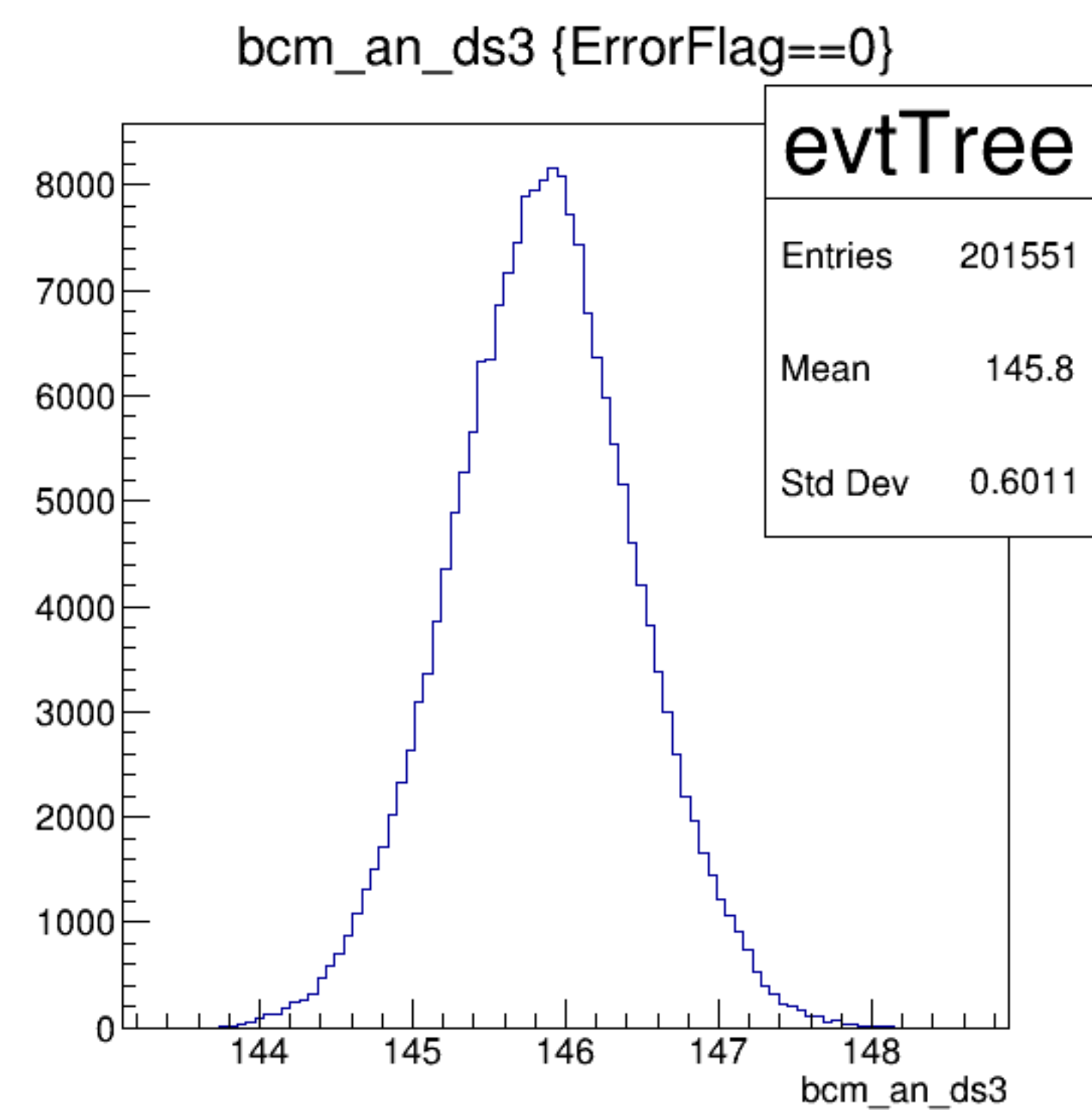
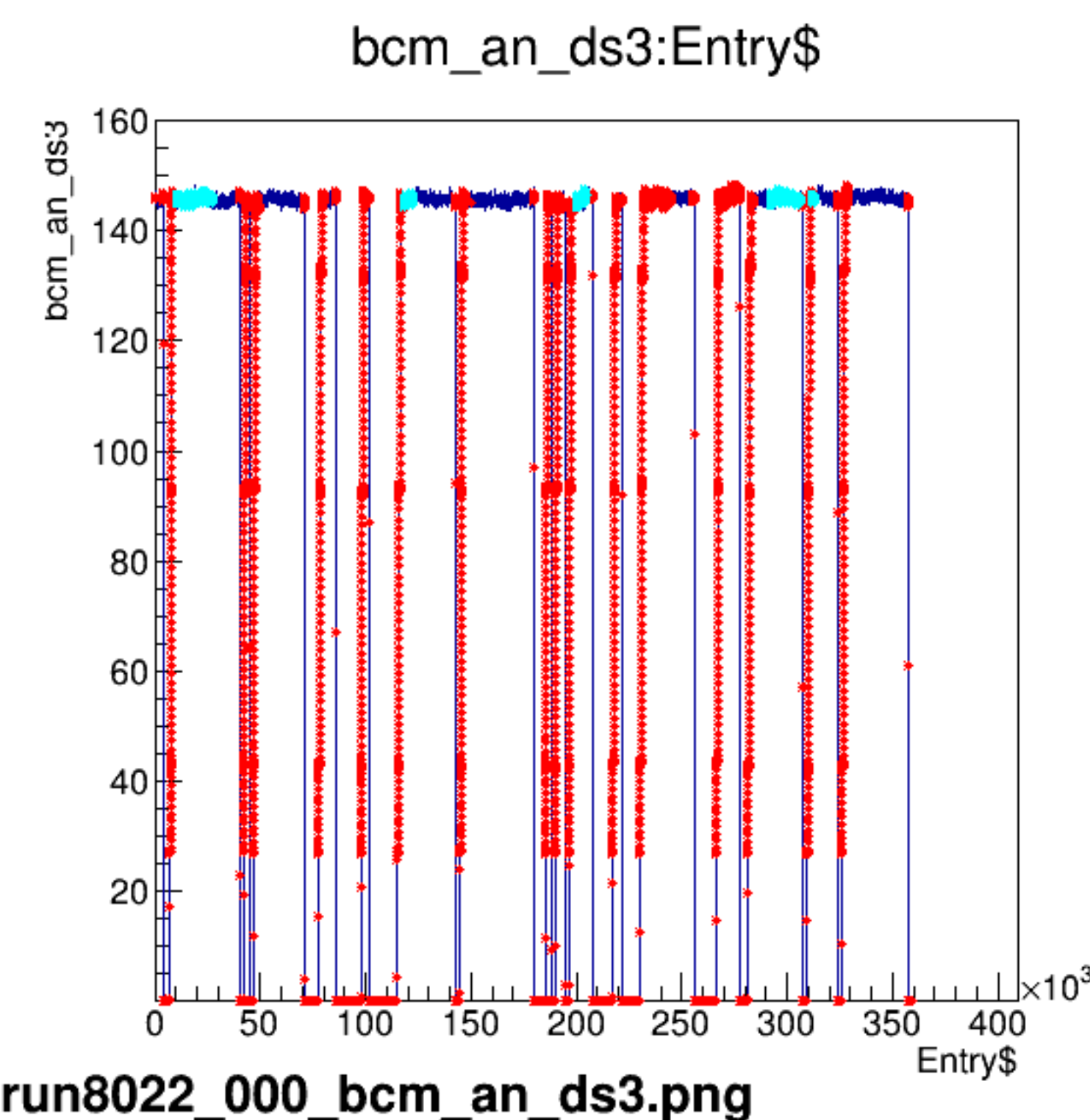
asym\_bcm\_an\_us/ppm:pattern\_number {ErrorFlag==0}

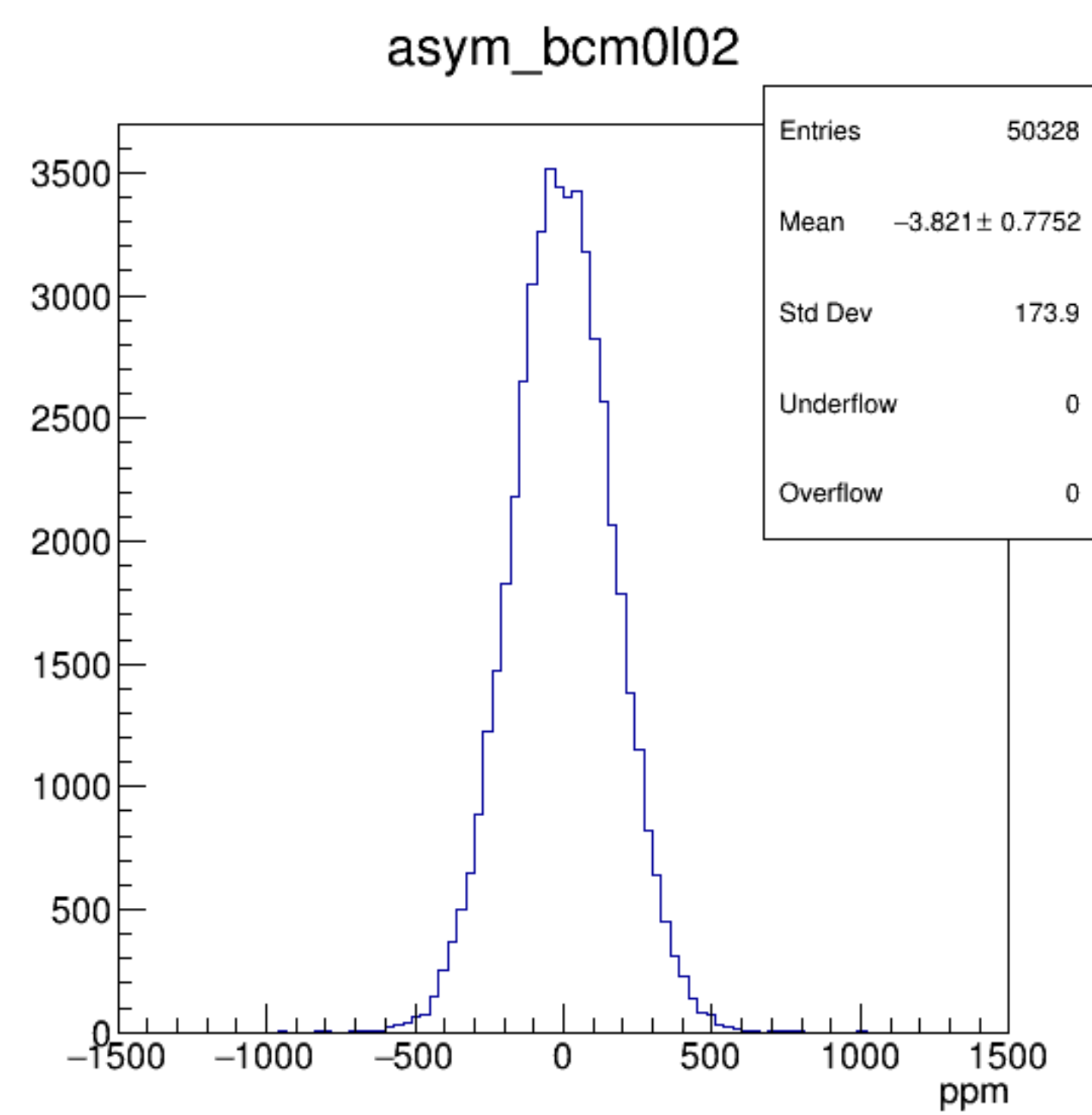
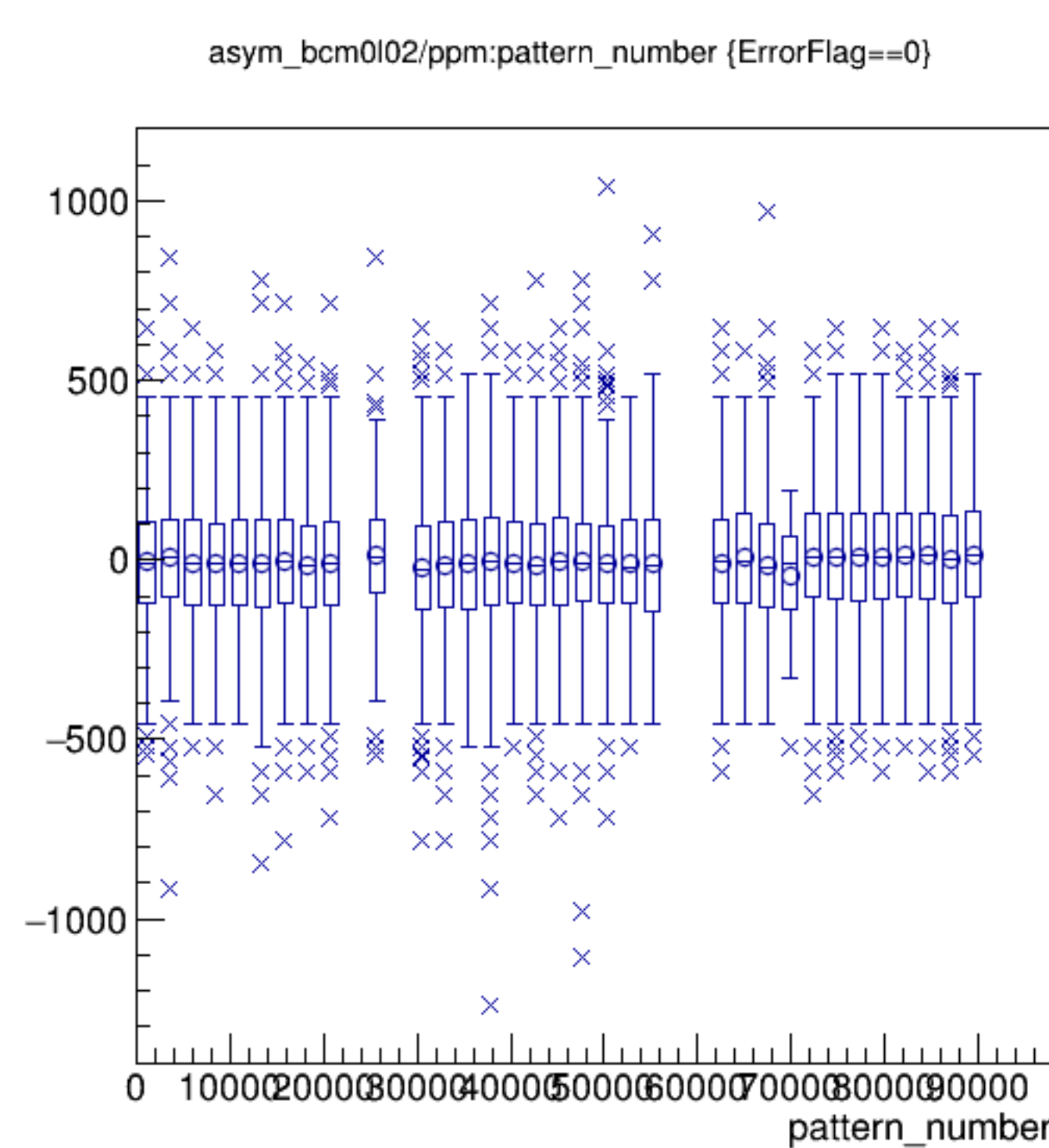
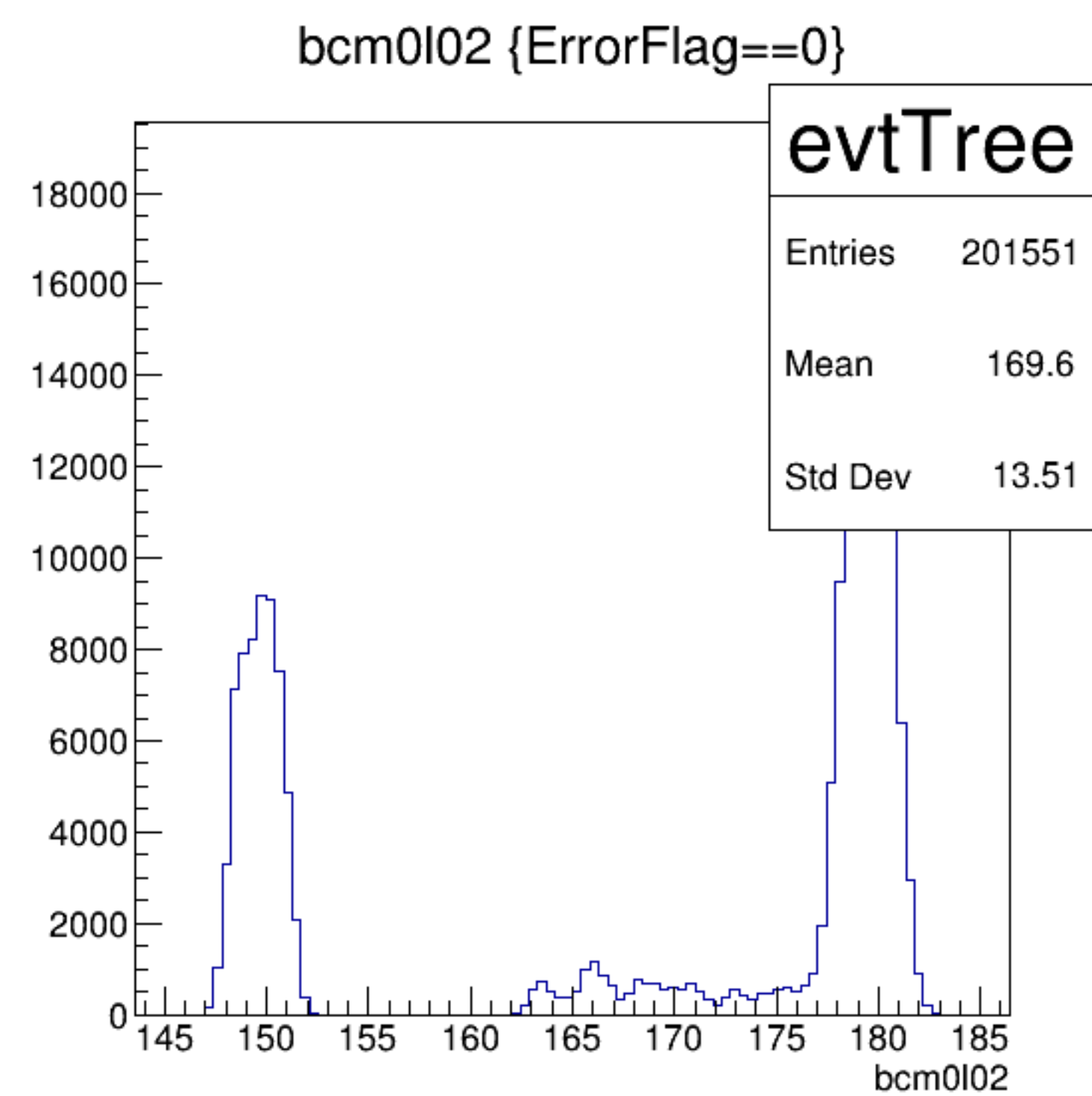
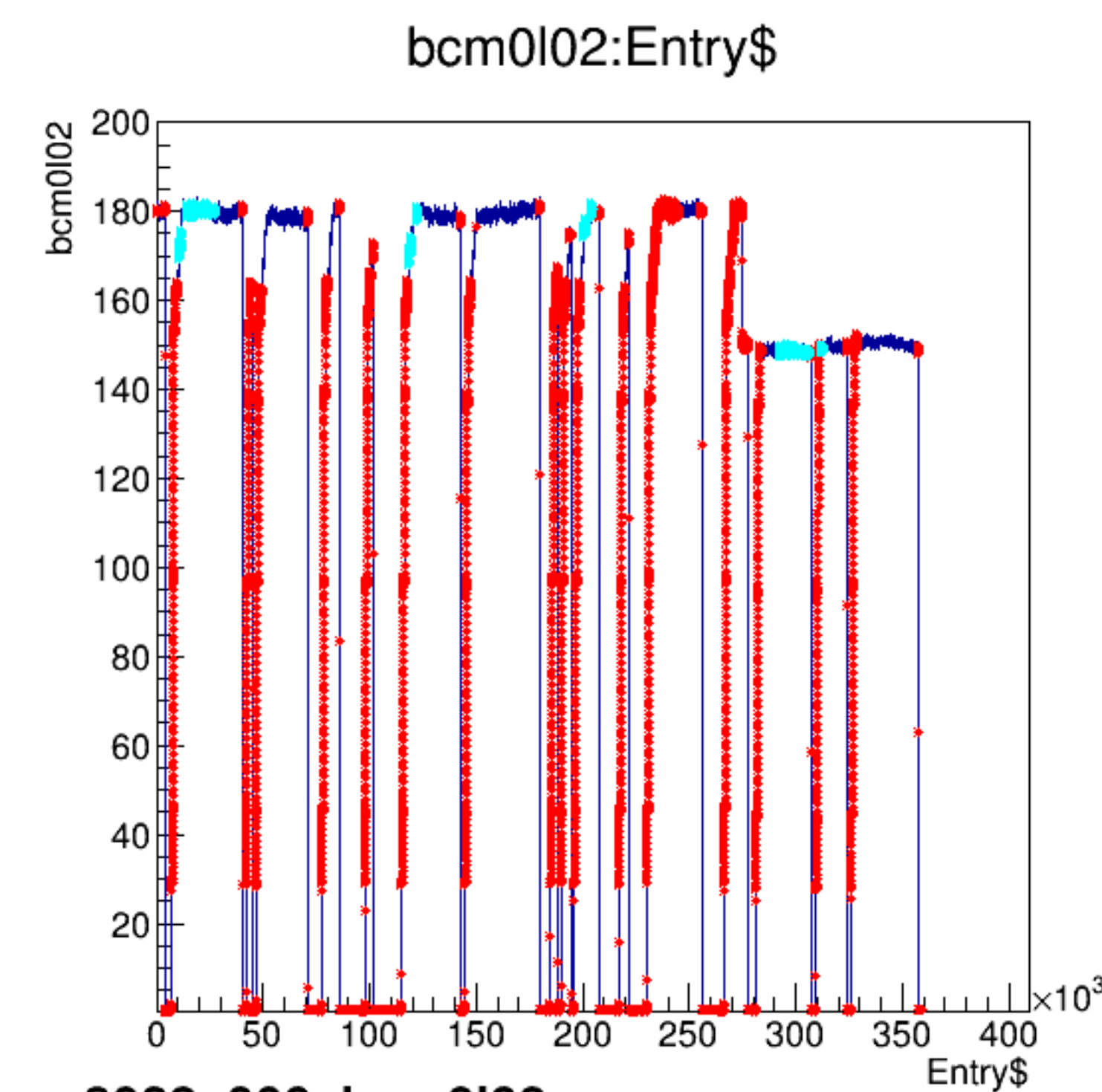


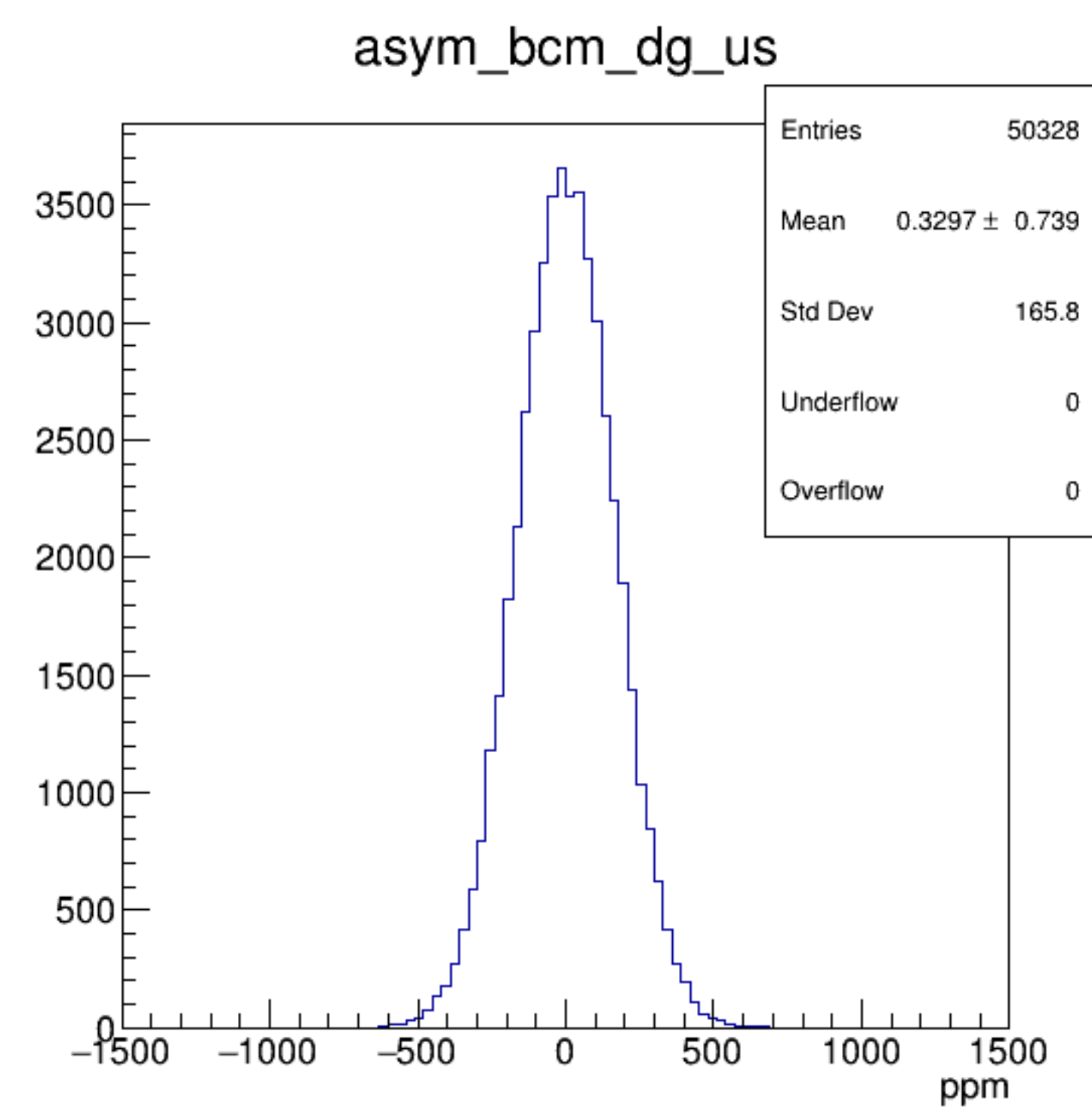
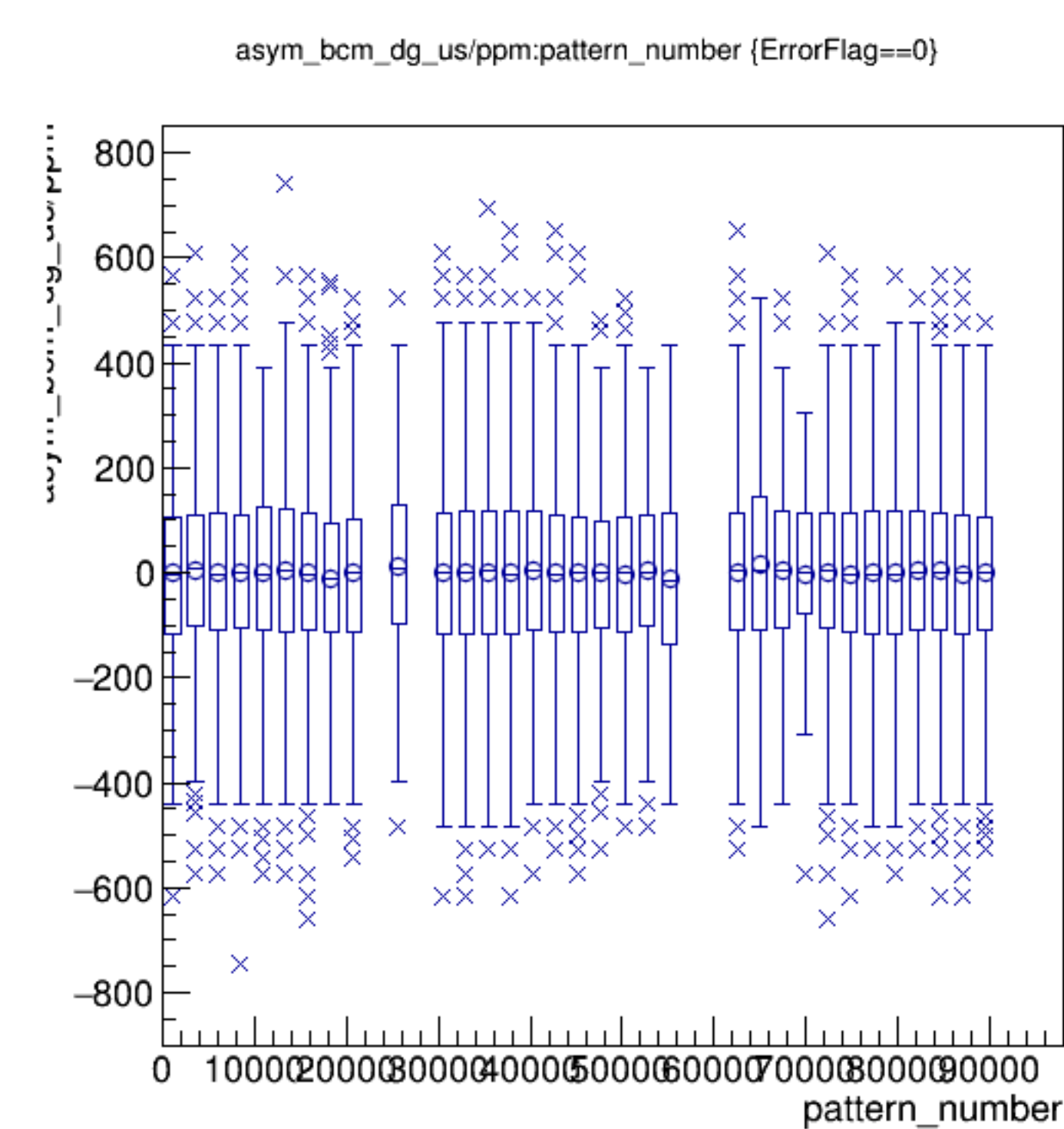
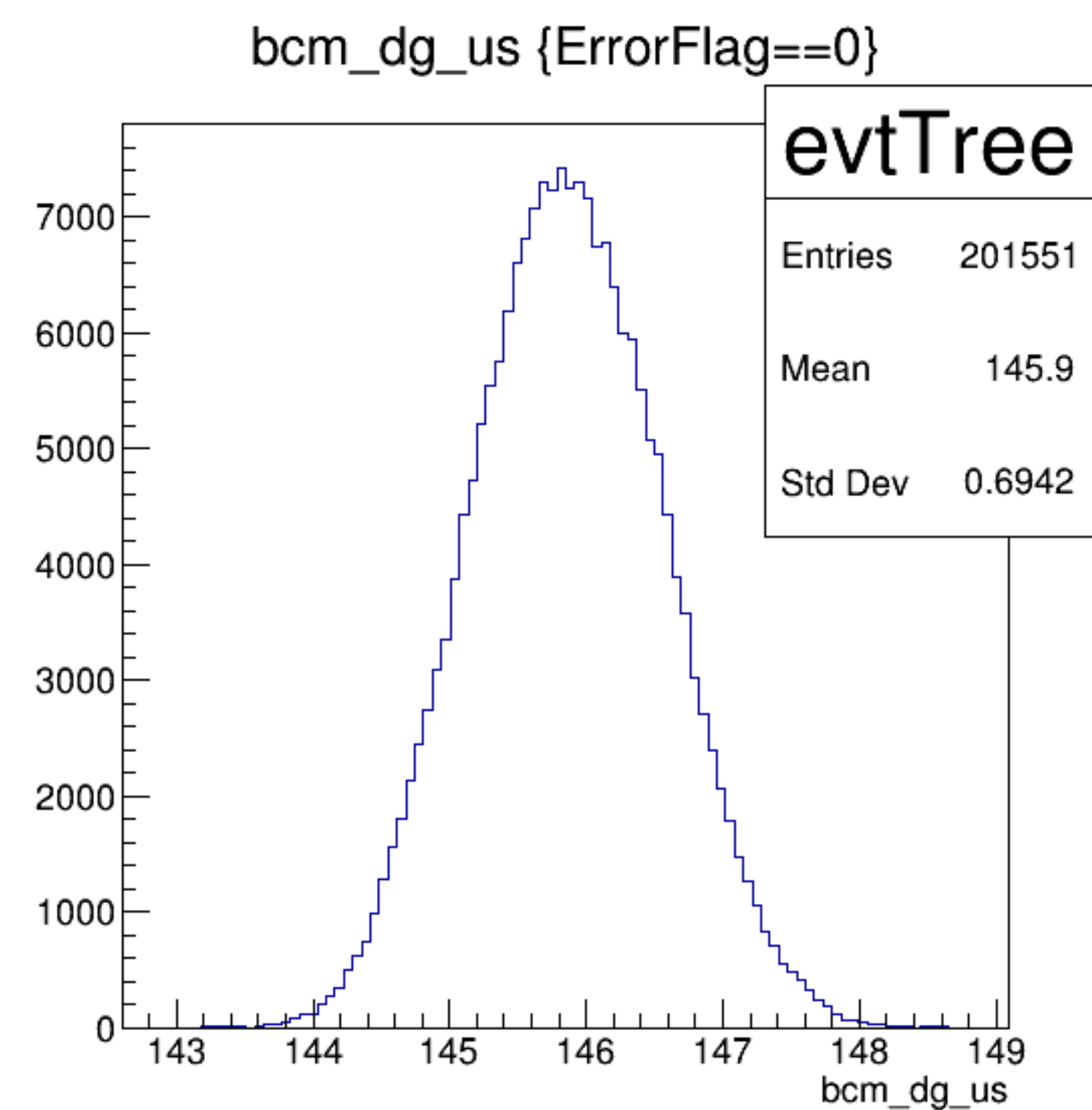
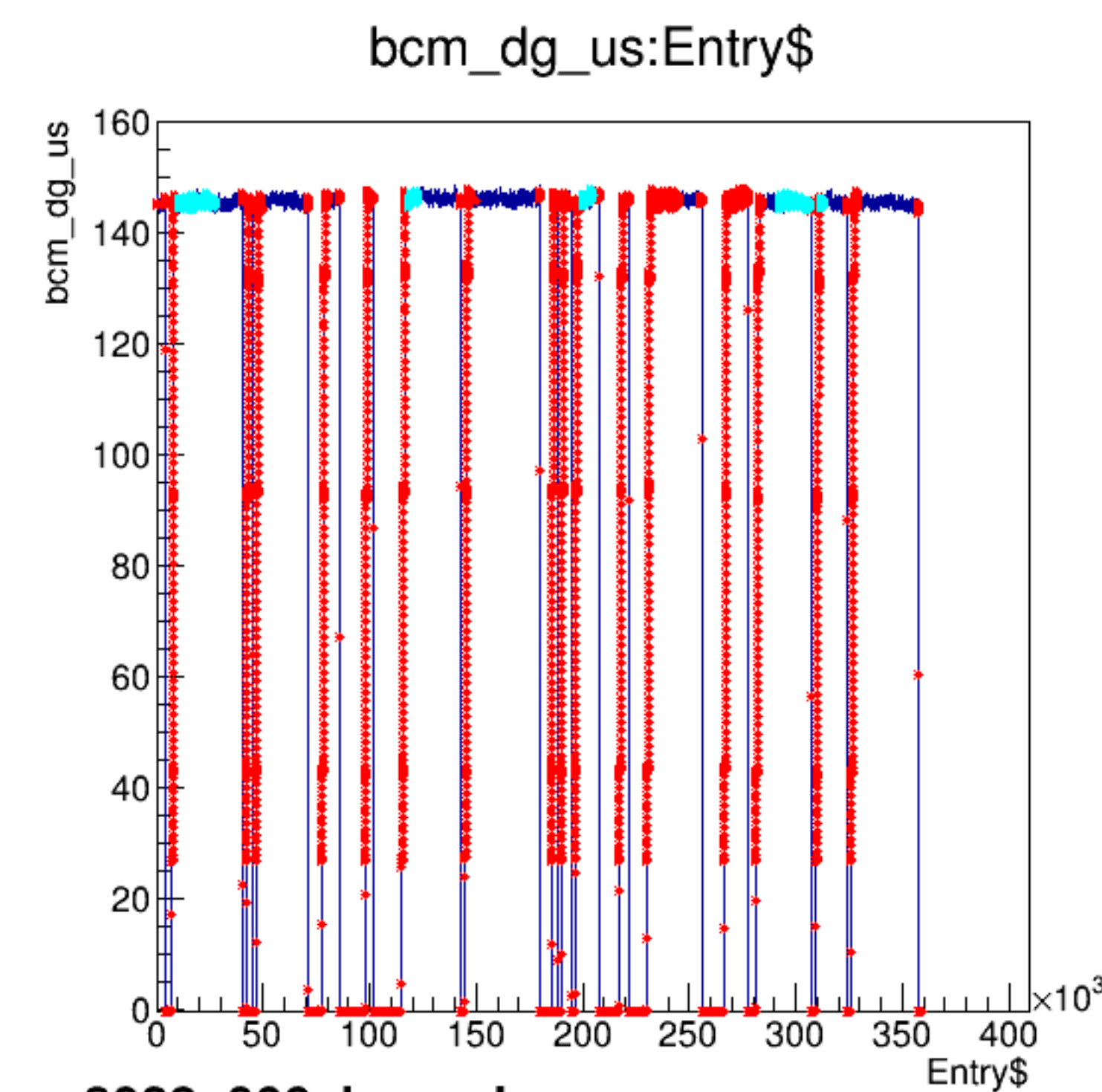
asym\_bcm\_an\_us

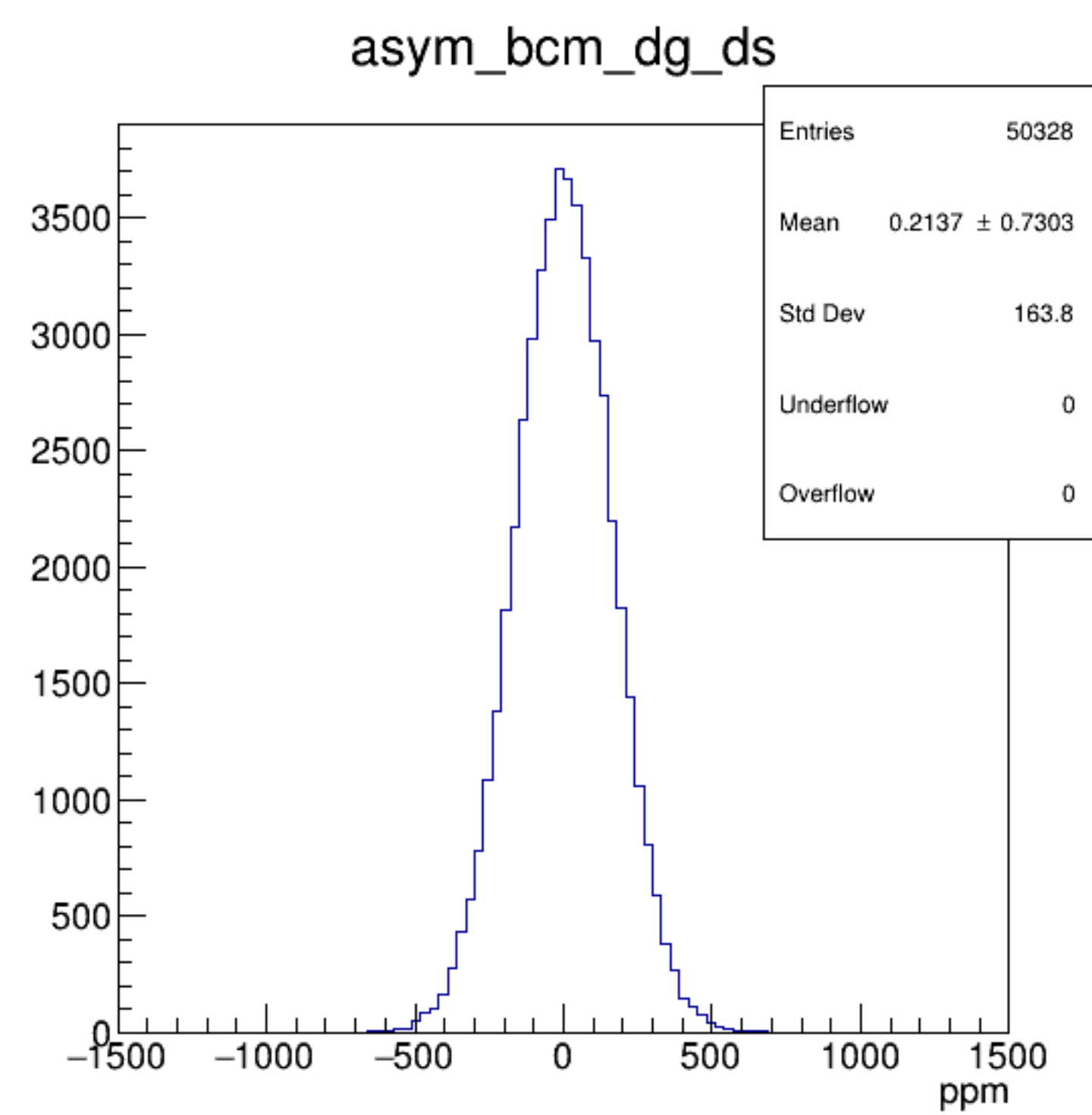
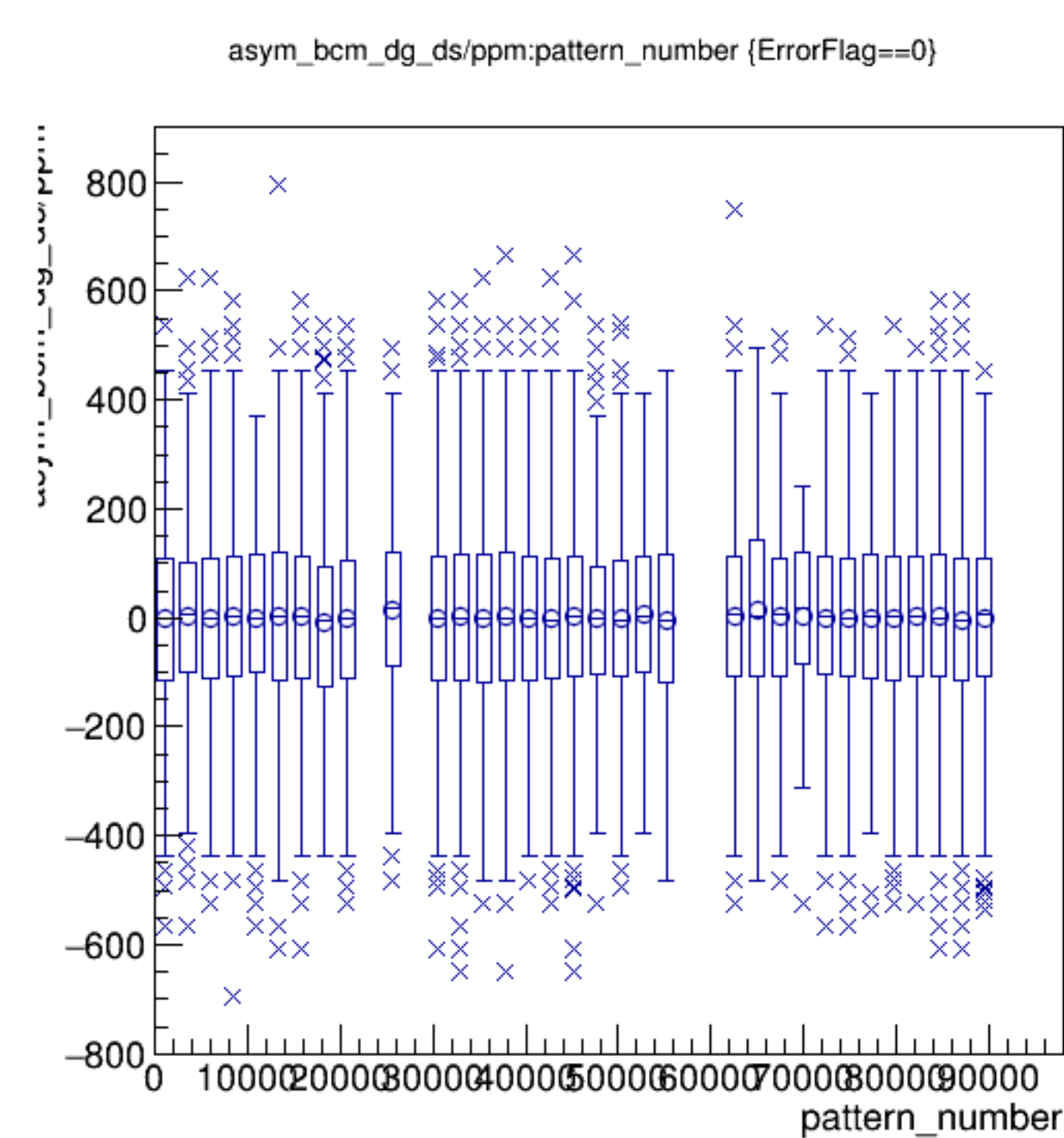
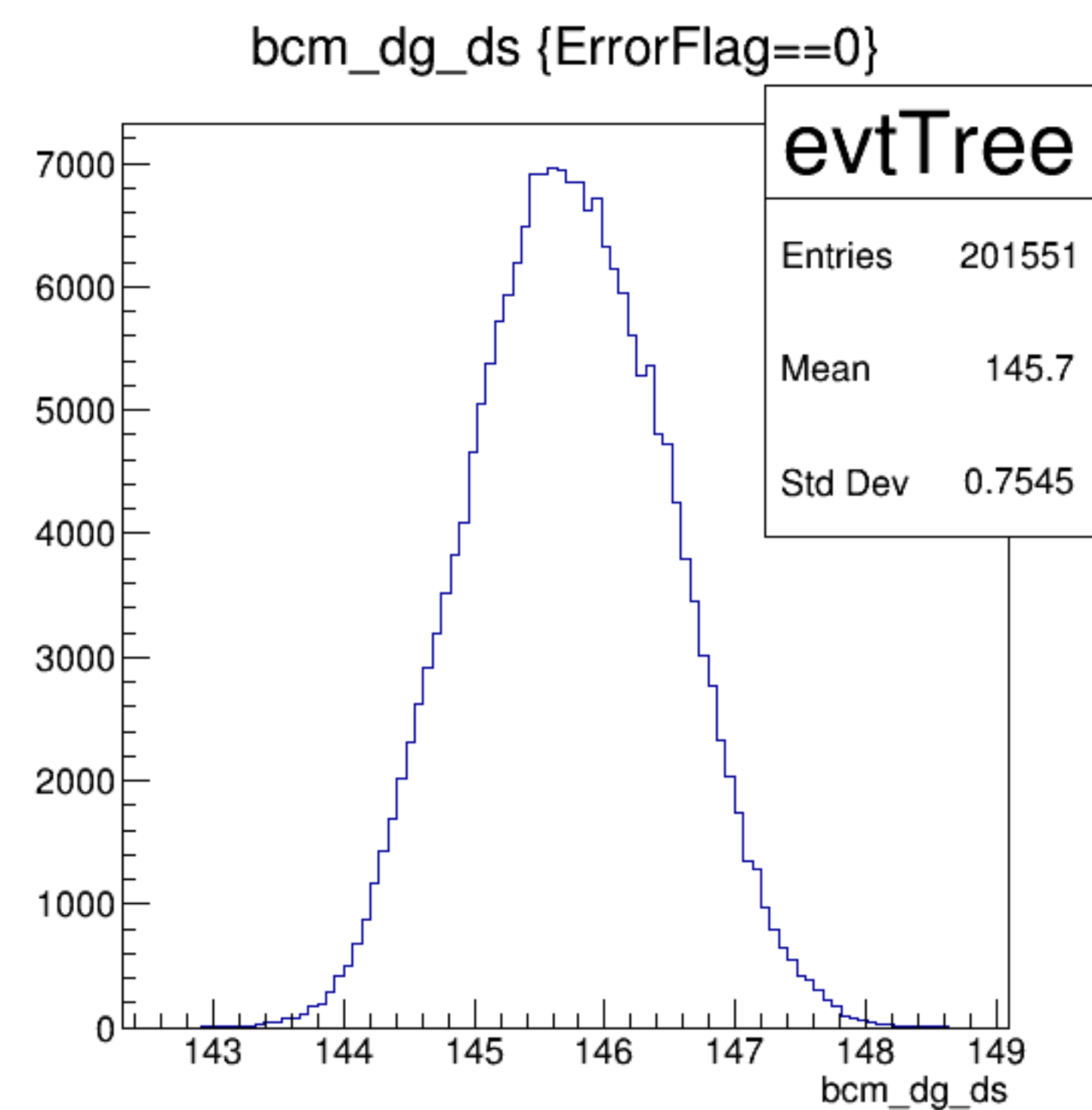
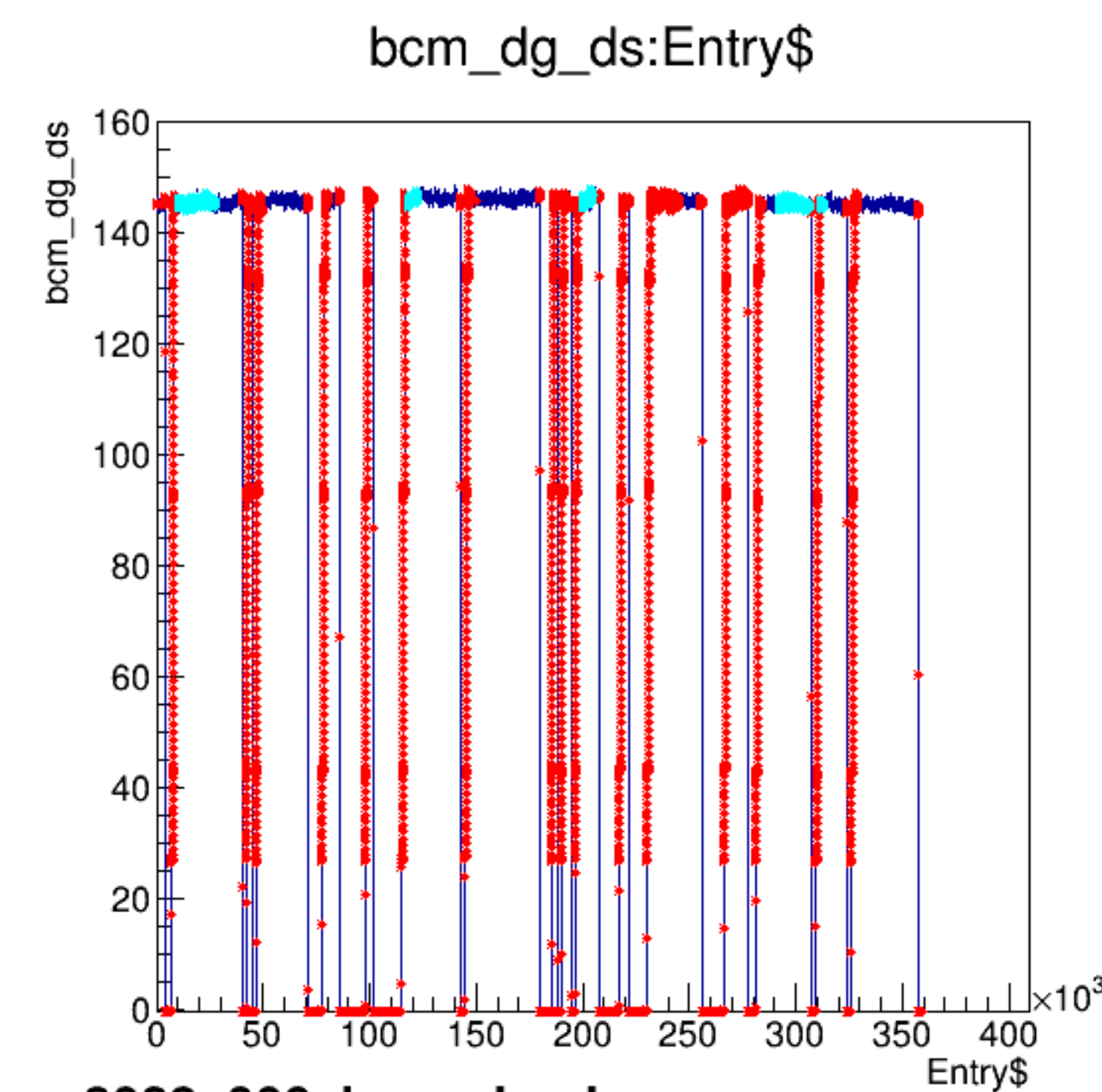




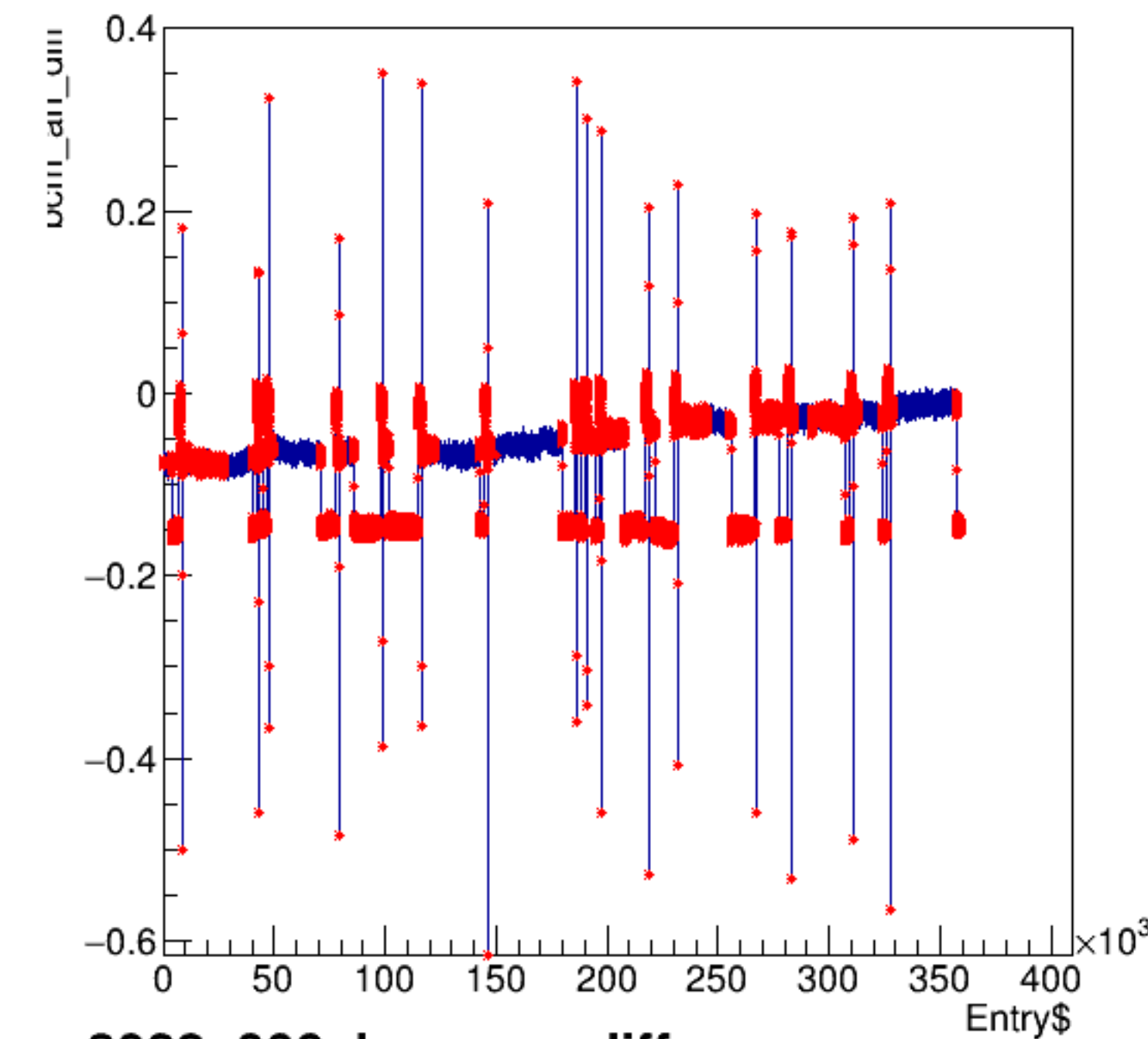




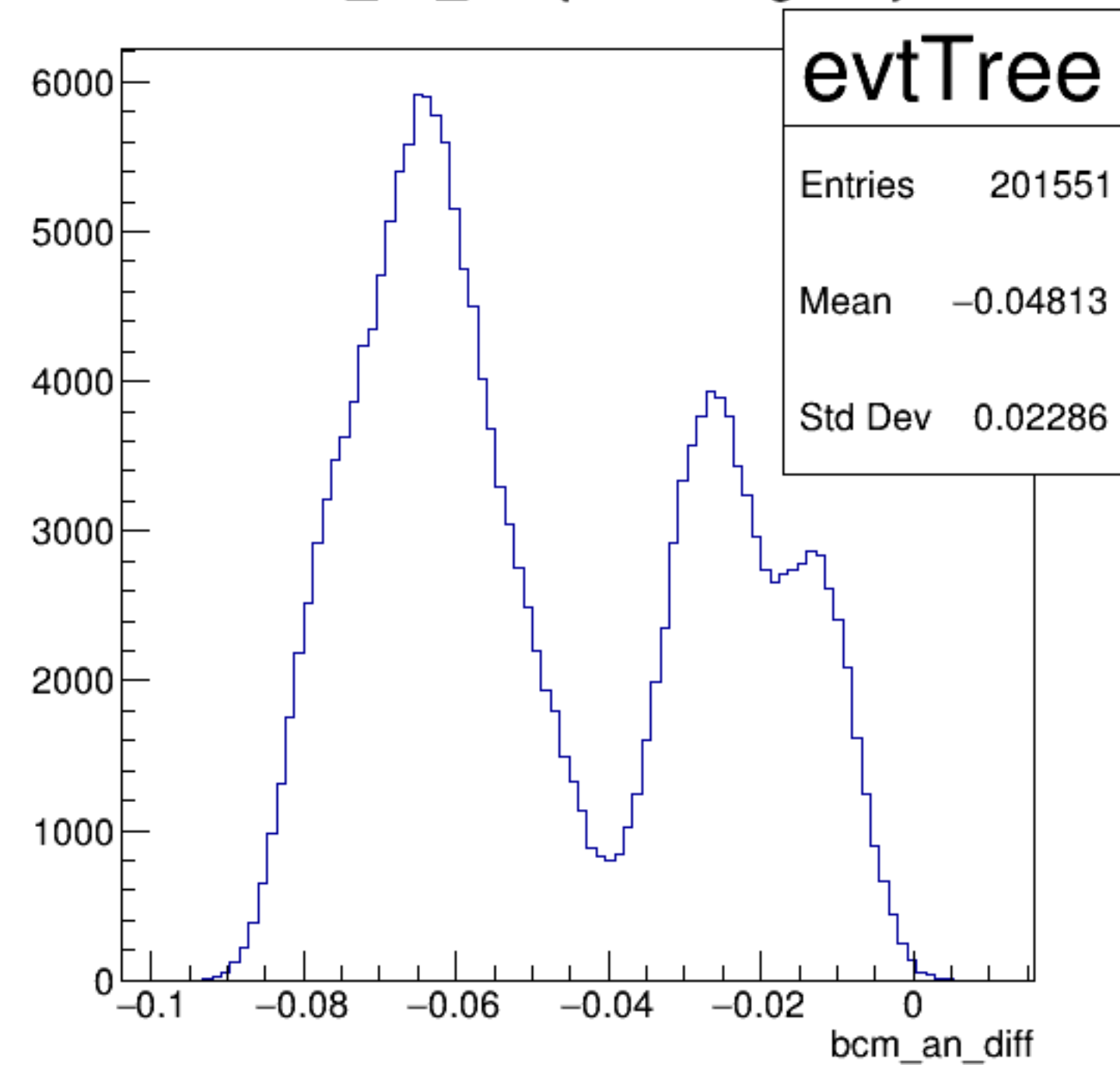




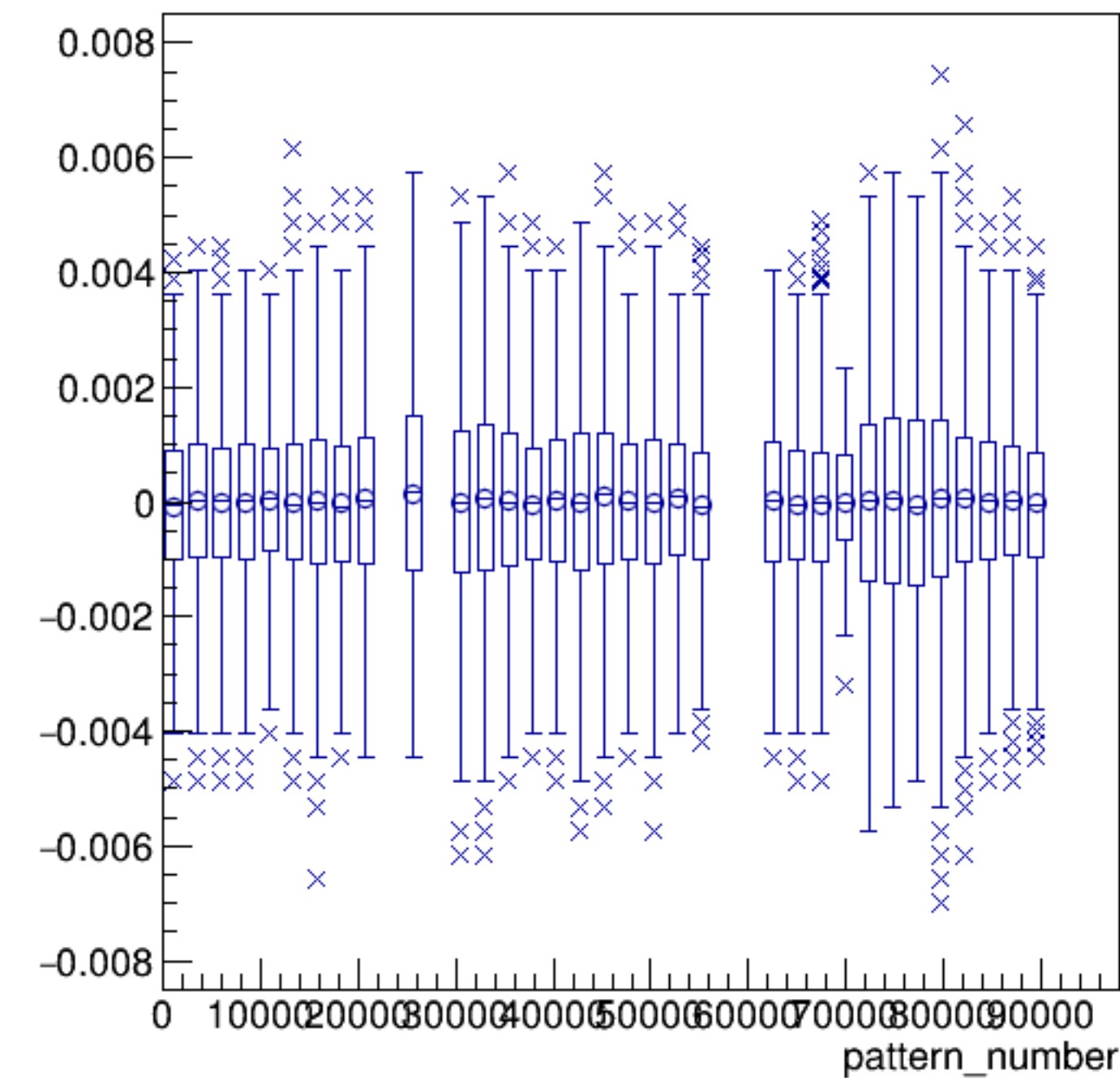
bcm\_an\_diff:Entry\$



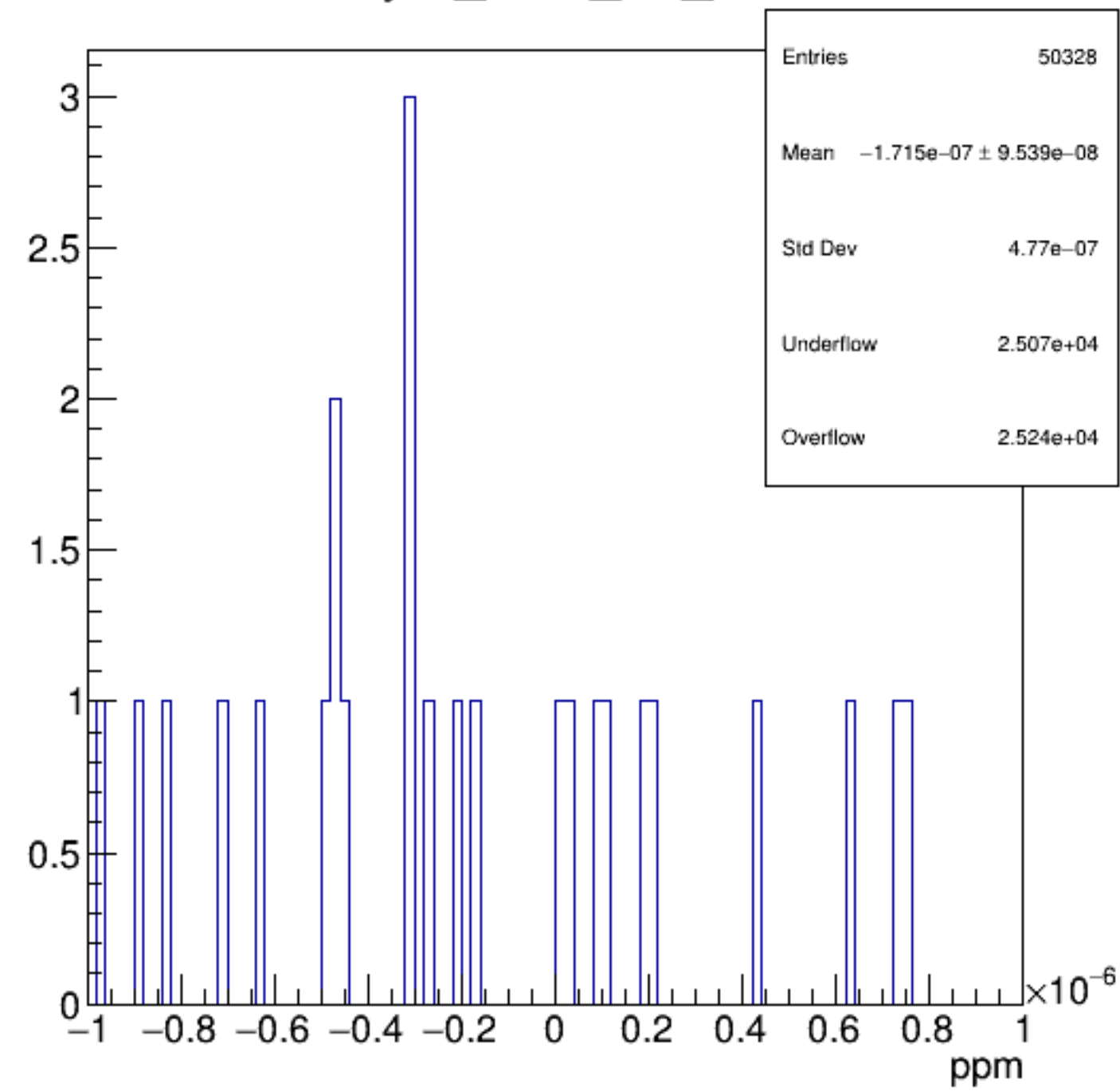
bcm\_an\_diff {ErrorFlag==0}



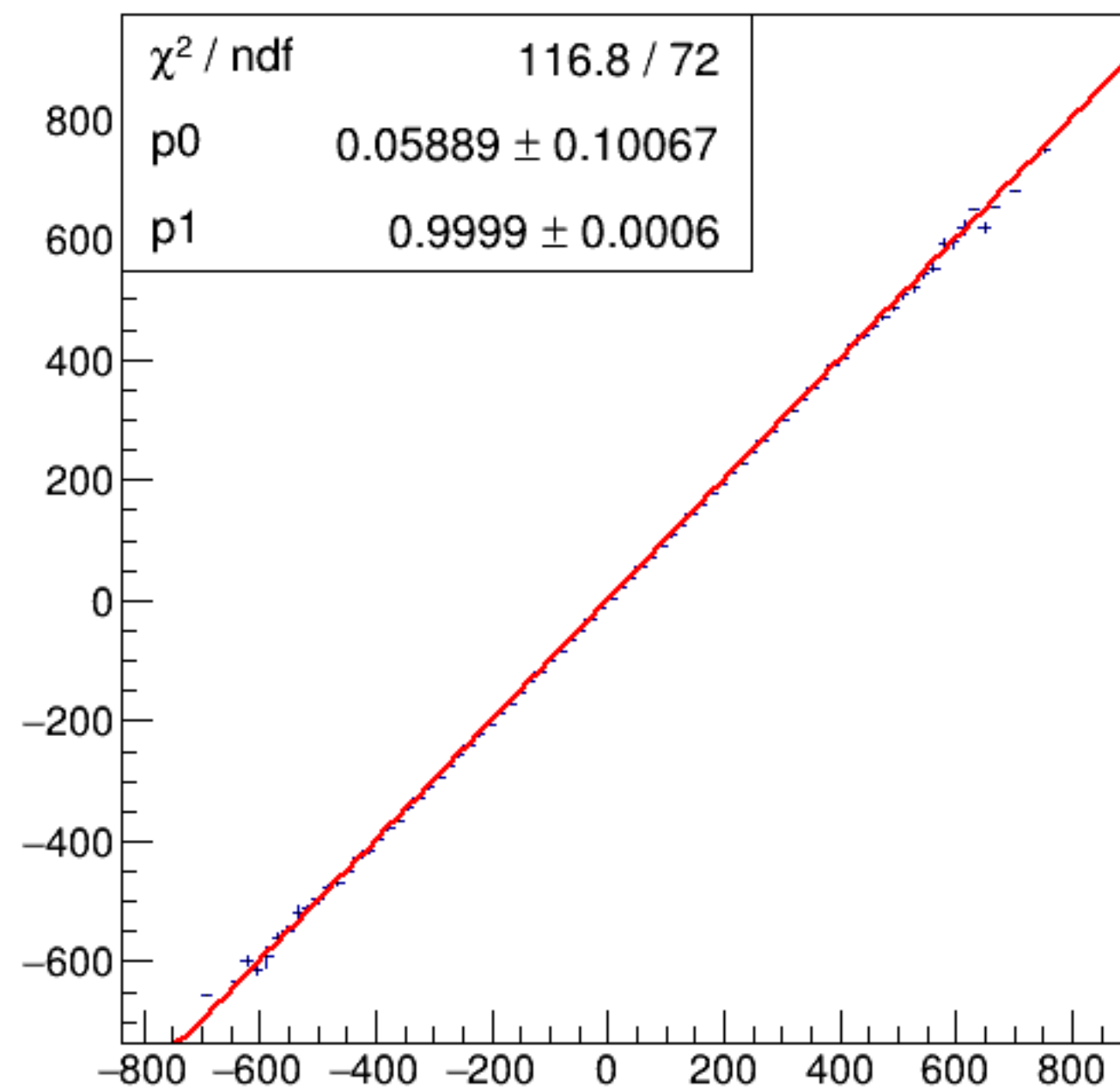
asym\_bcm\_an\_diff\*yield\_bcm\_an\_diff:pattern\_number {ErrorFlag==0}



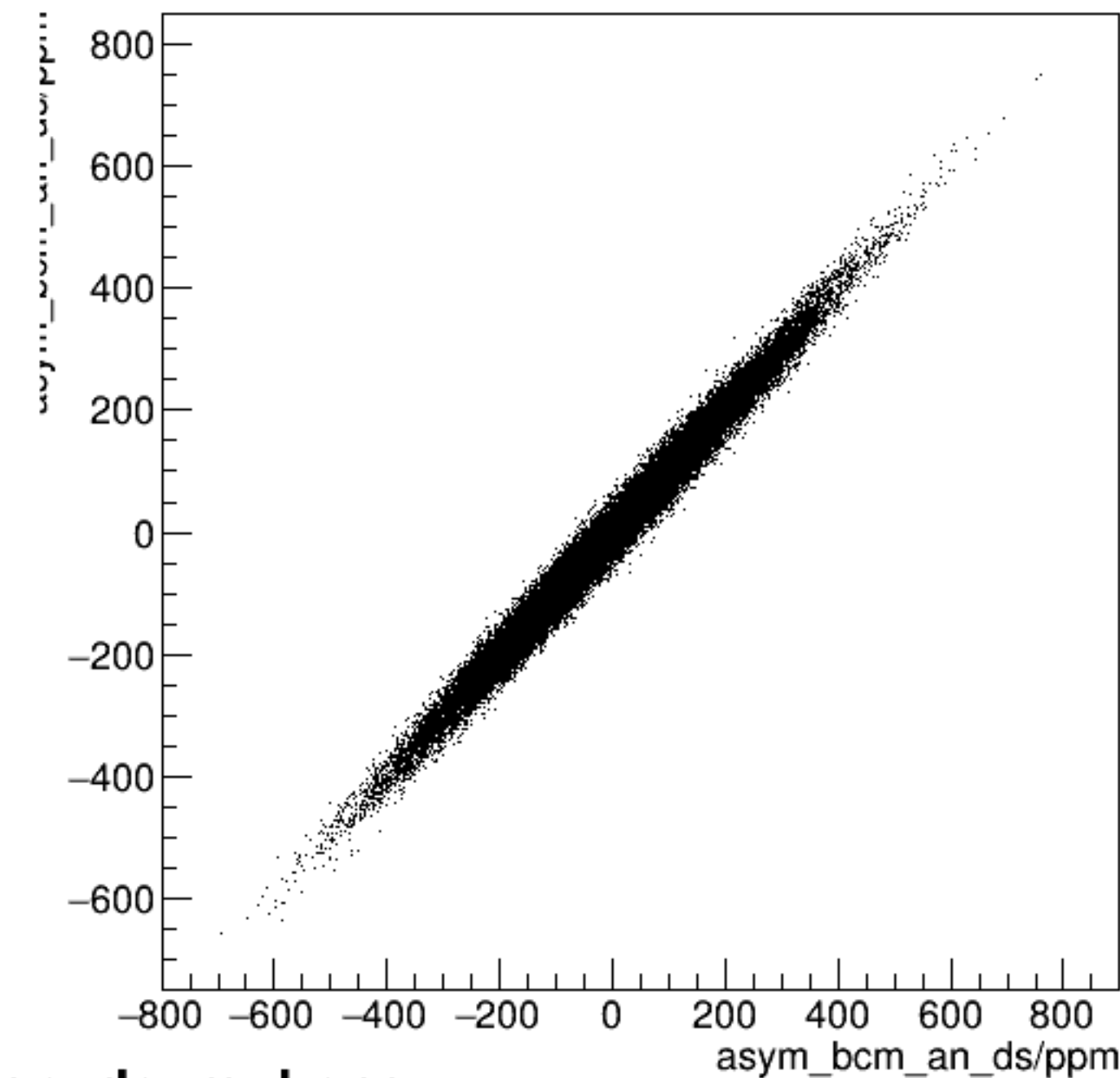
asym\_bcm\_an\_diff



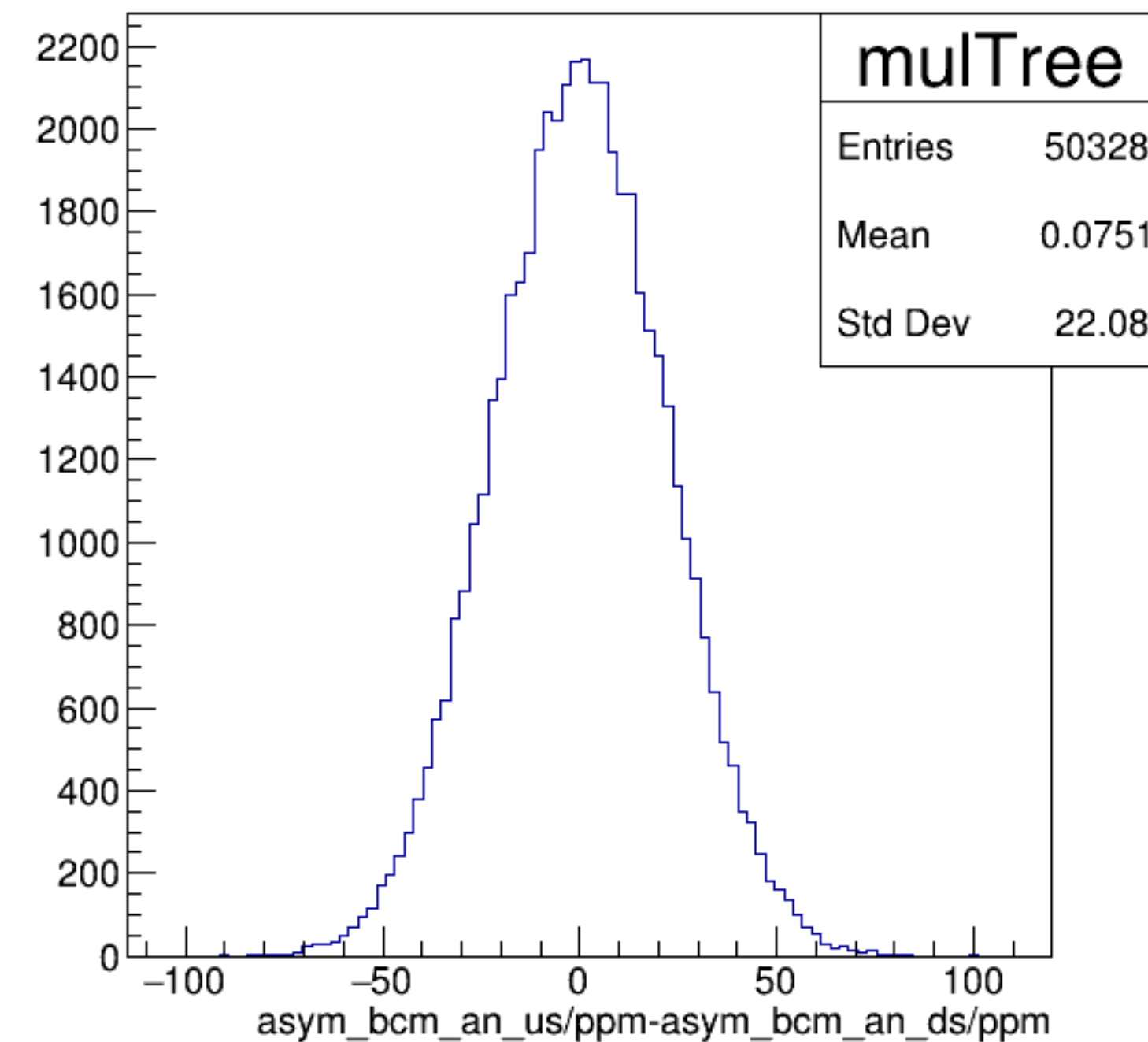
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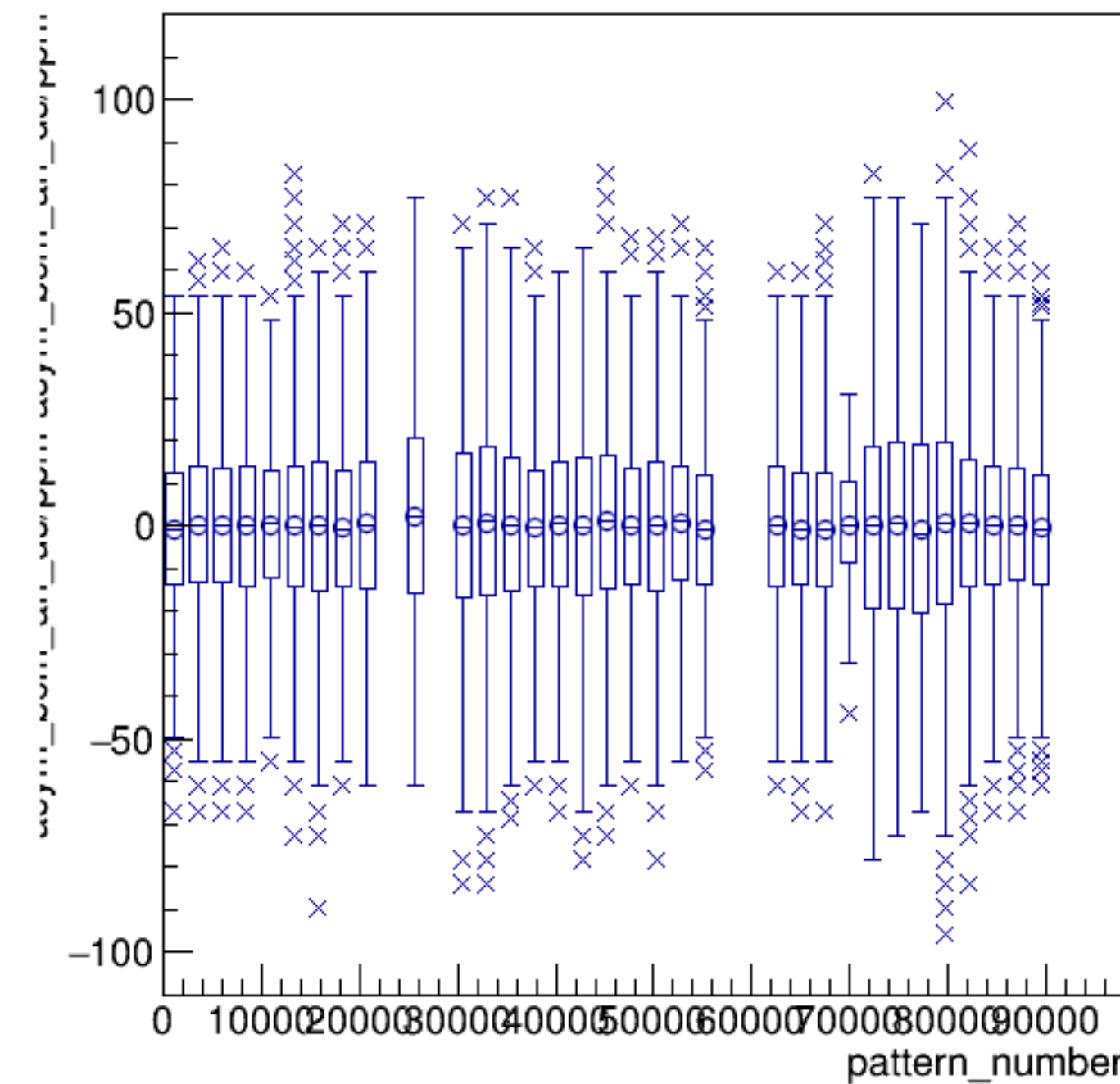
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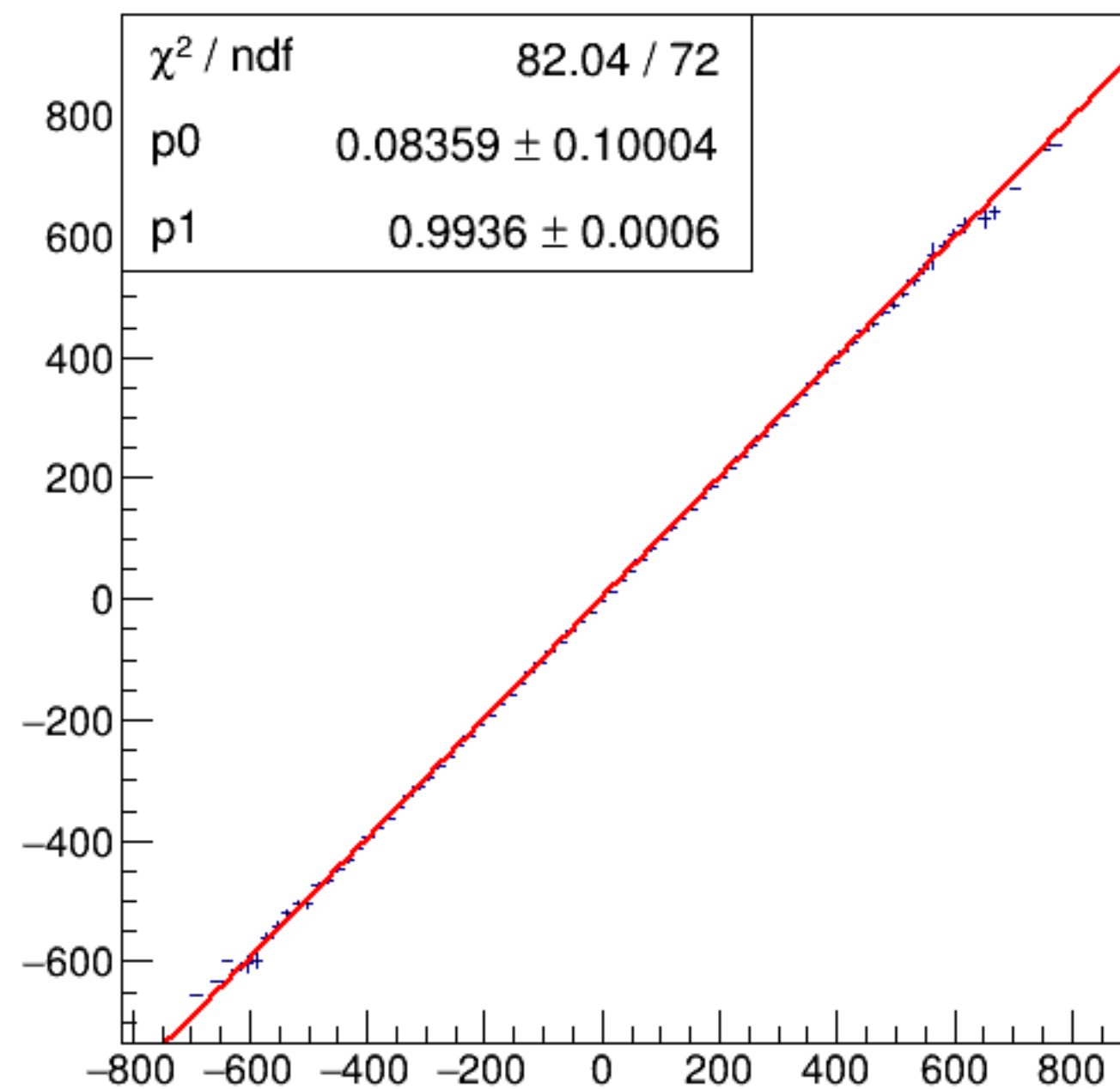


asym\_bcm\_an\_us/ppm-asym\_bcm\_an\_ds/ppm:pattern\_number {ErrorFlag==0}

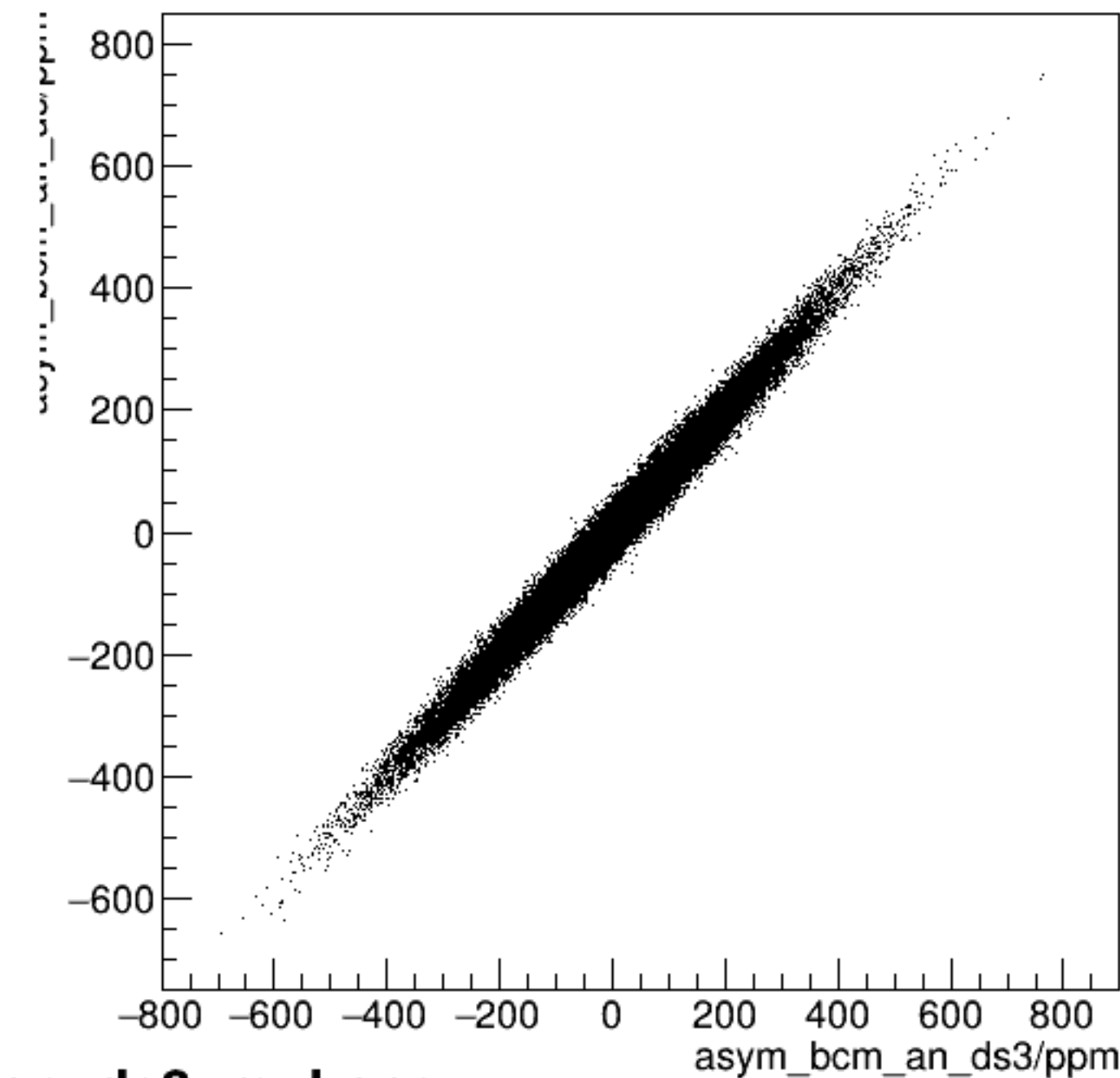


run8022\_000\_asym\_bcm\_an\_us\_vs\_asym\_bcm\_an\_ds\_mul.png

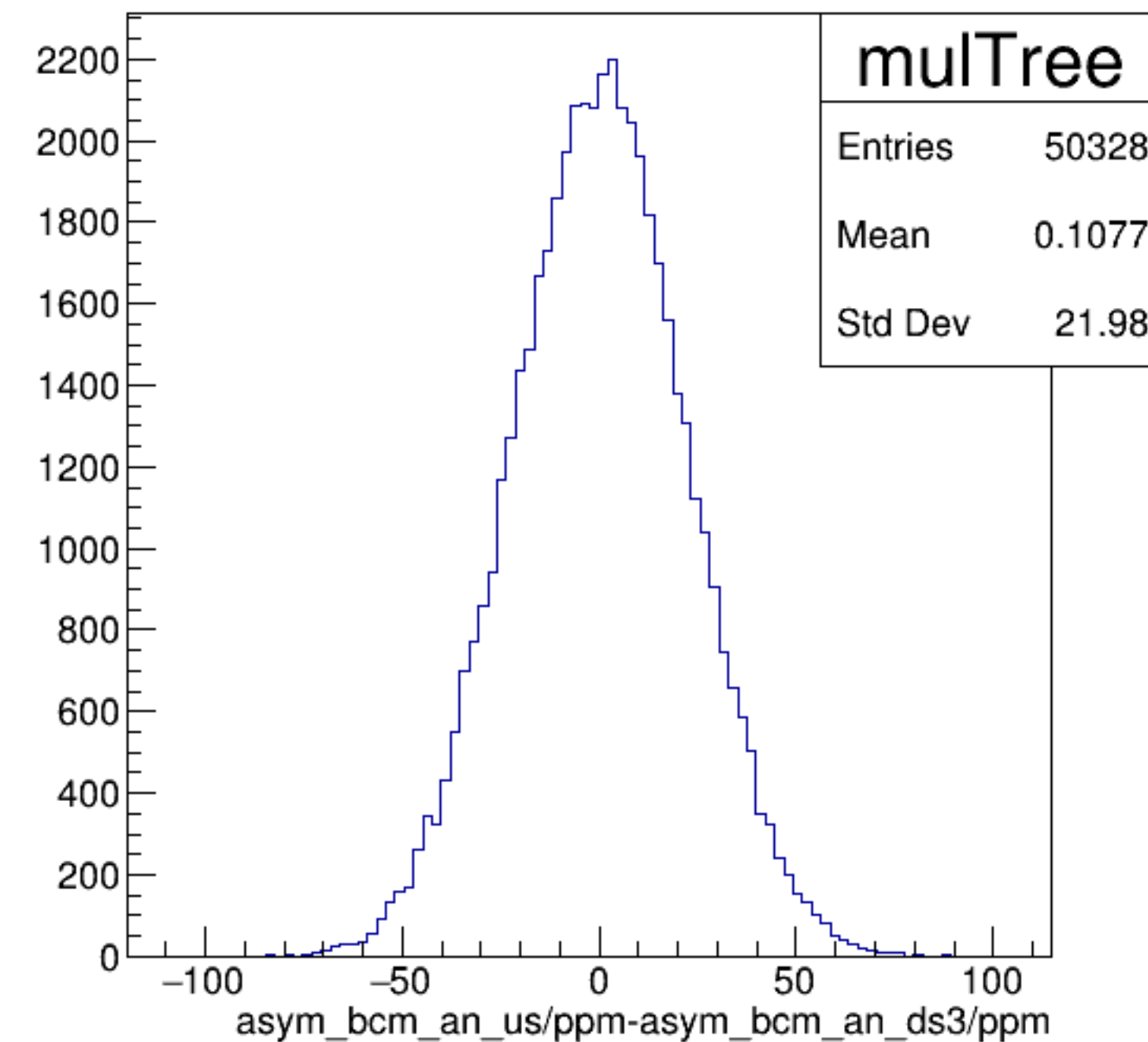
asym\_bcm\_an\_us/ppm:asym\_bcm\_an\_ds3/ppm {ErrorFlag==0}



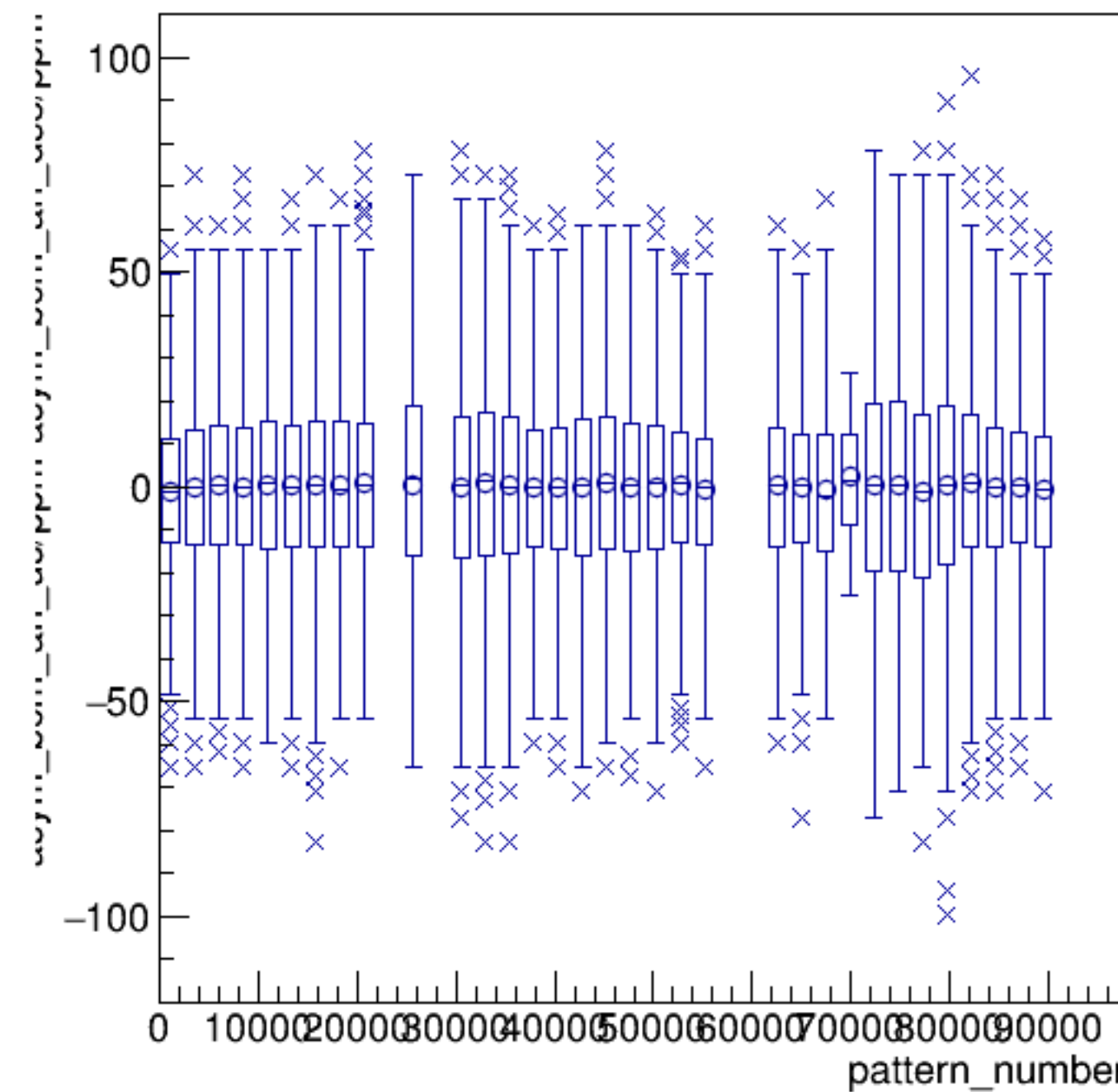
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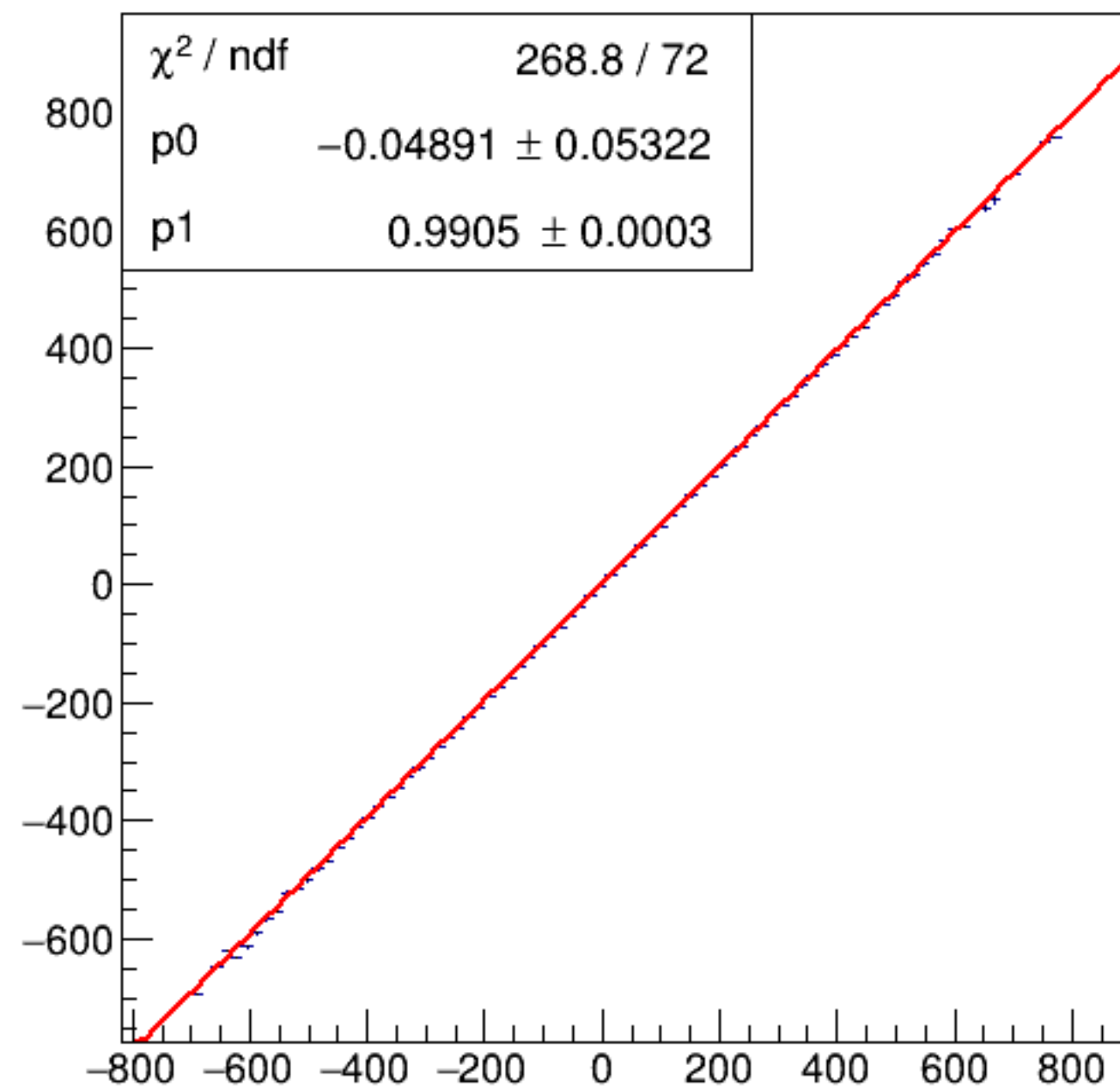
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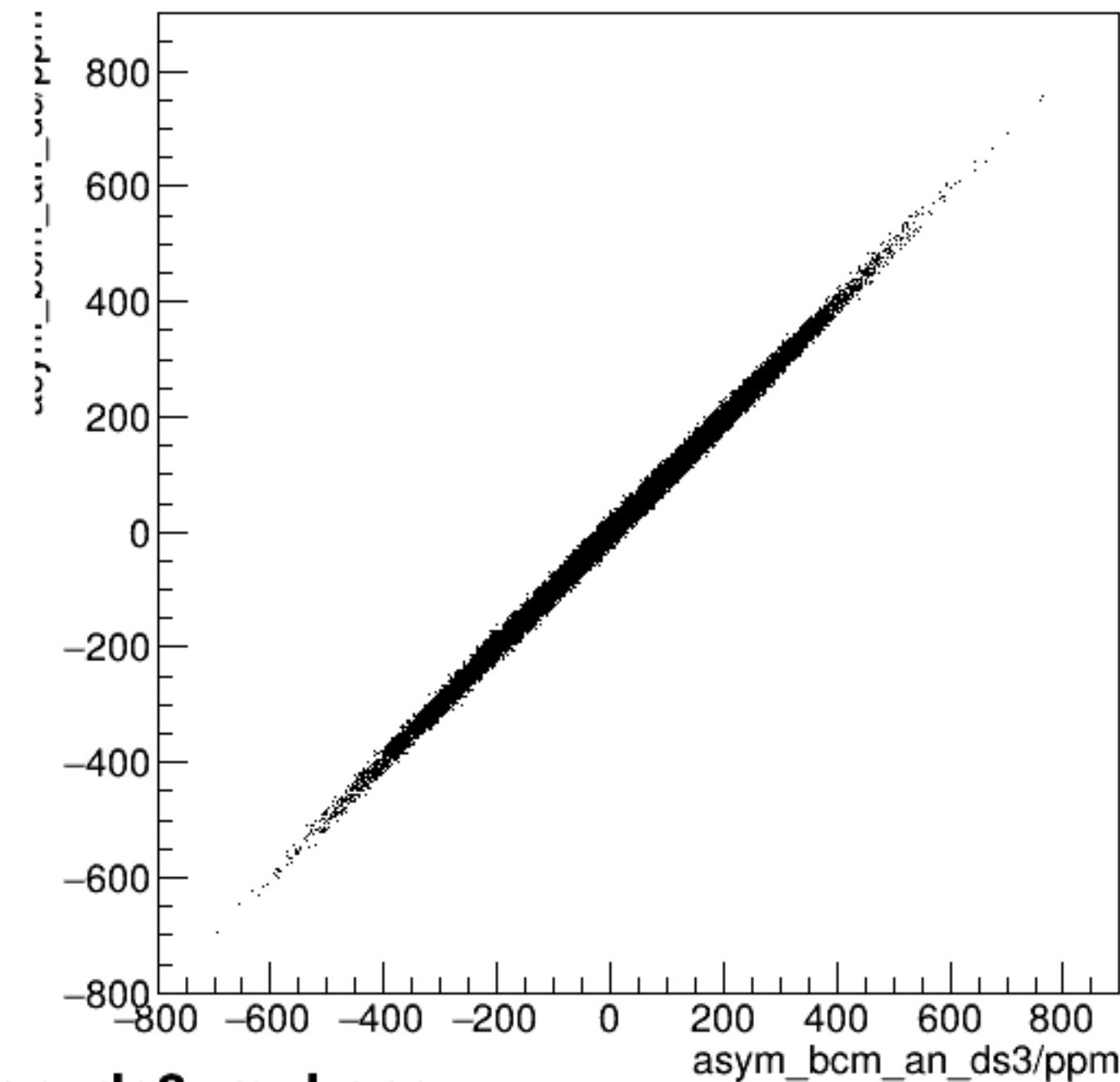
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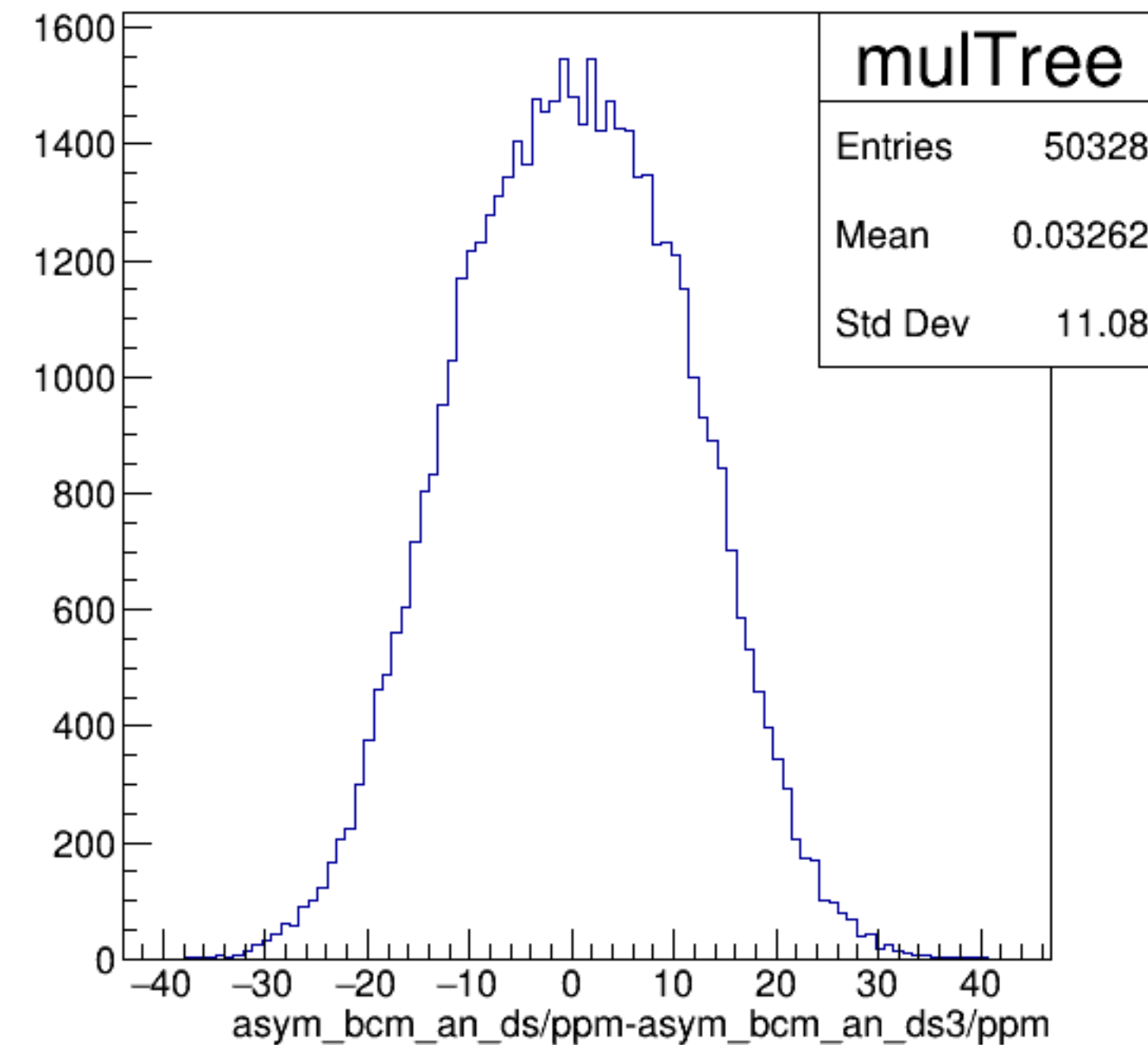
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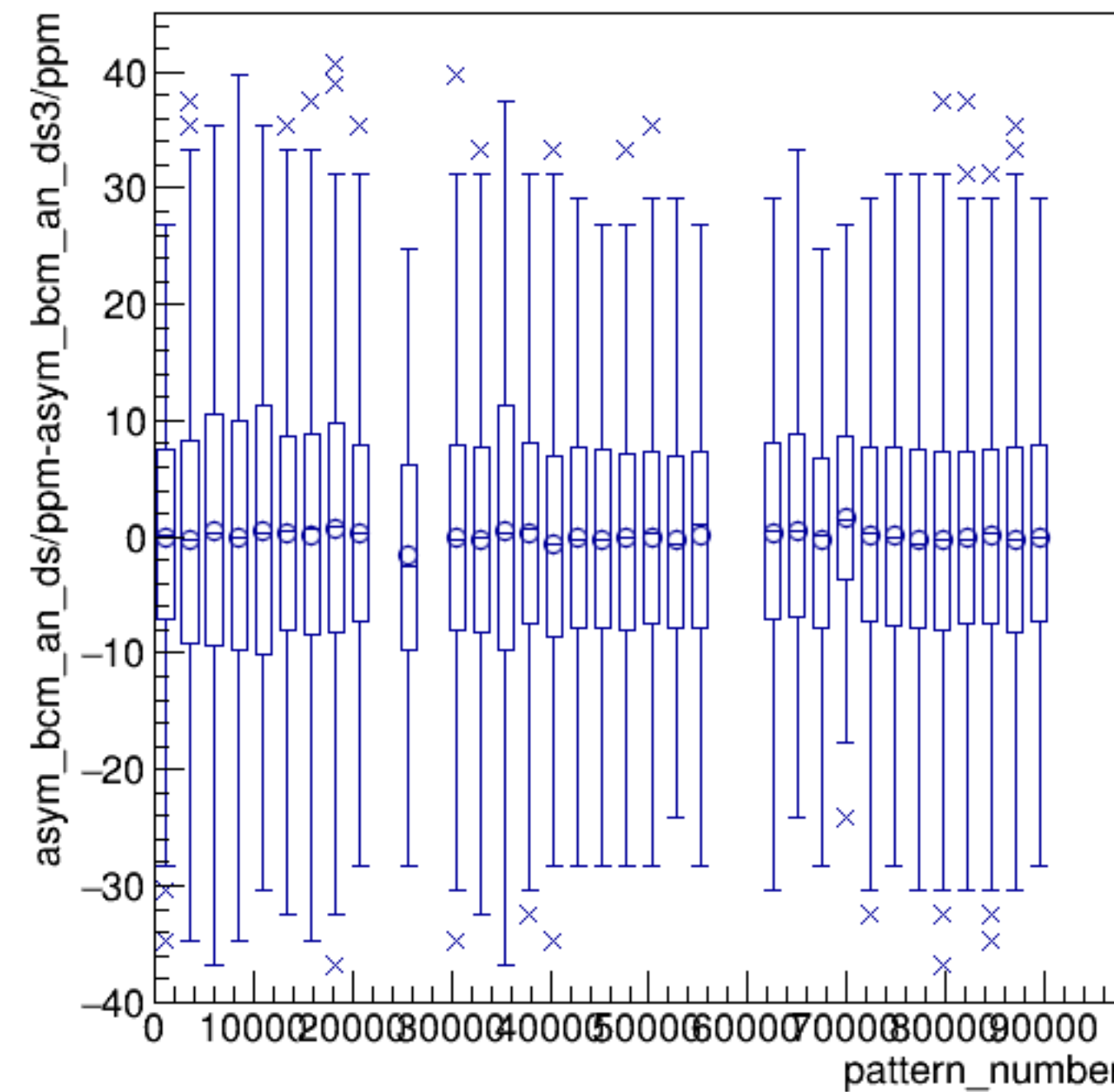
asym\_bcm\_an\_ds/ppm:asym\_bcm\_an\_ds3/ppm {ErrorFlag==0}



asym\_bcm\_an\_ds/ppm-asym\_bcm\_an\_ds3/ppm {ErrorFlag==0}

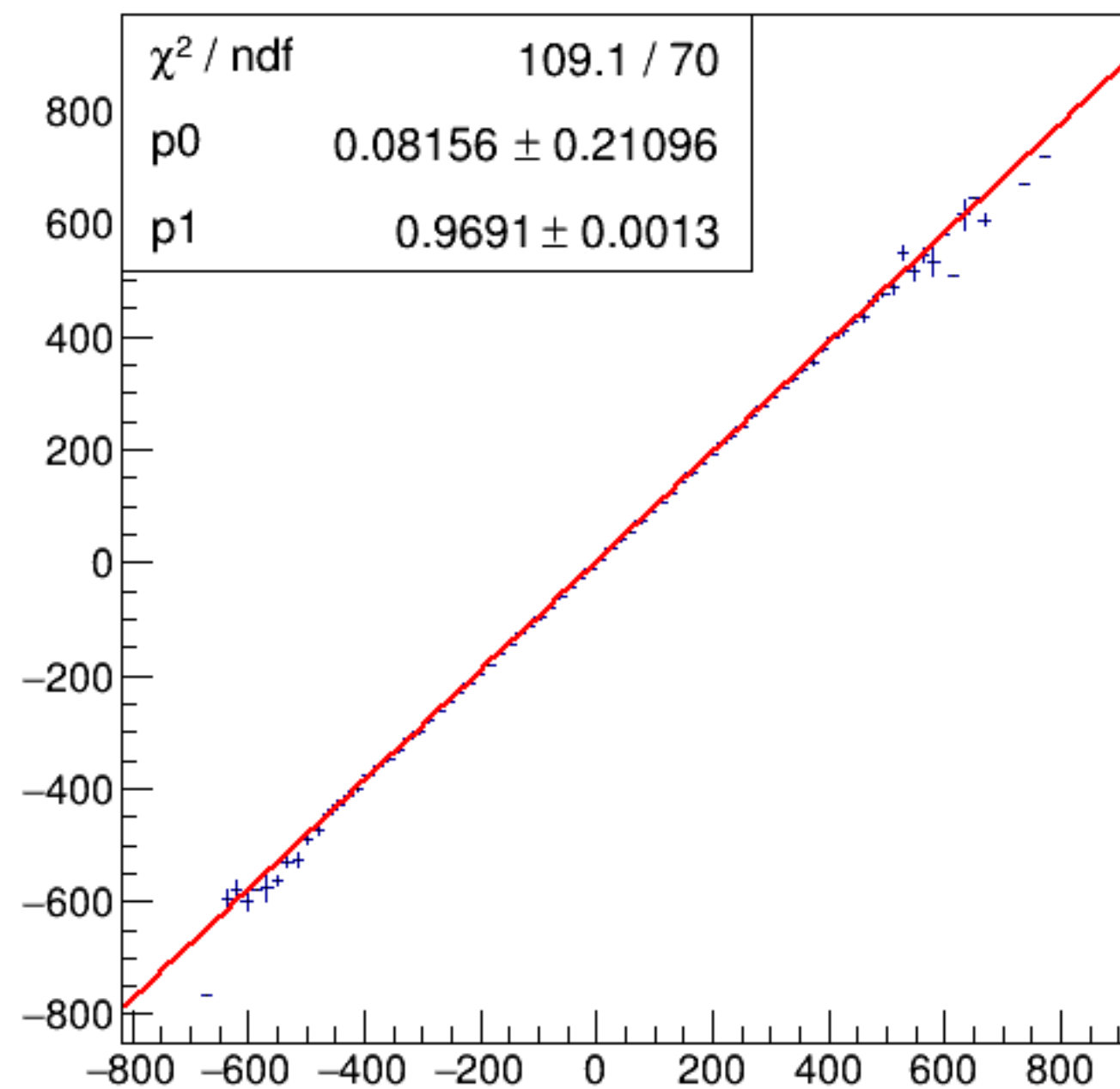


asym\_bcm\_an\_ds/ppm-asym\_bcm\_an\_ds3/ppm:pattern\_number {ErrorFlag==0}

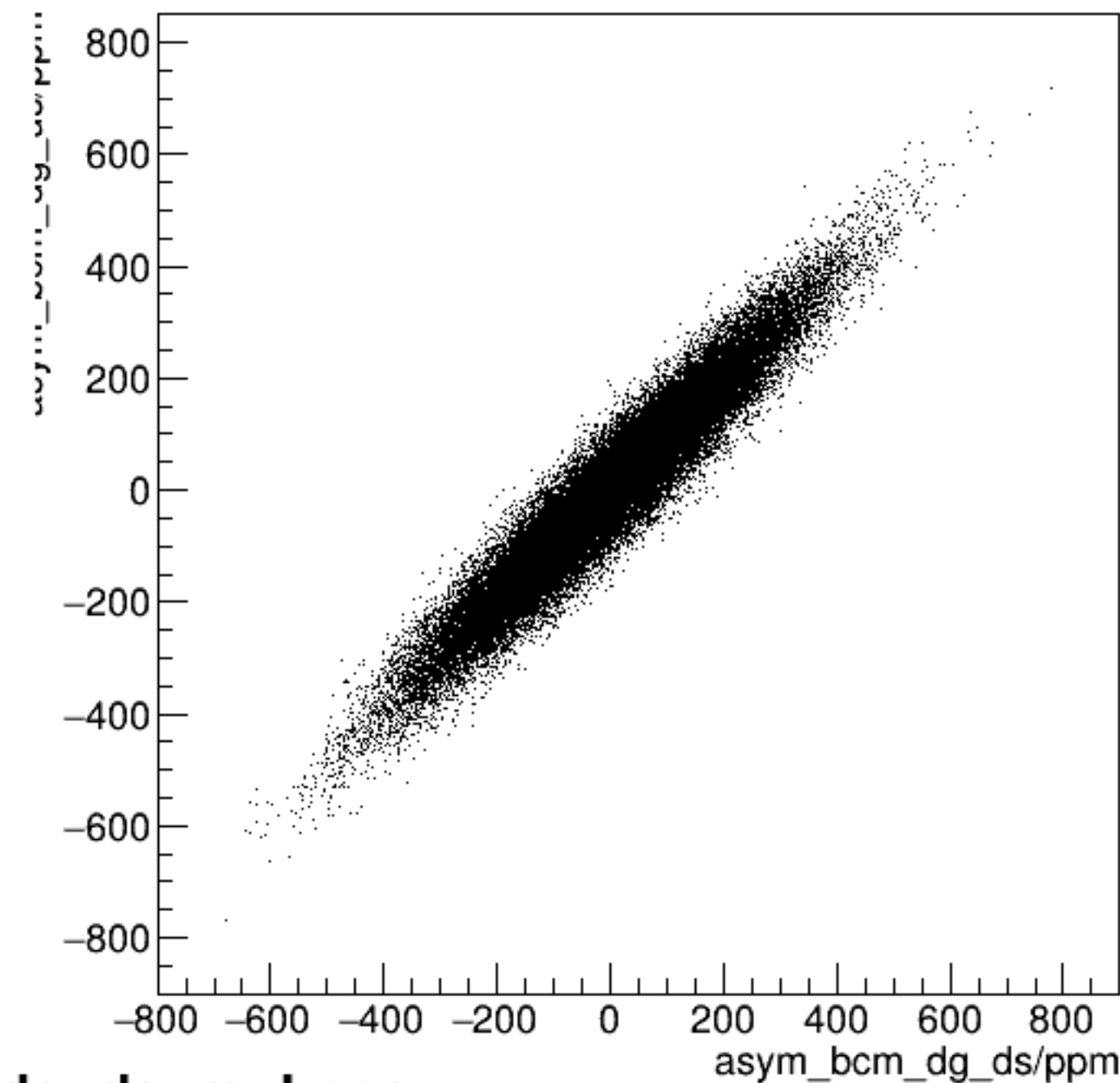


run8022\_000\_asym\_bcm\_an\_ds\_vs\_asym\_bcm\_an\_ds3\_mul.png

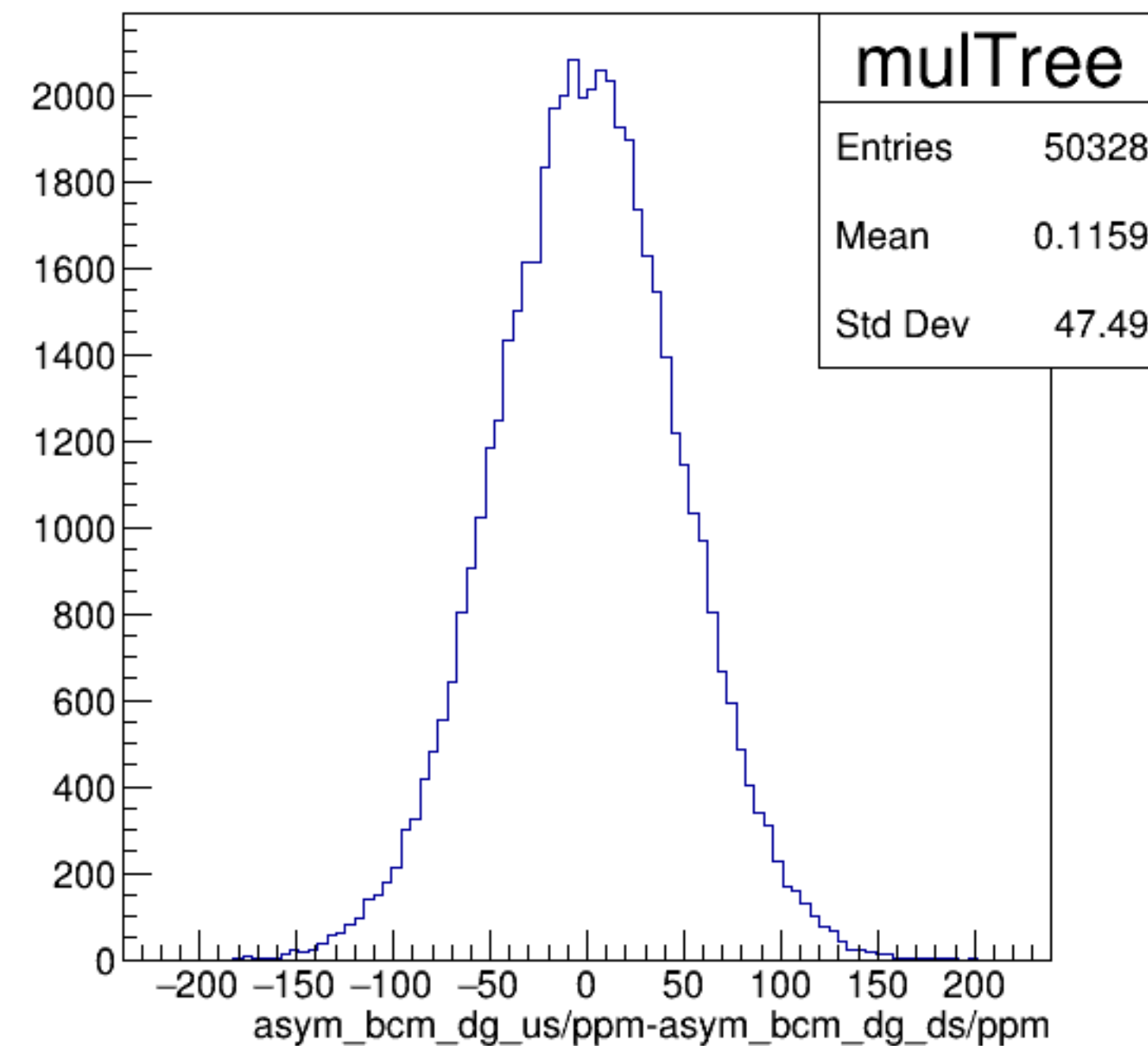
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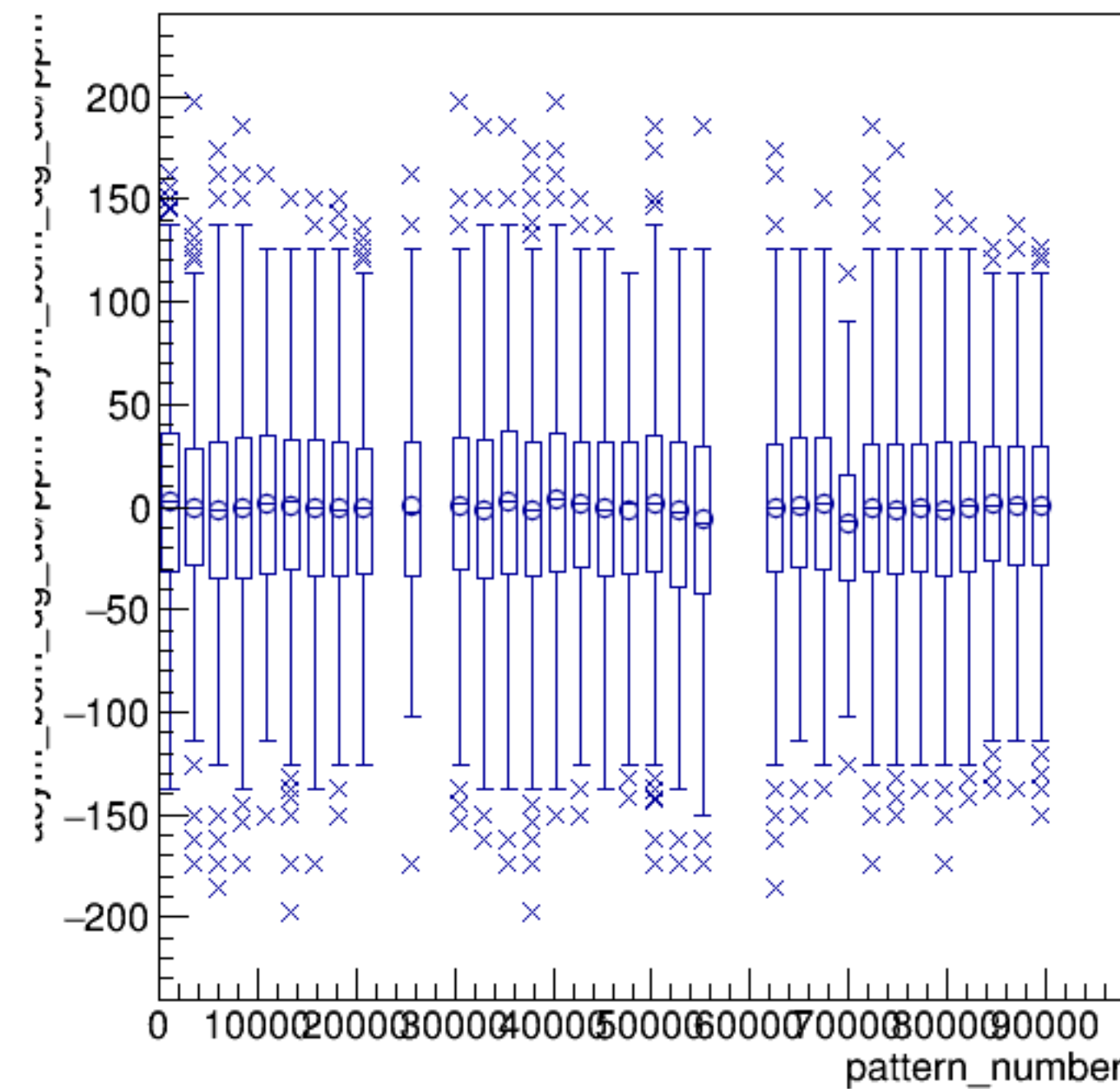
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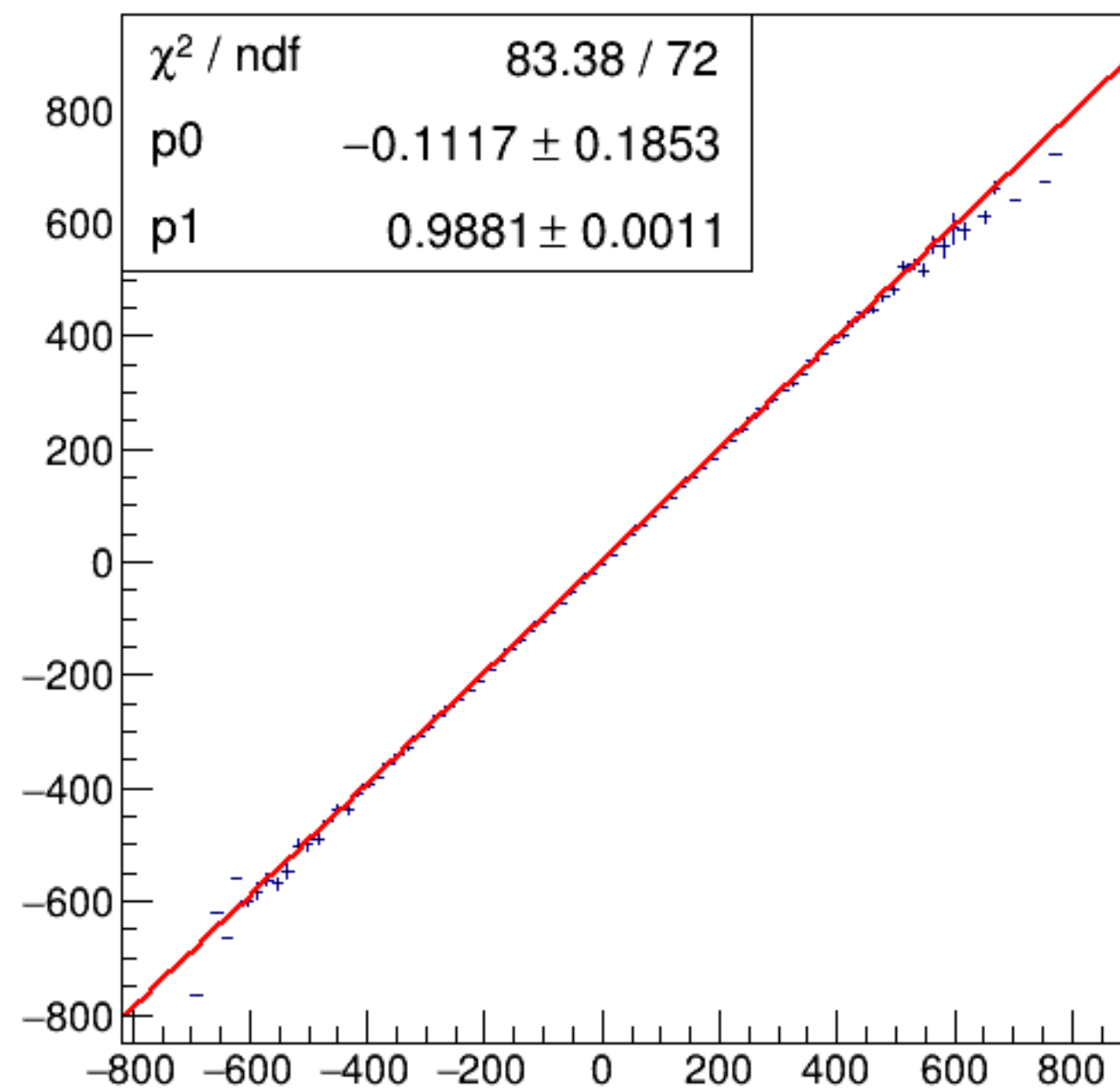
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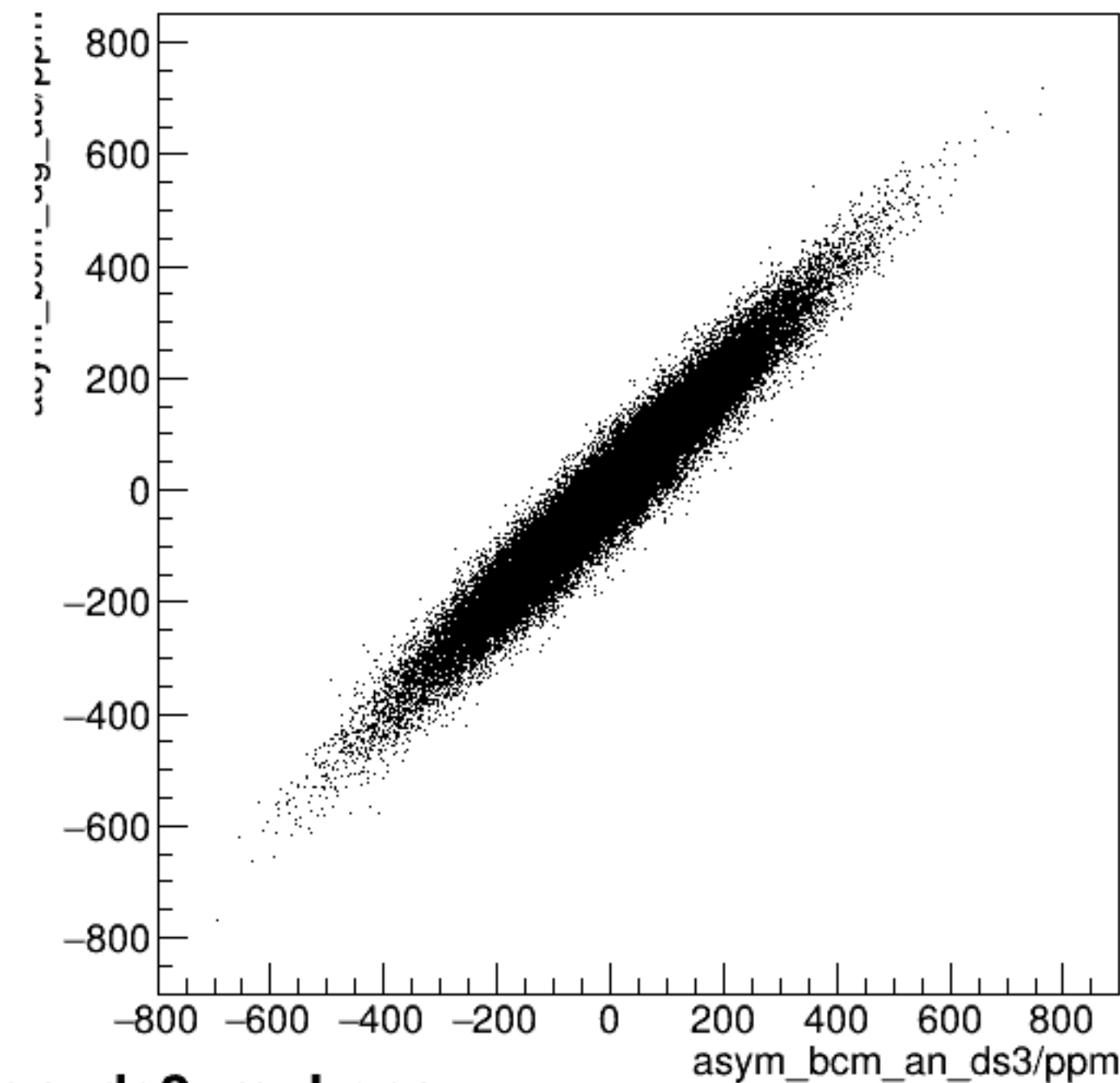
asym\_bcm\_dg\_us/ppm-asym\_bcm\_dg\_ds/ppm:pattern\_number {ErrorFlag==0}



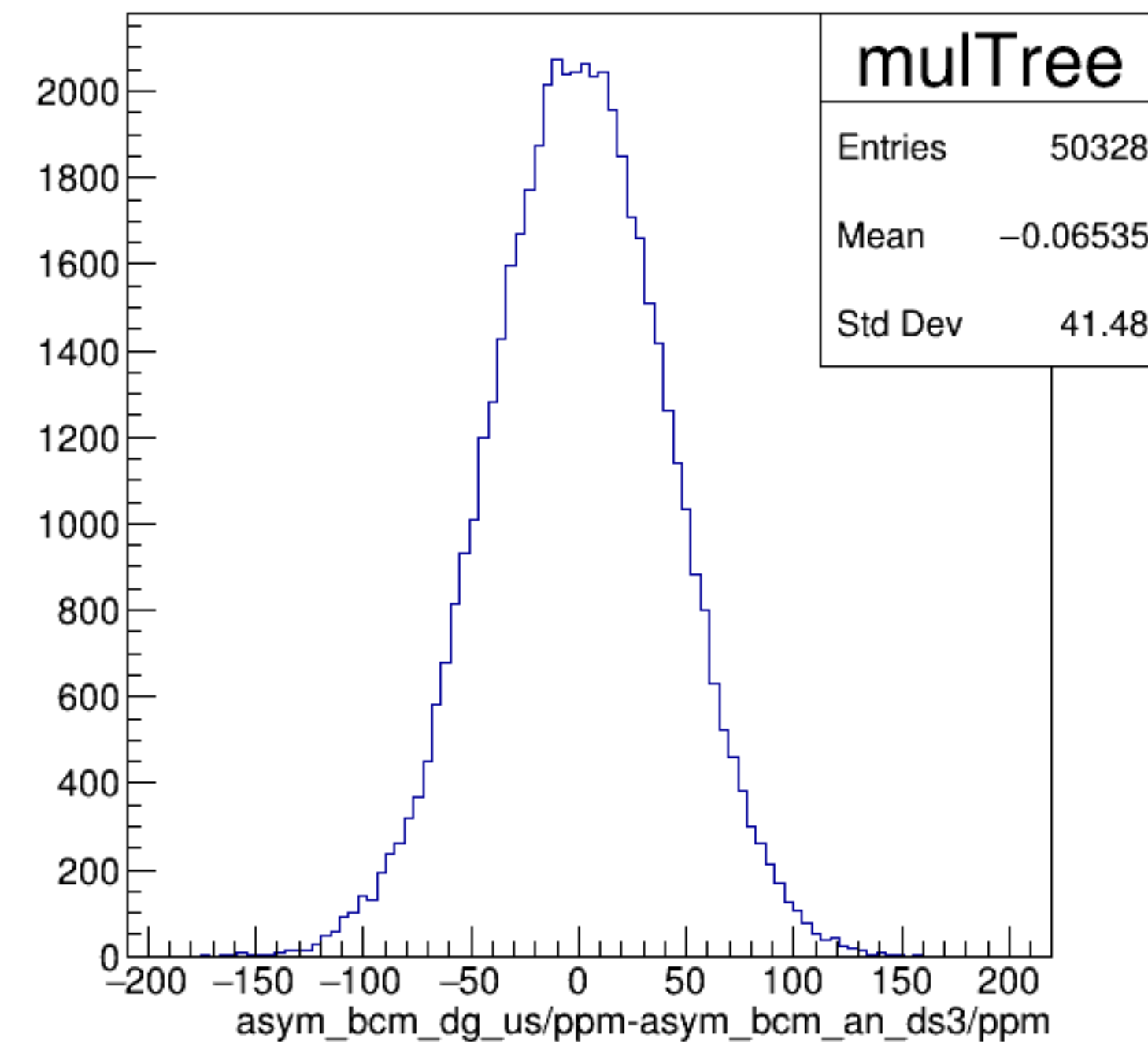
asym\_bcm\_dg\_us/ppm:asym\_bcm\_an\_ds3/ppm {ErrorFlag==0}



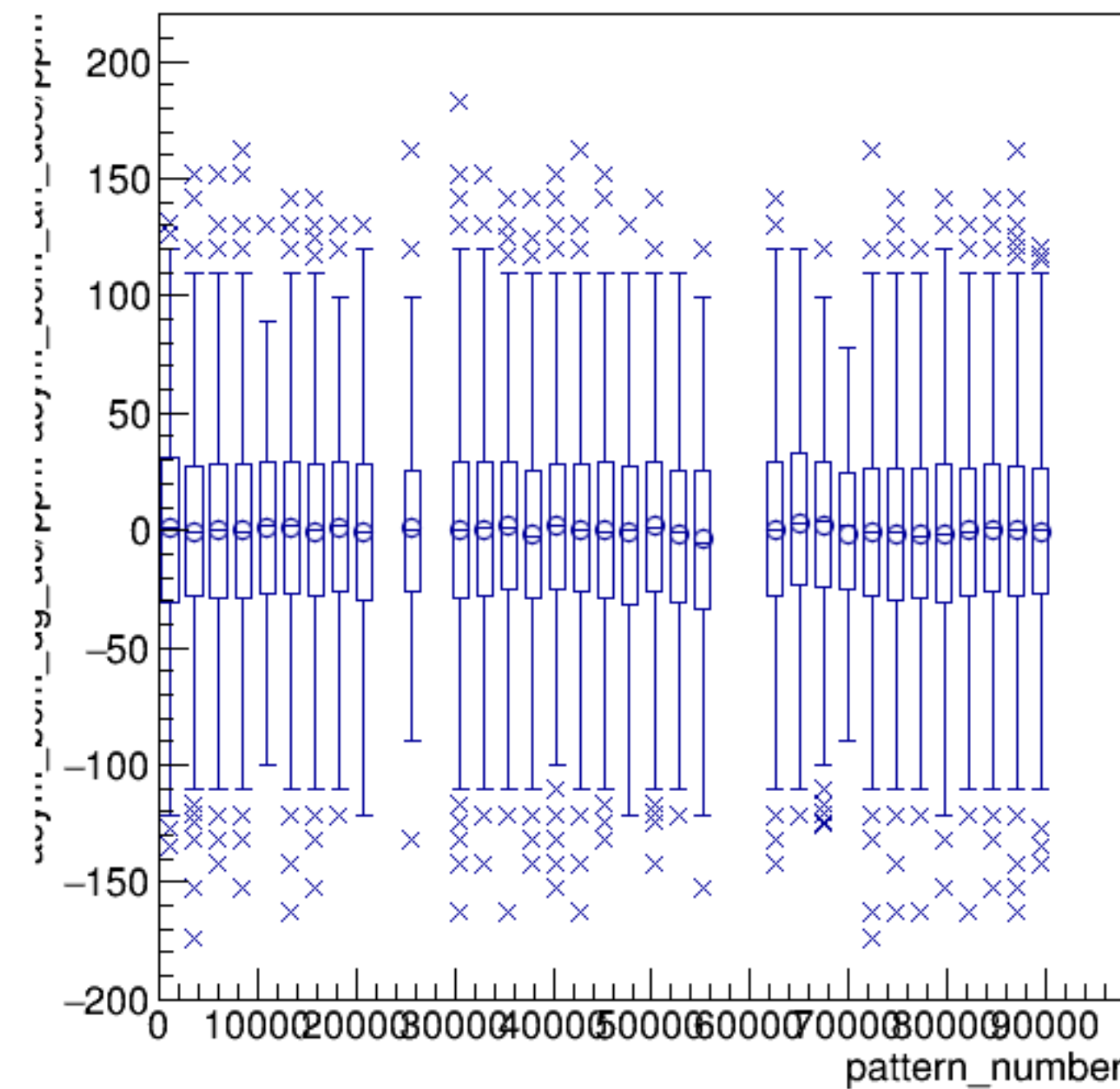
asym\_bcm\_dg\_us/ppm:asym\_bcm\_an\_ds3/ppm {ErrorFlag==0}



asym\_bcm\_dg\_us/ppm-asym\_bcm\_an\_ds3/ppm {ErrorFlag==0}

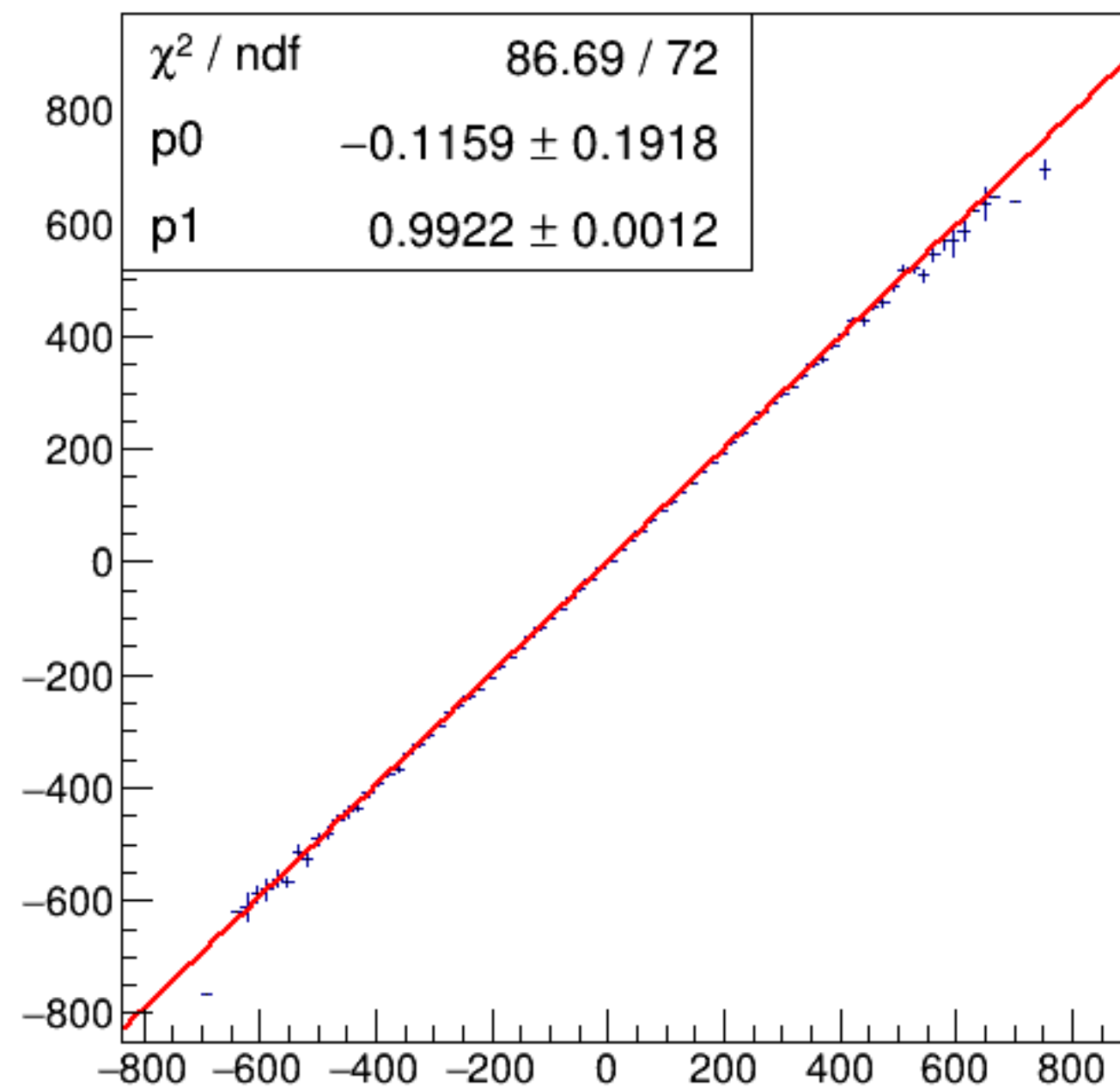


asym\_bcm\_dg\_us/ppm-asym\_bcm\_an\_ds3/ppm:pattern\_number {ErrorFlag==0}

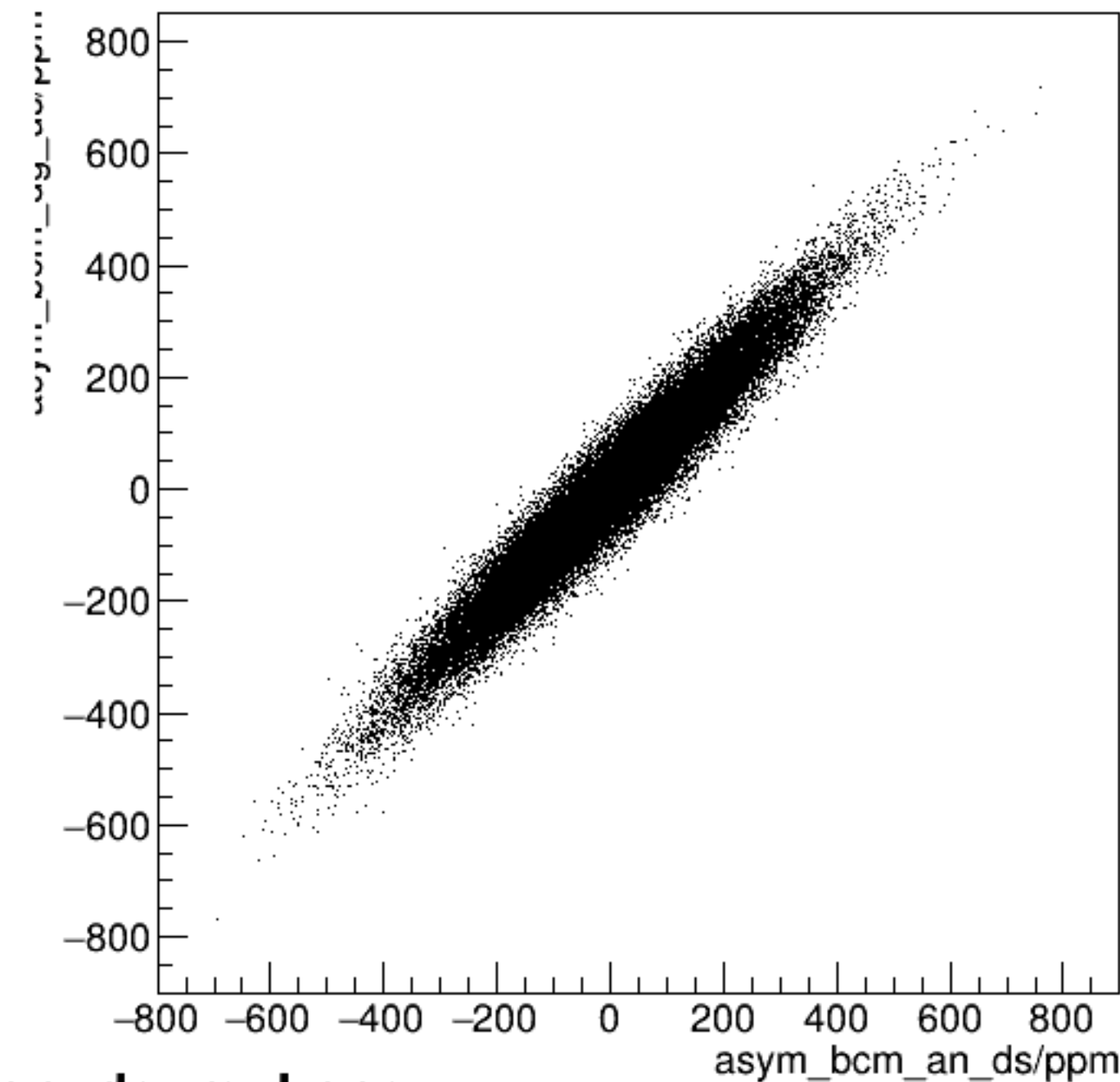


run8022\_000\_asym\_bcm\_dg\_us\_vs\_asym\_bcm\_an\_ds3\_mul.png

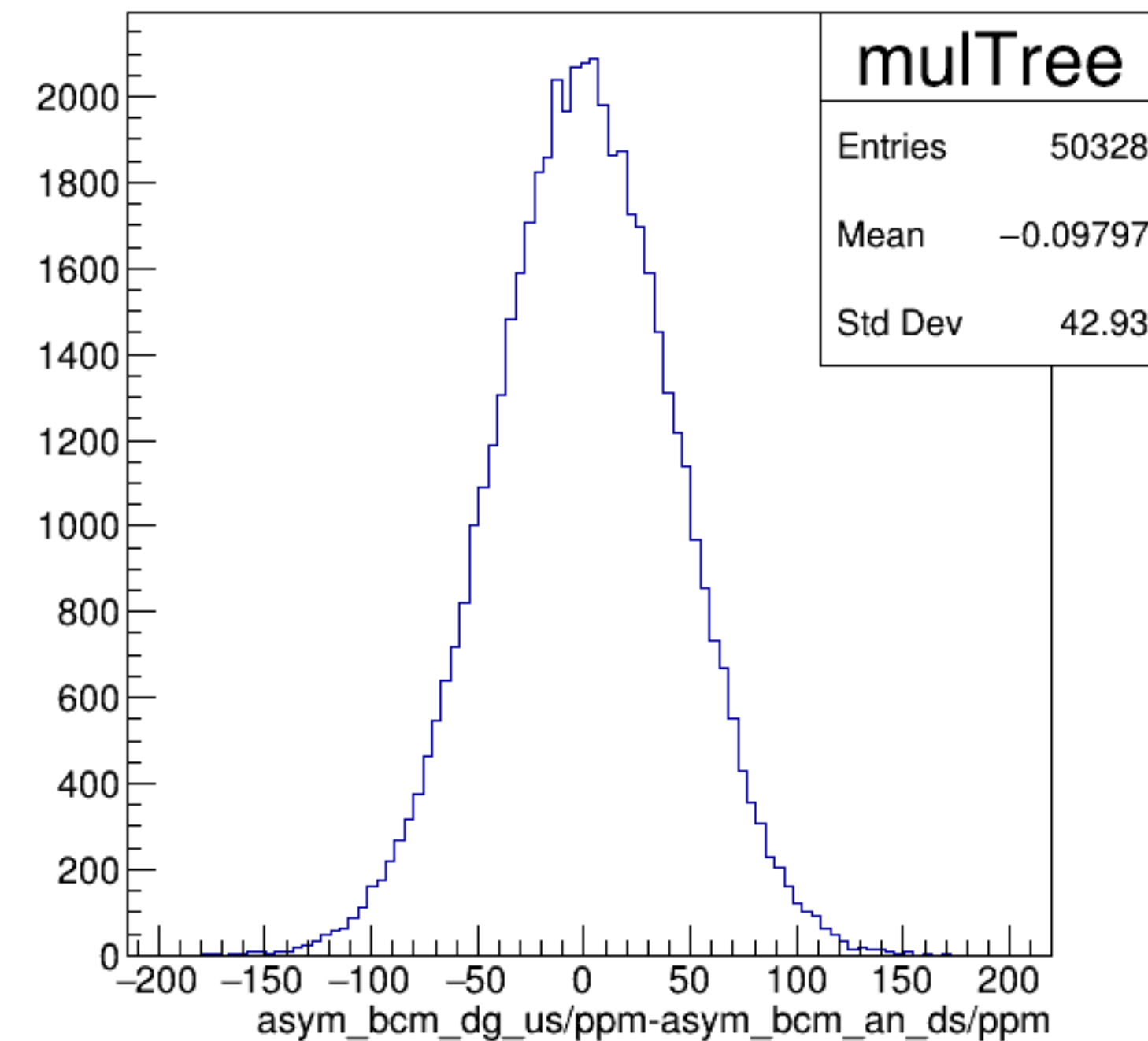
asym\_bcm\_dg\_us/ppm:asym\_bcm\_an\_ds/ppm {ErrorFlag==0}



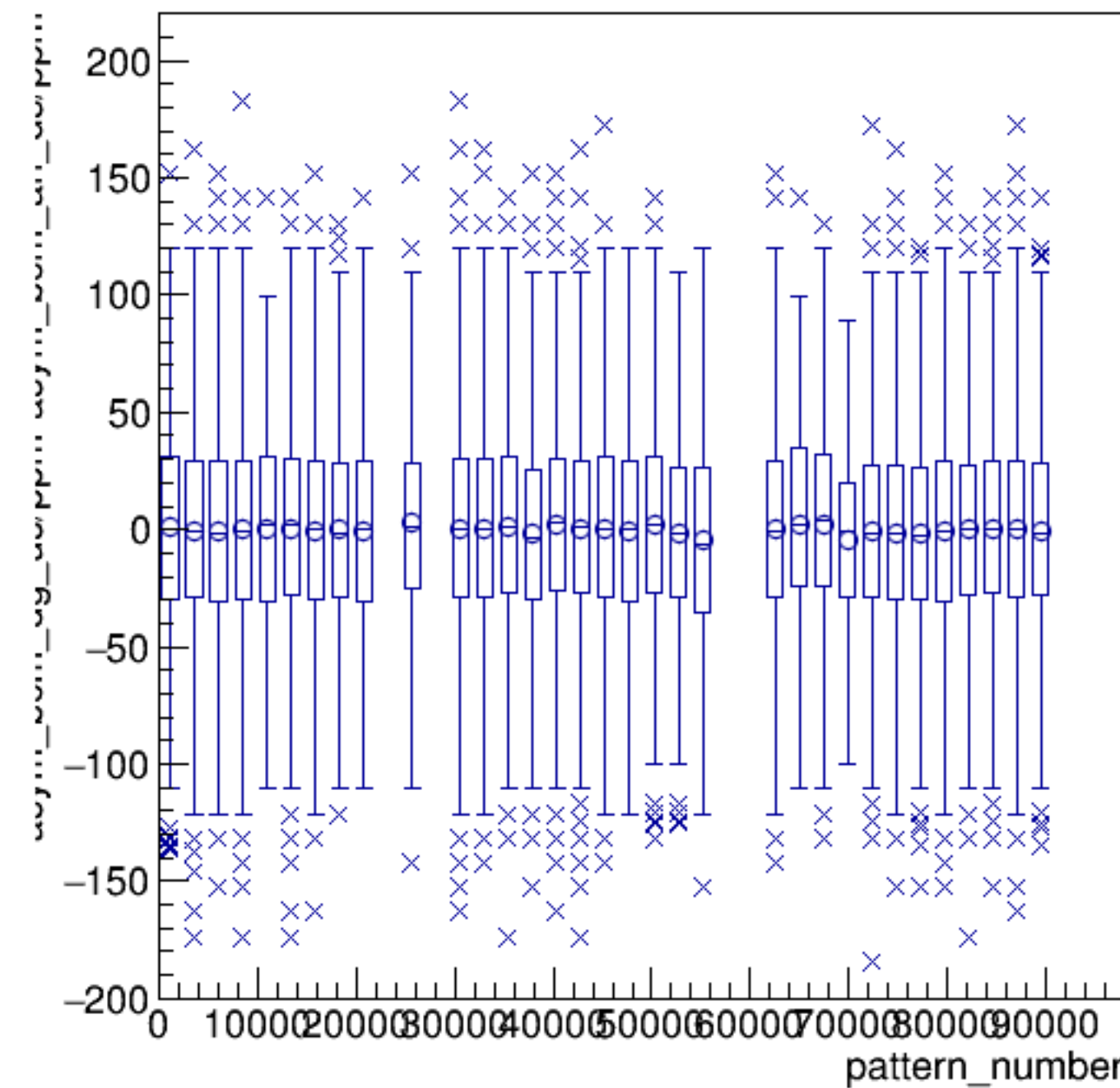
asym\_bcm\_dg\_us/ppm:asym\_bcm\_an\_ds/ppm {ErrorFlag==0}



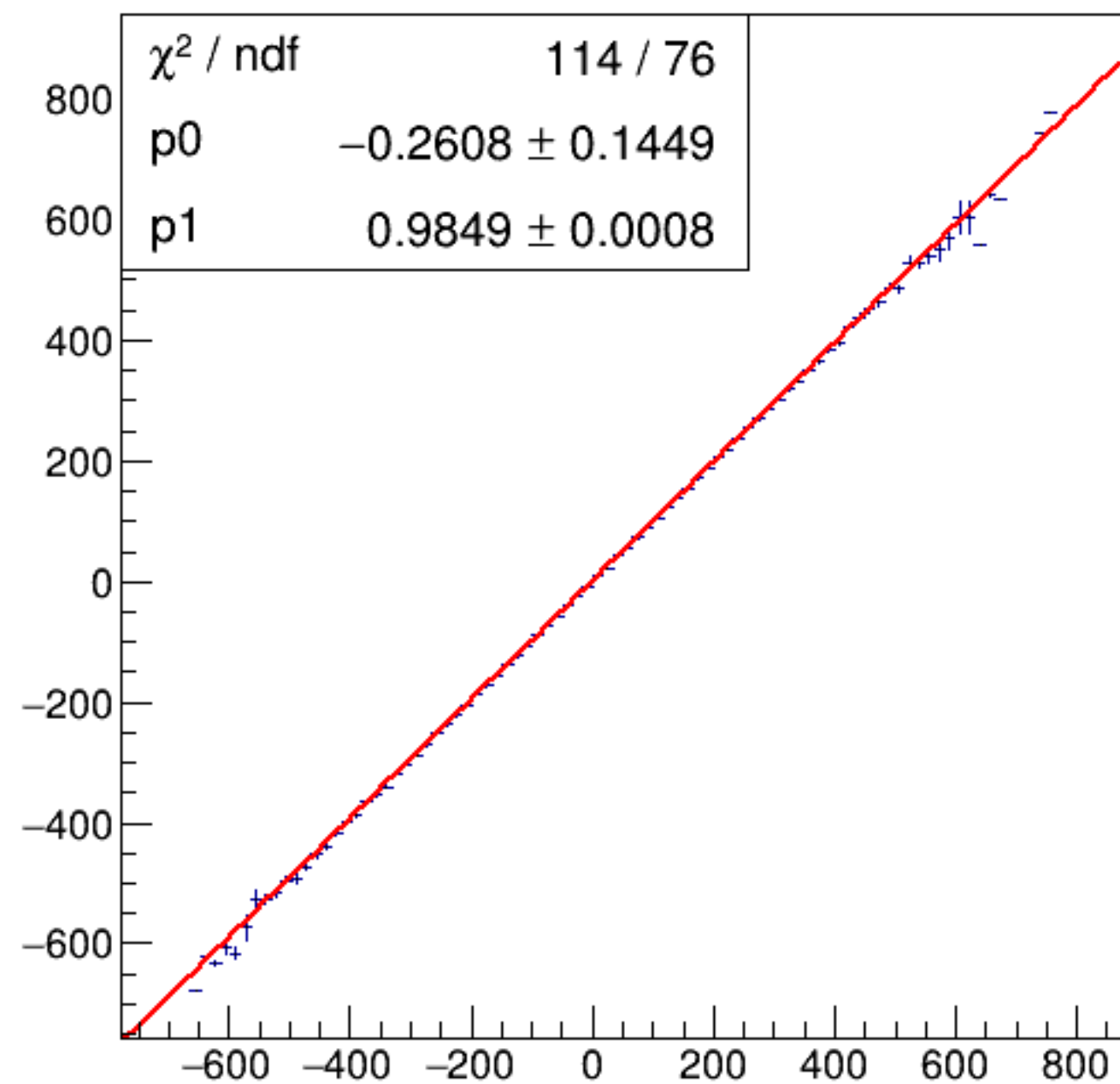
asym\_bcm\_dg\_us/ppm-asym\_bcm\_an\_ds/ppm {ErrorFlag==0}



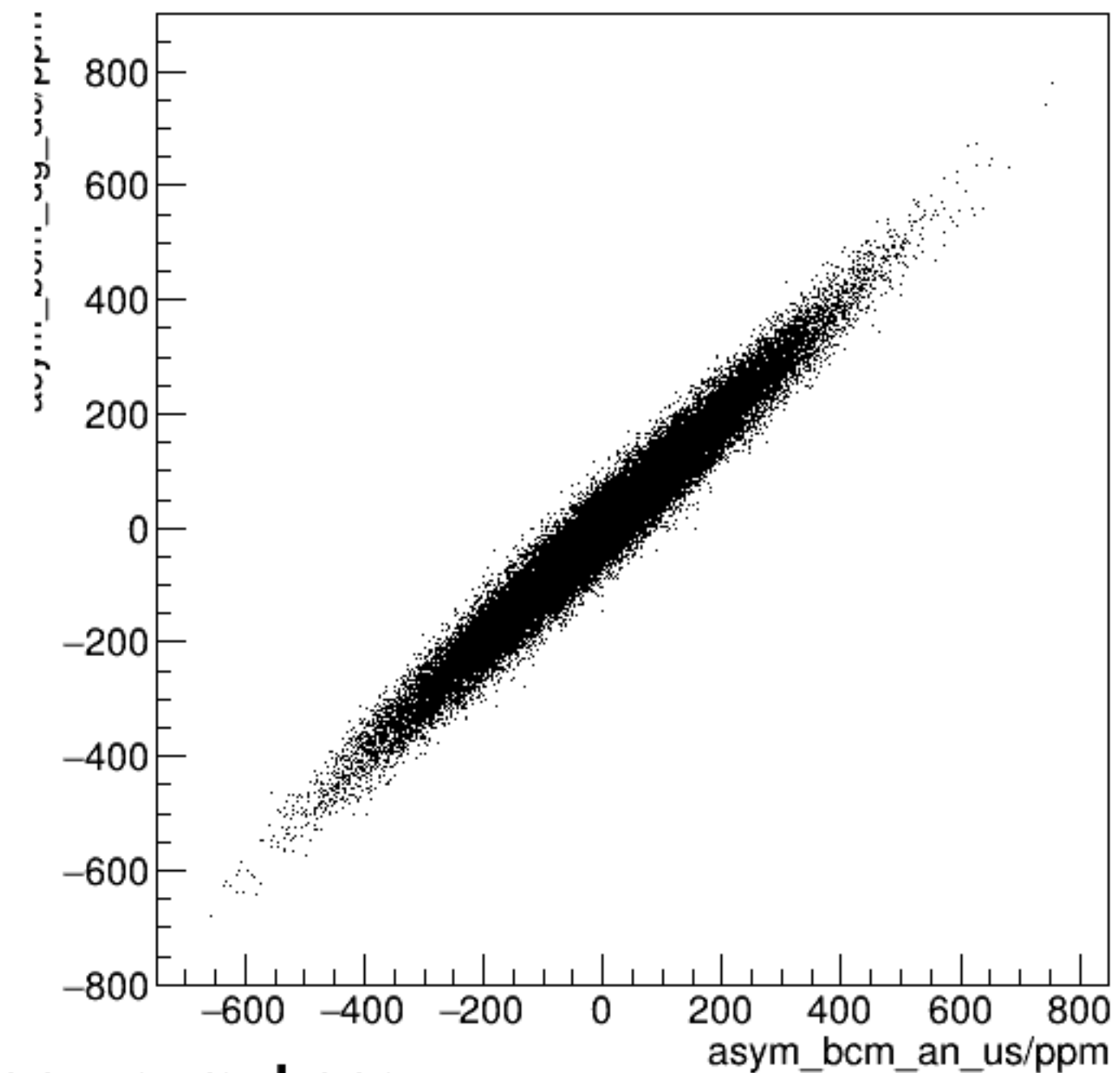
asym\_bcm\_dg\_us/ppm-asym\_bcm\_an\_ds/ppm:pattern\_number {ErrorFlag==0}



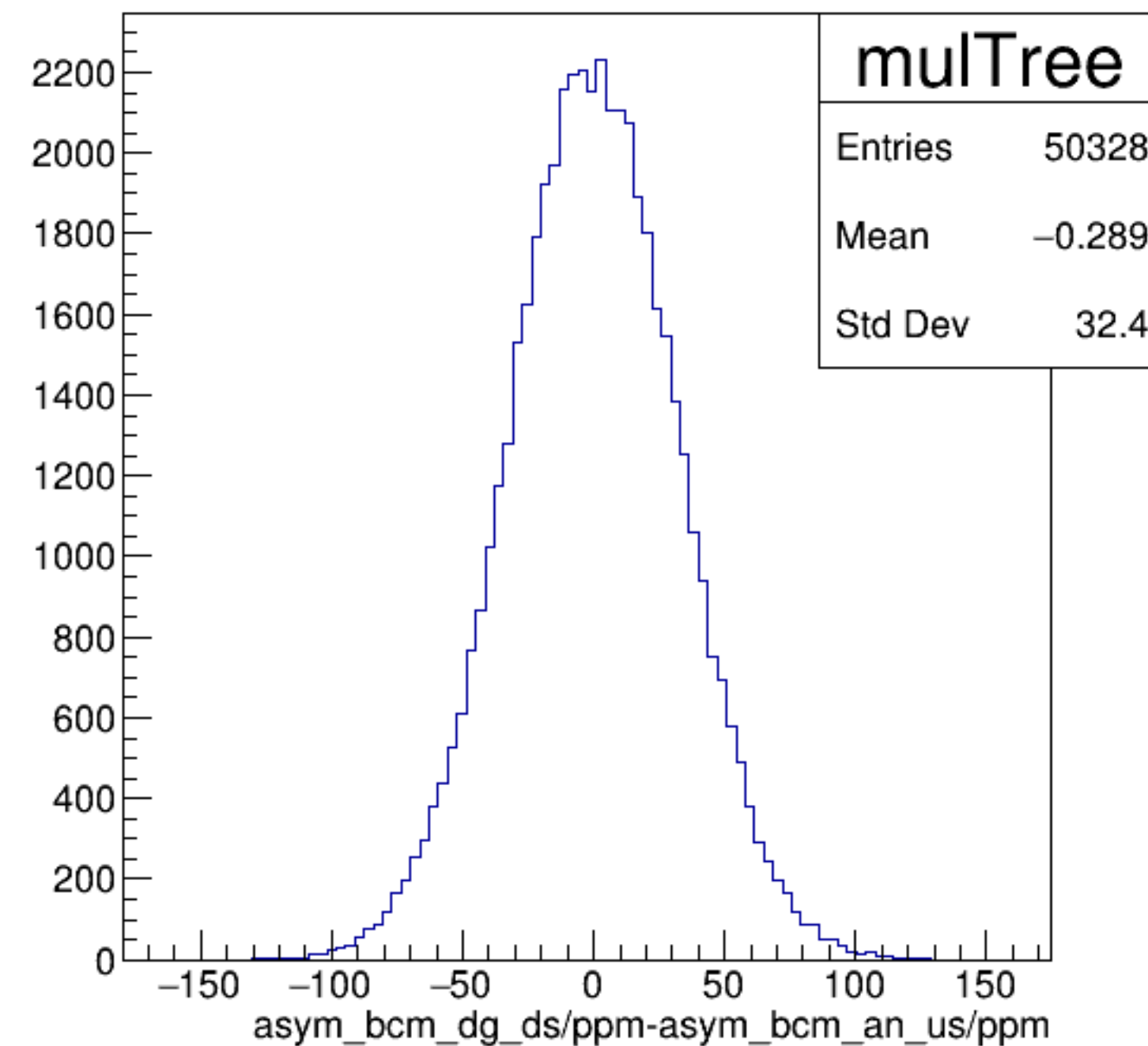
asym\_bcm\_dg\_ds/ppm:asym\_bcm\_an\_us/ppm {ErrorFlag==0}



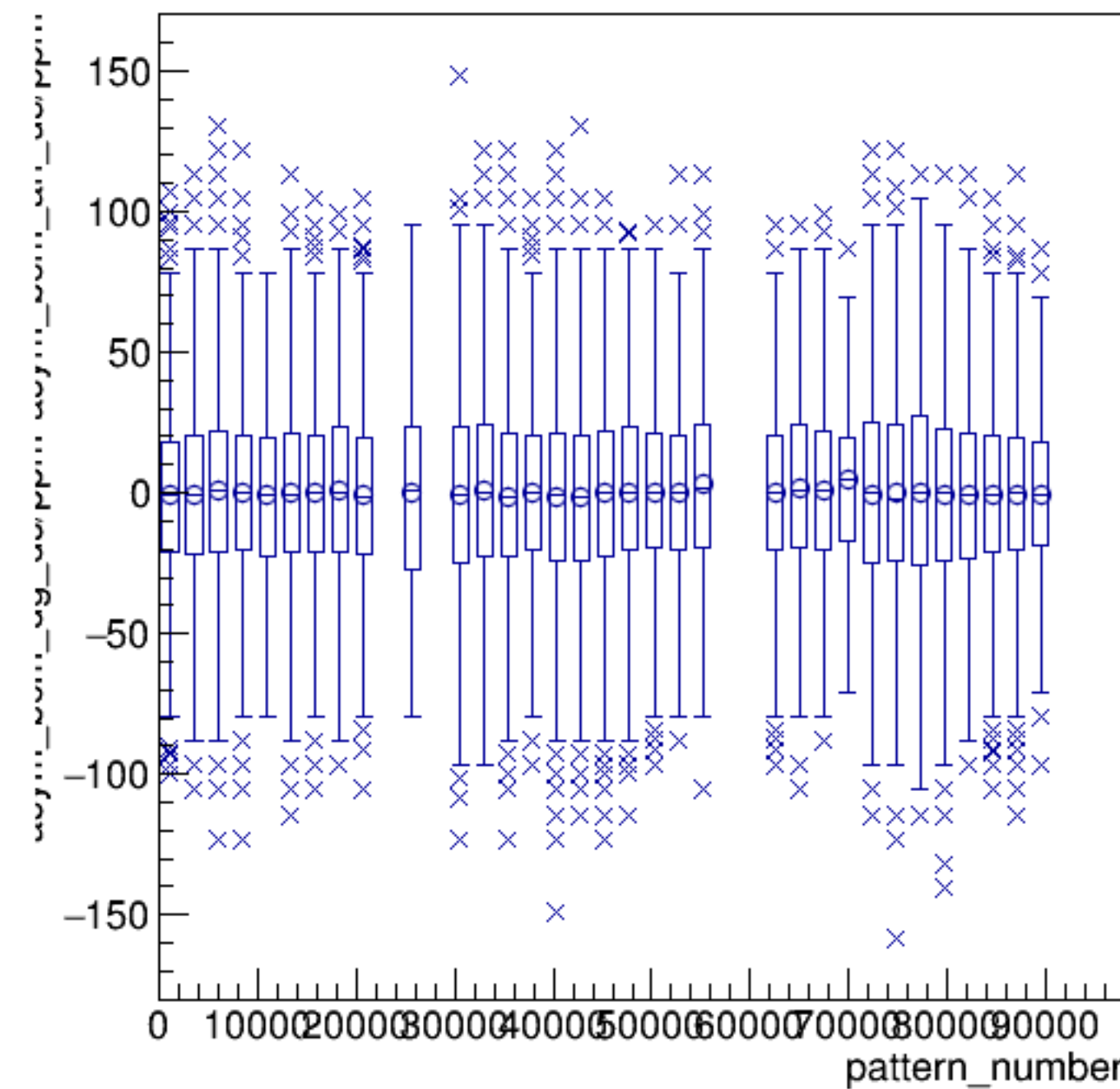
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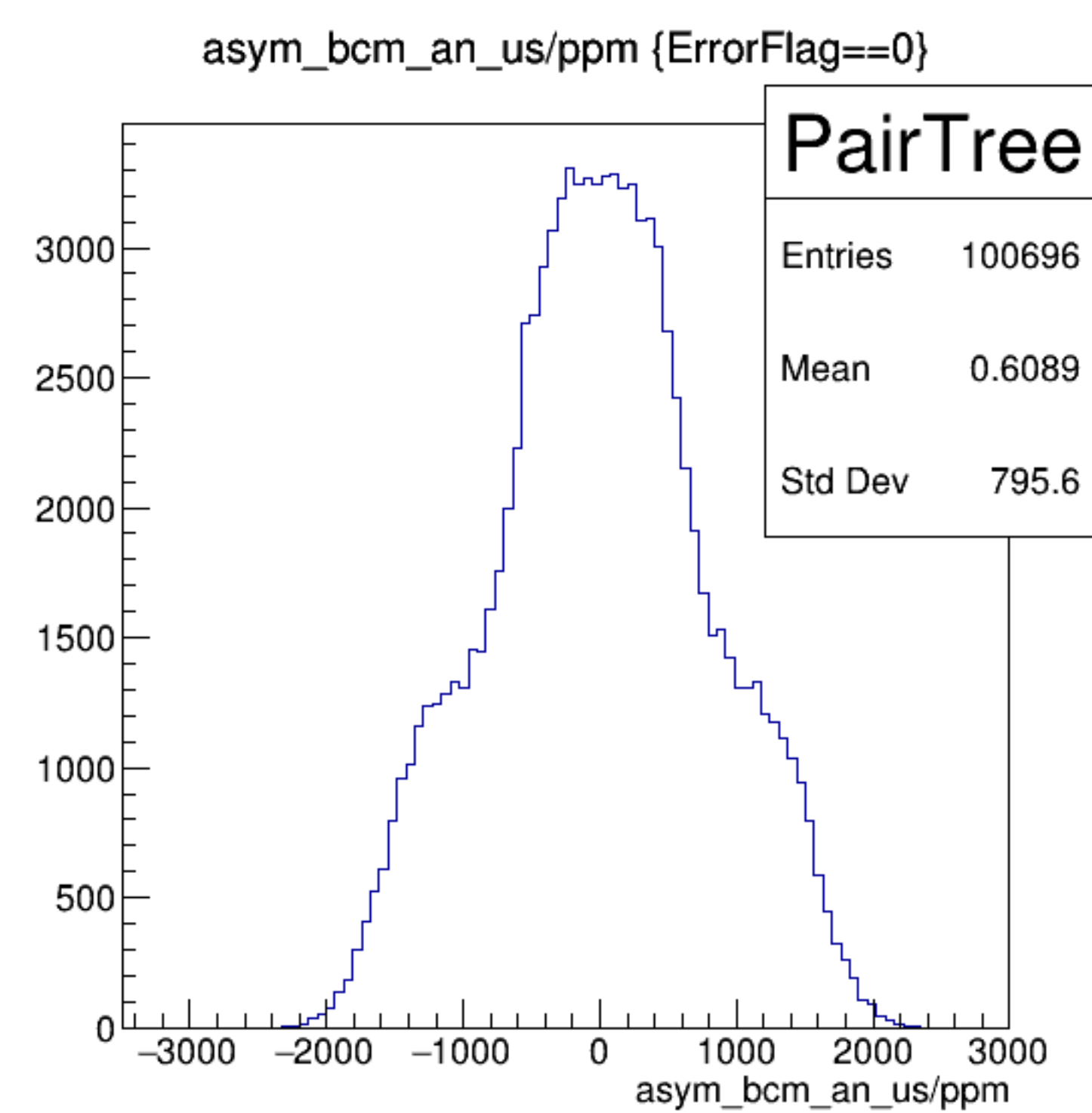
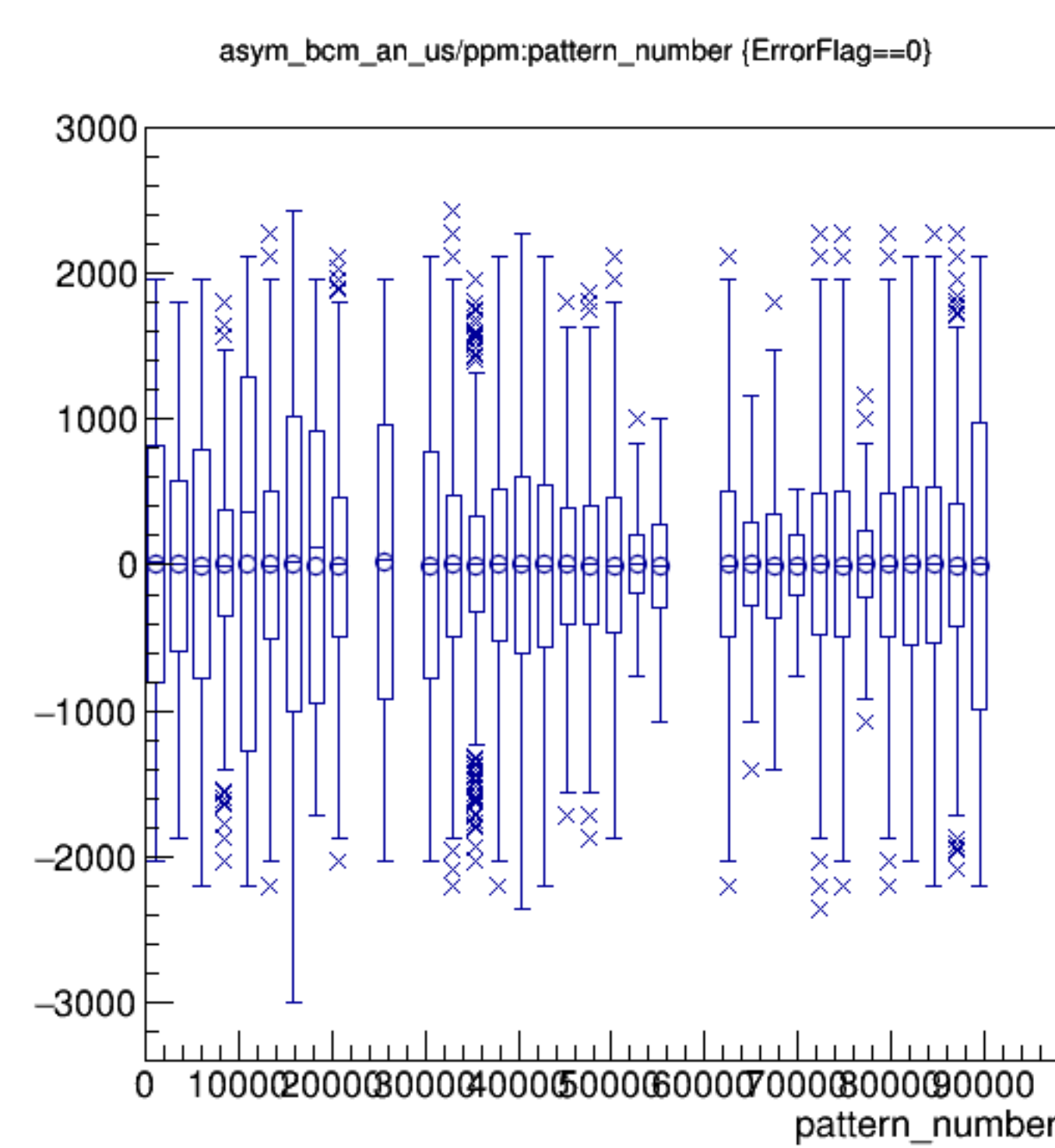
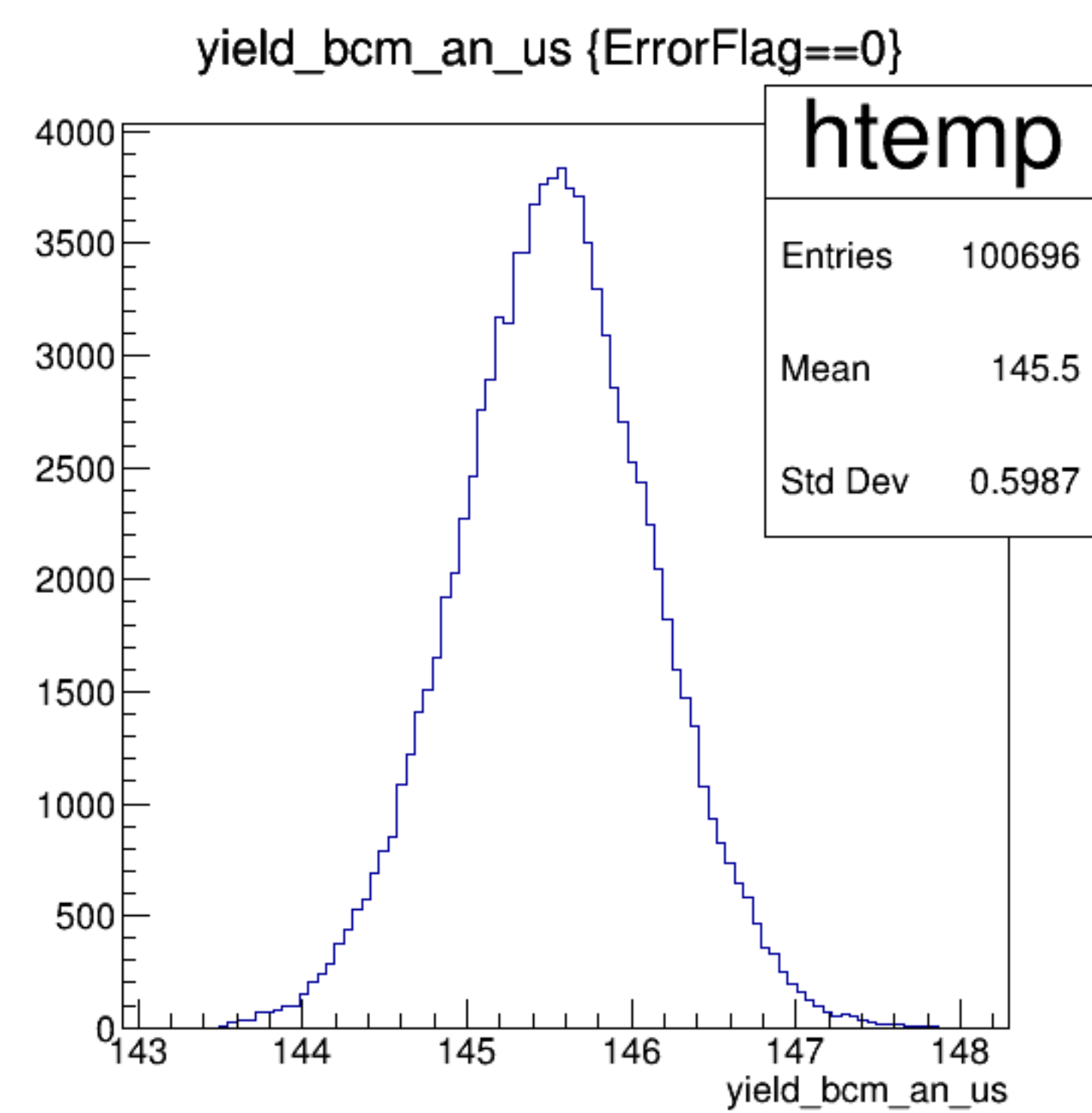
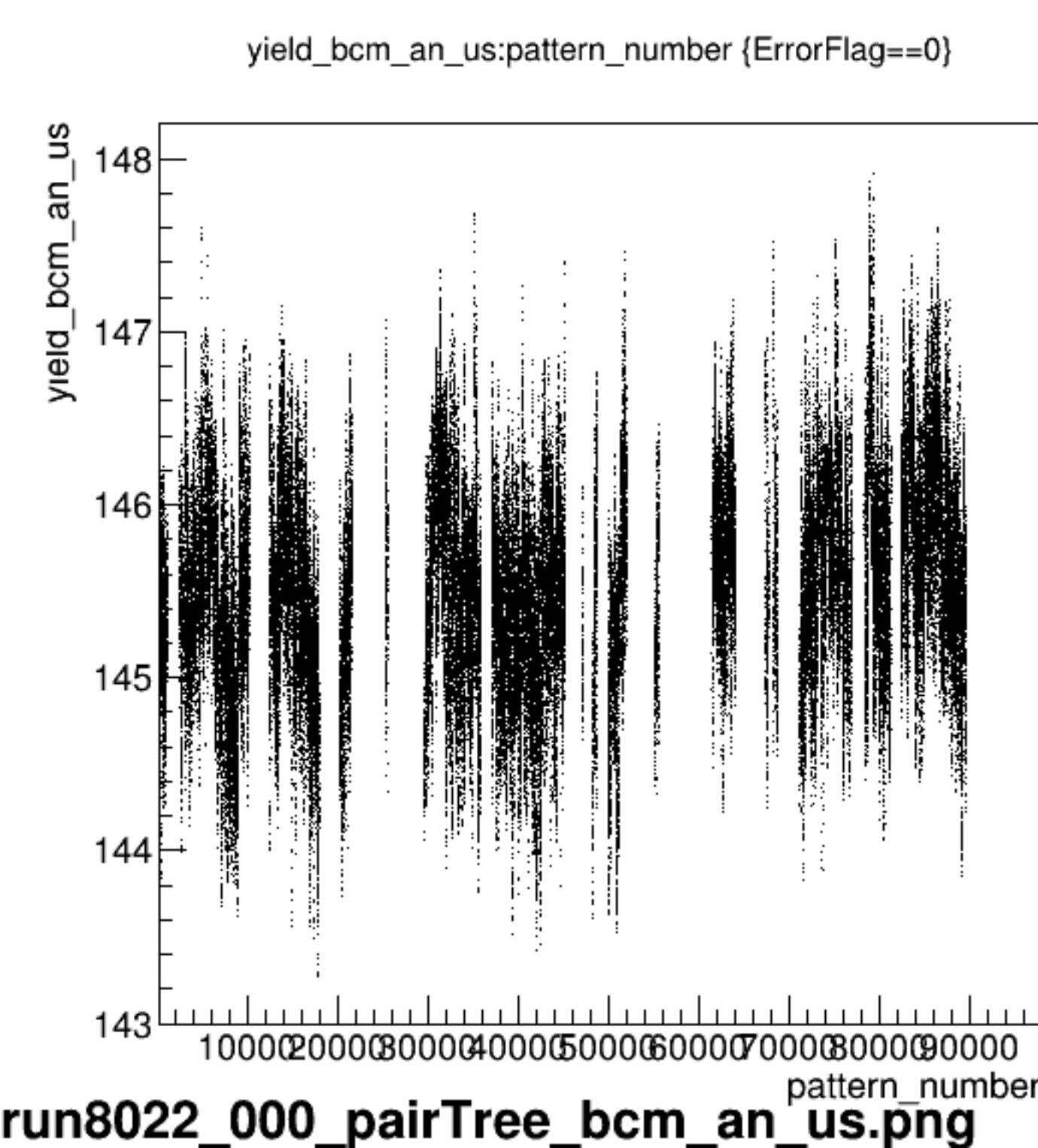
asym\_bcm\_dg\_ds/ppm-asym\_bcm\_an\_us/ppm {ErrorFlag==0}

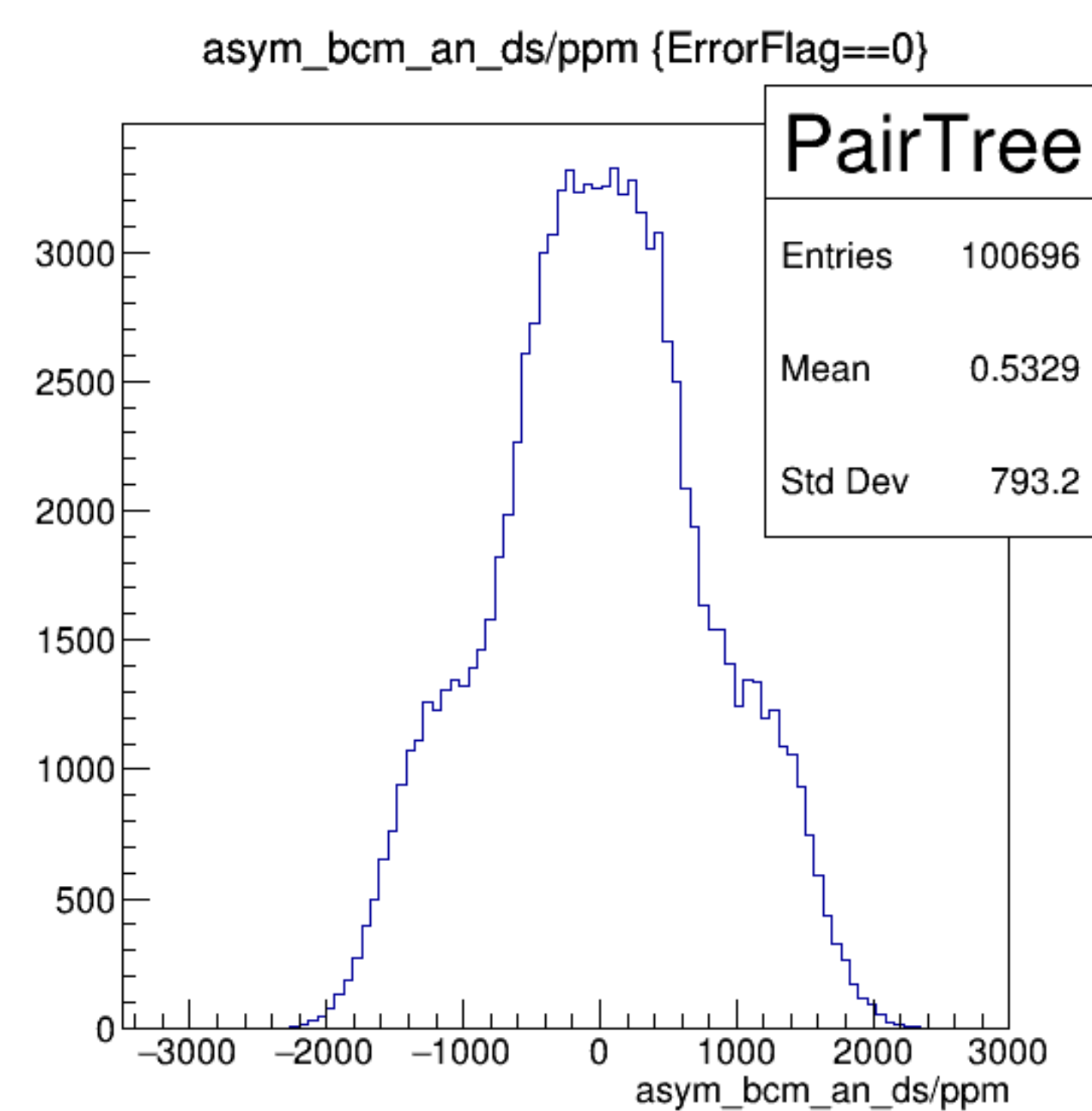
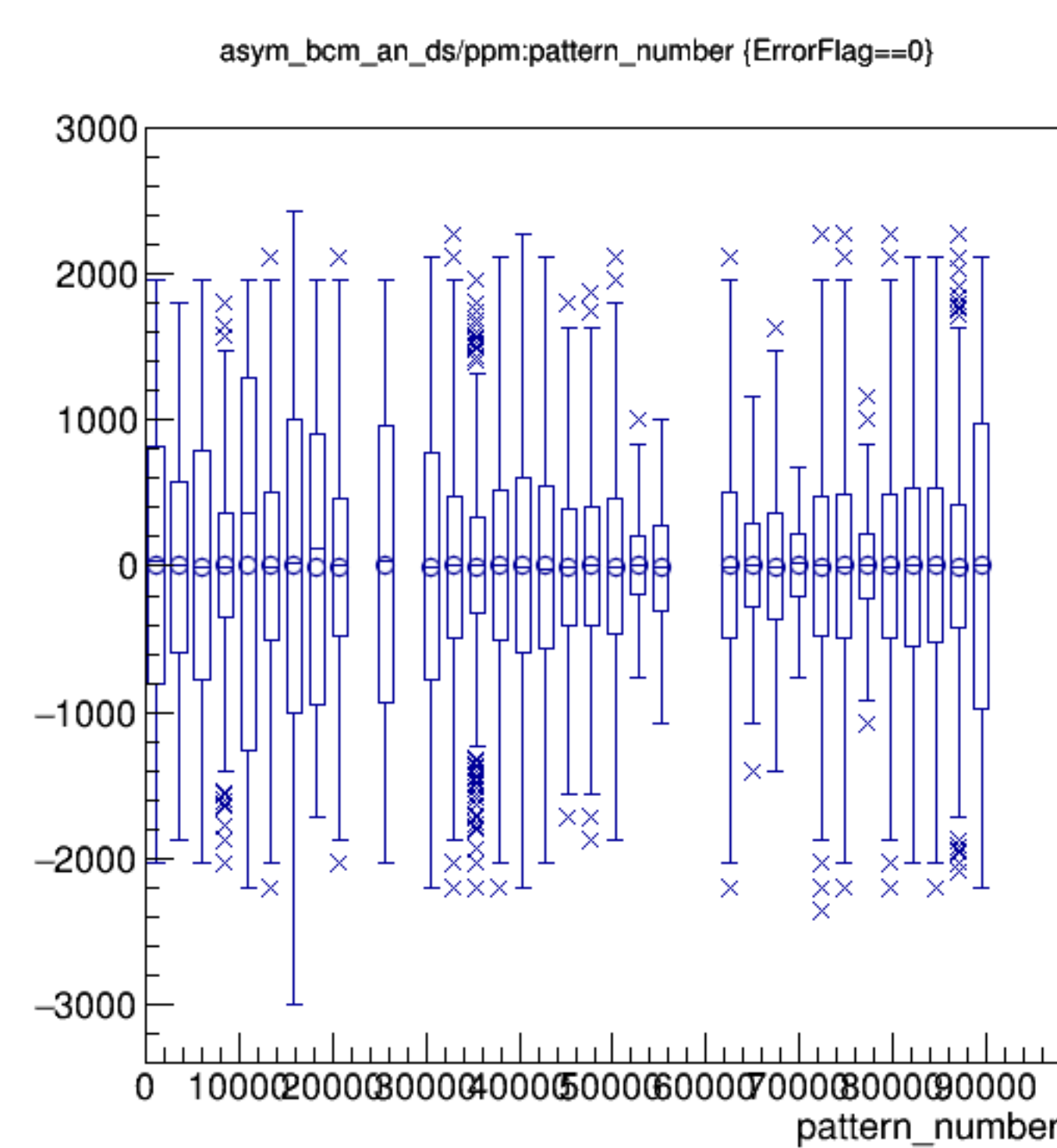
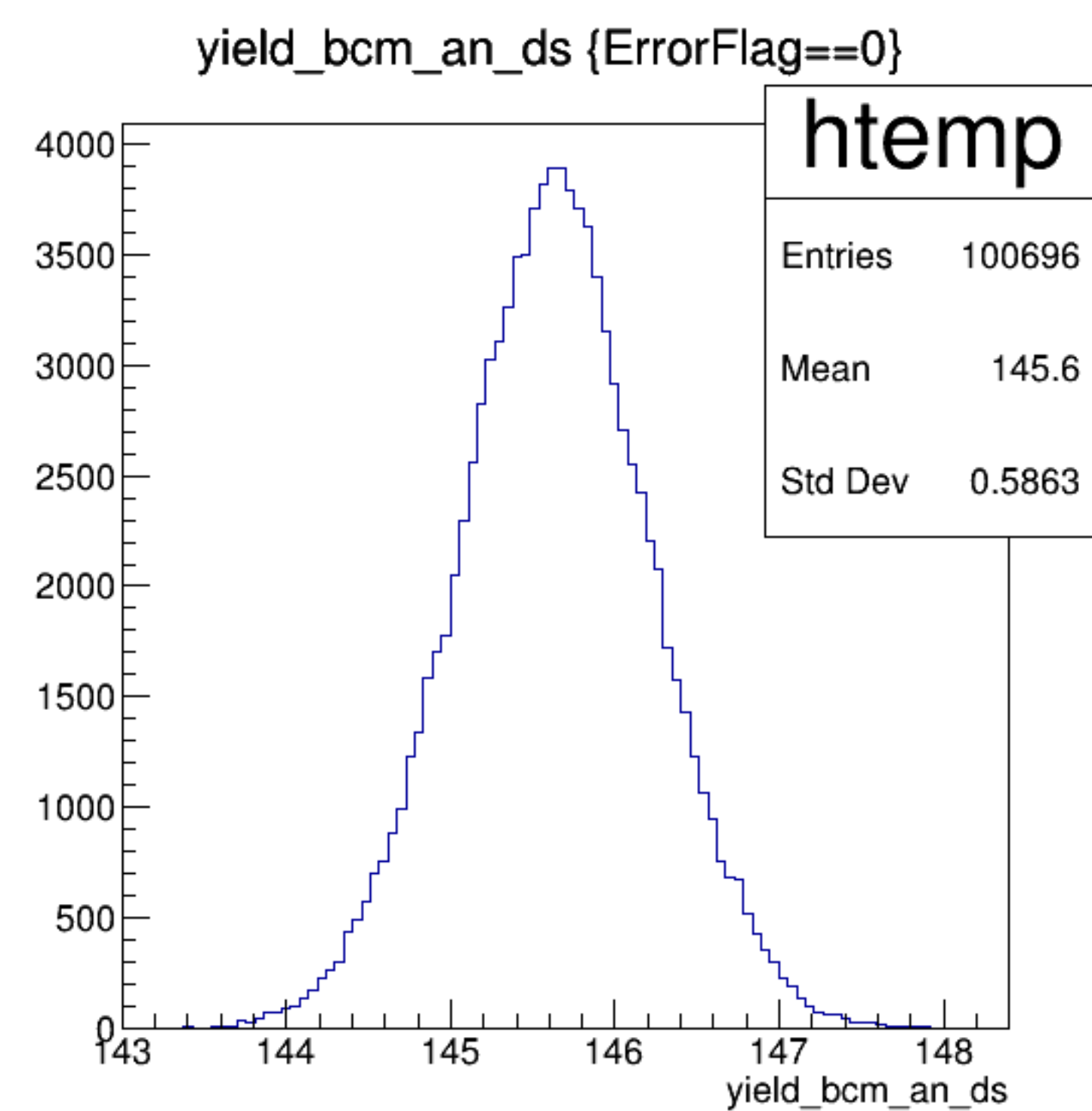
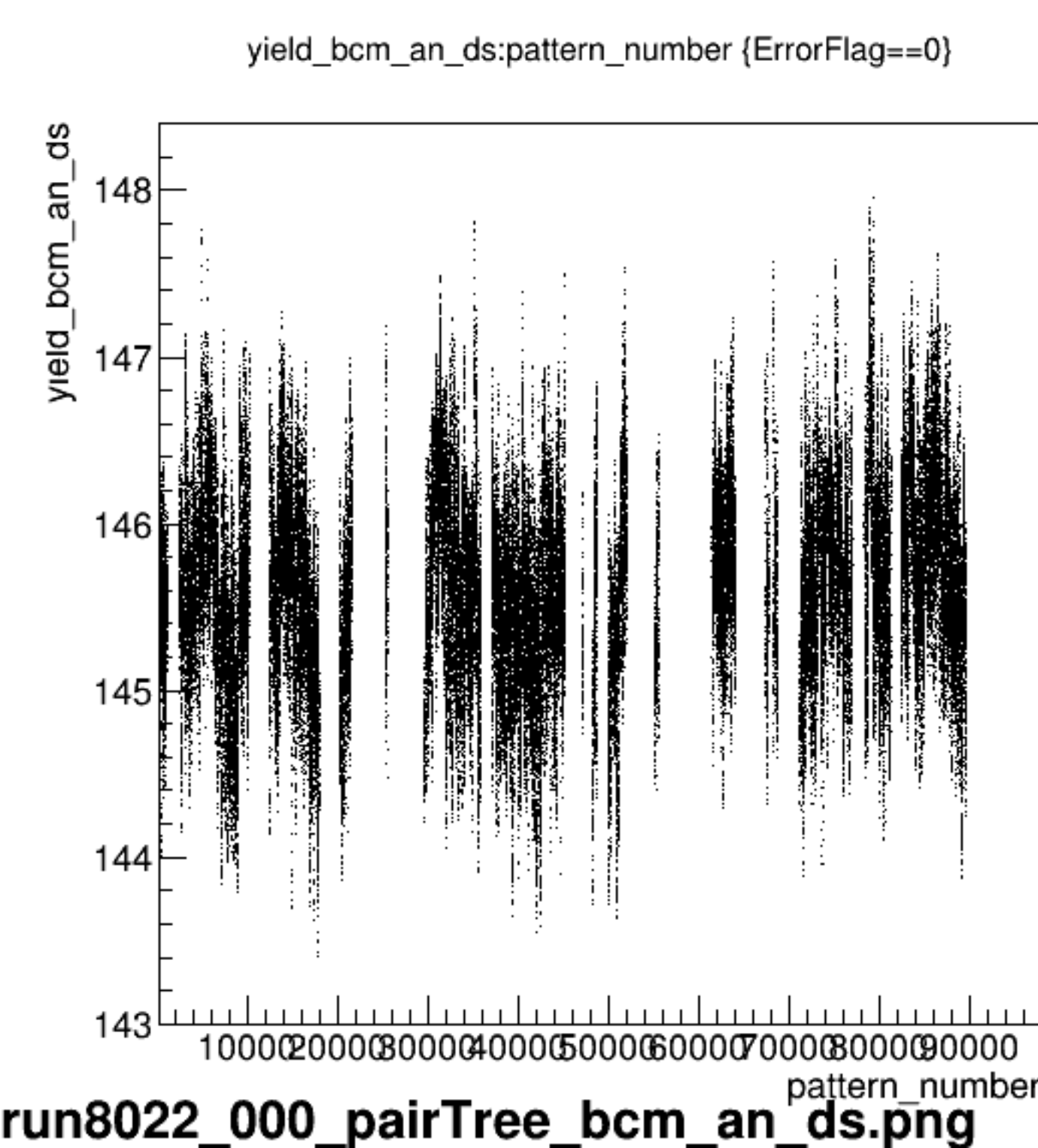


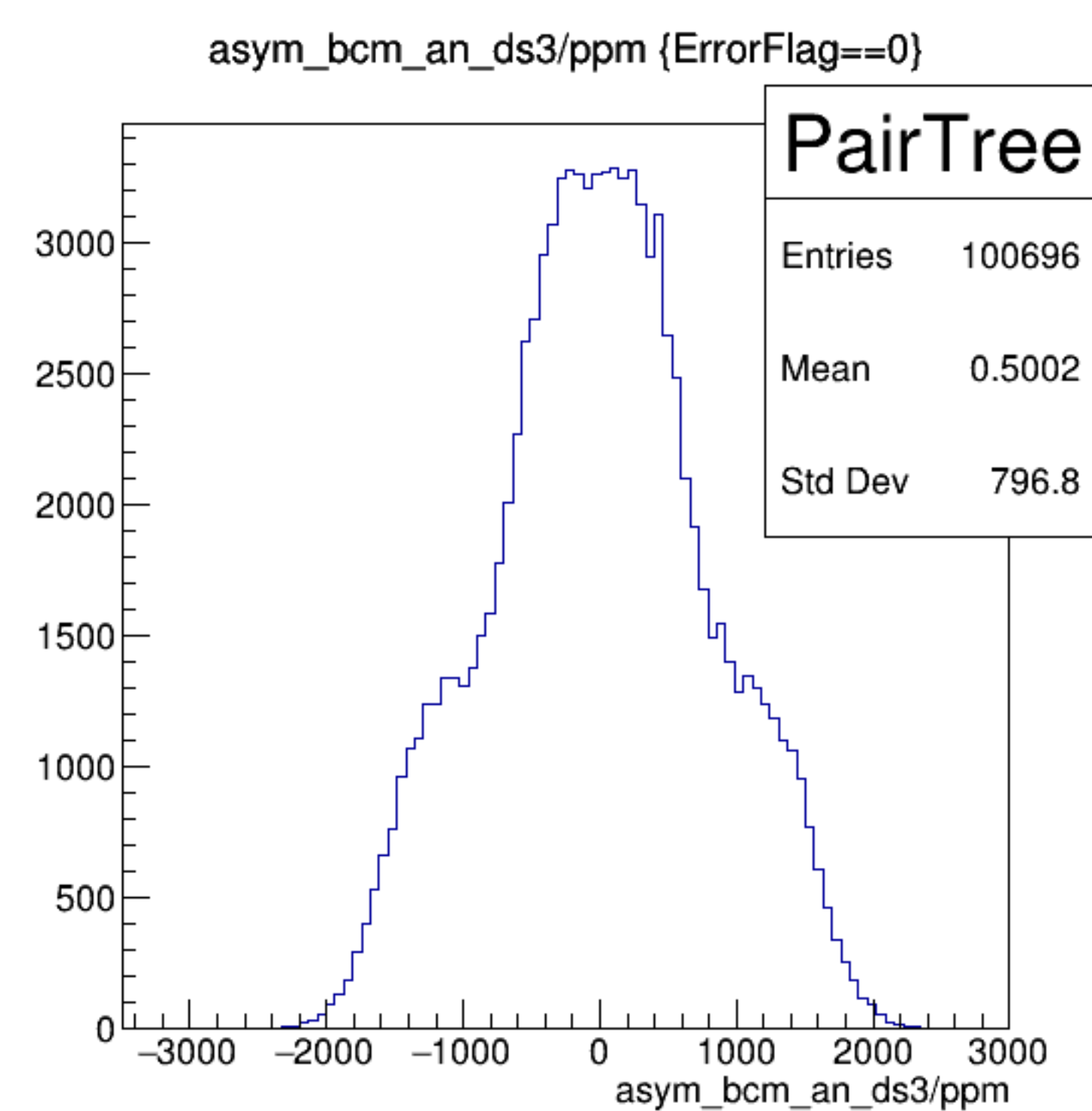
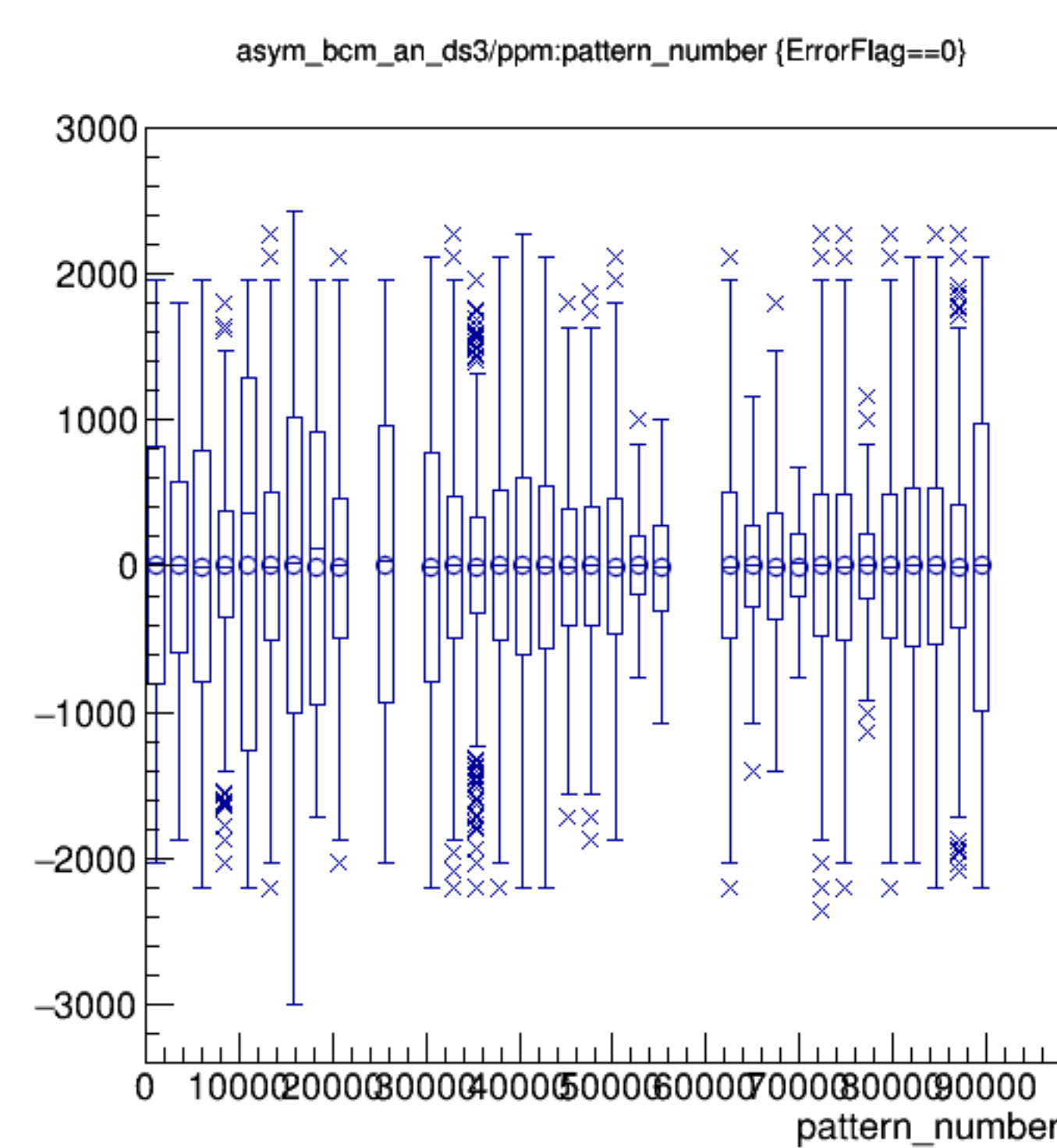
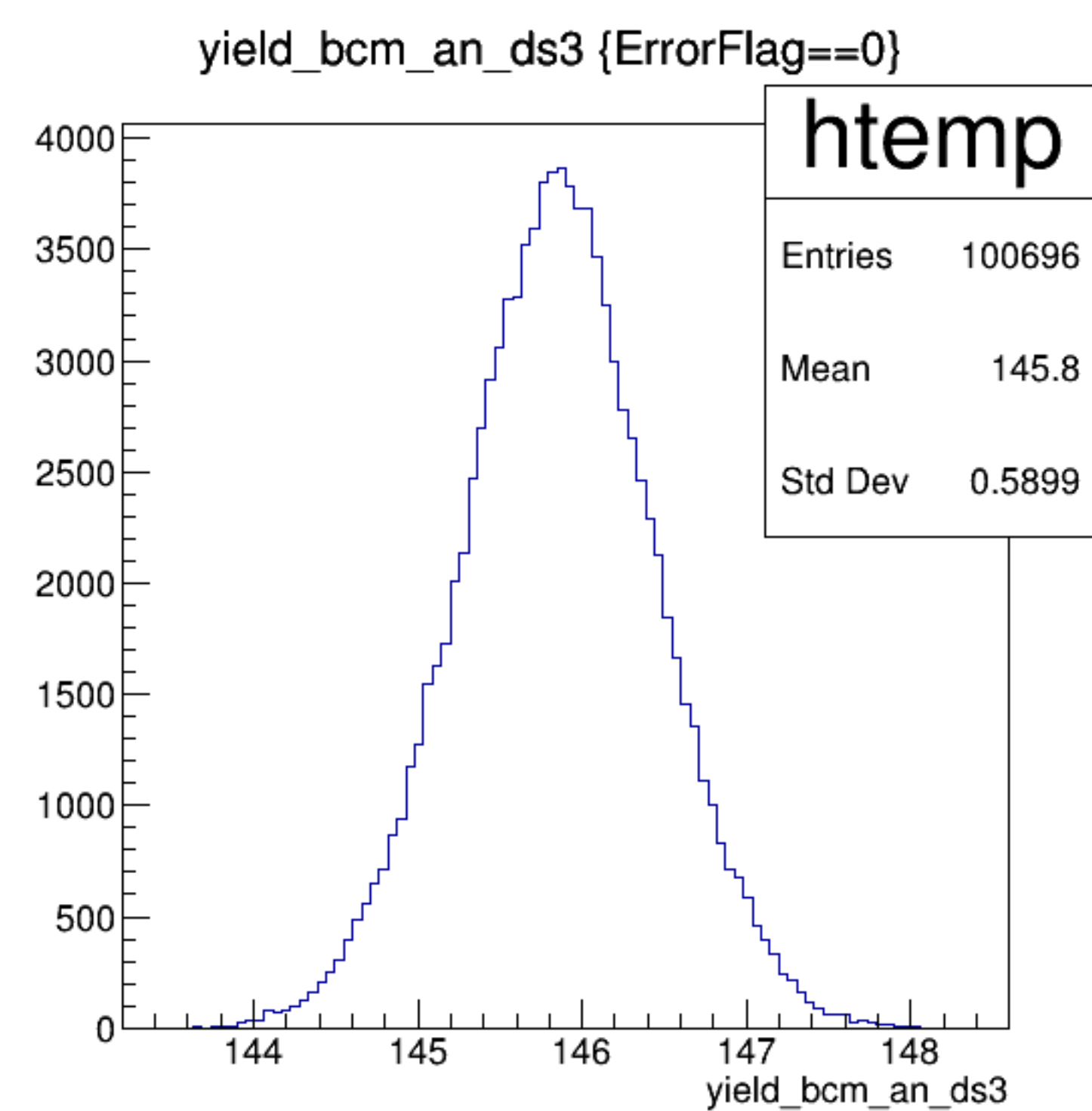
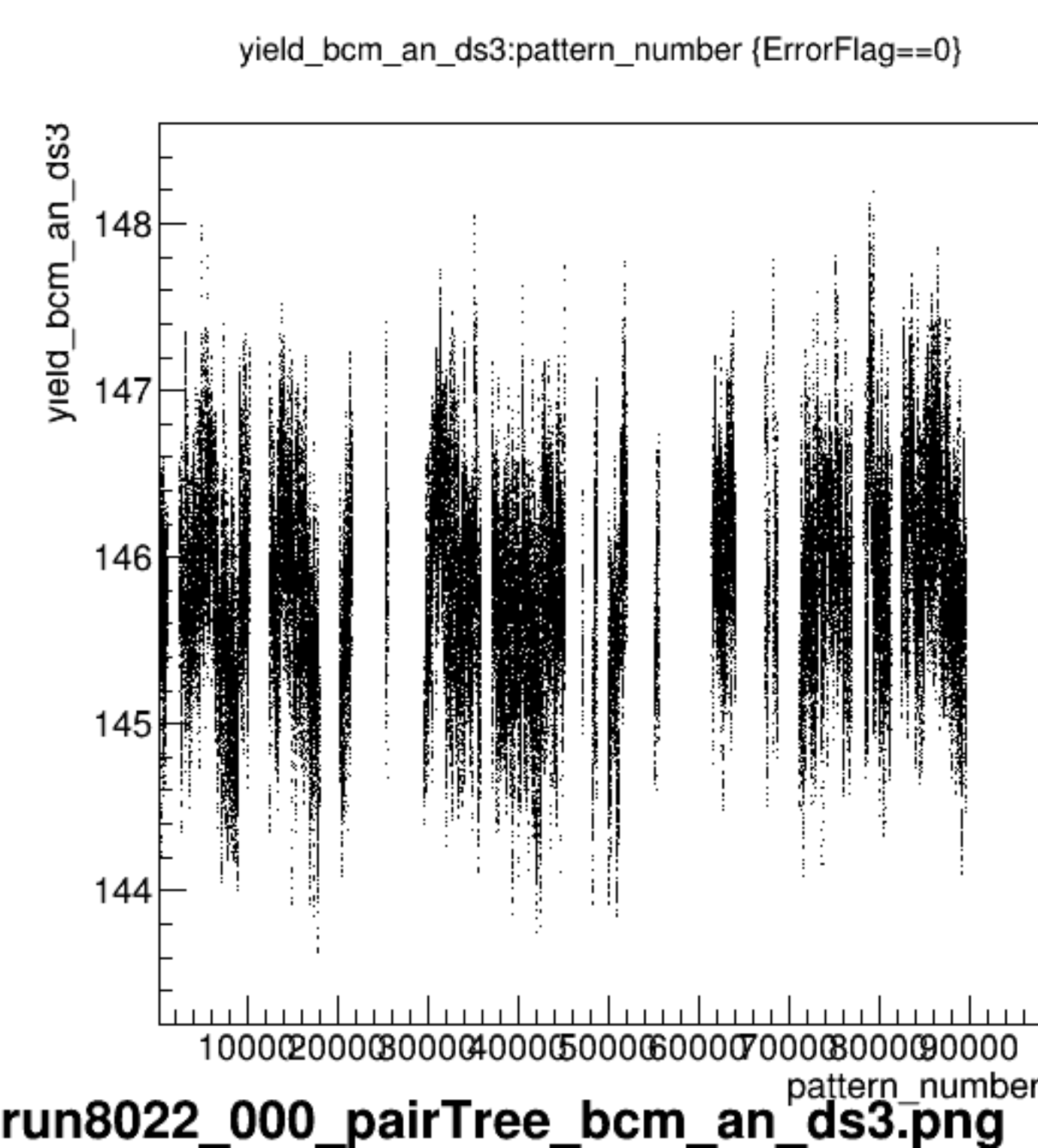
asym\_bcm\_dg\_ds/ppm-asym\_bcm\_an\_us/ppm:pattern\_number {ErrorFlag==0}



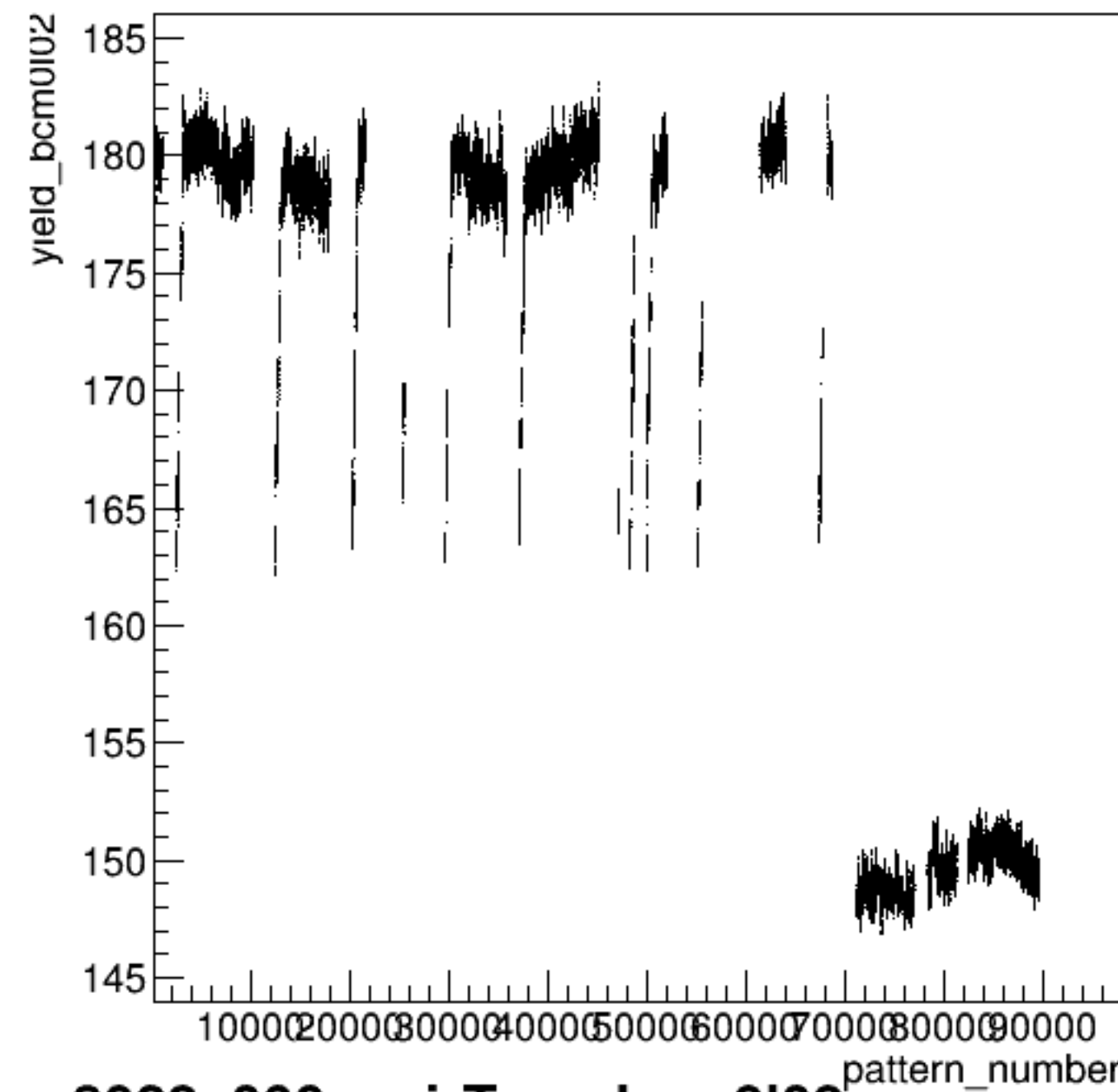
run8022\_000\_asym\_bcm\_dg\_ds\_vs\_asym\_bcm\_an\_us\_mul.png





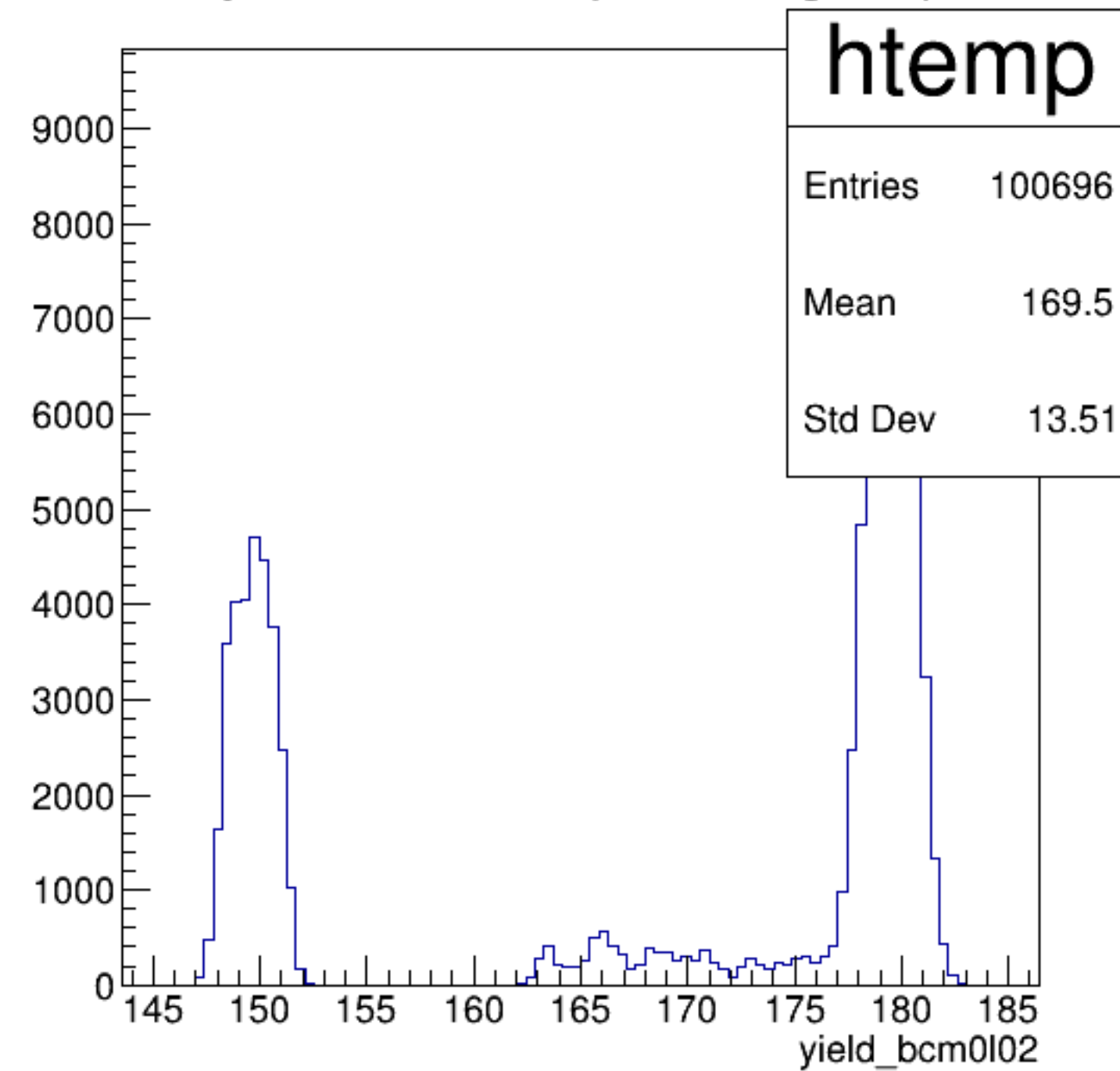


yield\_bcm0l02:pattern\_number {ErrorFlag==0}

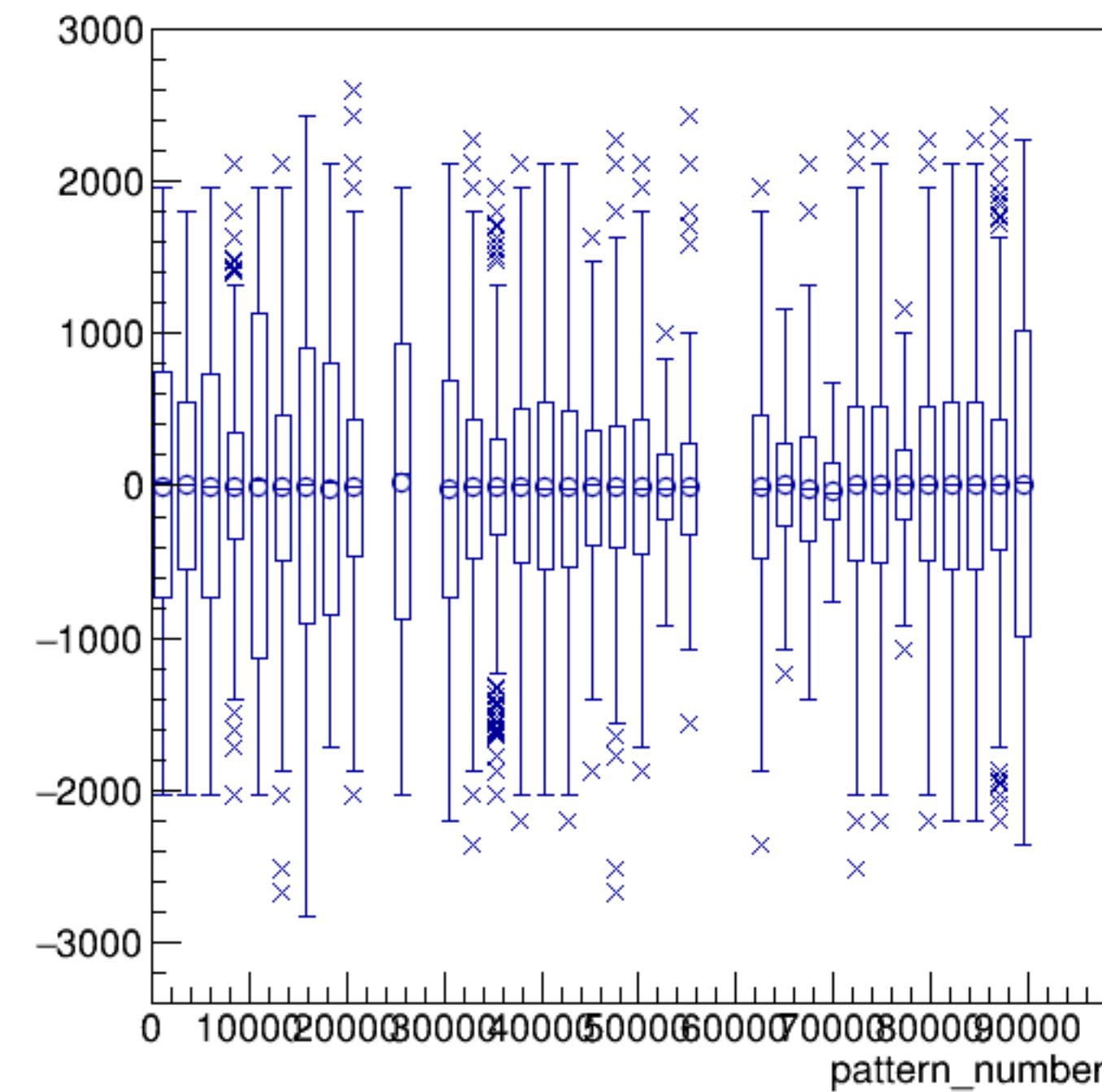


run8022\_000\_pairTree\_bcm0l02.png

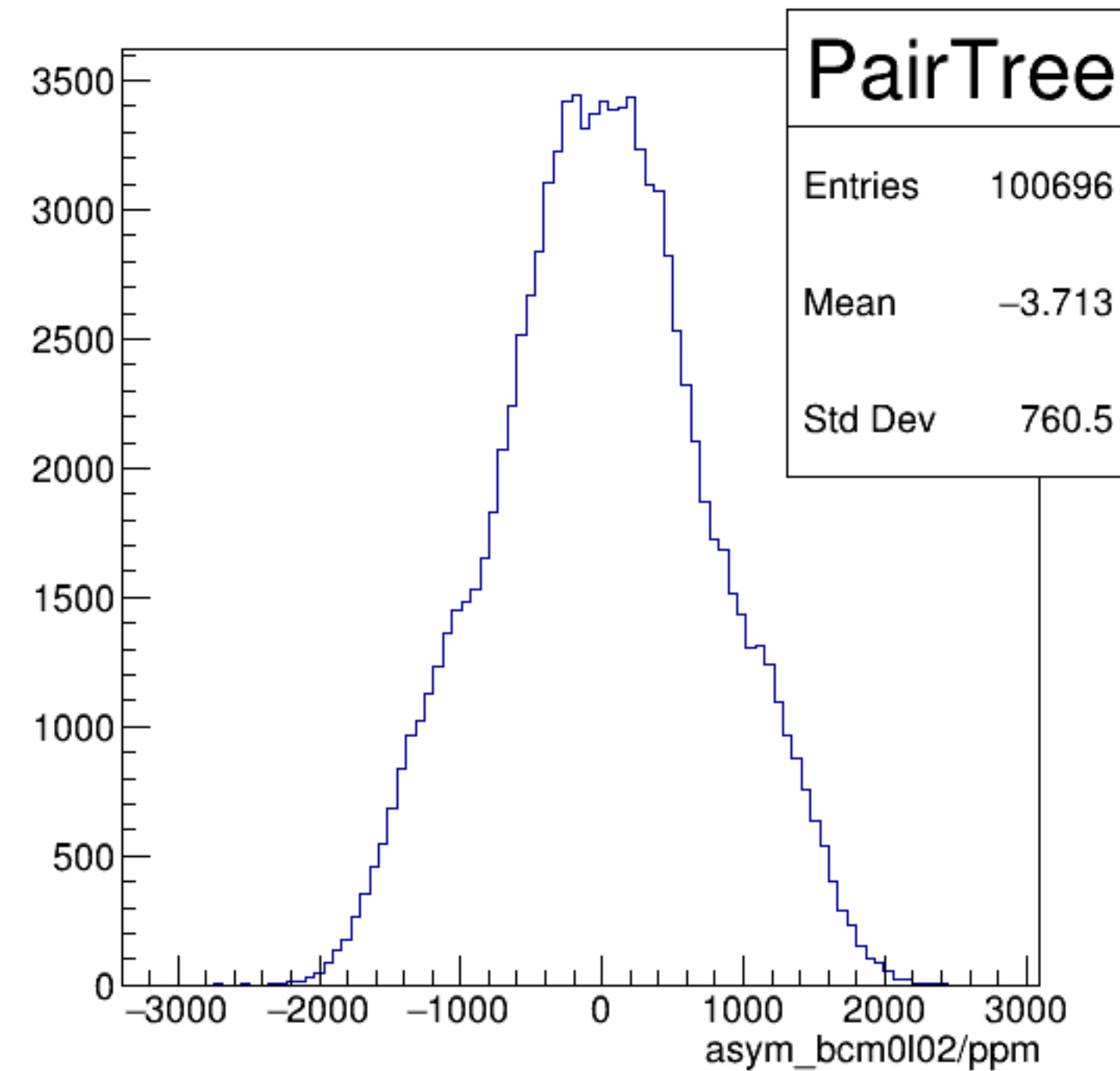
yield\_bcm0l02 {ErrorFlag==0}



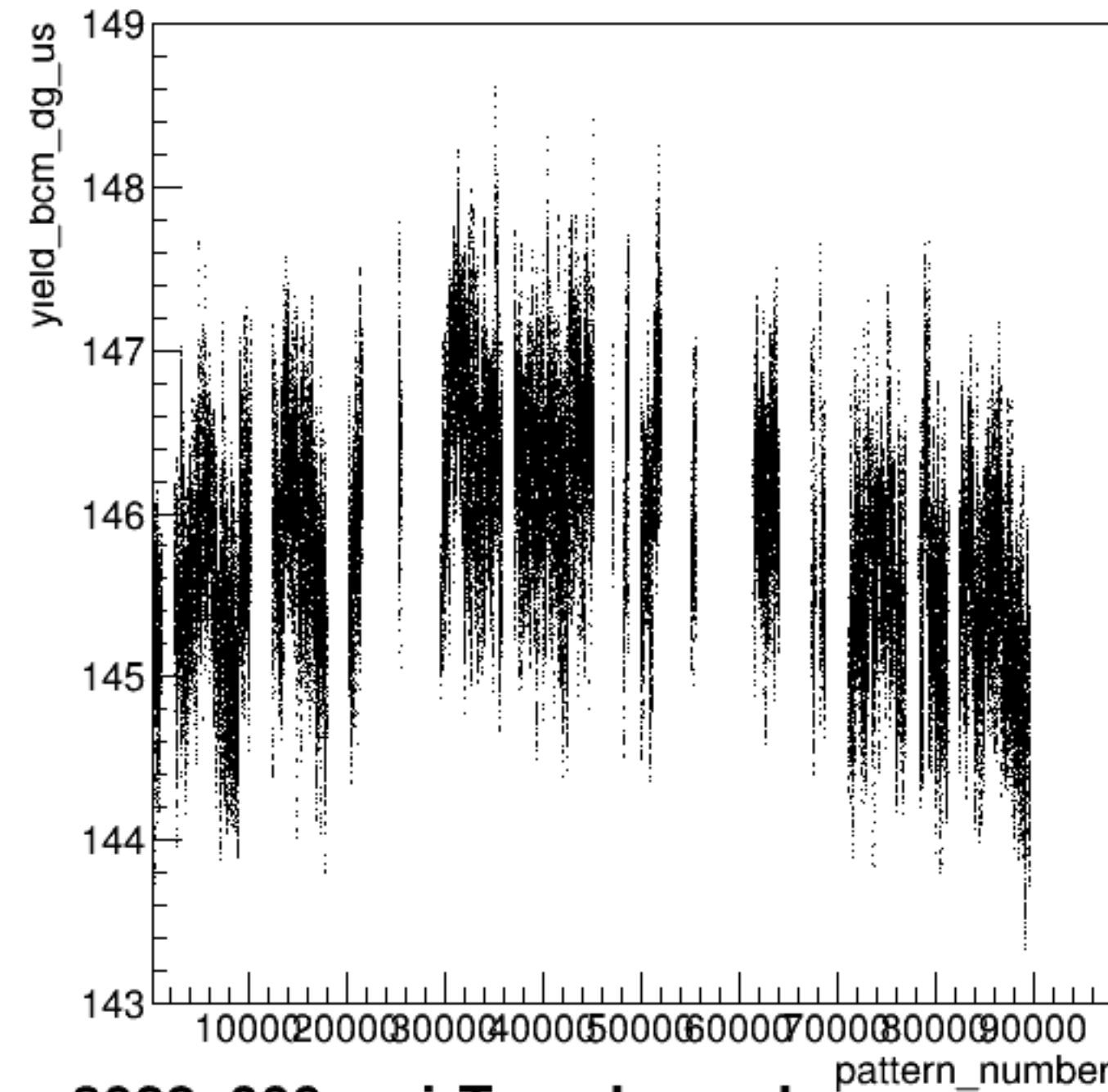
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asym\_bcm0l02/ppm {ErrorFlag==0}

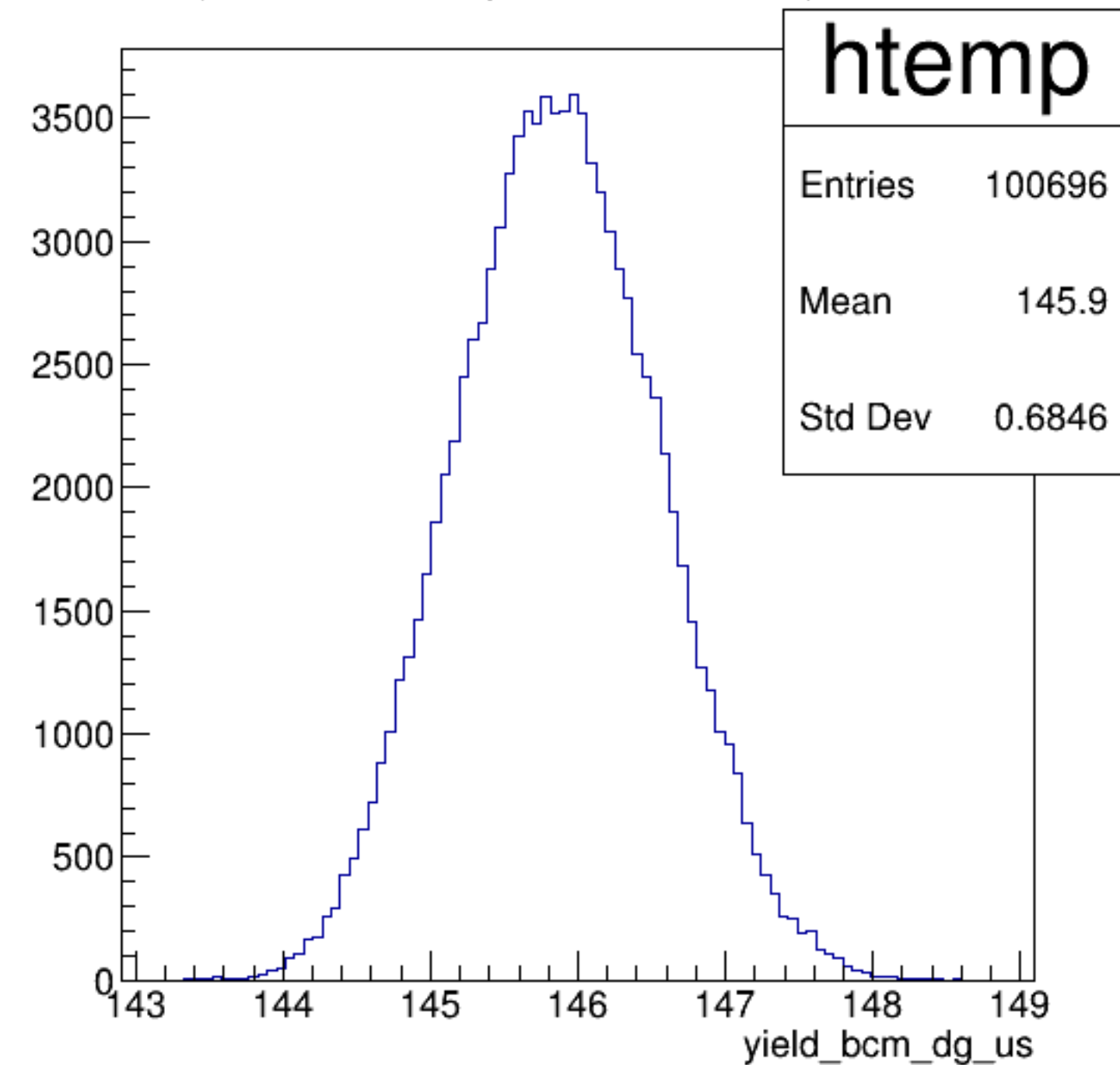


yield\_bcm\_dg\_us:pattern\_number {ErrorFlag==0}

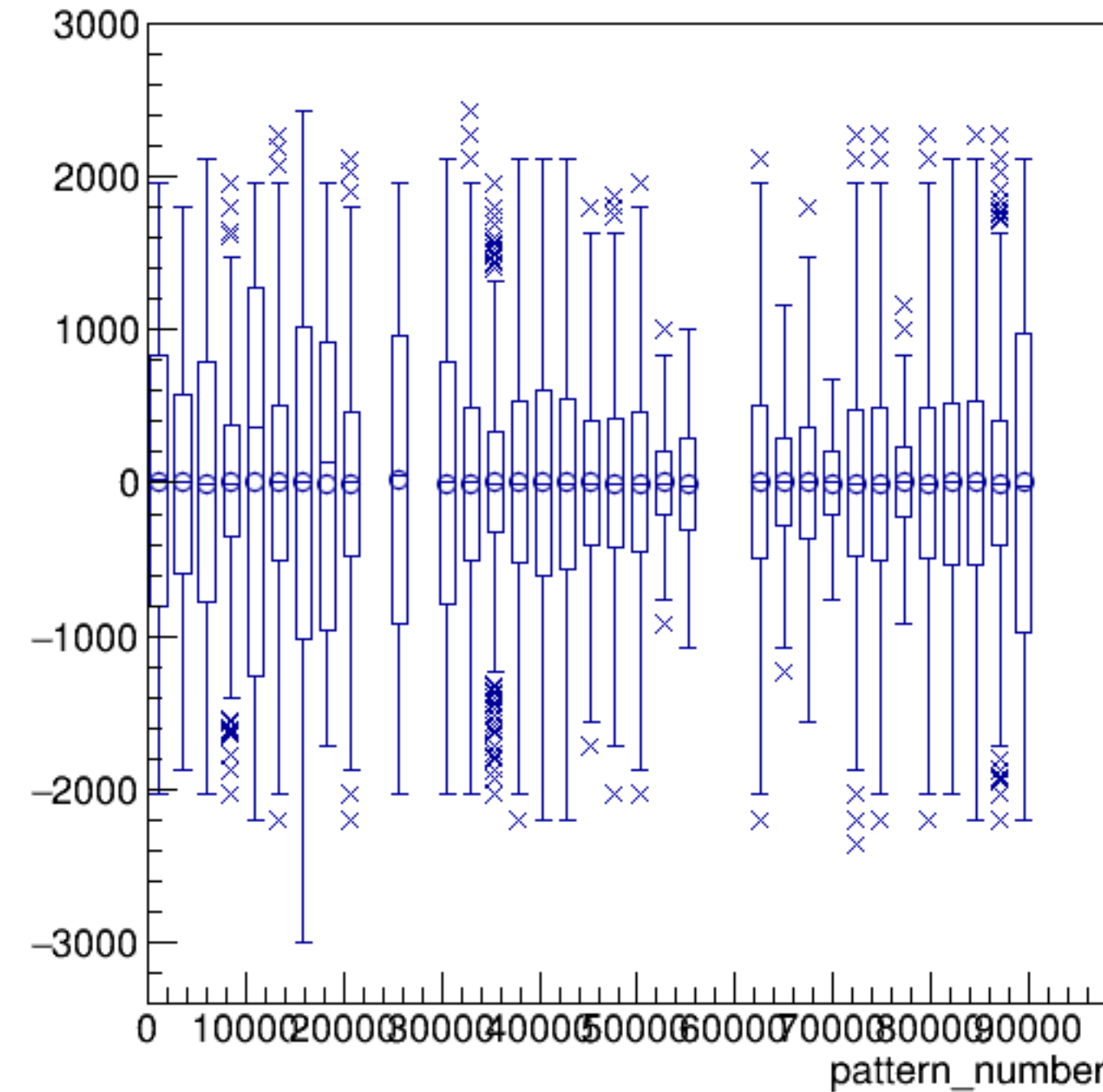


run8022\_000\_pairTree\_bcm\_dg\_us.png

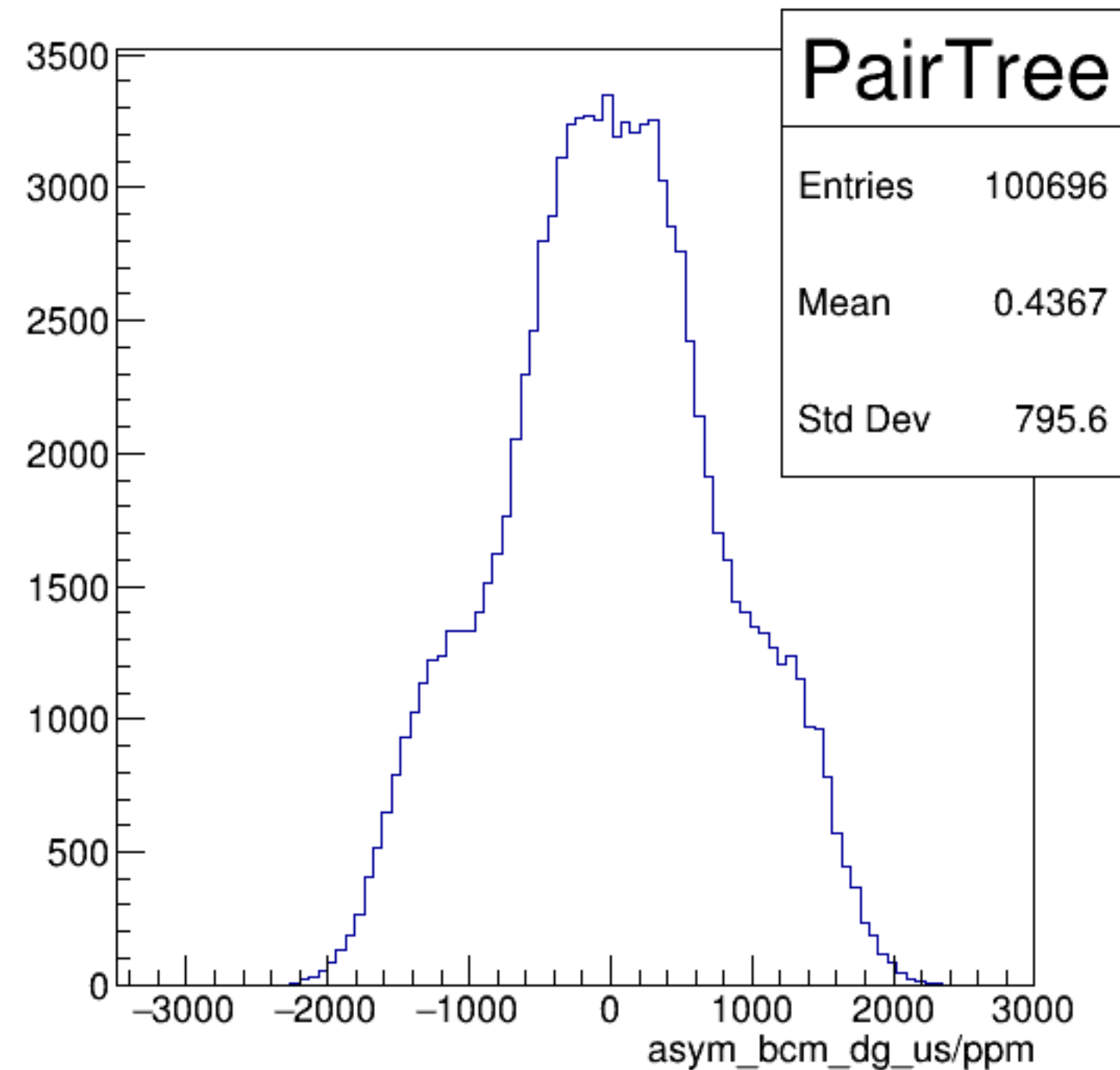
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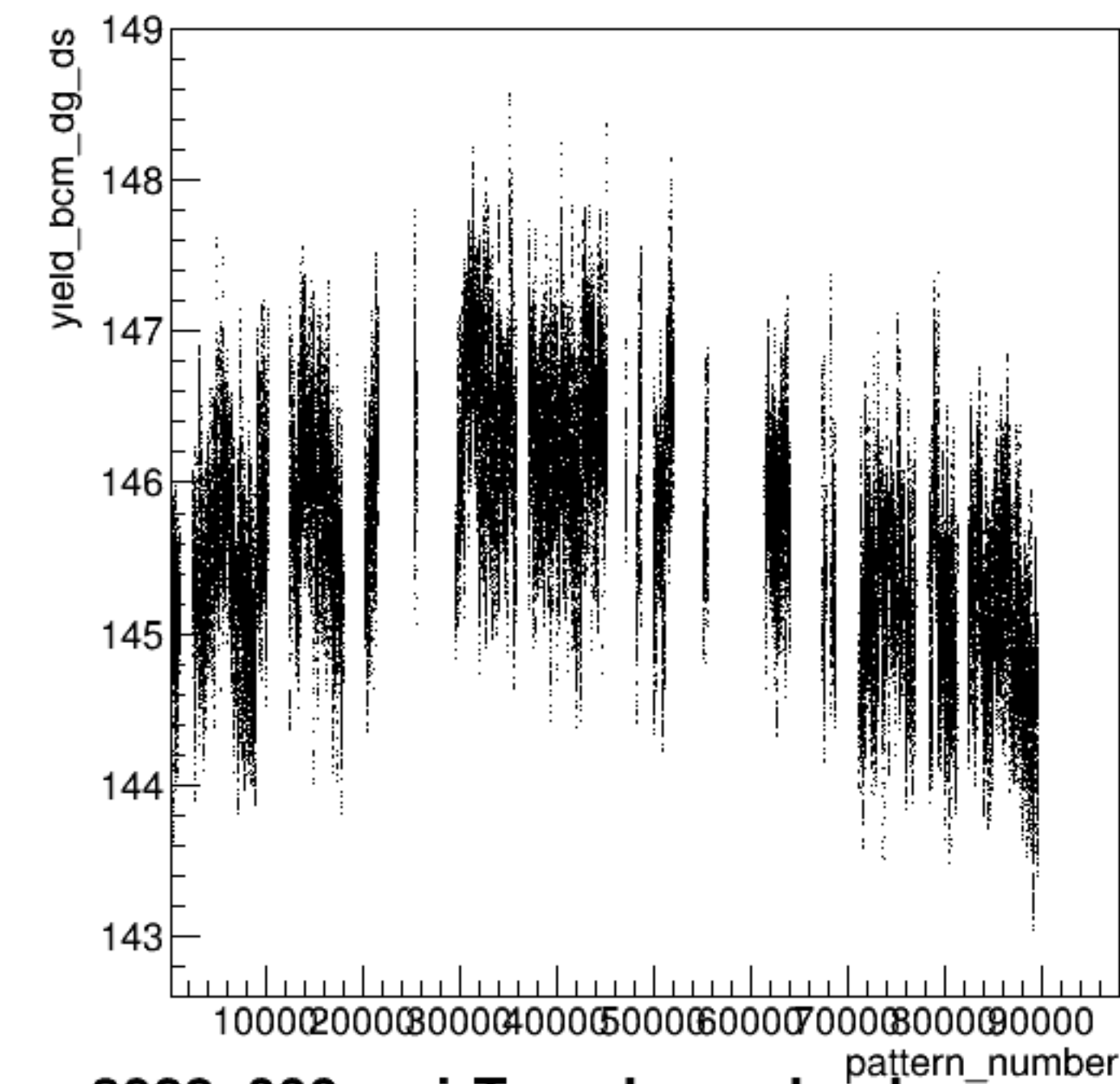
asym\_bcm\_dg\_us/ppm:pattern\_number {ErrorFlag==0}



asym\_bcm\_dg\_us/ppm {ErrorFlag==0}

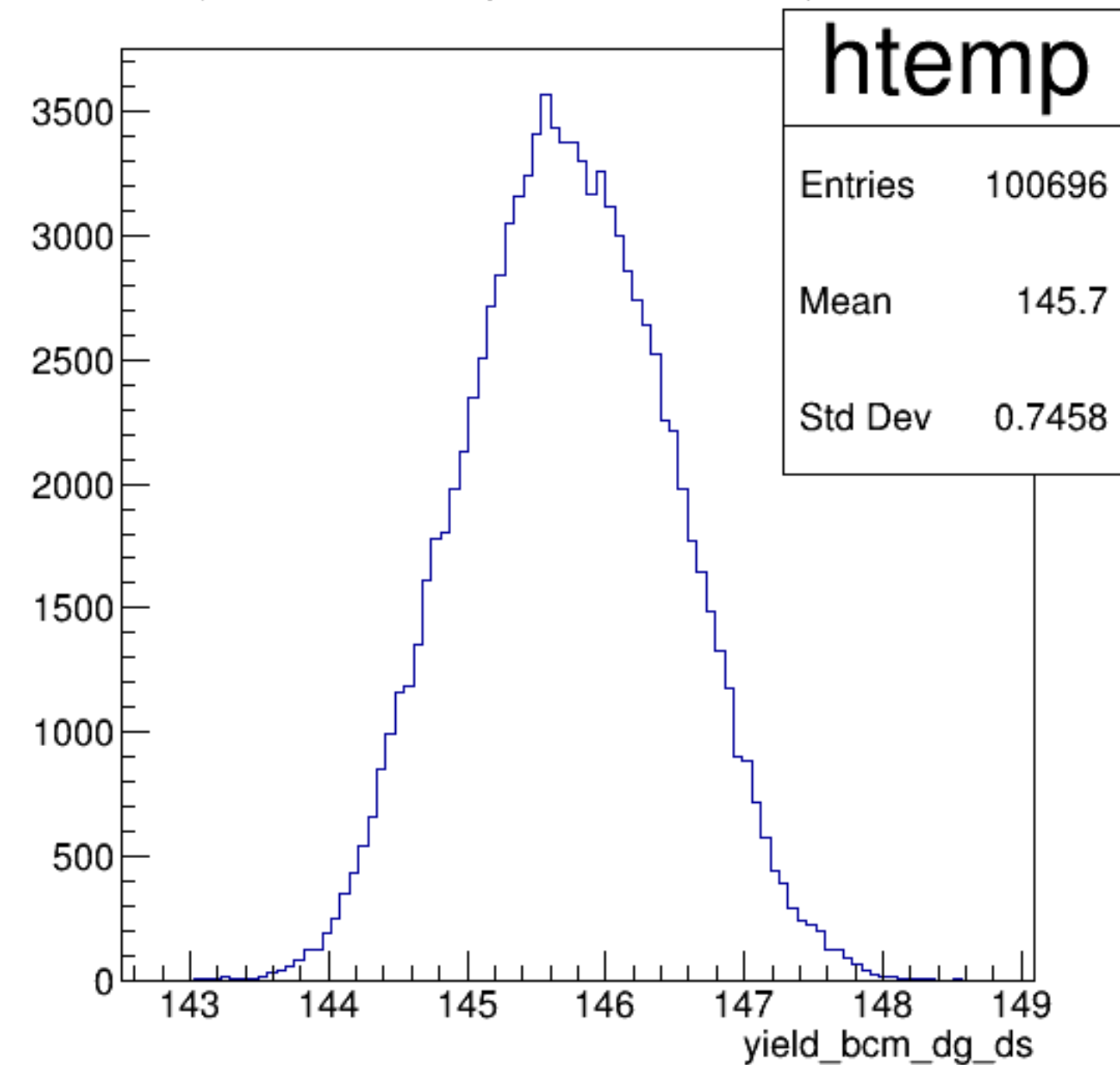


yield\_bcm\_dg\_ds:pattern\_number {ErrorFlag==0}

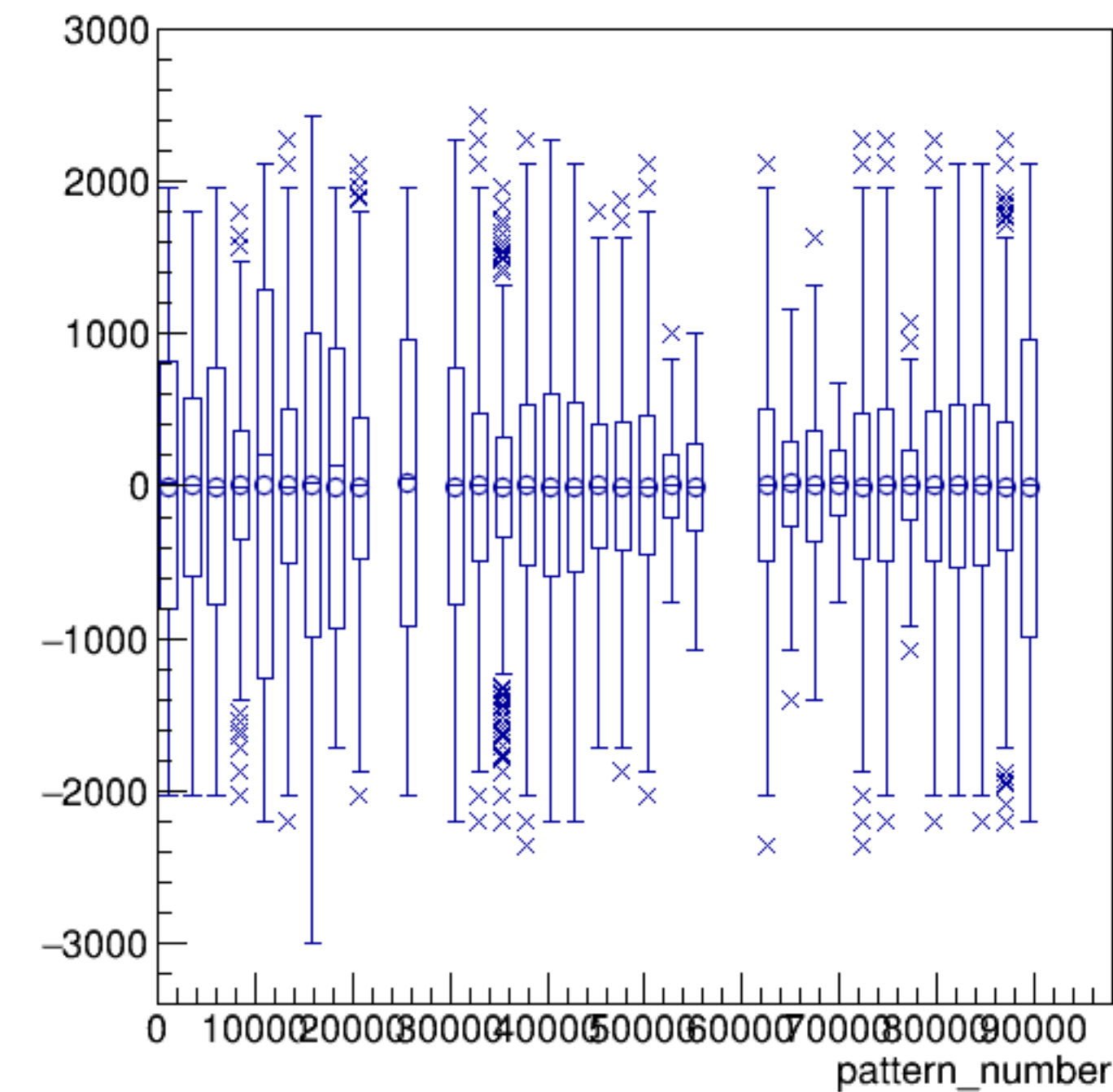


run8022\_000\_pairTree\_bcm\_dg\_ds.png

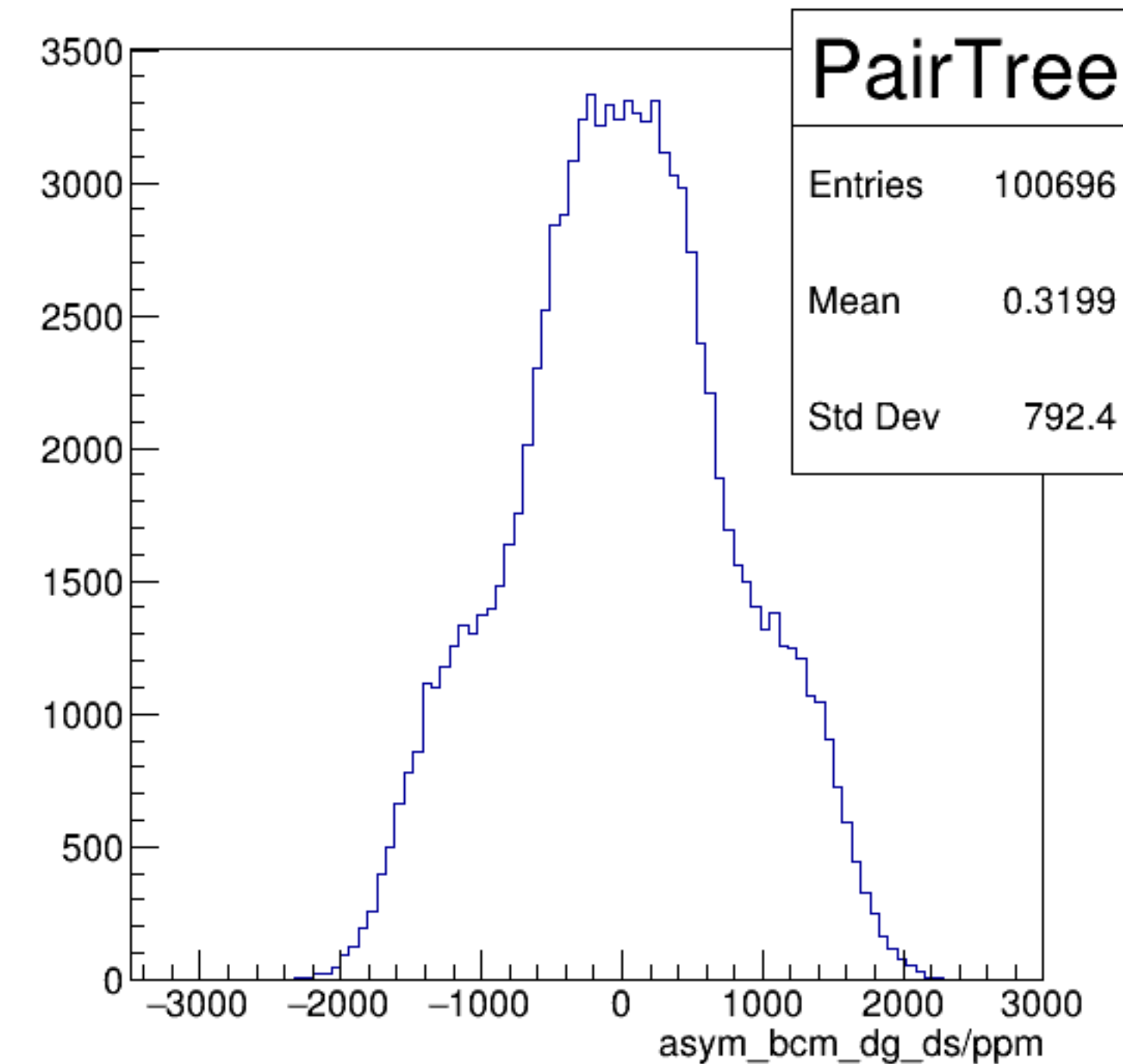
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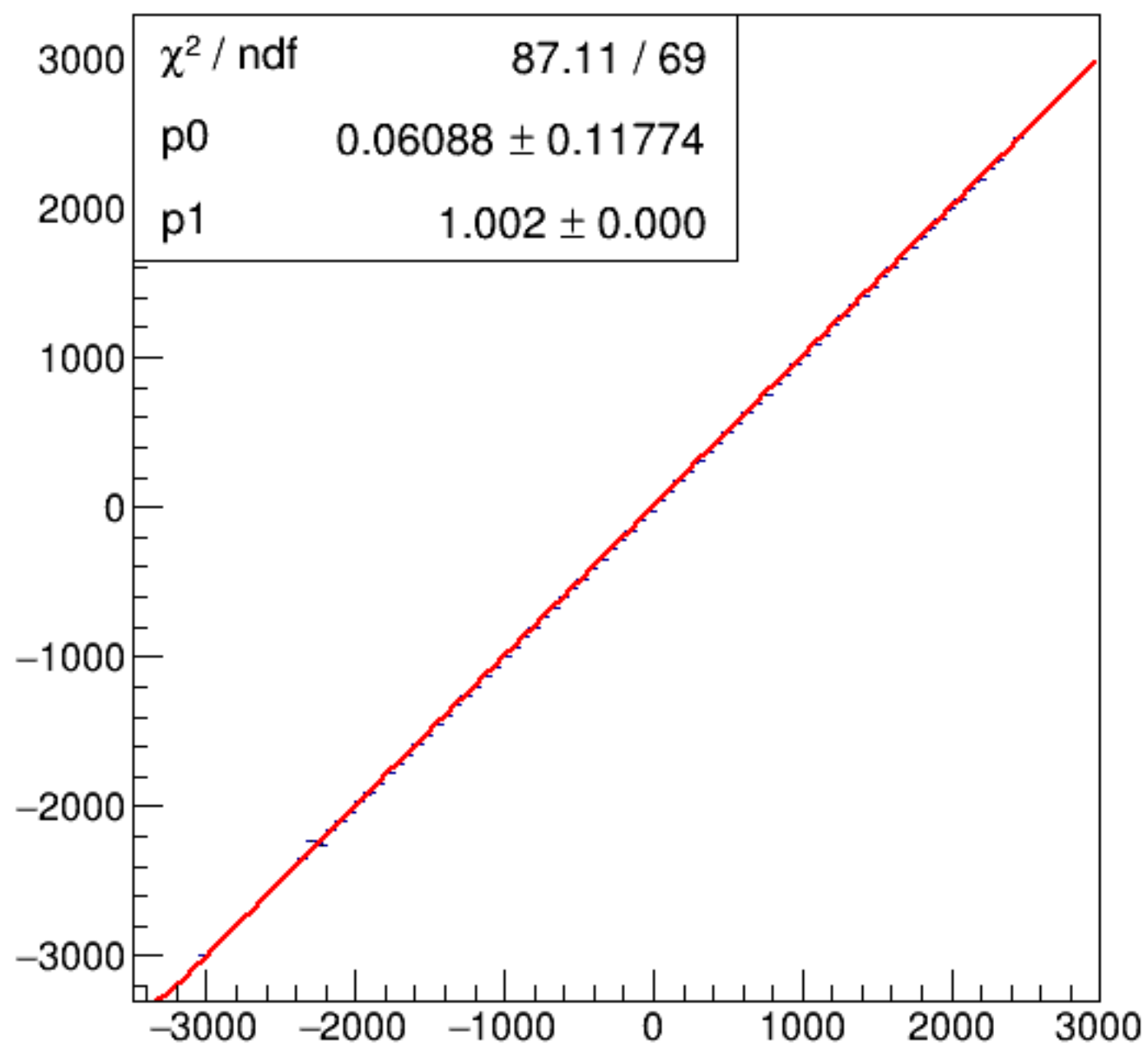
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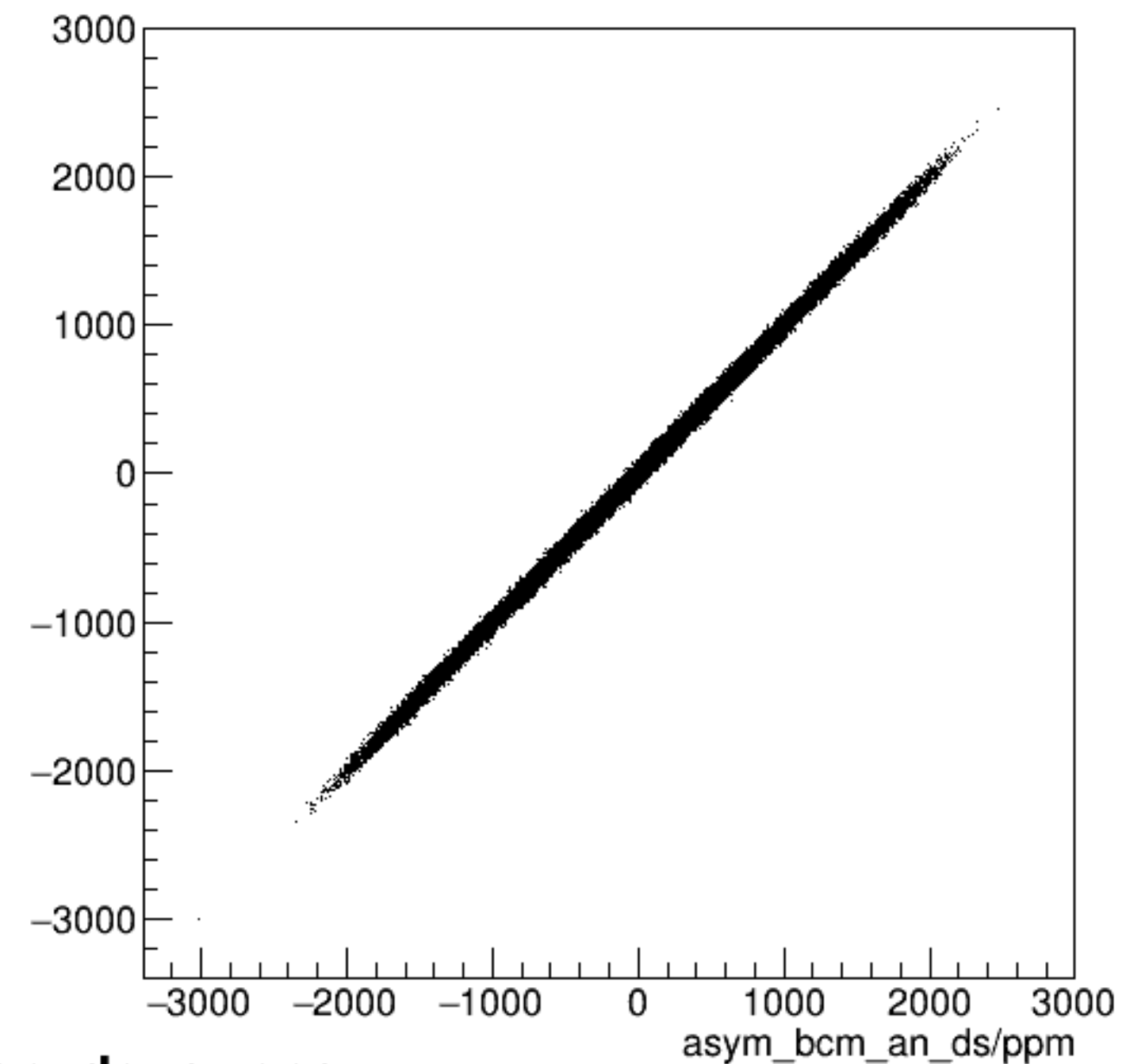
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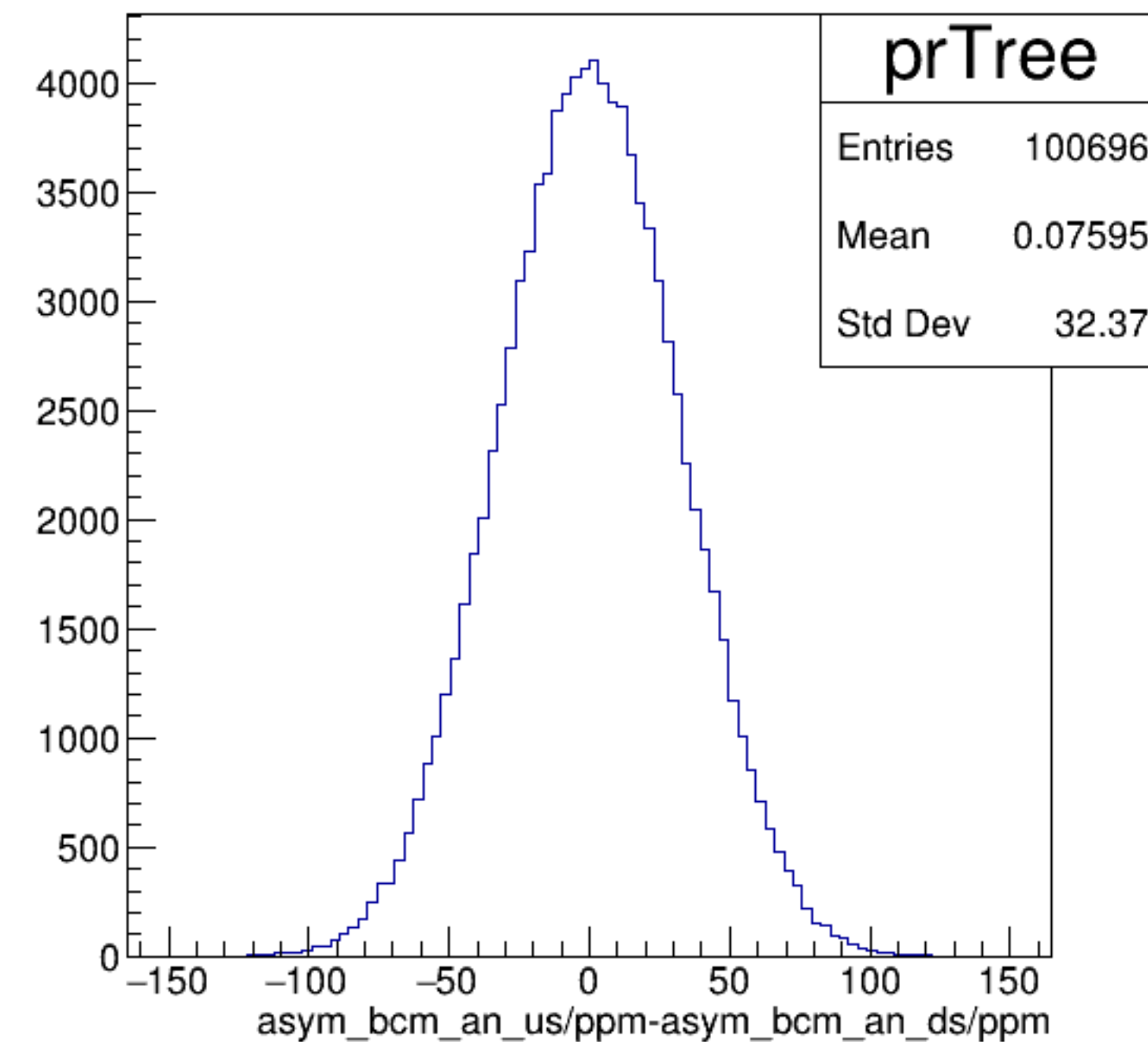
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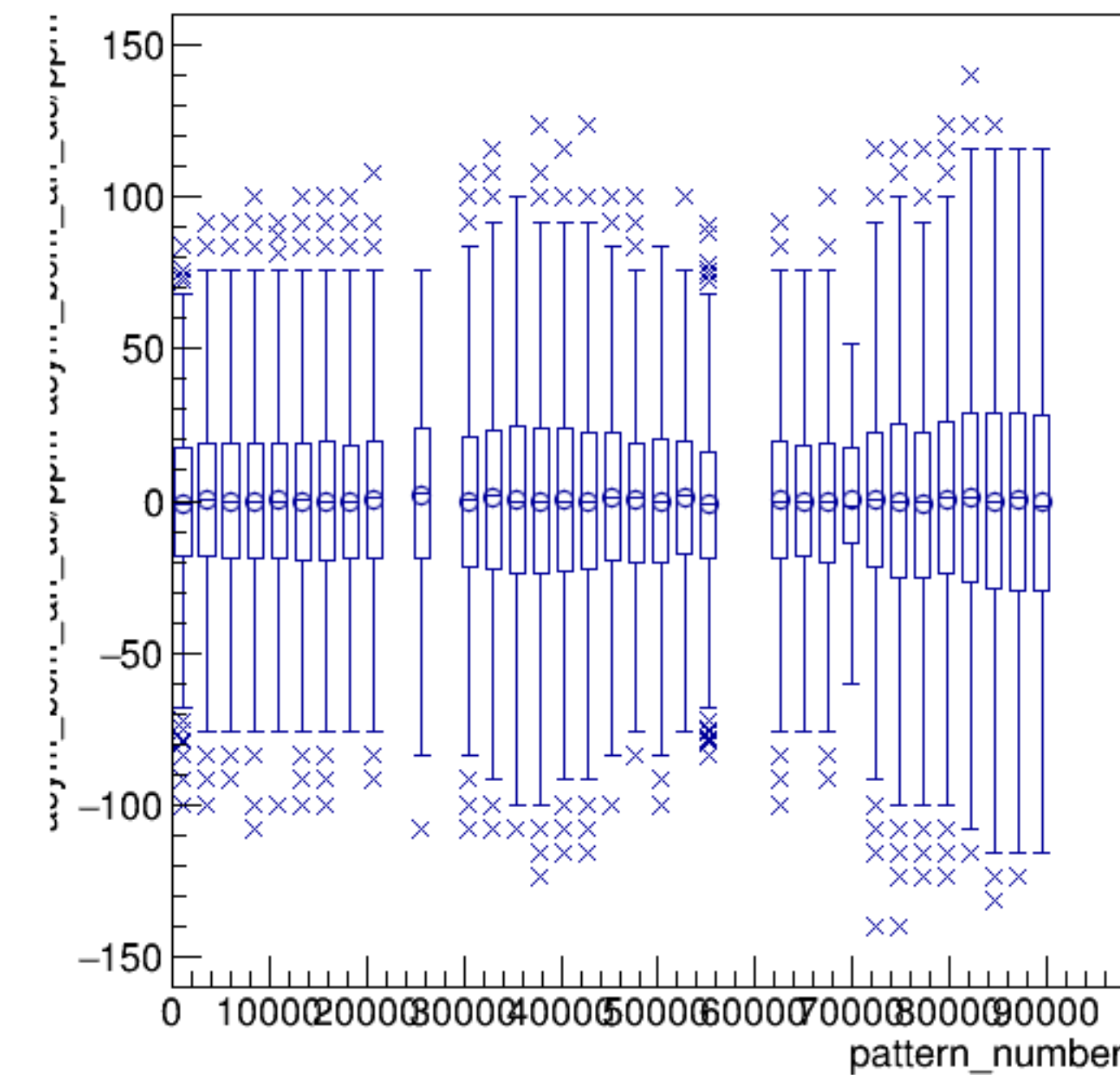
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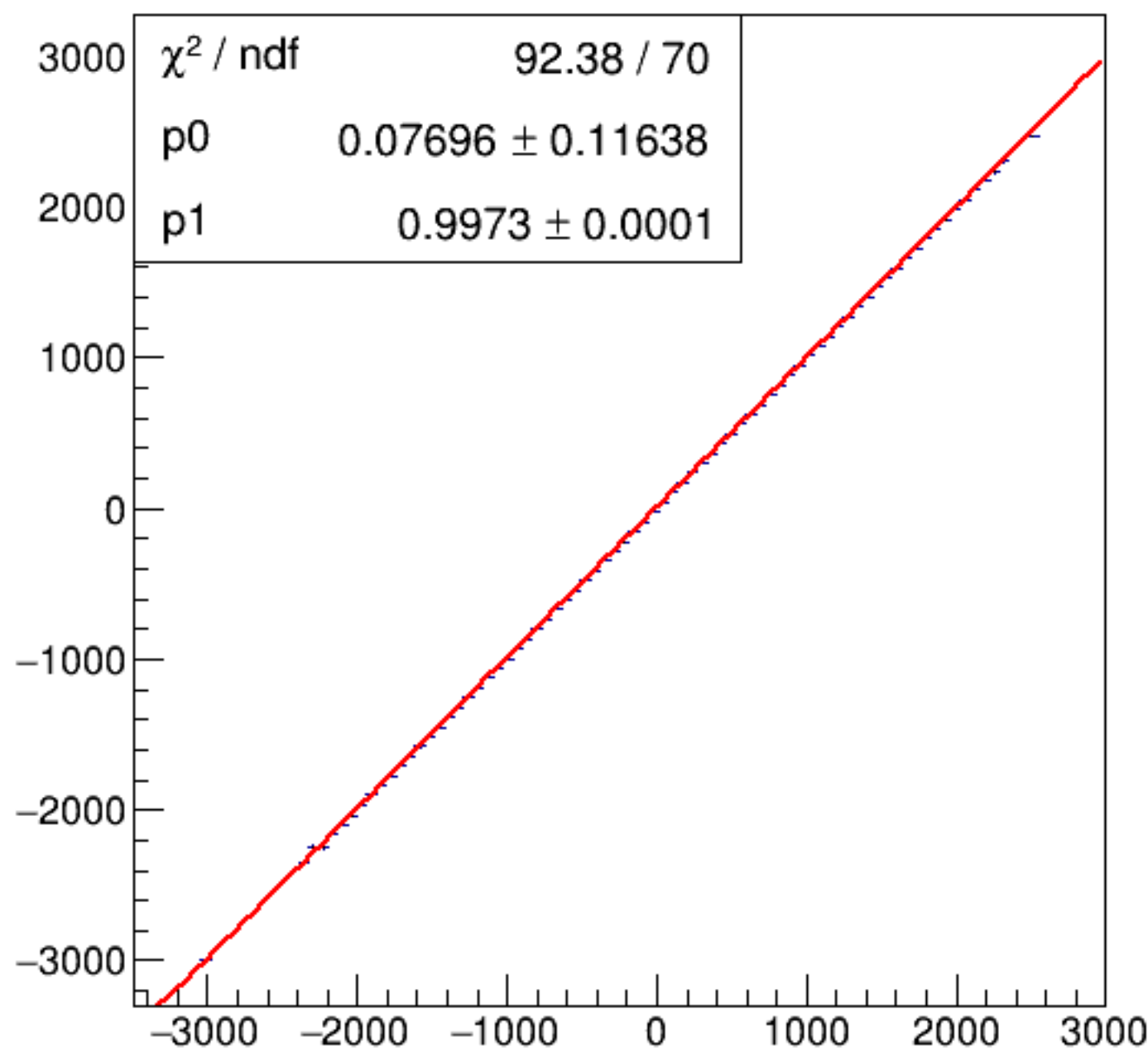


asym\_bcm\_an\_us/ppm-asym\_bcm\_an\_ds/ppm:pattern\_number {ErrorFlag==0}

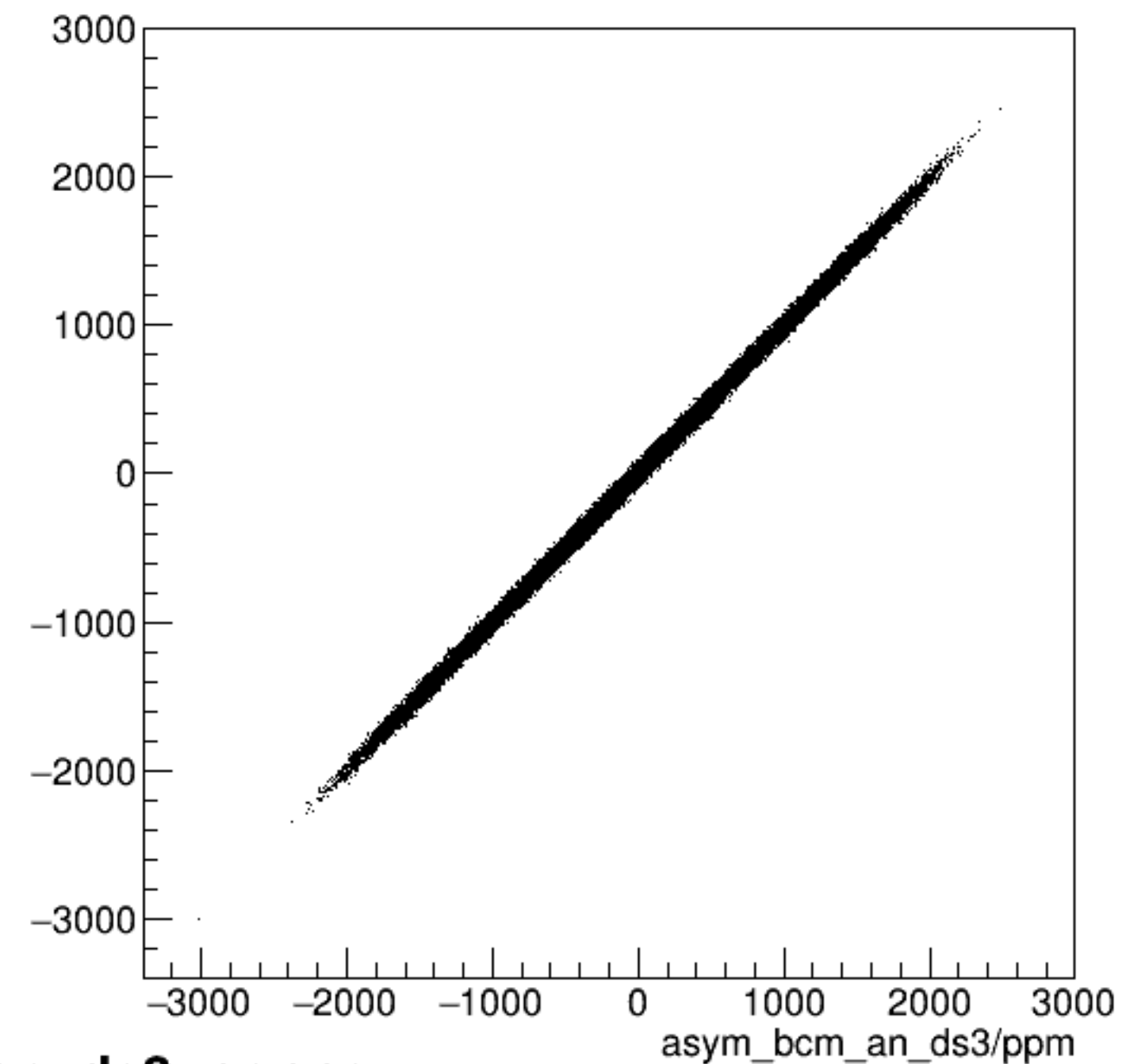


run8022\_000\_asym\_bcm\_an\_us\_vs\_asym\_bcm\_an\_ds\_pr.png

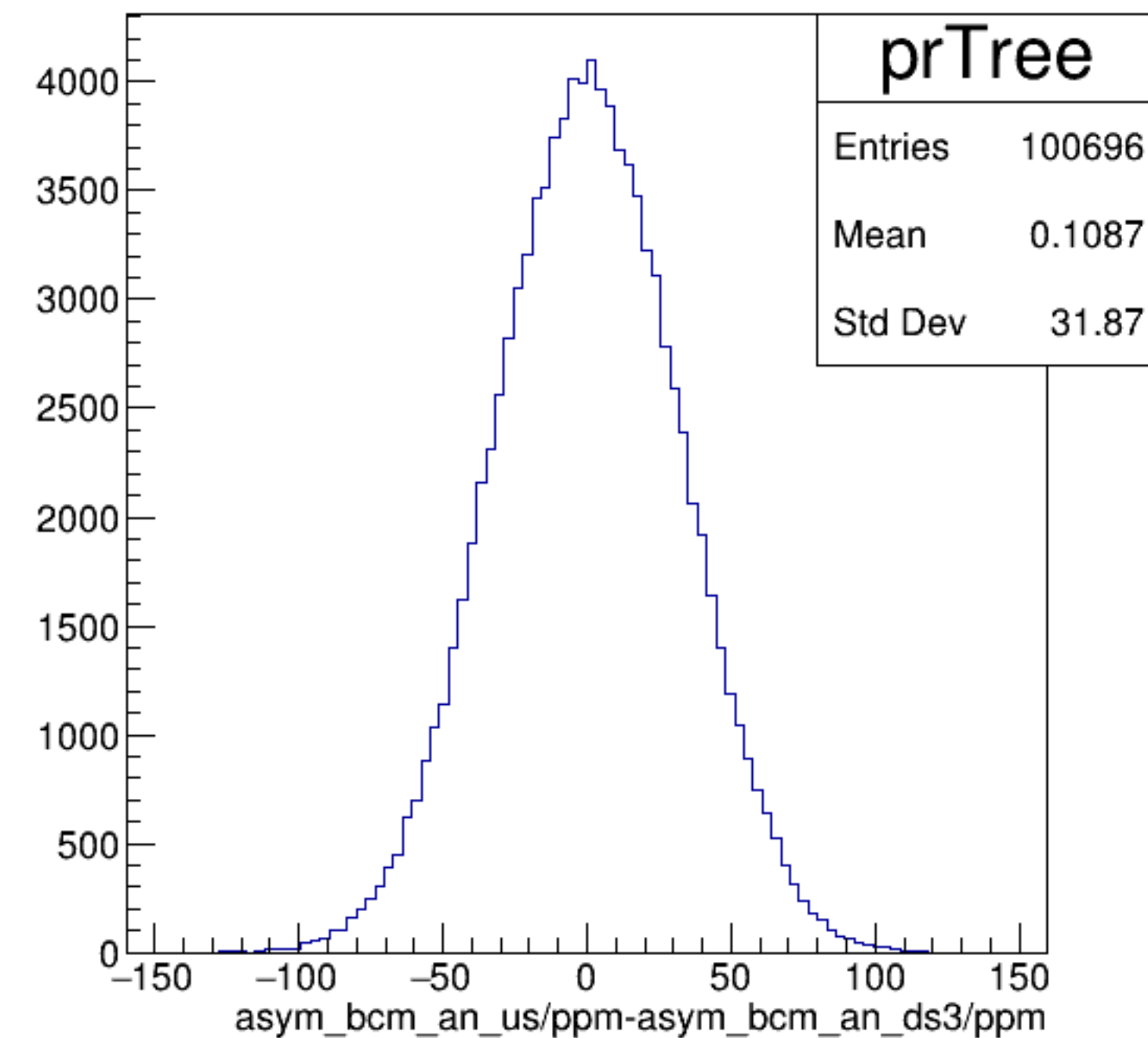
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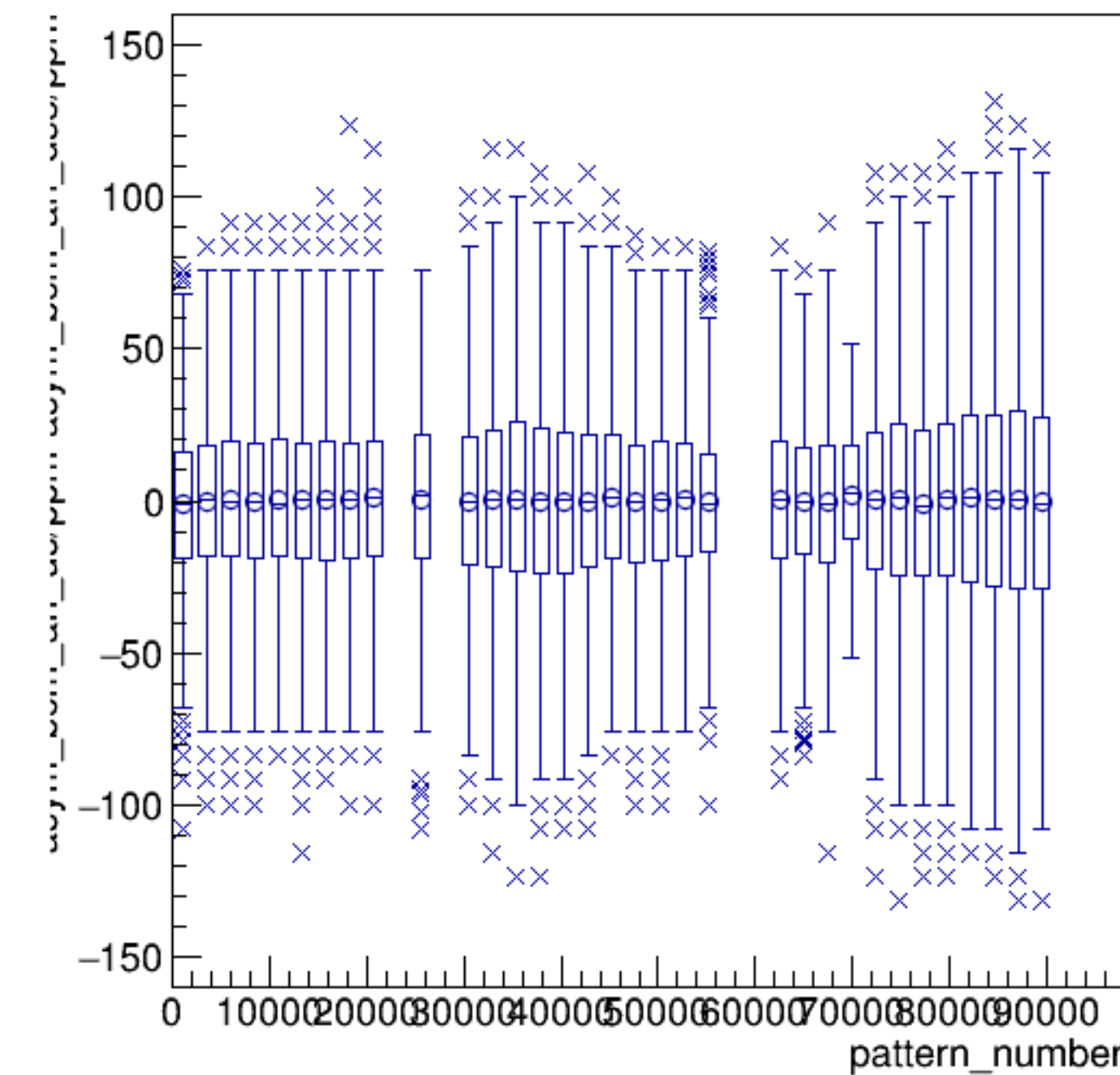
asym\_bcm\_an\_us/ppm:asym\_bcm\_an\_ds3/ppm {ErrorFlag==0}



asym\_bcm\_an\_us/ppm-asym\_bcm\_an\_ds3/ppm {ErrorFlag==0}

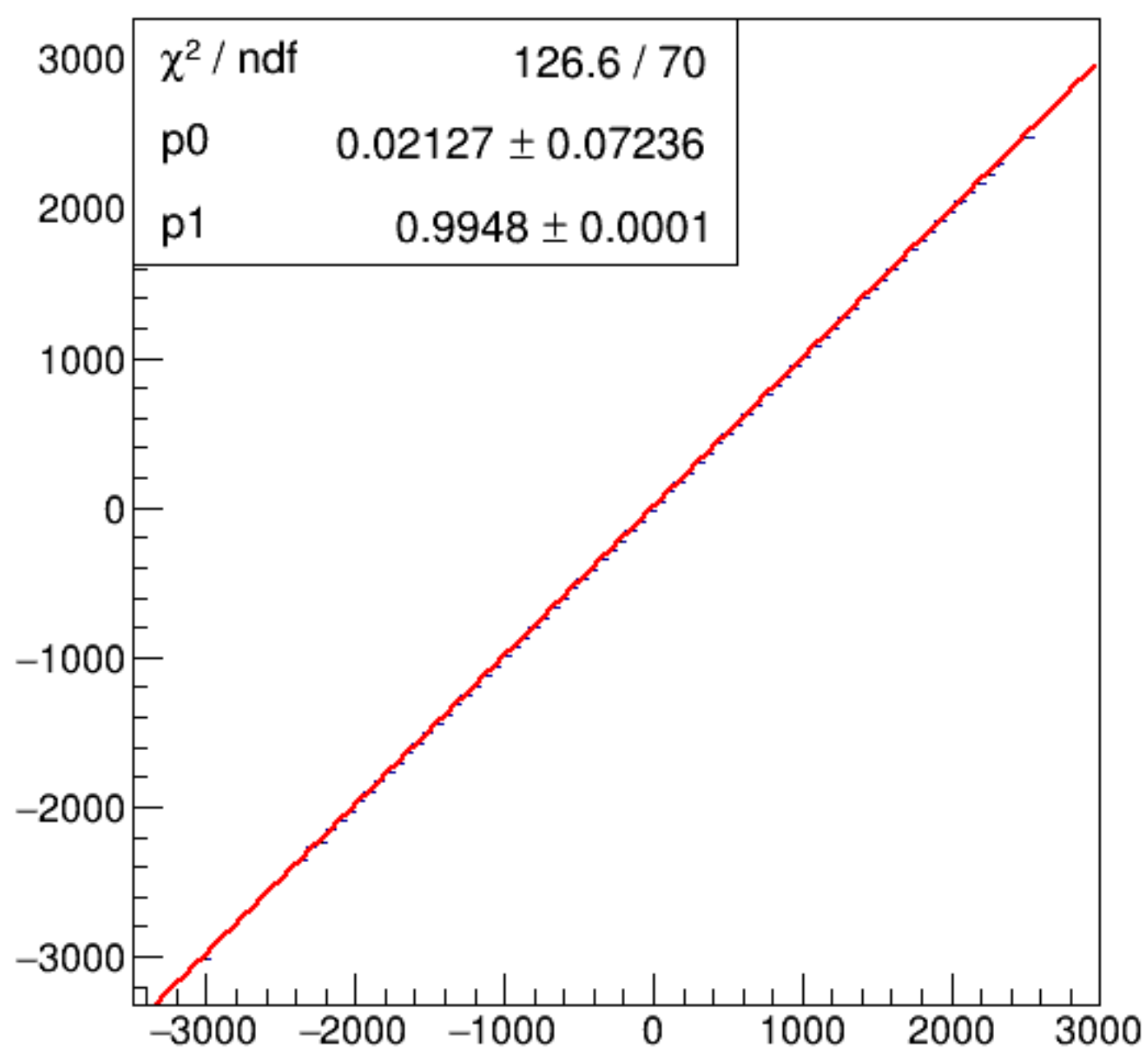


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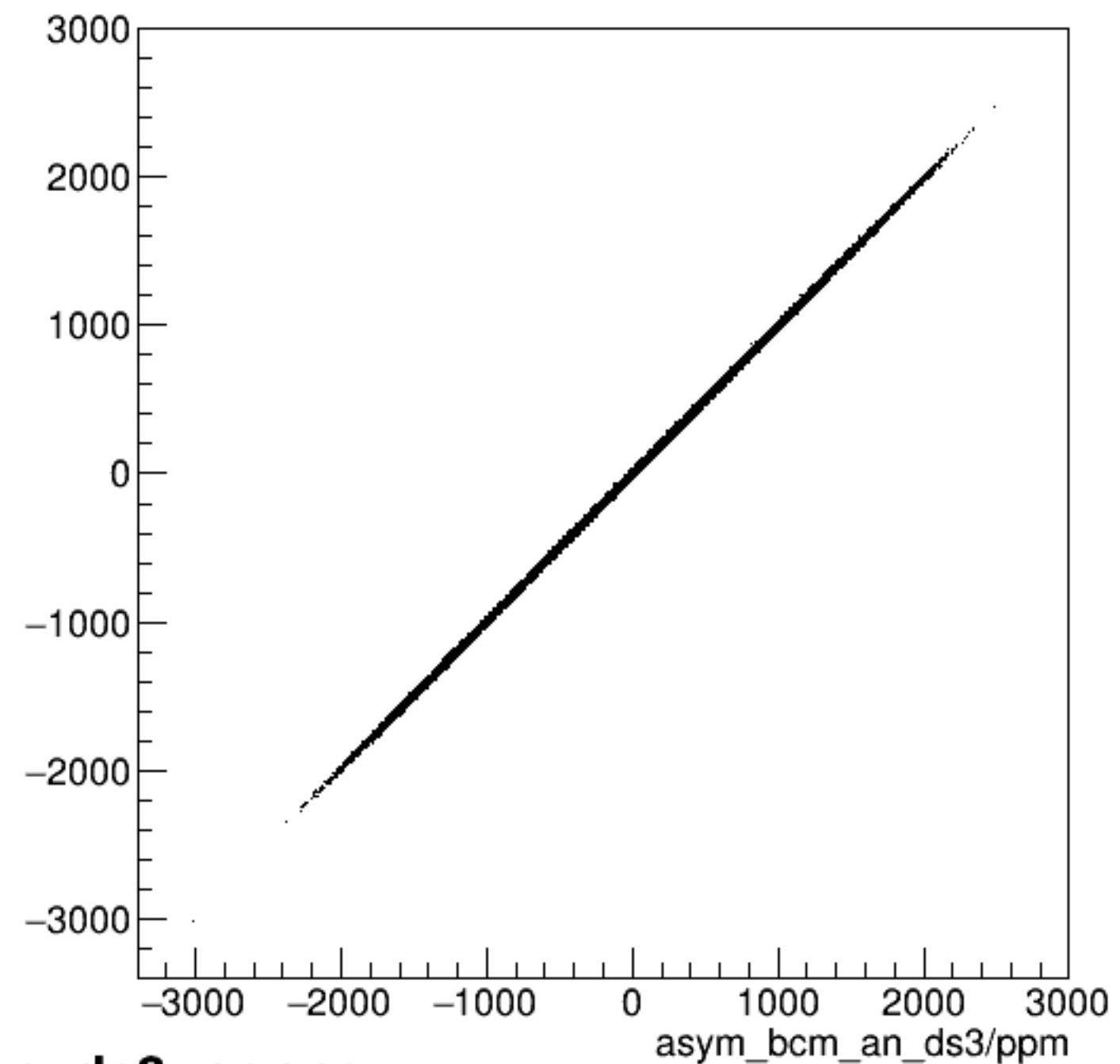


run8022\_000\_asym\_bcm\_an\_us\_vs\_asym\_bcm\_an\_ds3\_pr.png

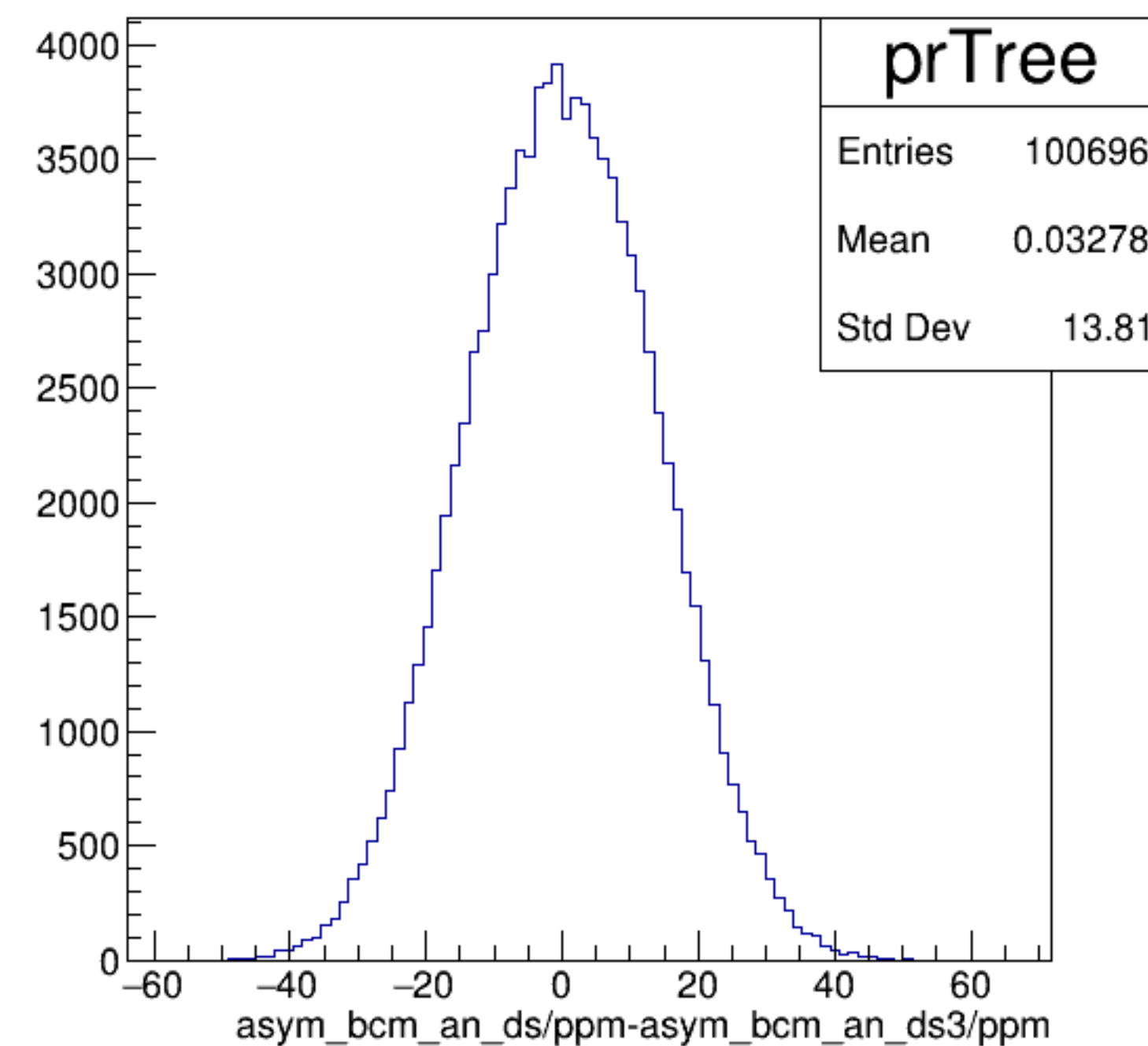
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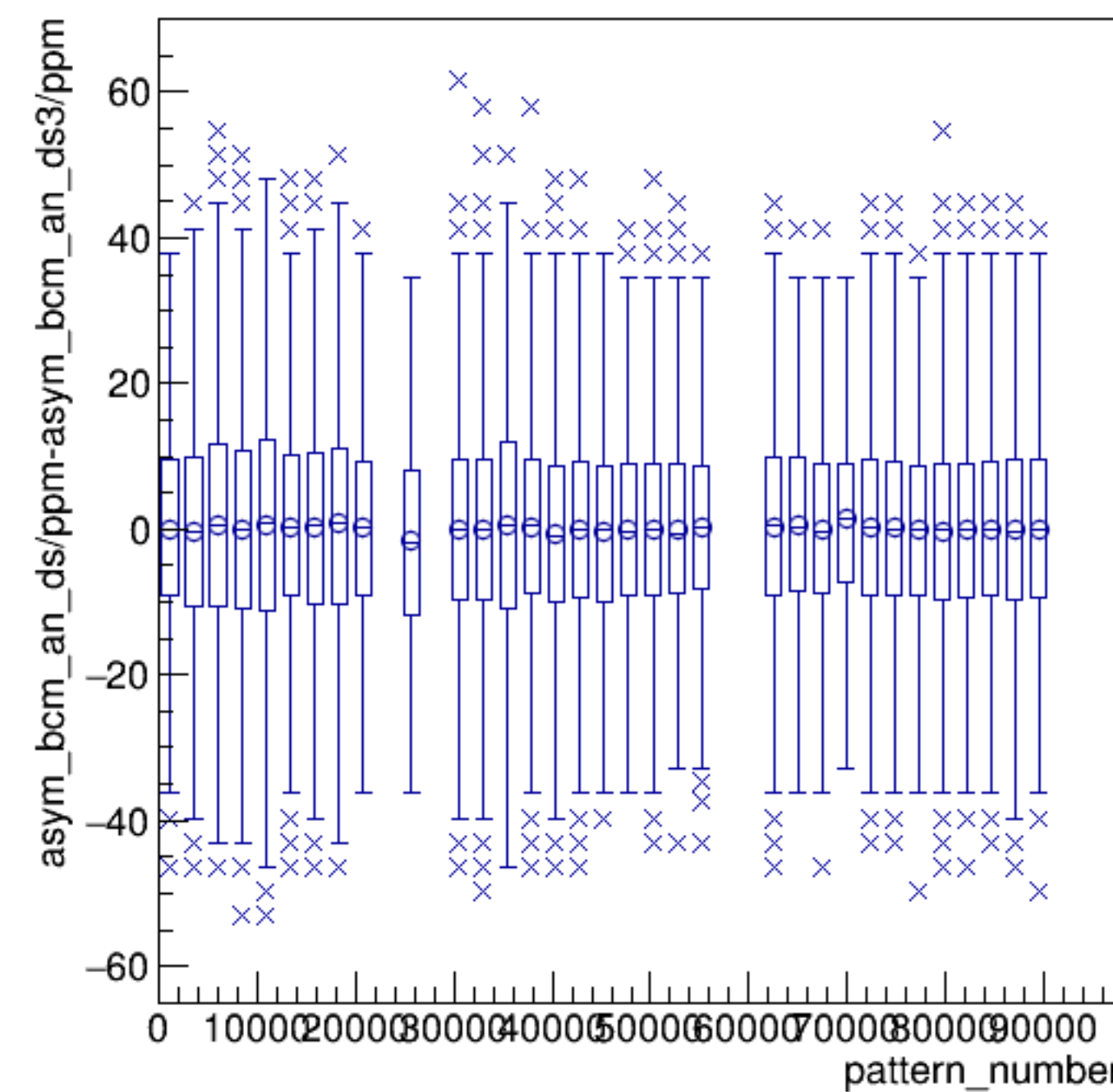
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asym\_bcm\_an\_ds/ppm-asym\_bcm\_an\_ds3/ppm {ErrorFlag==0}

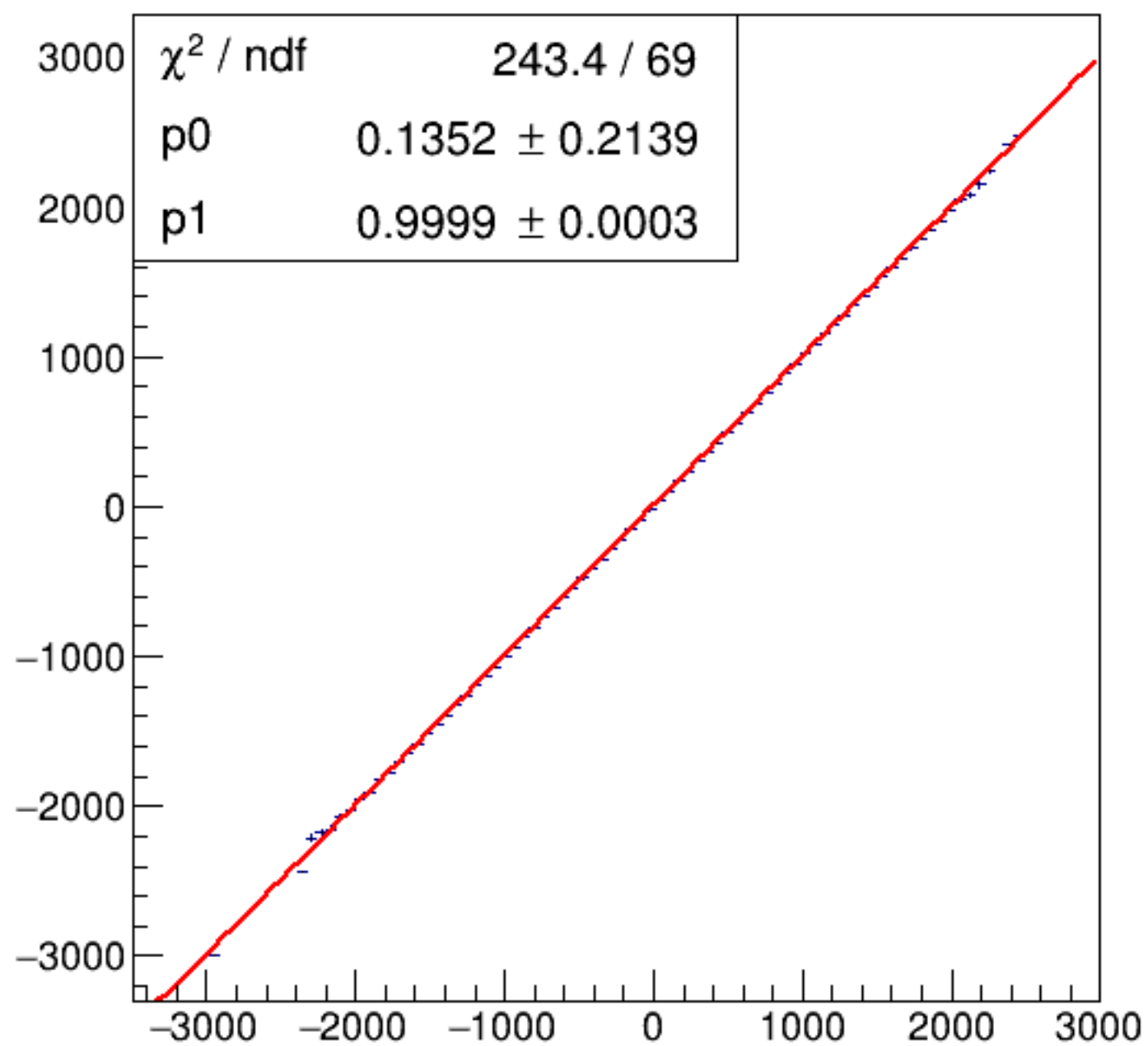


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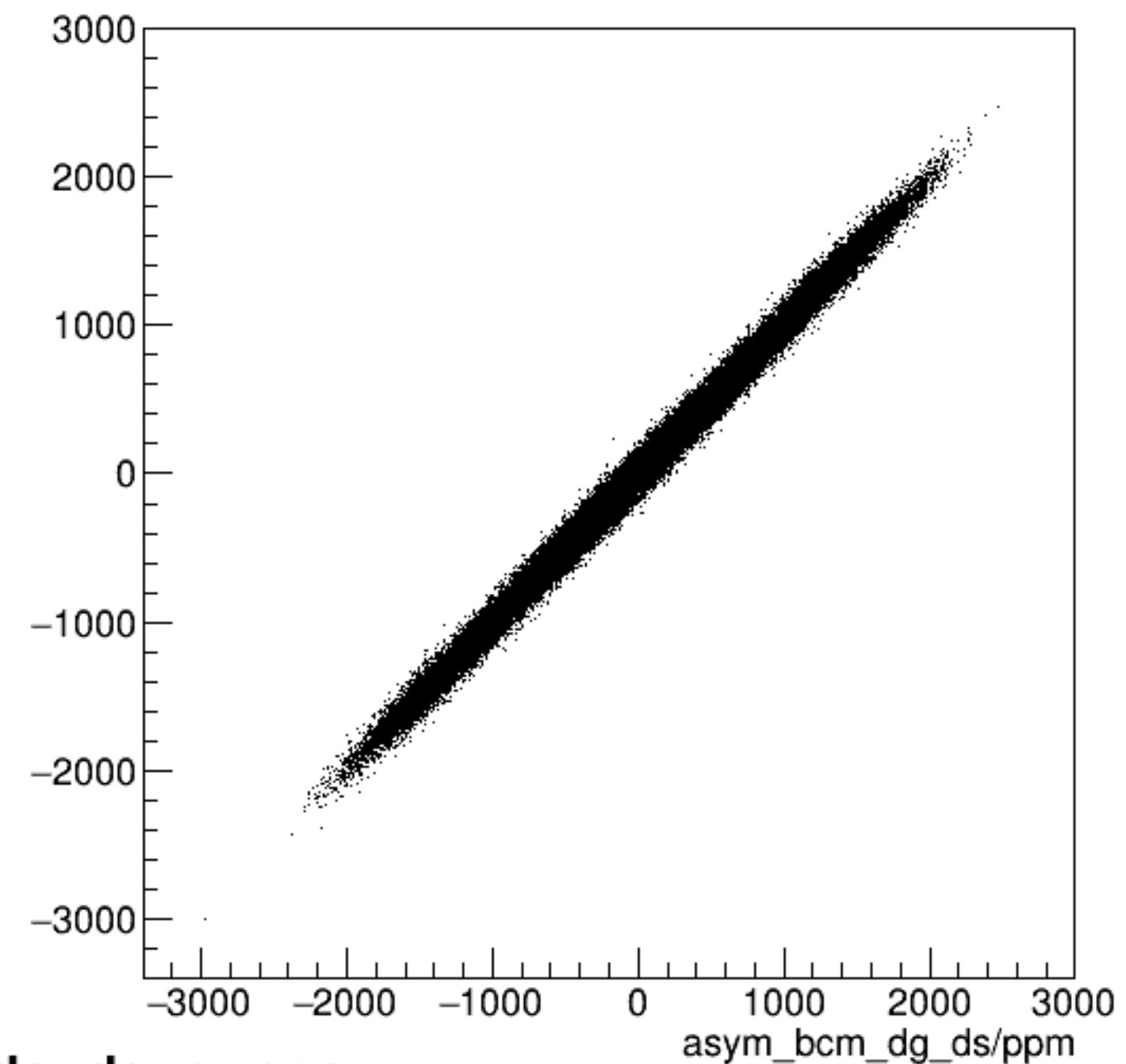
run8022\_000\_asym\_bcm\_an\_ds\_vs\_asym\_bcm\_an\_ds3\_pr.png

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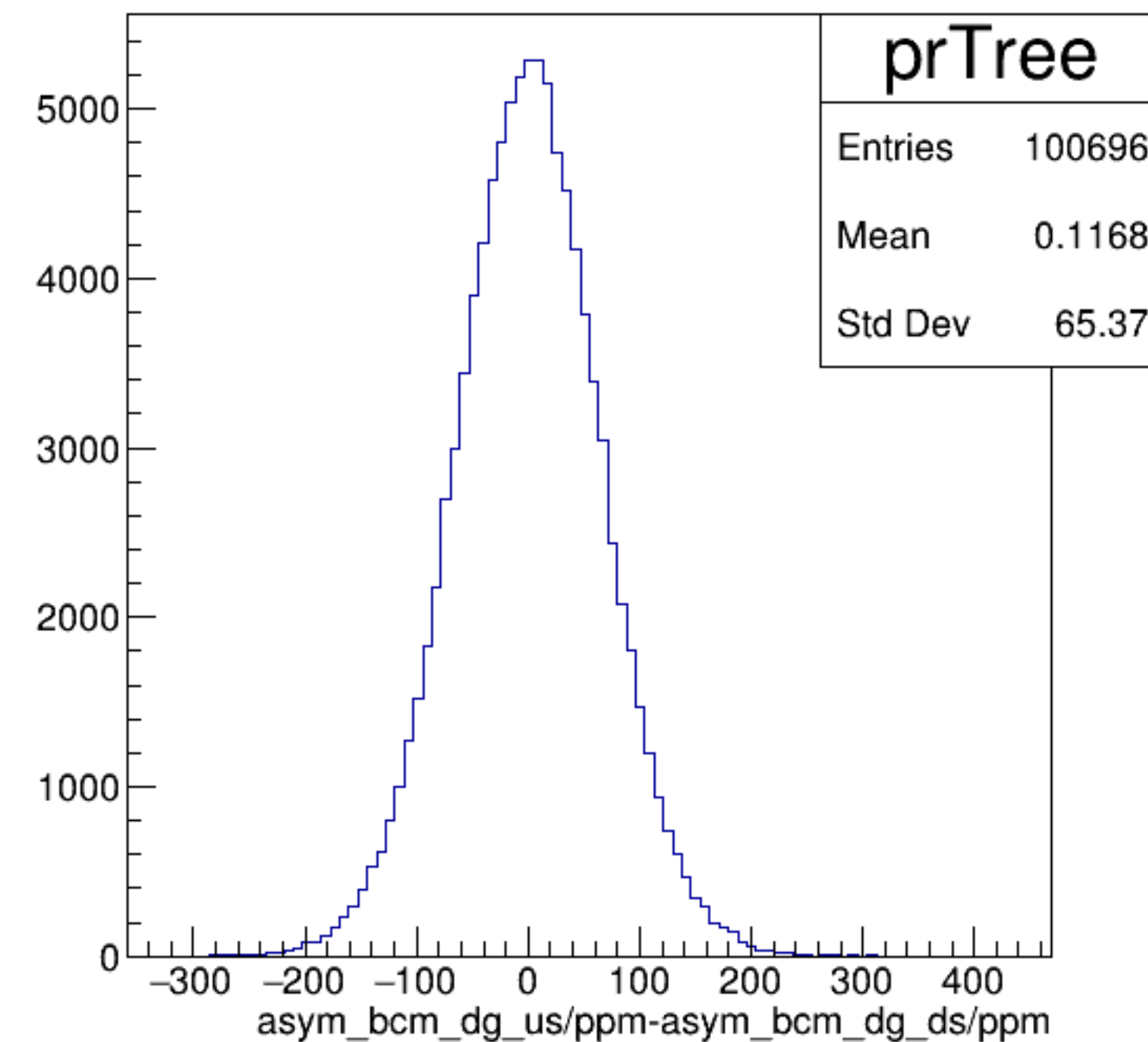


run8022\_000\_asym\_bcm\_dg\_us\_vs\_asym\_bcm\_dg\_ds\_pr.png

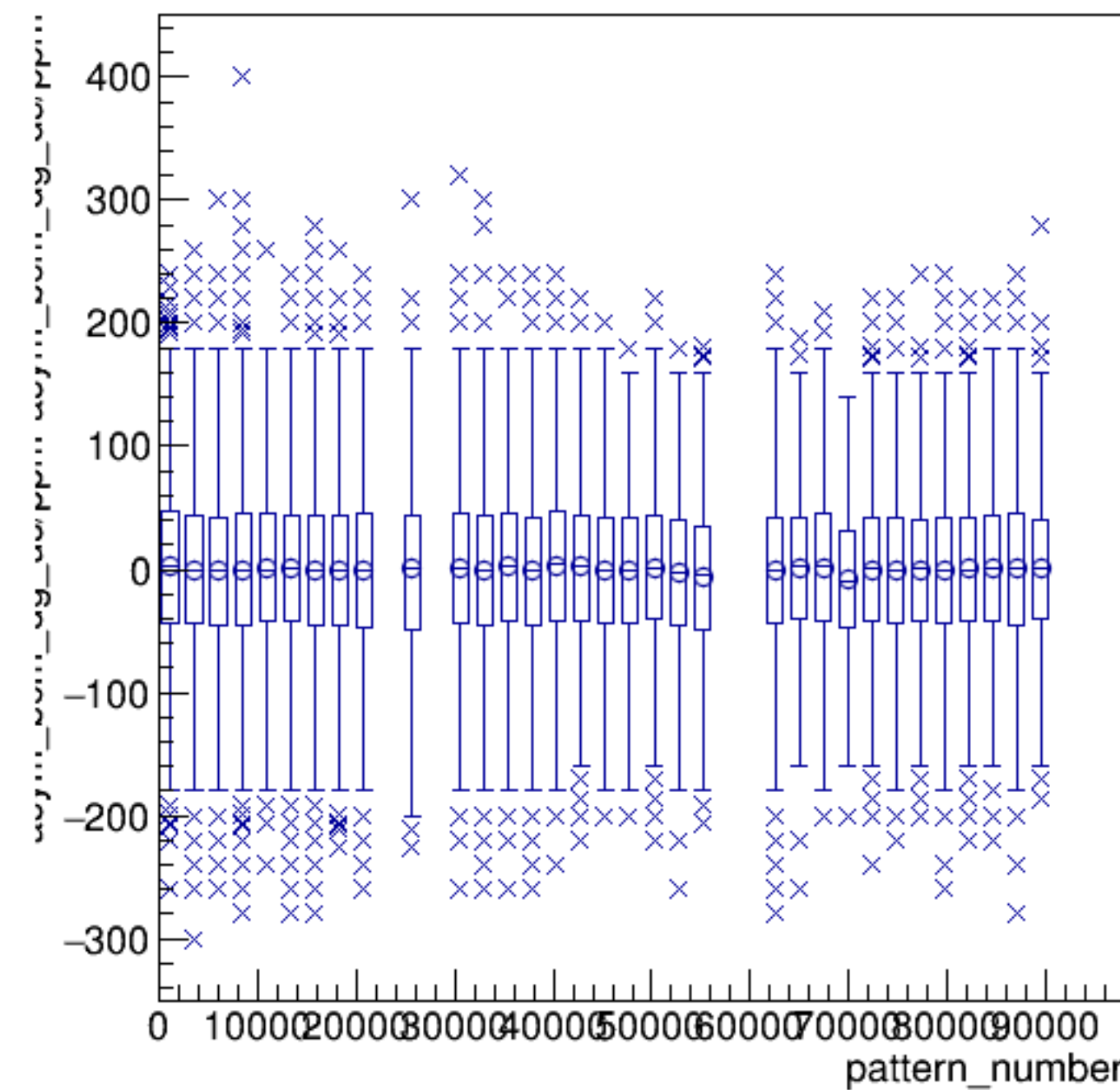
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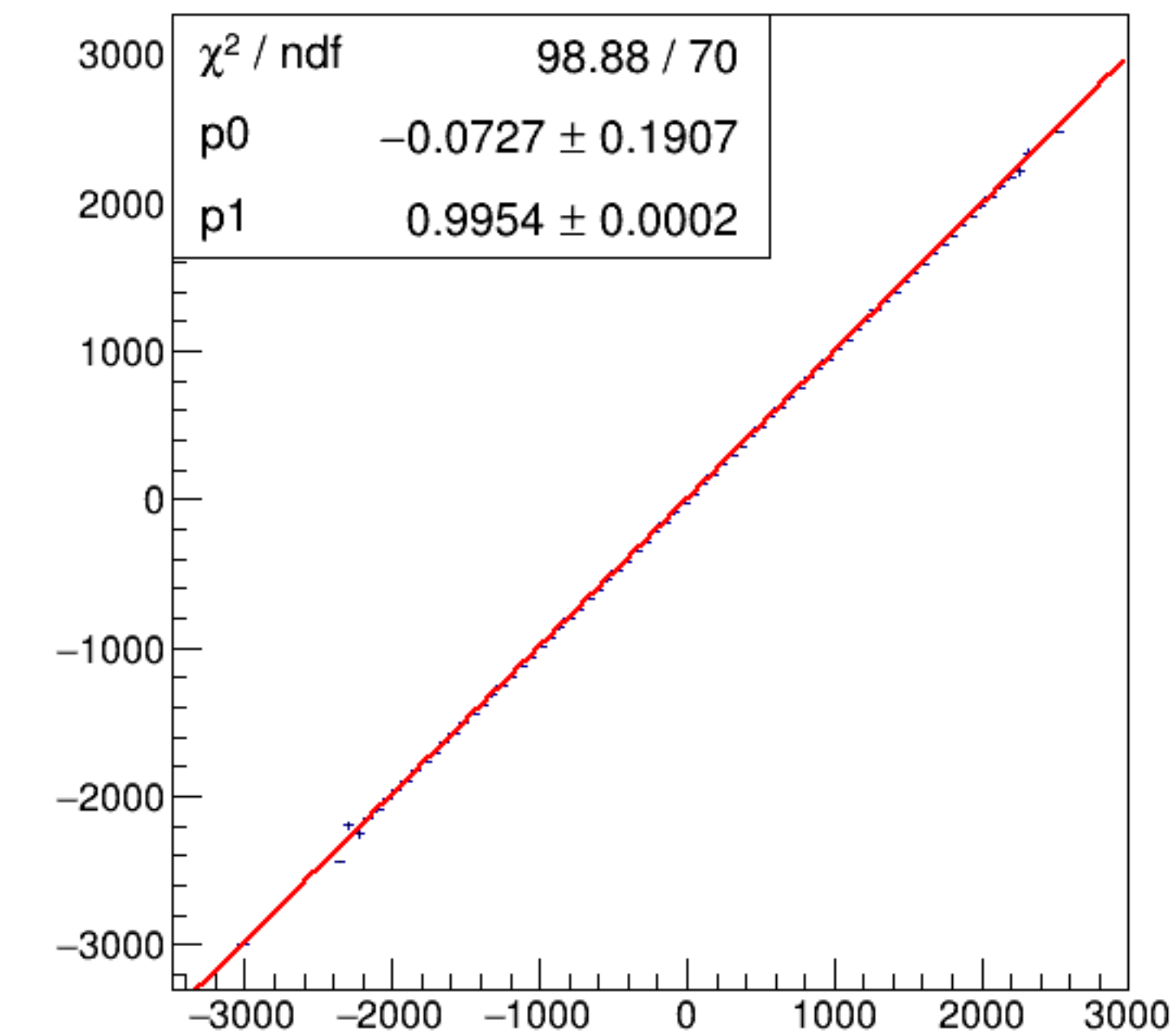
asym\_bcm\_dg\_us/ppm-asym\_bcm\_dg\_ds/ppm {ErrorFlag==0}



asym\_bcm\_dg\_us/ppm-asym\_bcm\_dg\_ds/ppm:pattern\_number {ErrorFlag==0}

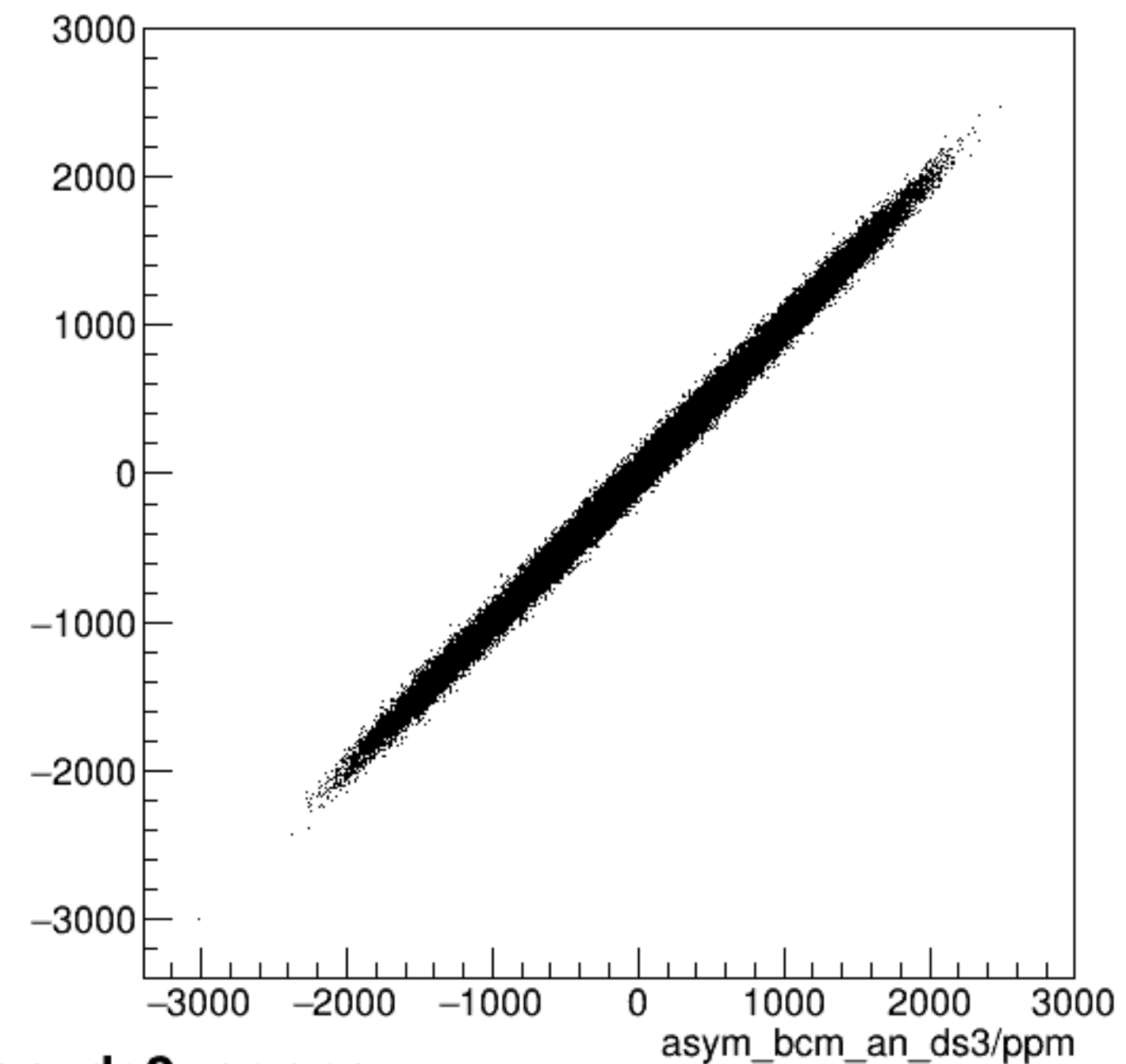


asym\_bcm\_dg\_us/ppm:asym\_bcm\_an\_ds3/ppm {ErrorFlag==0}

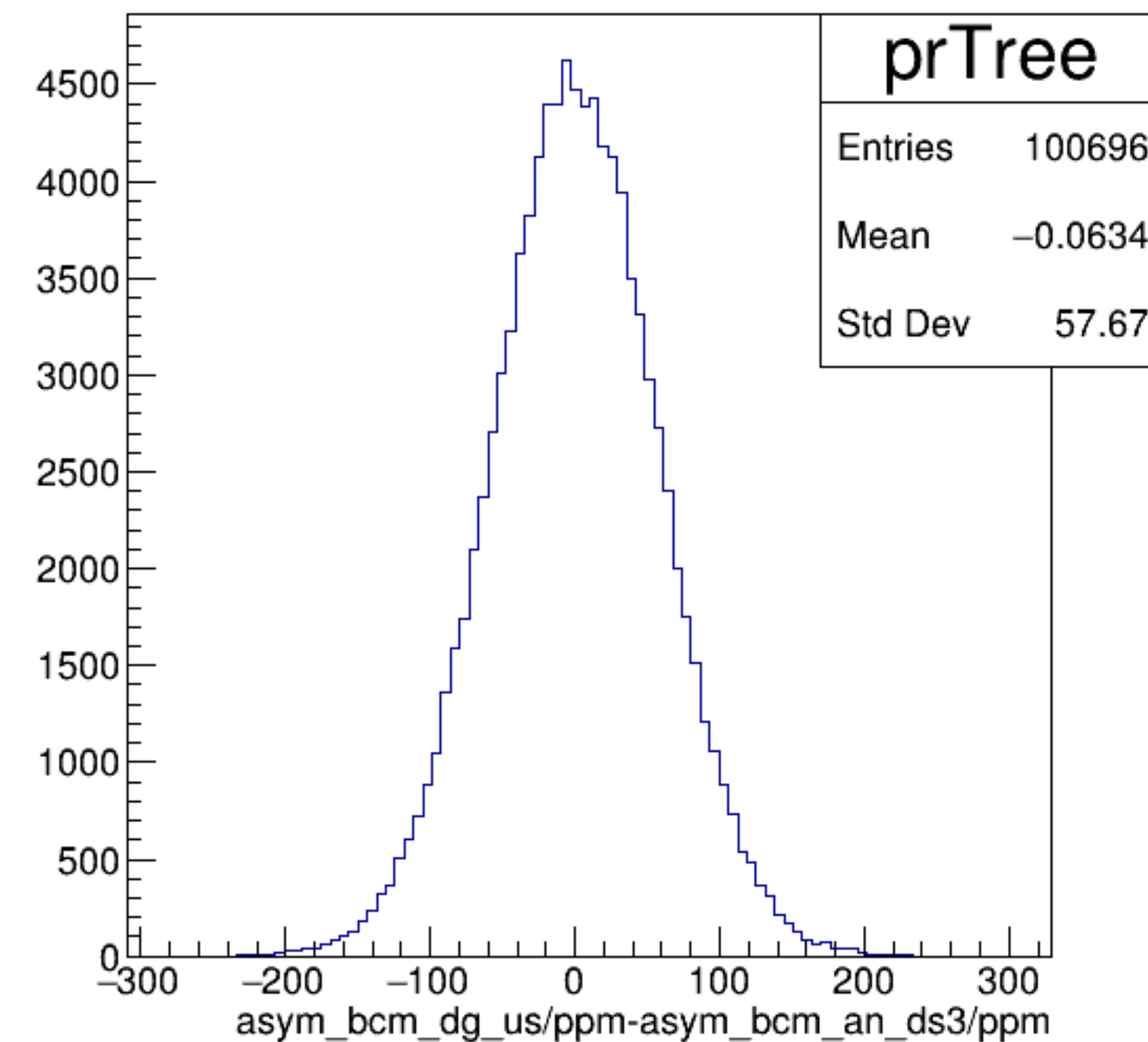


run8022\_000\_asym\_bcm\_dg\_us\_vs\_asym\_bcm\_an\_ds3\_pr.png

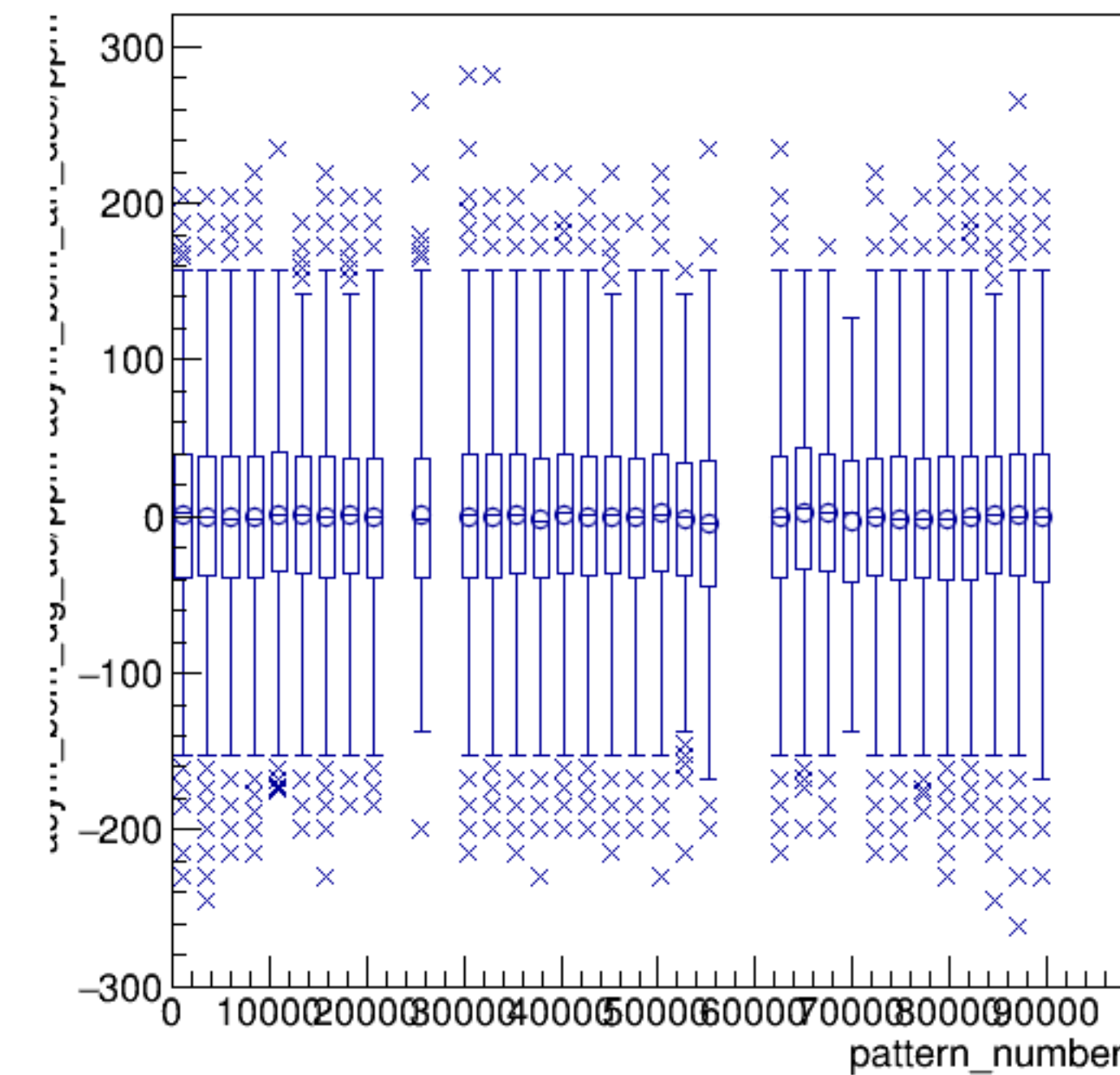
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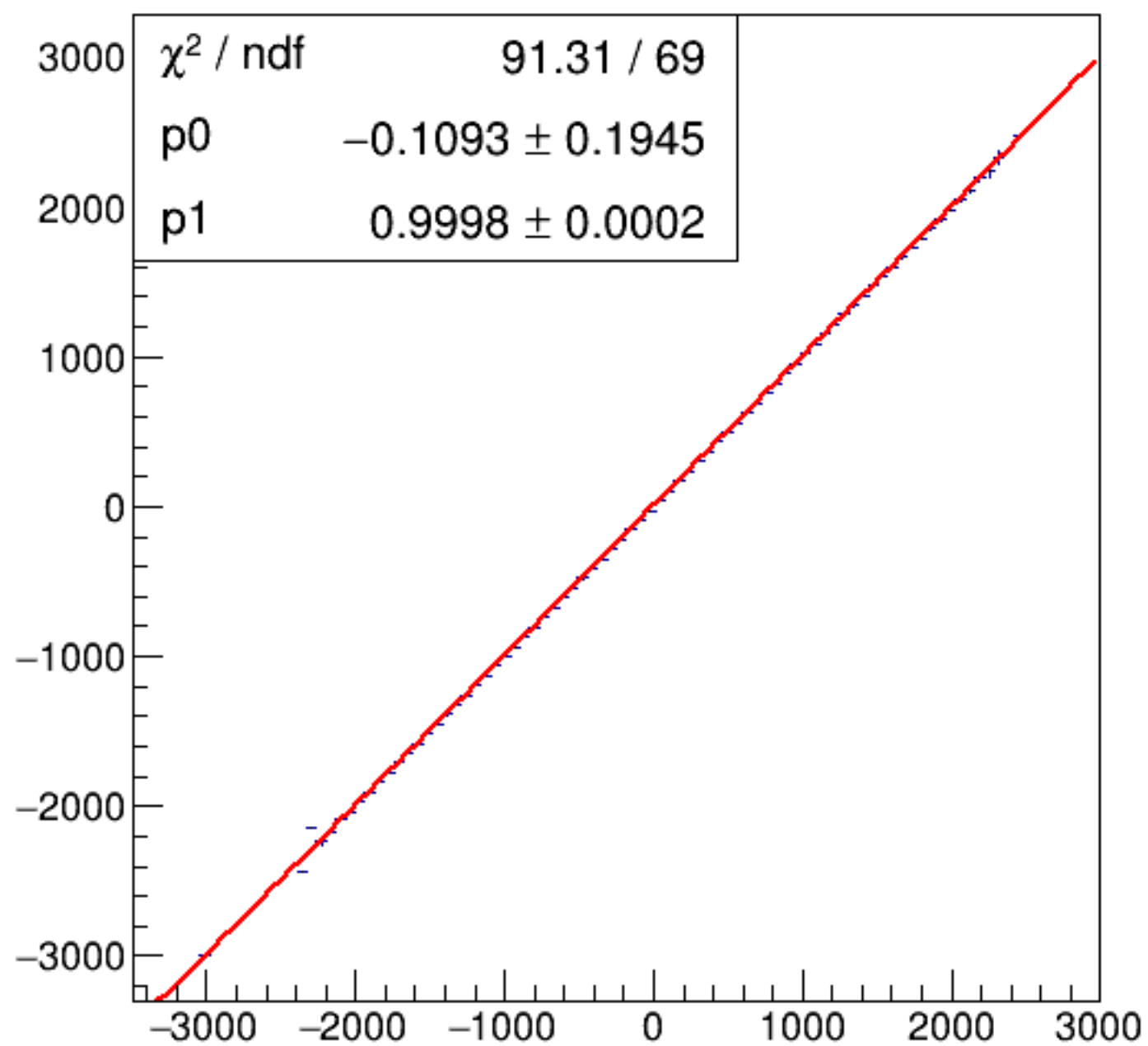
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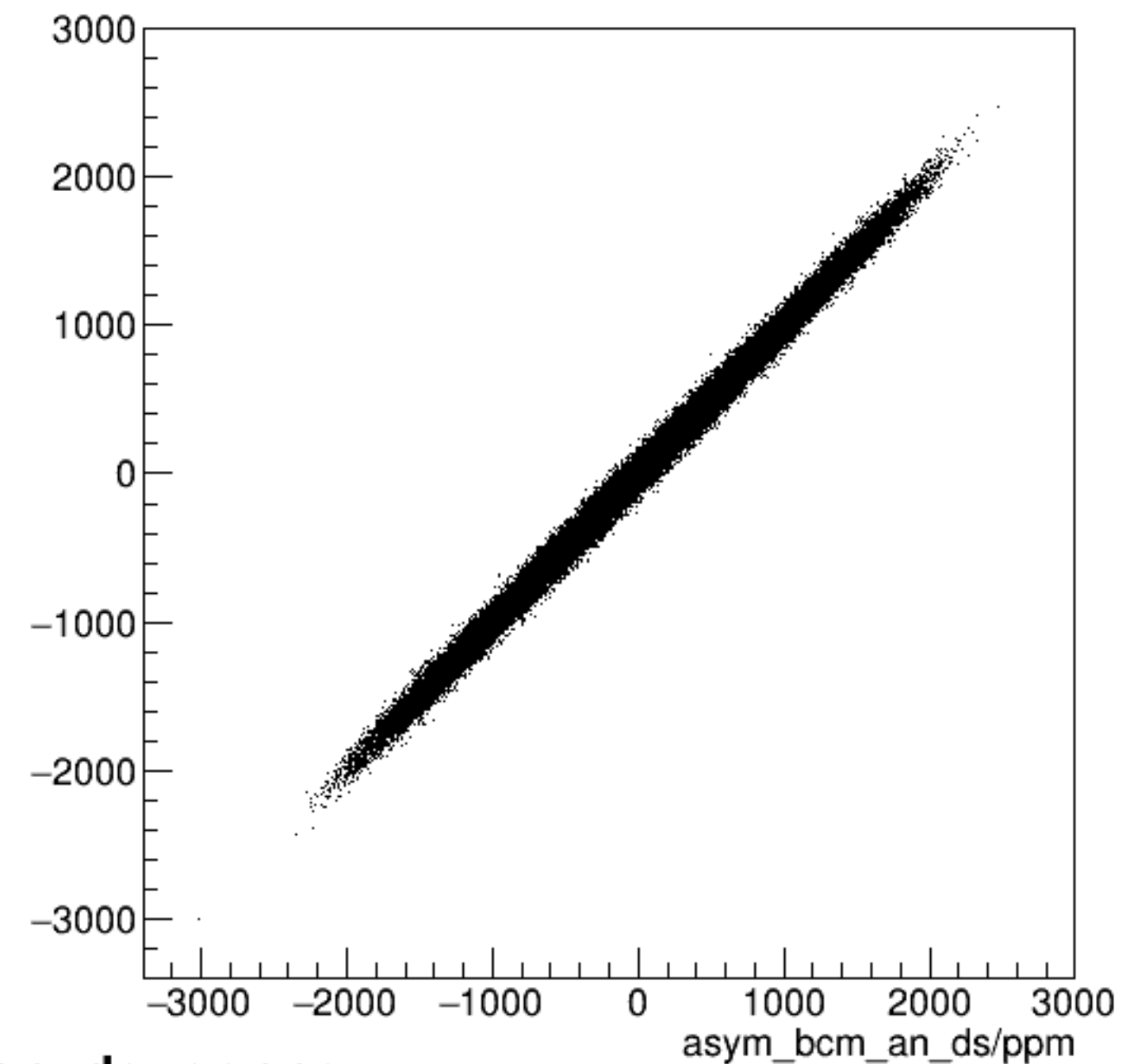
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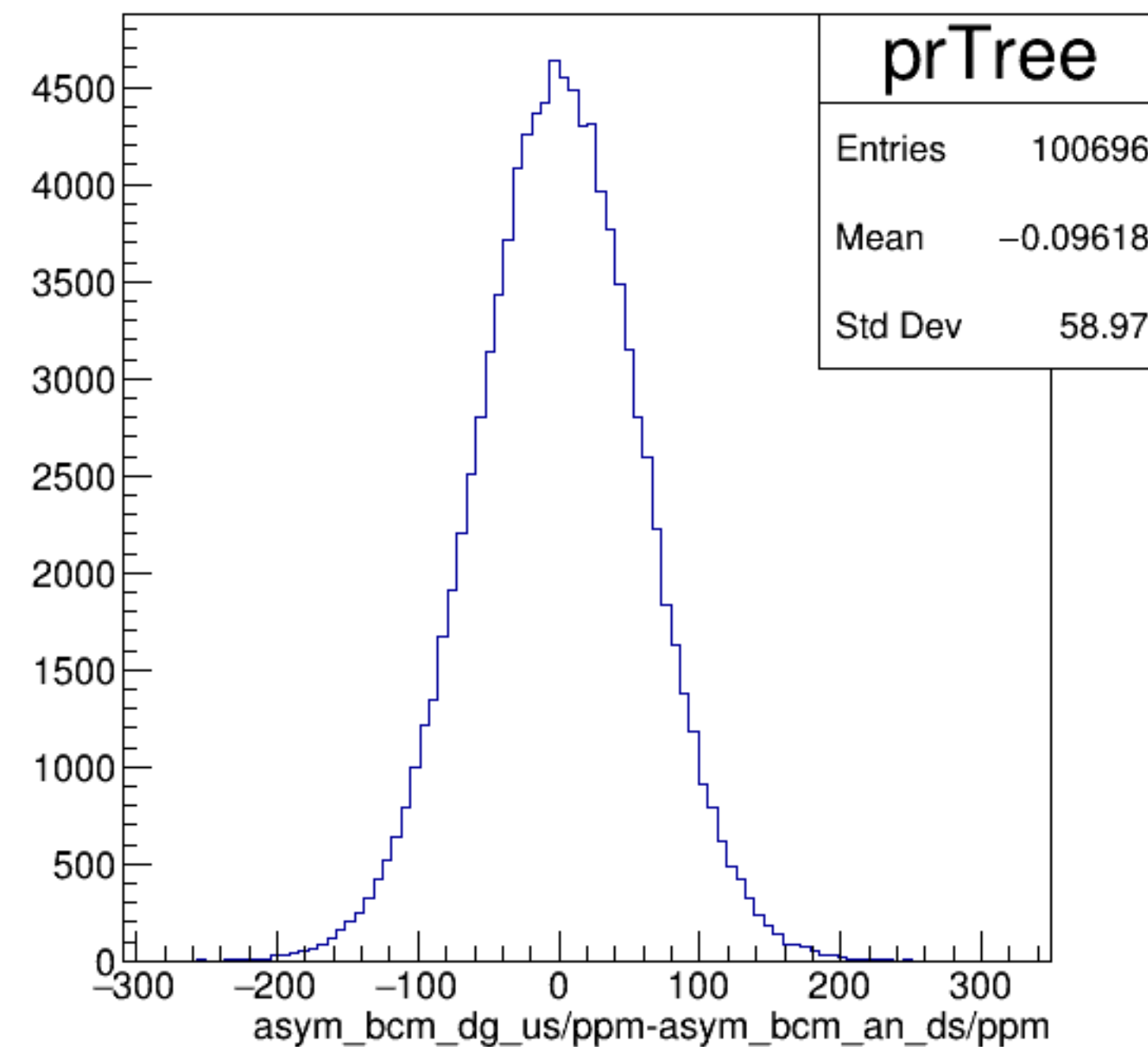
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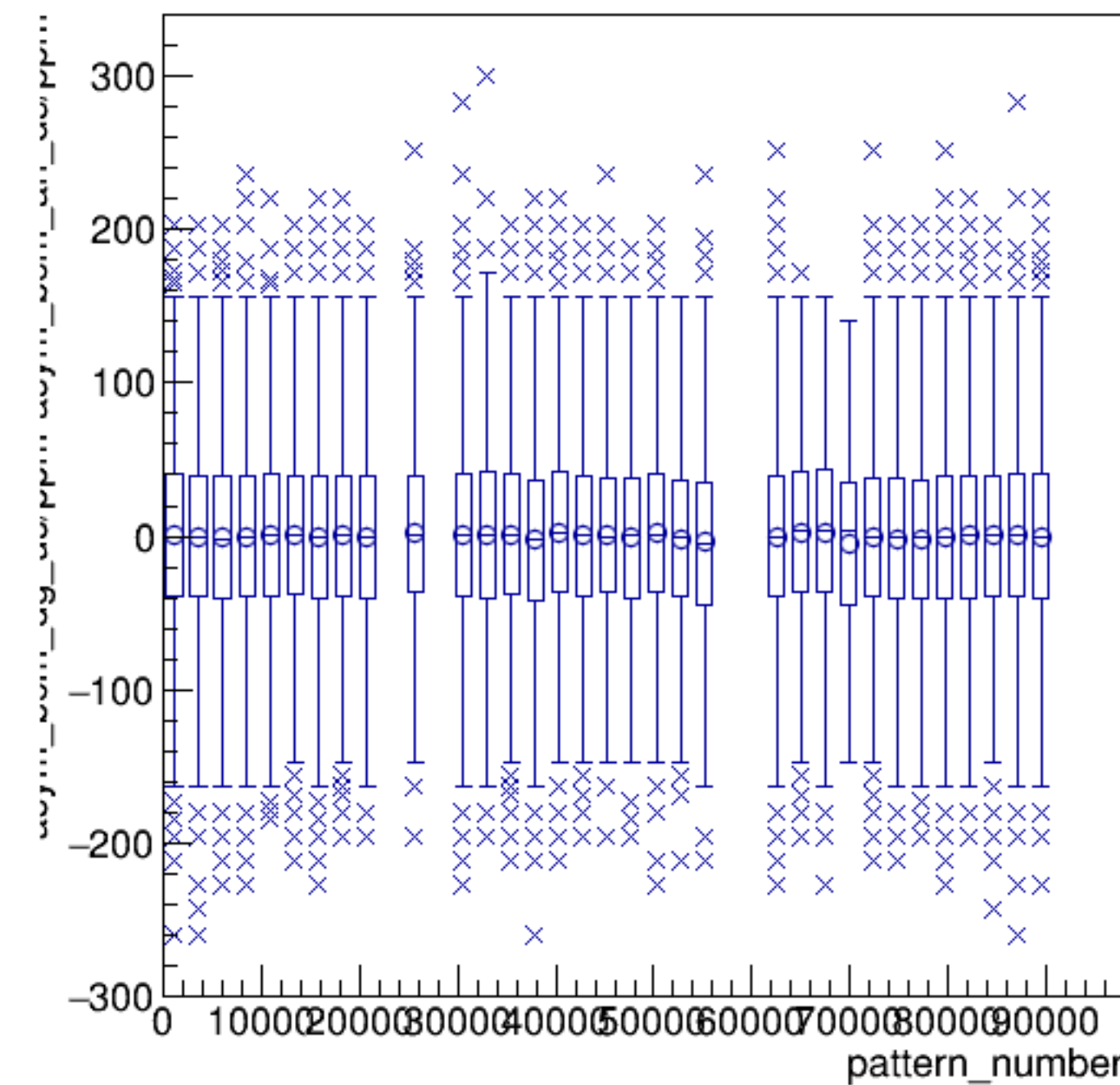
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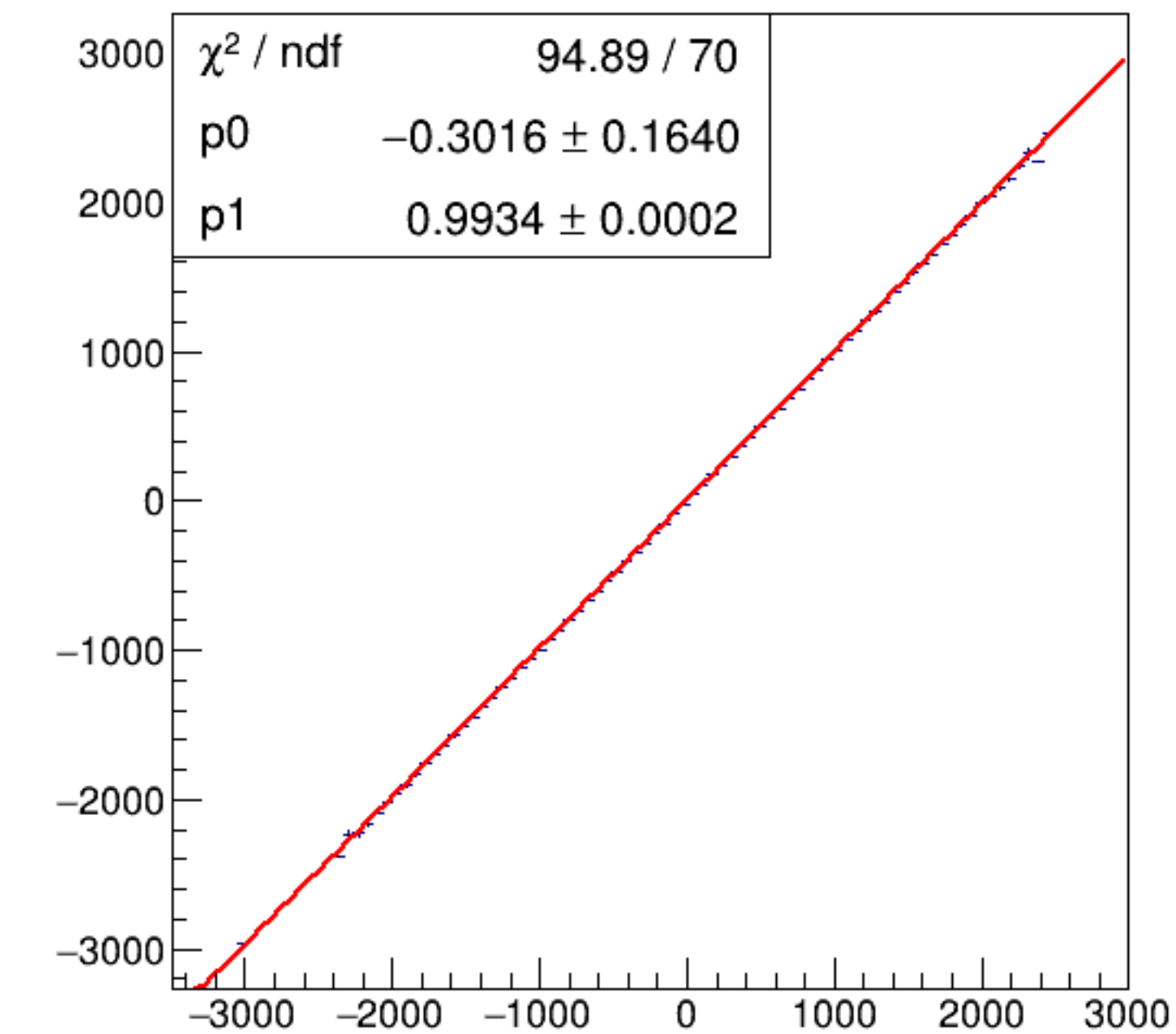
asym\_bcm\_dg\_us/ppm-asym\_bcm\_an\_ds/ppm {ErrorFlag==0}



asym\_bcm\_dg\_us/ppm-asym\_bcm\_an\_ds/ppm:pattern\_number {ErrorFlag==0}

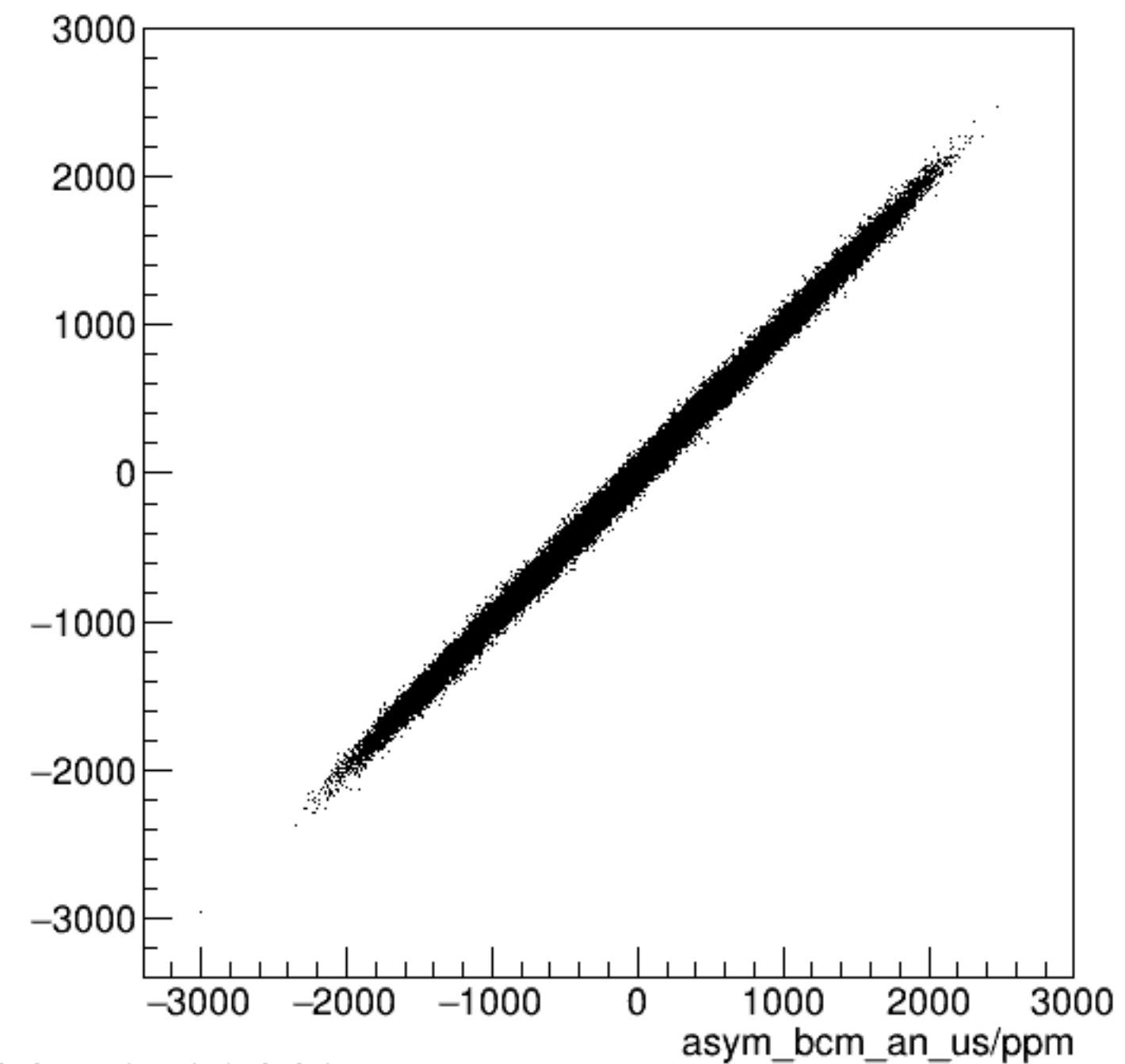


asym\_bcm\_dg\_ds/ppm:asym\_bcm\_an\_us/ppm {ErrorFlag==0}

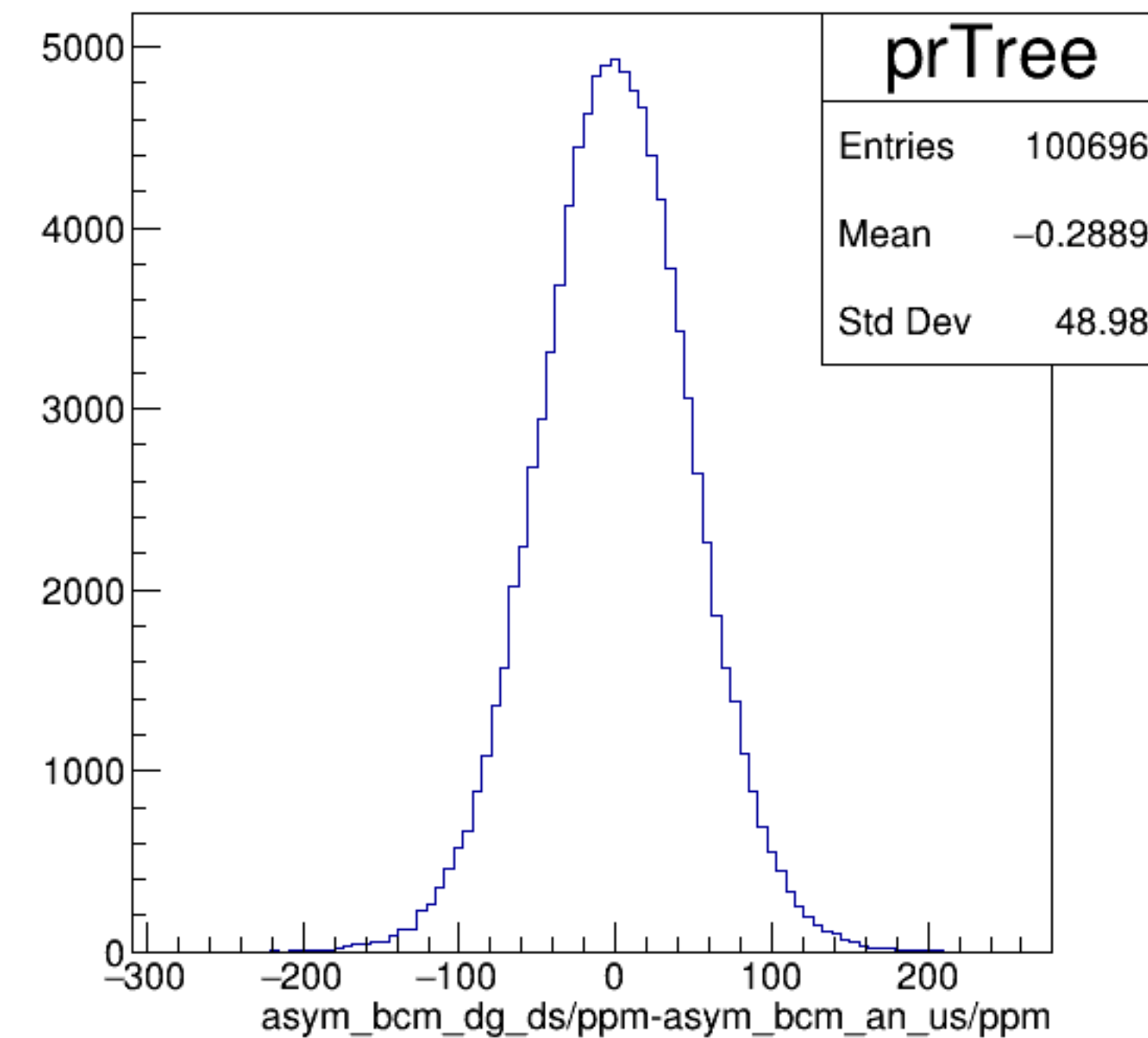


run8022\_000\_asym\_bcm\_dg\_ds\_vs\_asym\_bcm\_an\_us\_pr.png

asym\_bcm\_dg\_ds/ppm:asym\_bcm\_an\_us/ppm {ErrorFlag==0}



asym\_bcm\_dg\_ds/ppm-asym\_bcm\_an\_us/ppm {ErrorFlag==0}



asym\_bcm\_dg\_ds/ppm-asym\_bcm\_an\_us/ppm:pattern\_number {ErrorFlag==0}

