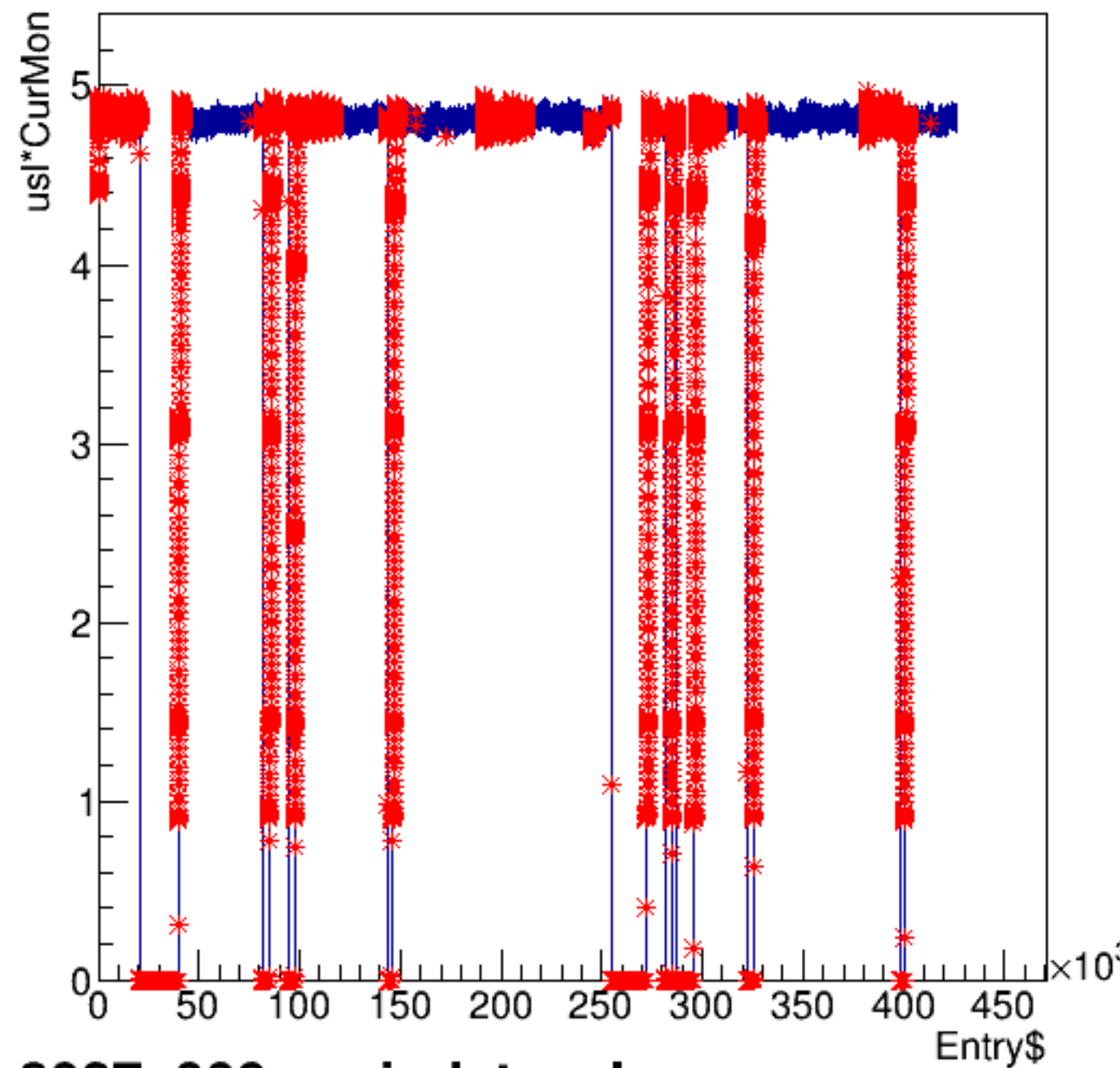
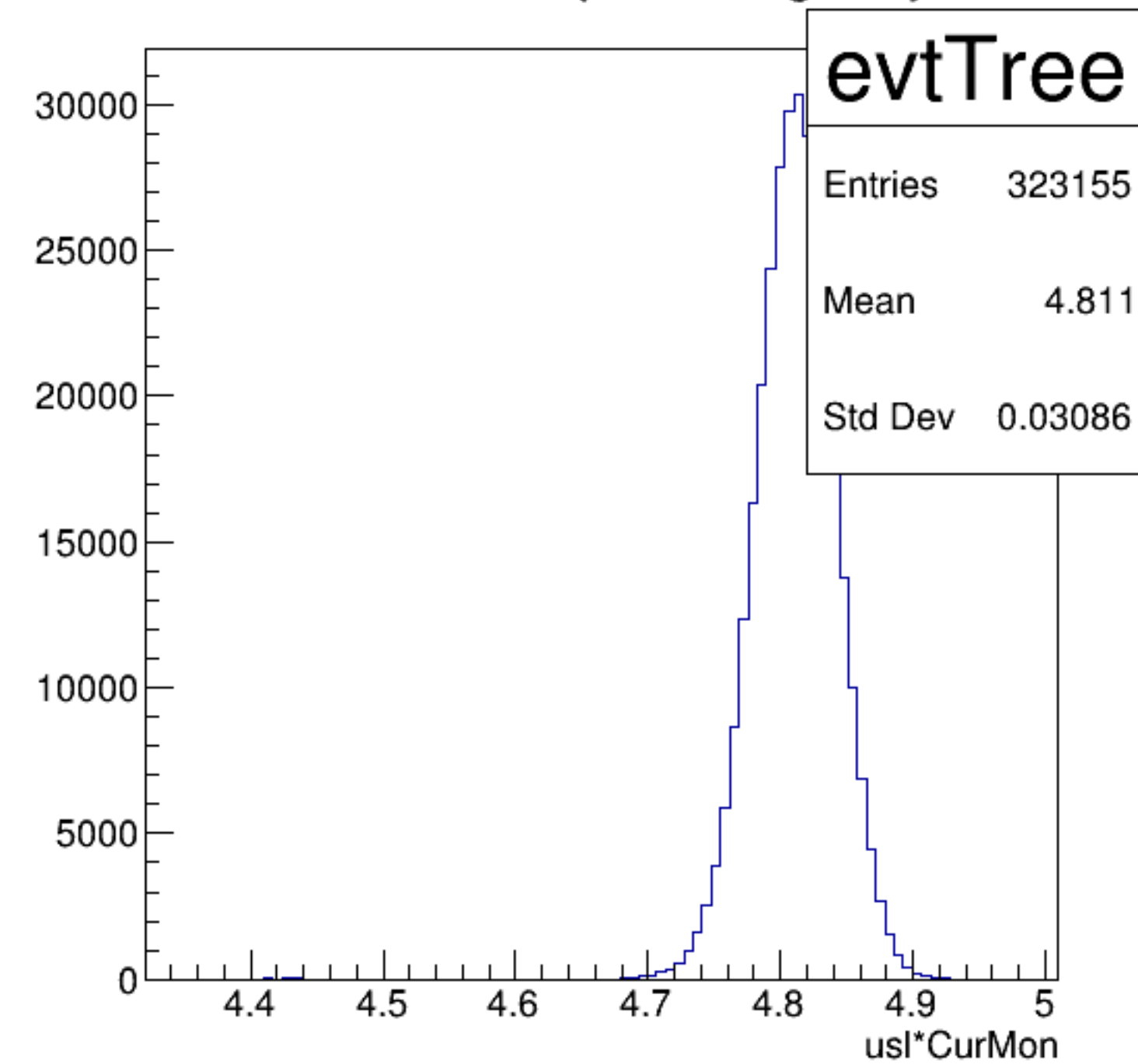


usl*CurMon:Entry\$

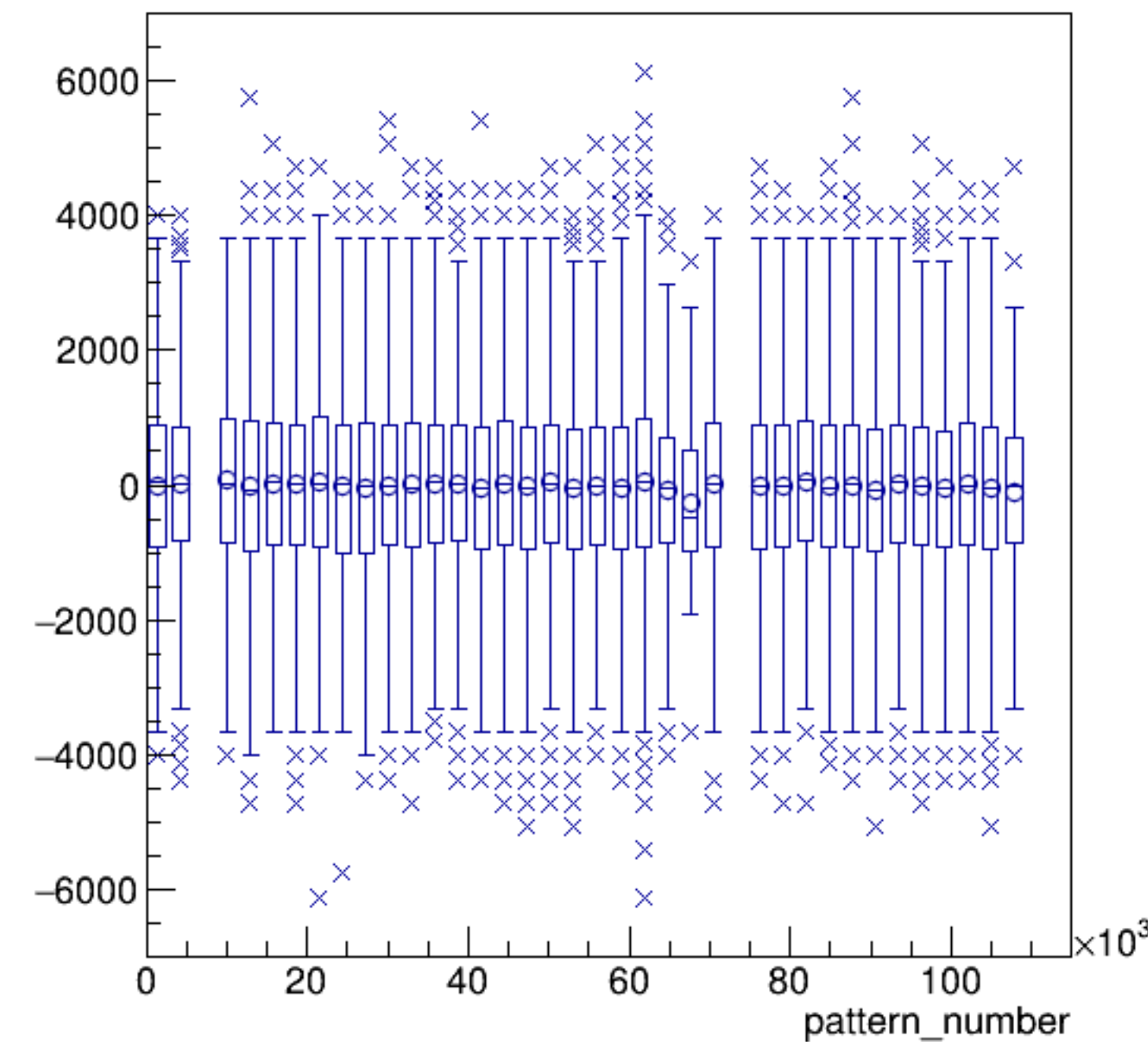


run8027_000_maindet_usl.png

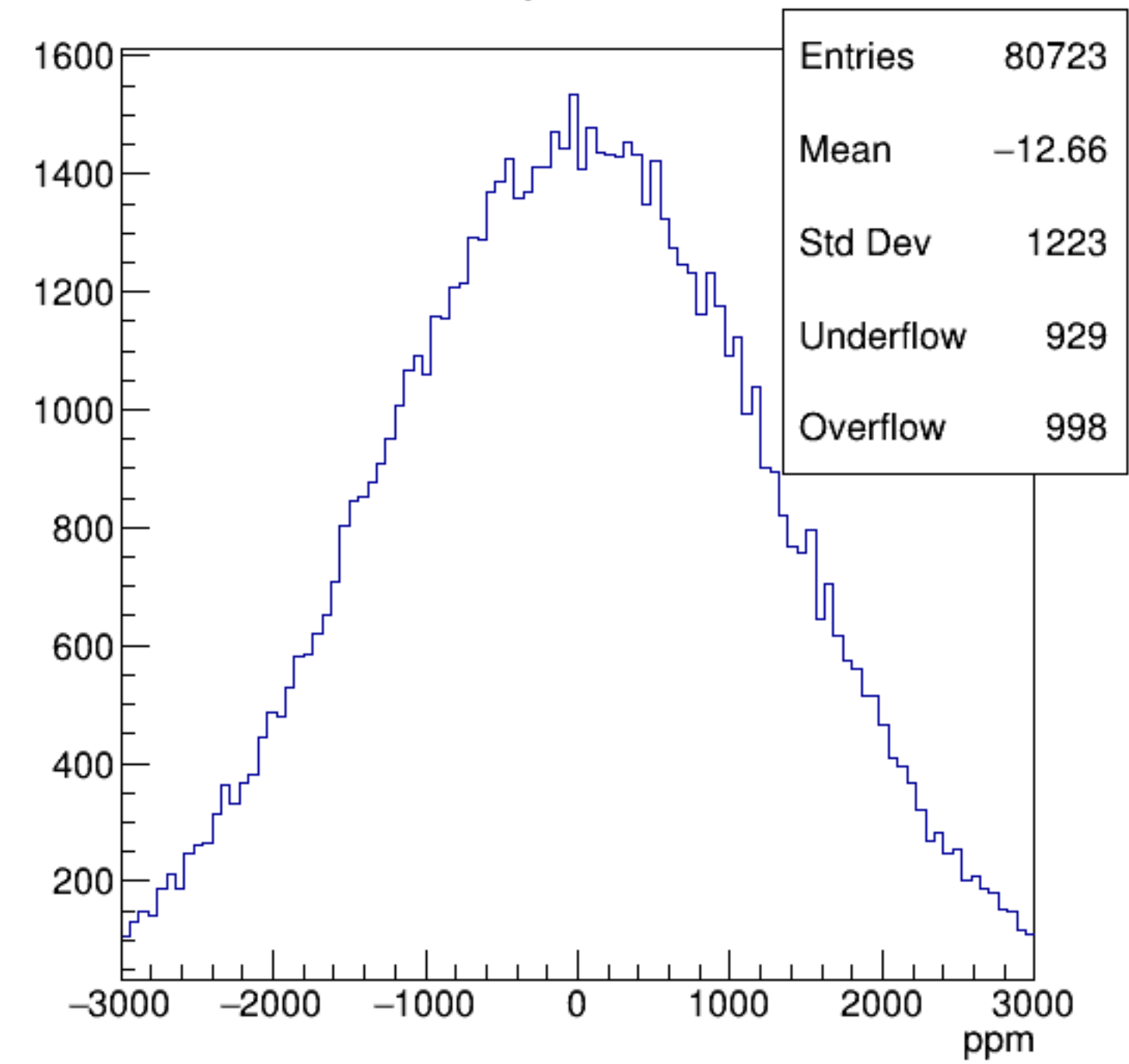
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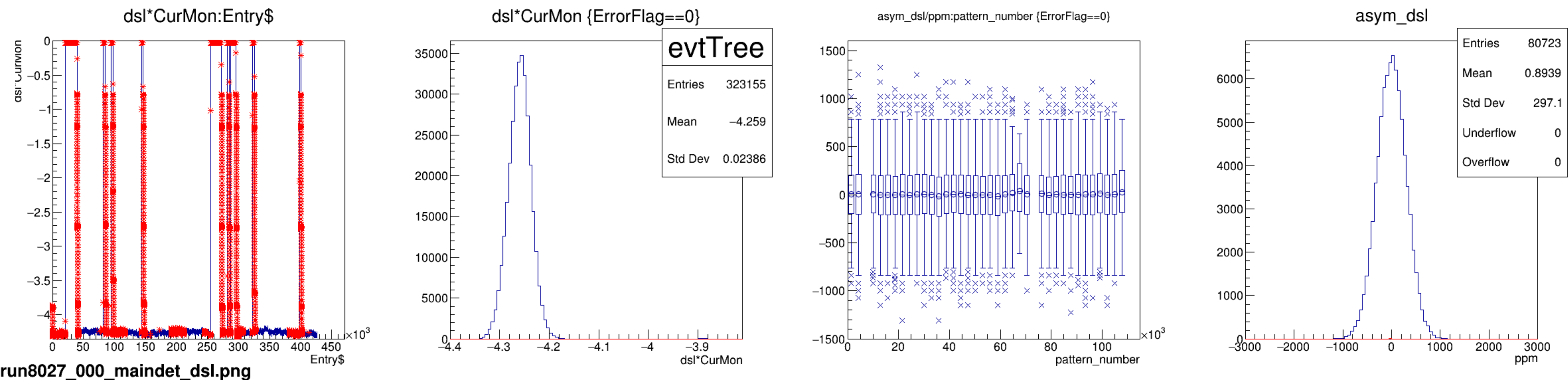


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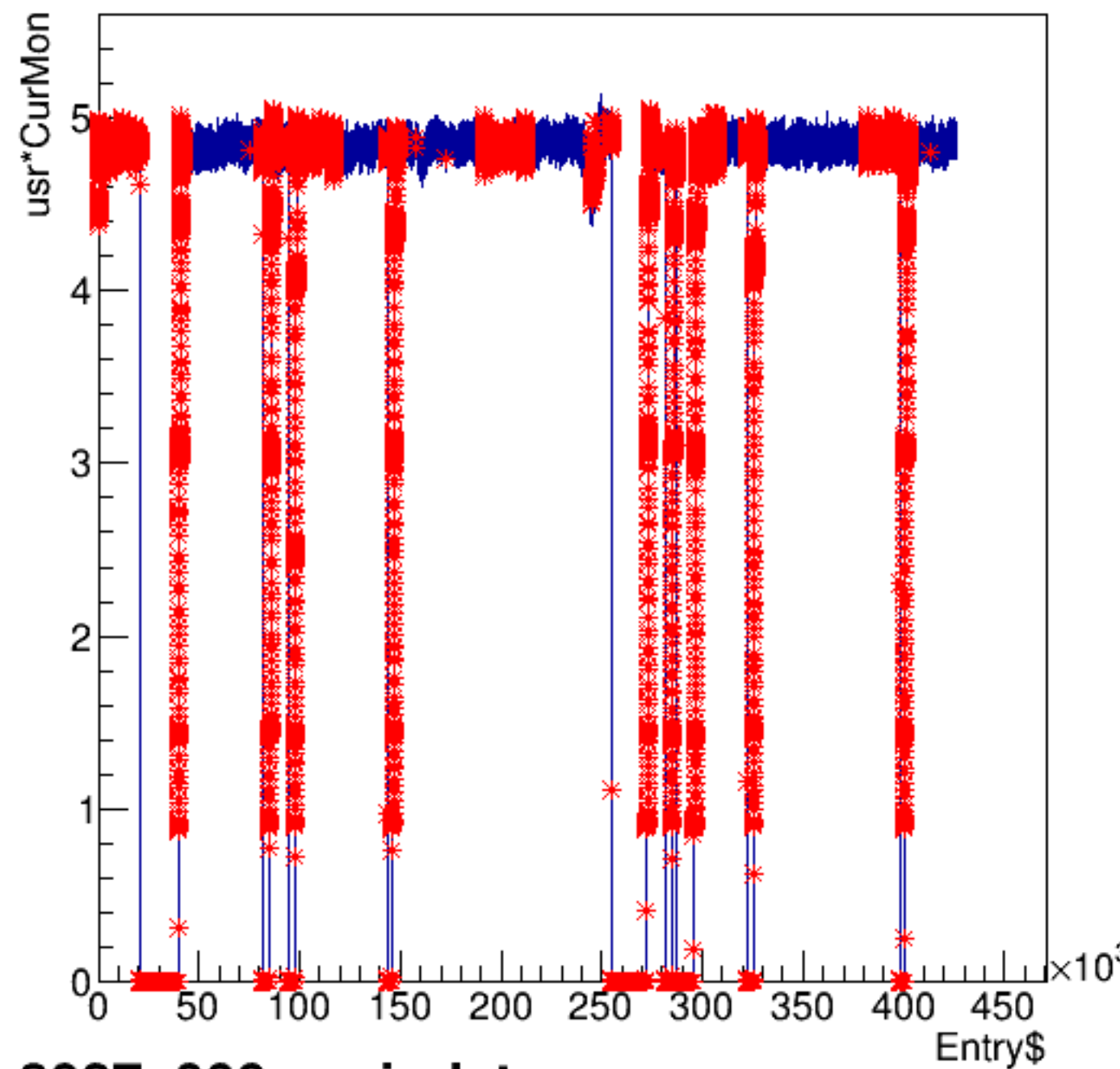


asym_usl



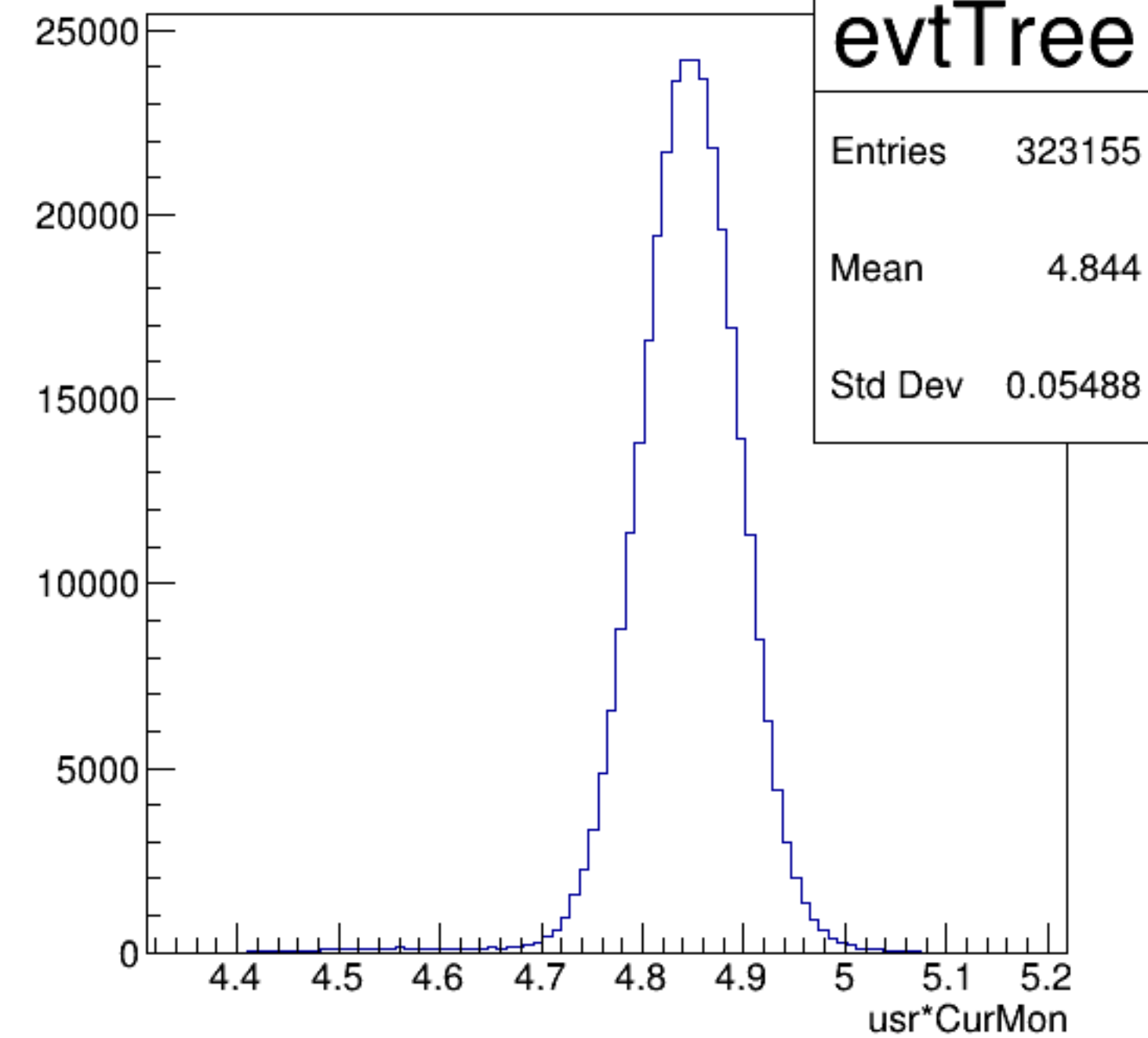


usr*CurMon:Entry\$

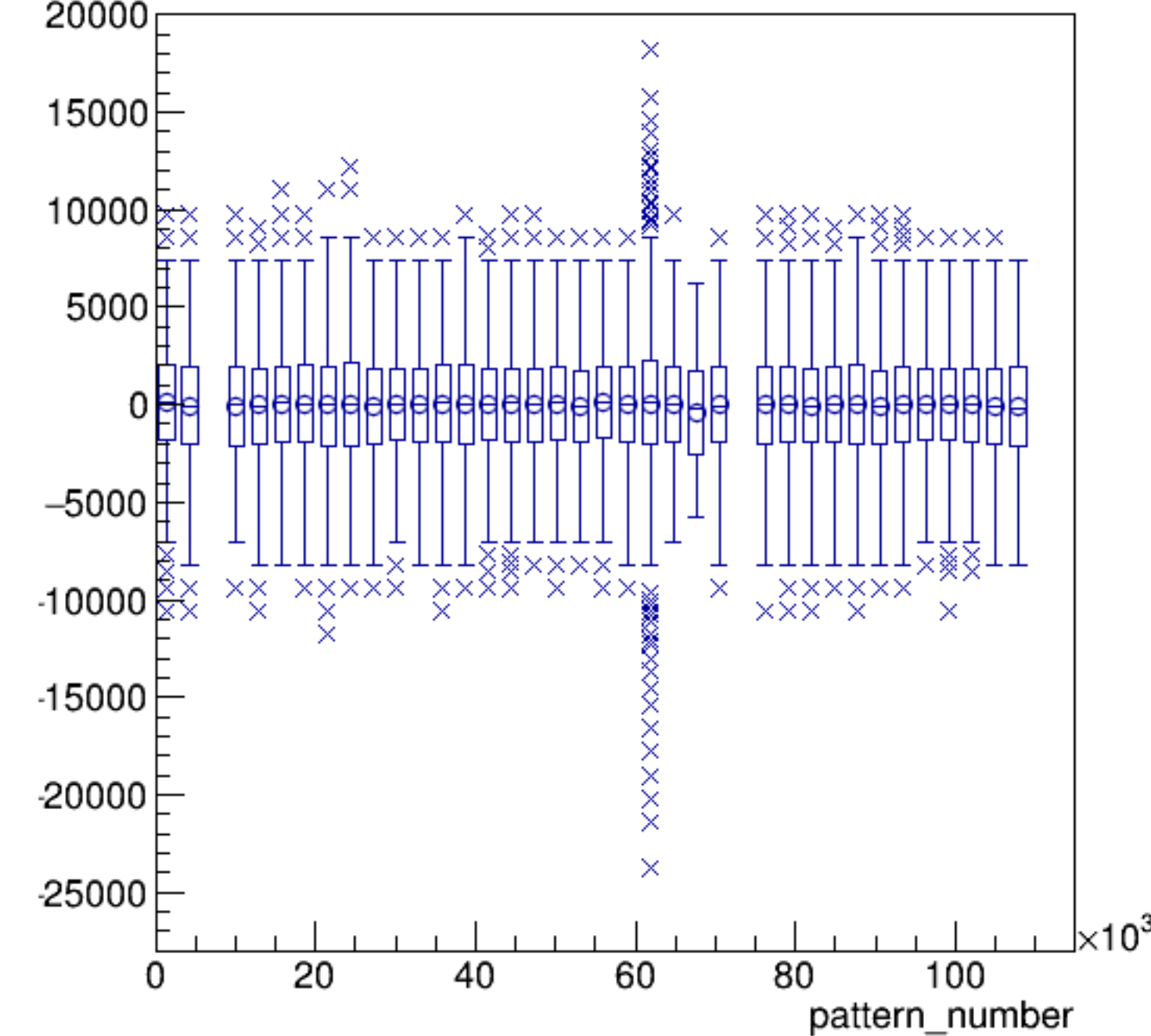


run8027_000_maindet_usr.png

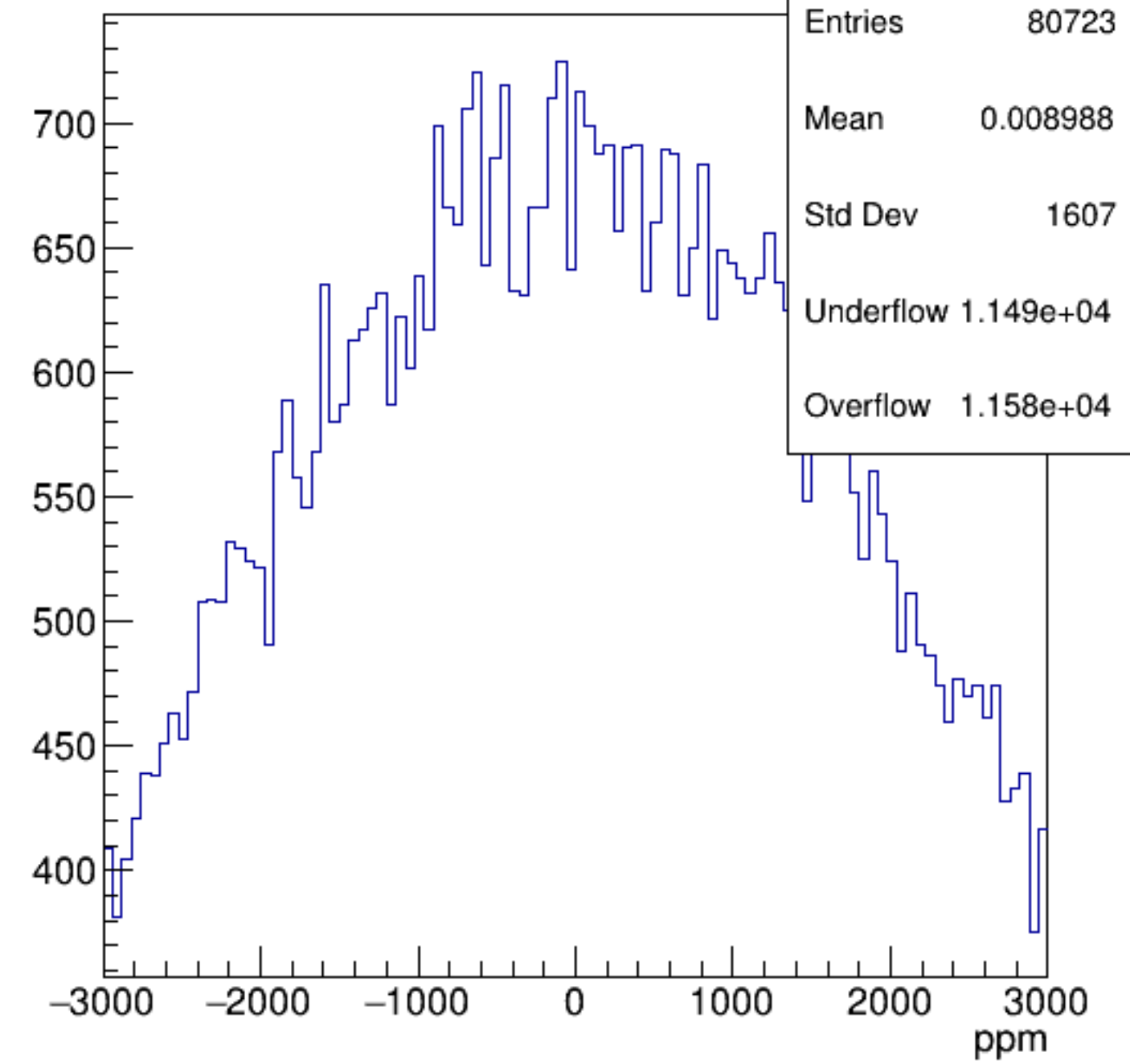
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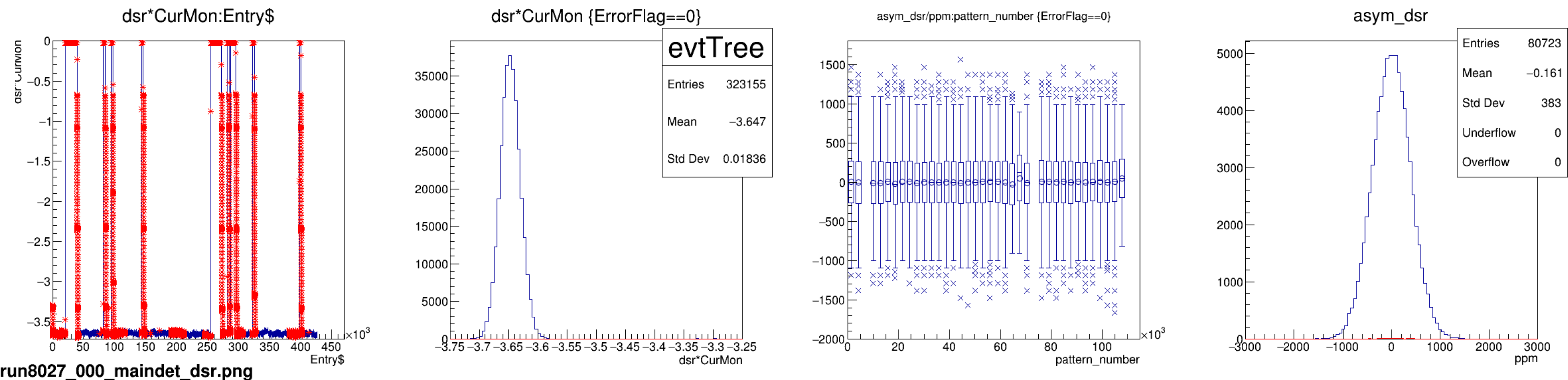


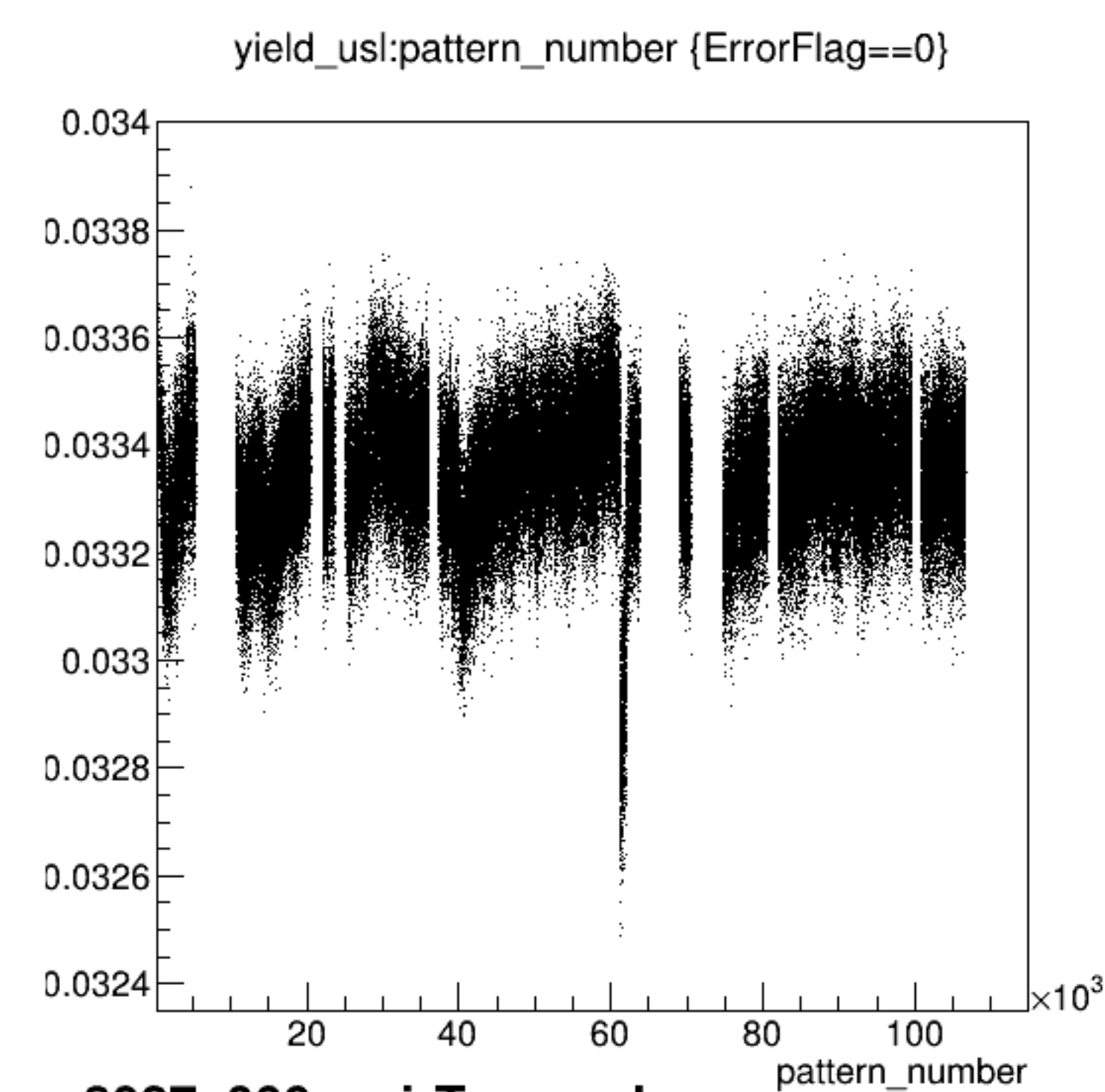
asym_usr/ppm:pattern_number {ErrorFlag==0}



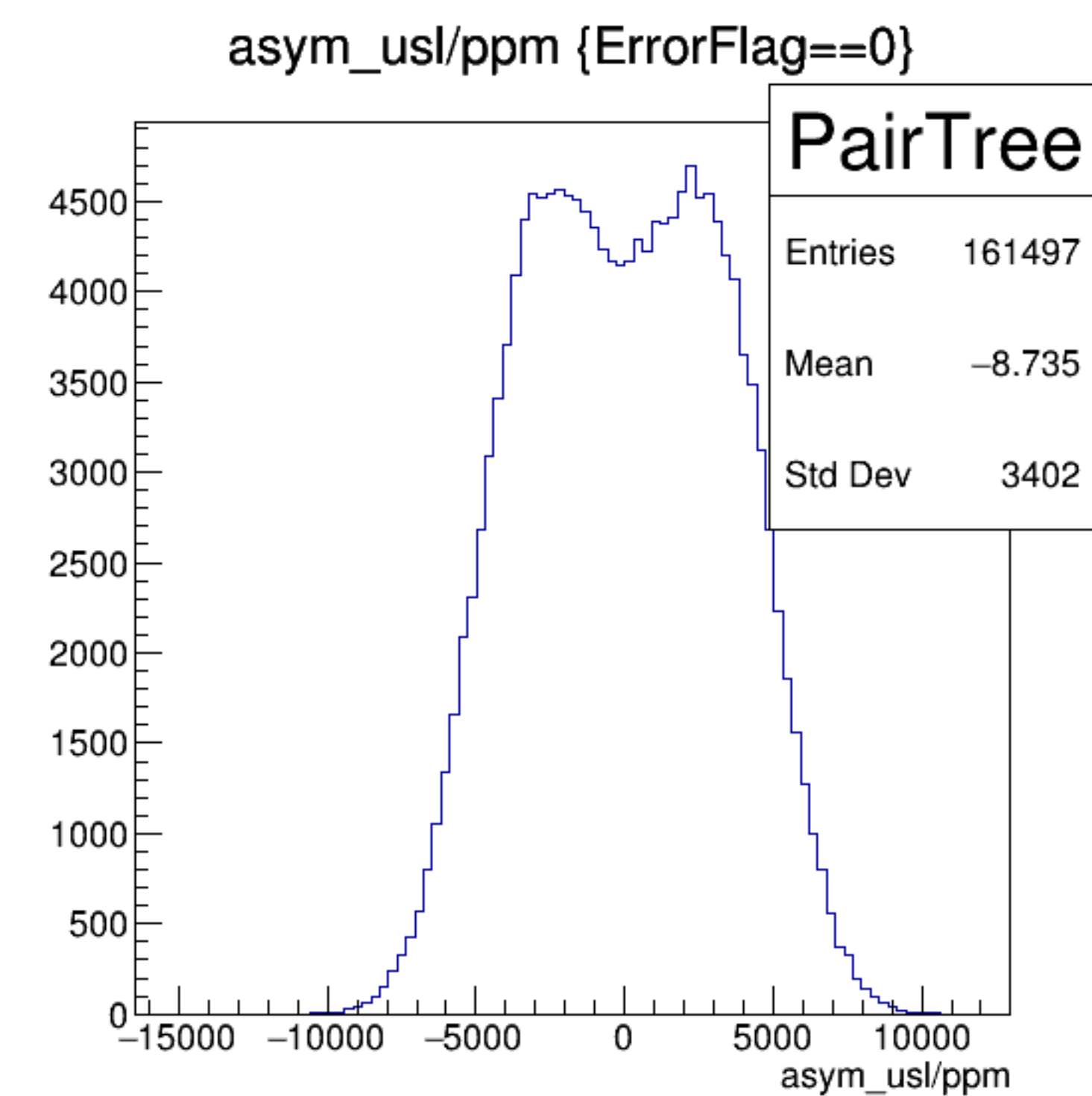
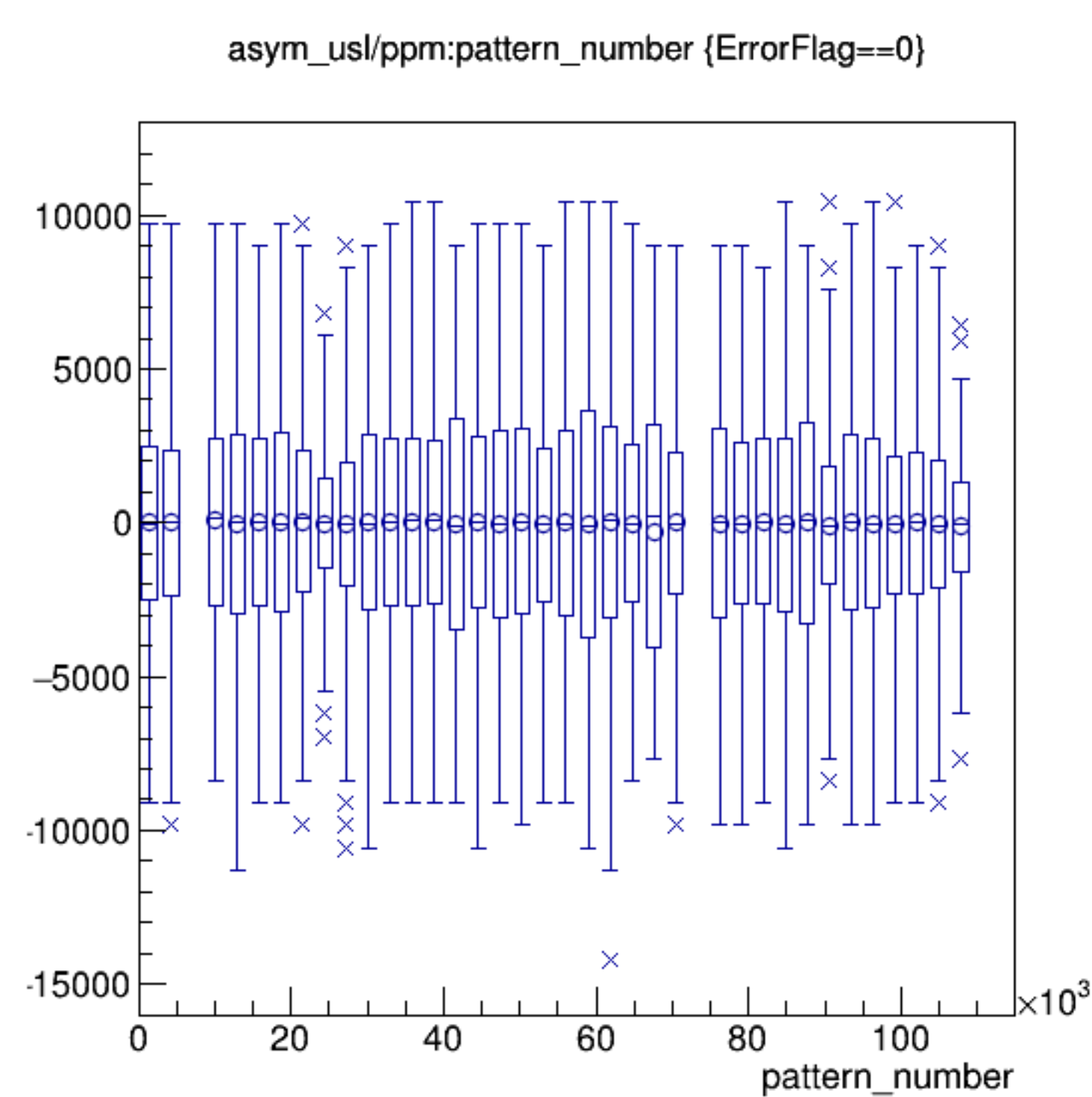
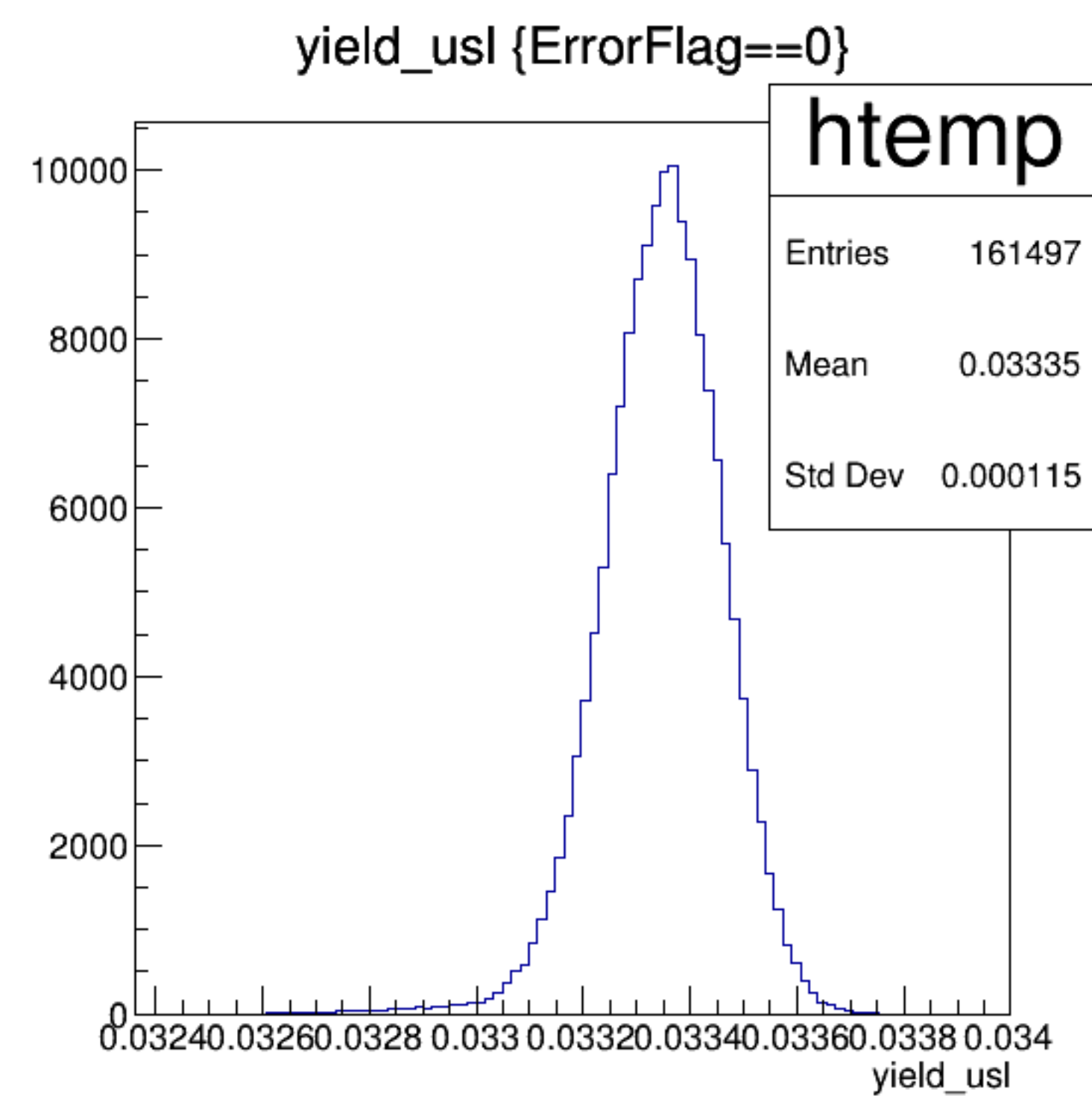
asym_usr



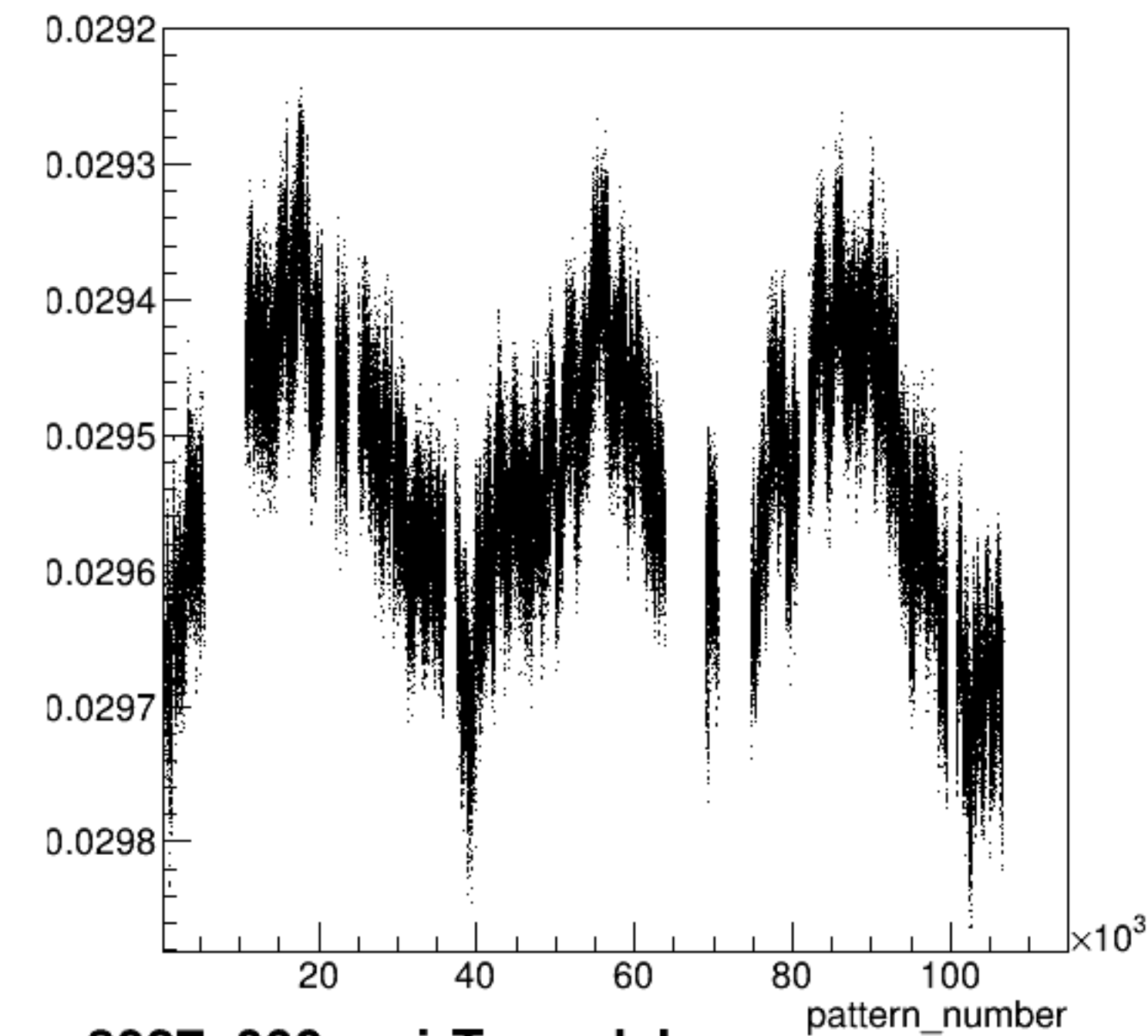




run8027_000_pairTree_usl.png

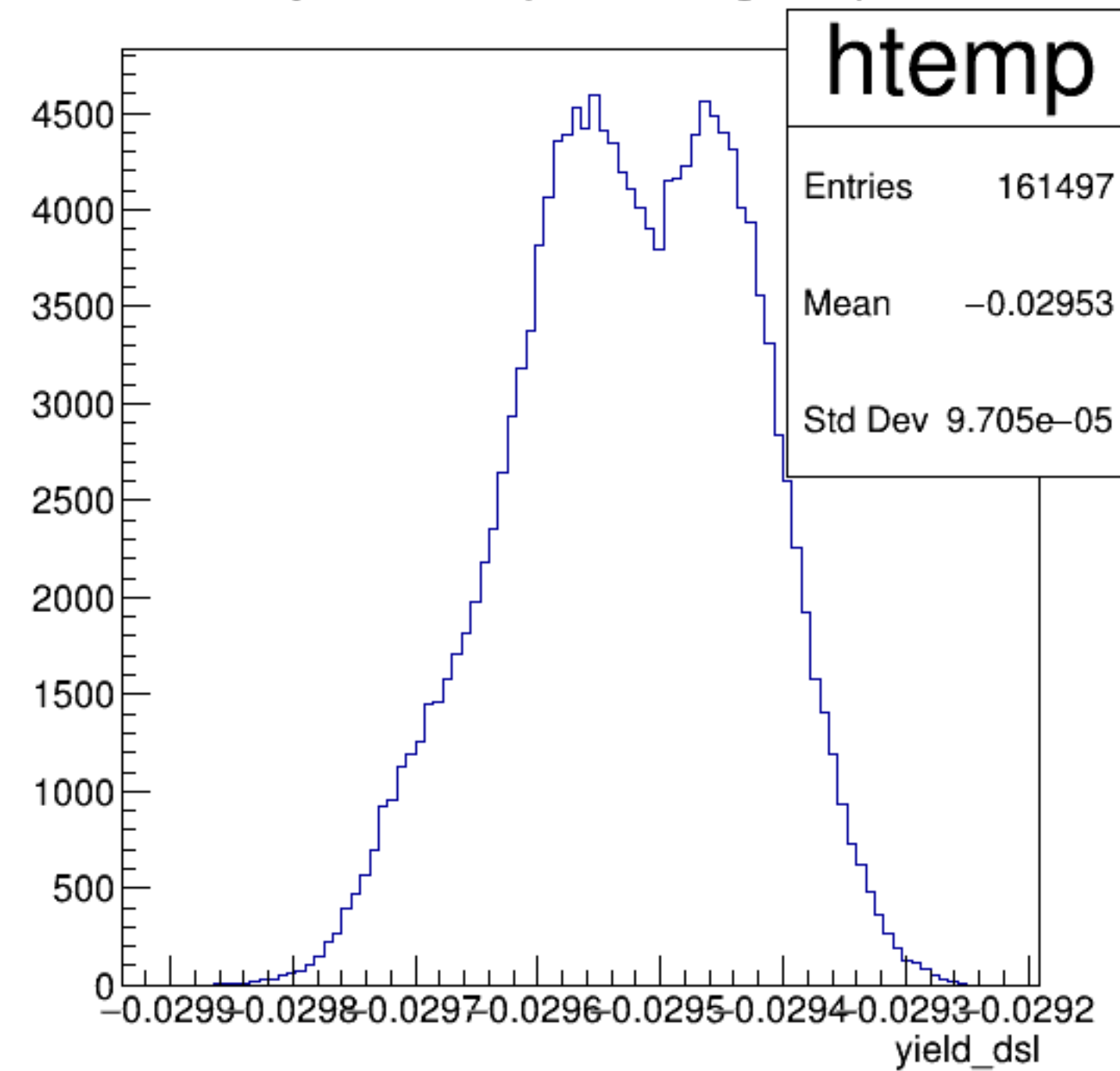


yield_dsl:pattern_number {ErrorFlag==0}

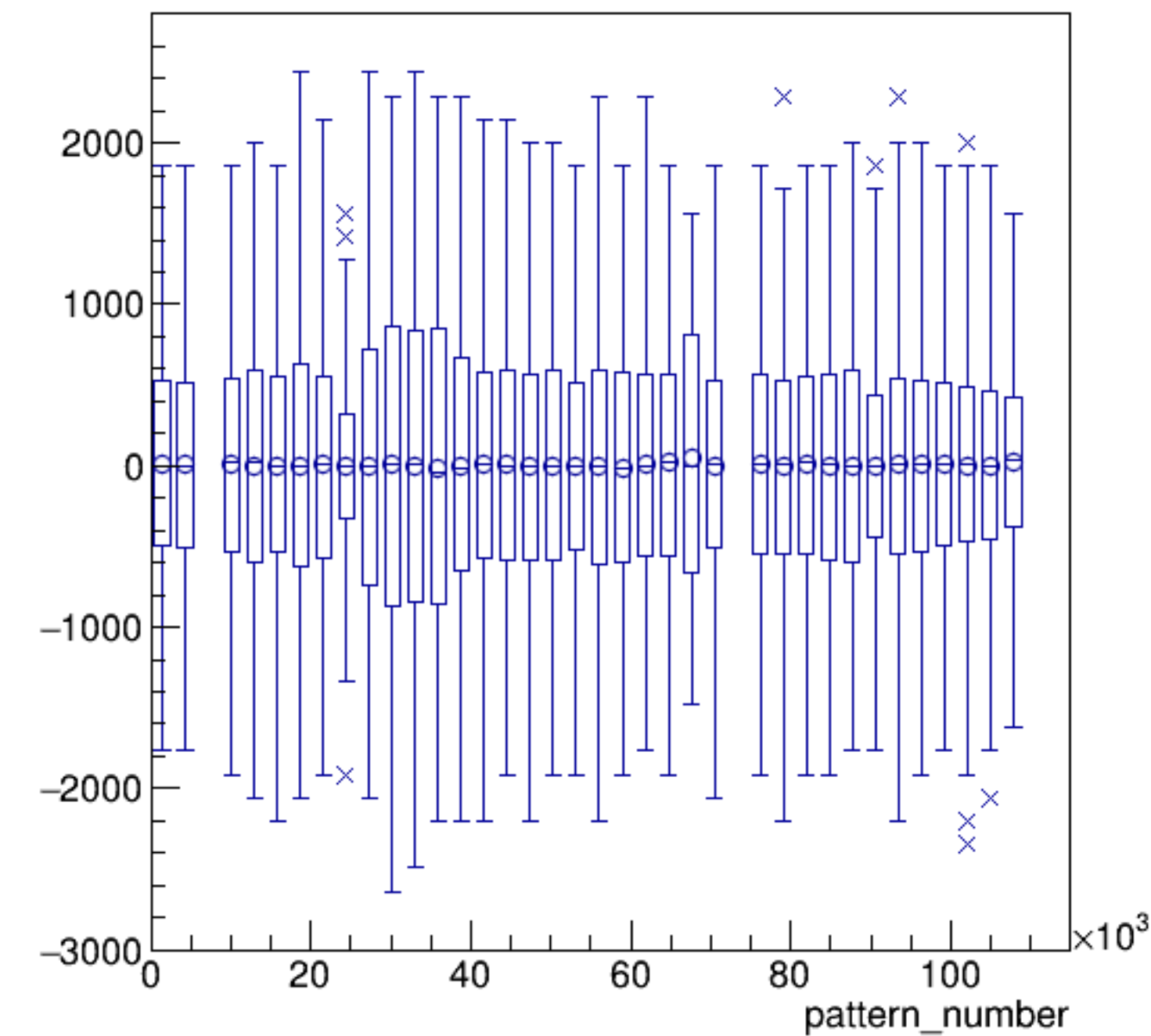


run8027_000_pairTree_dsl.png

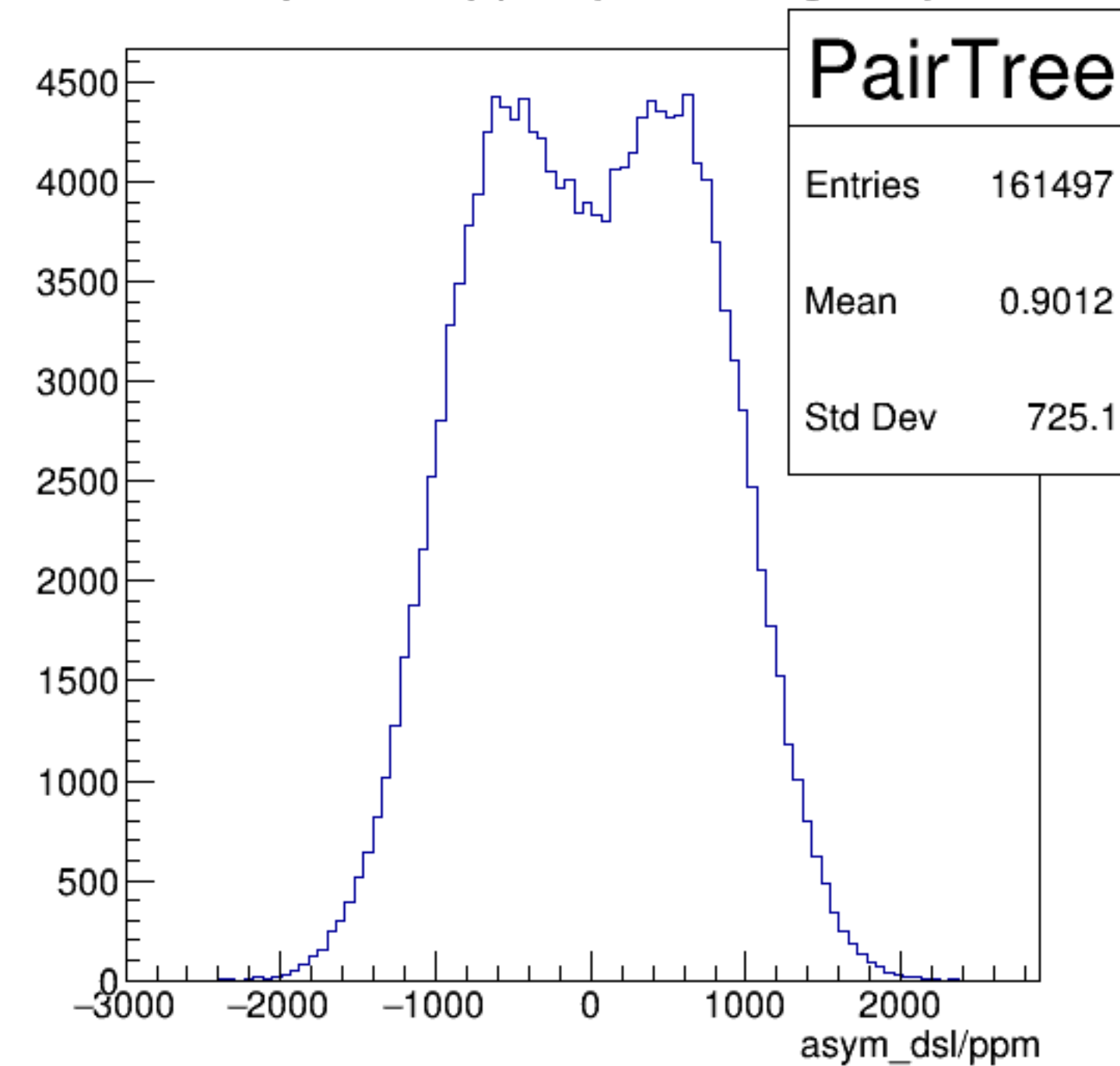
yield_dsl {ErrorFlag==0}



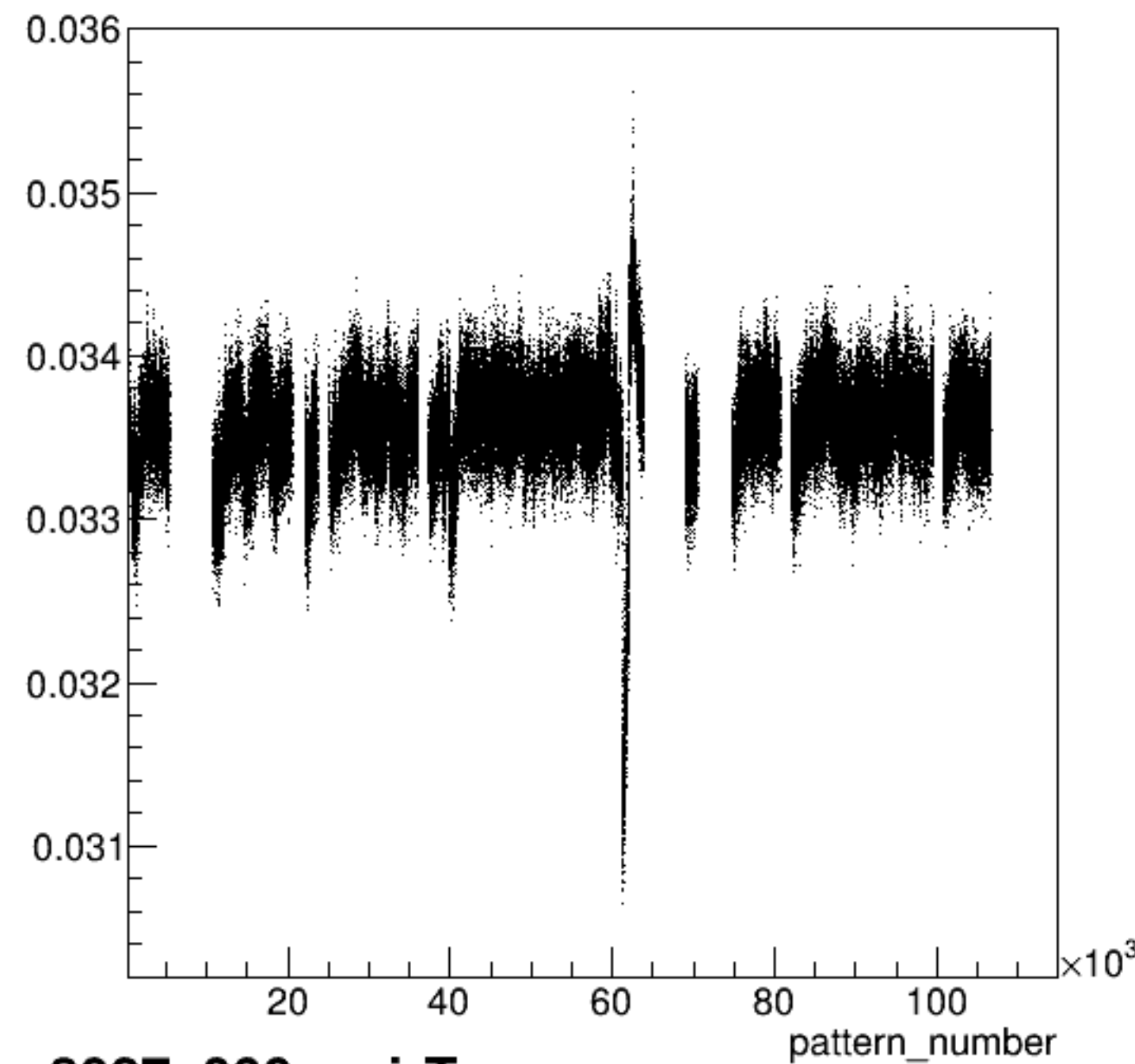
asym_dsl/ppm:pattern_number {ErrorFlag==0}



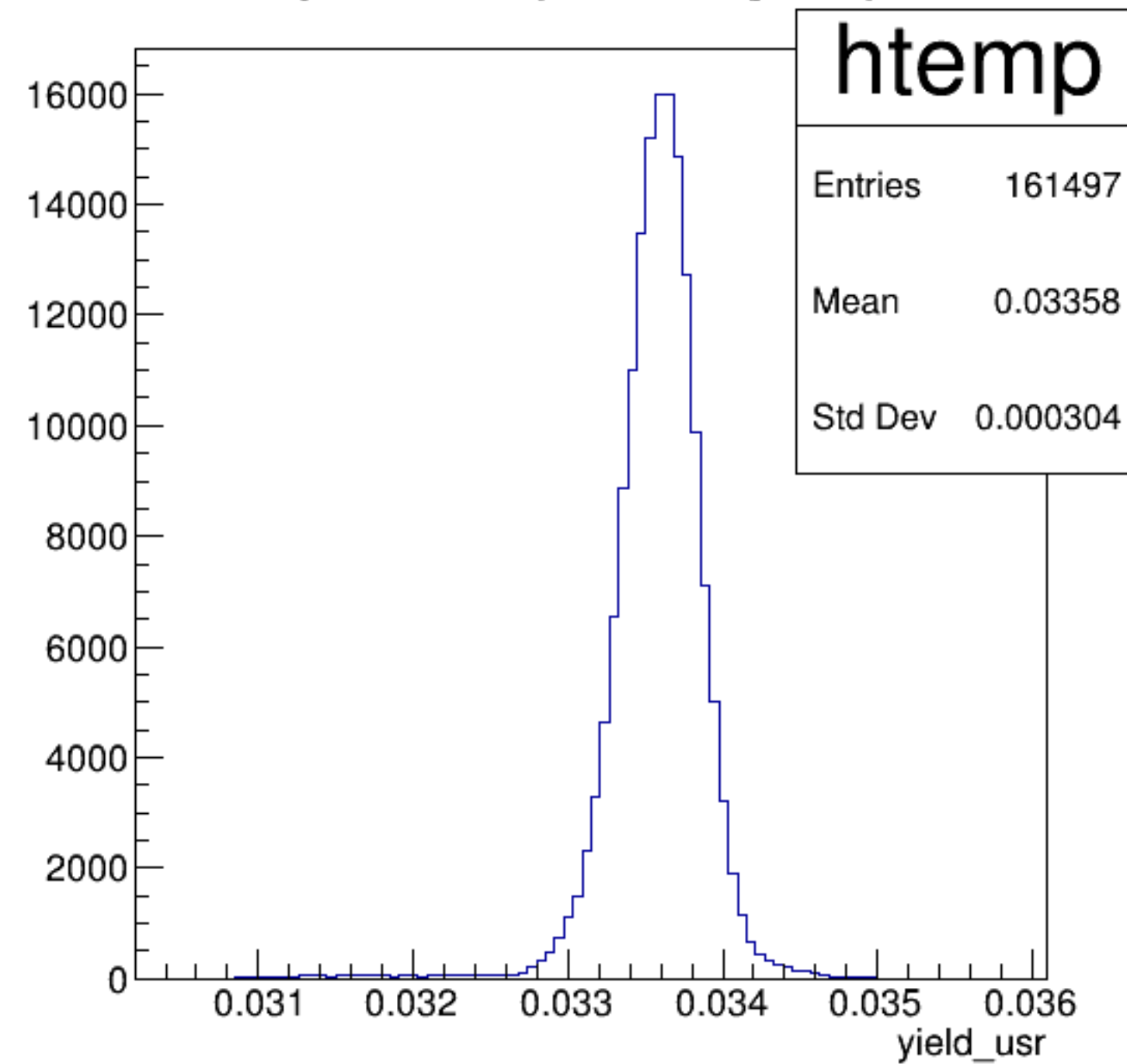
asym_dsl/ppm {ErrorFlag==0}



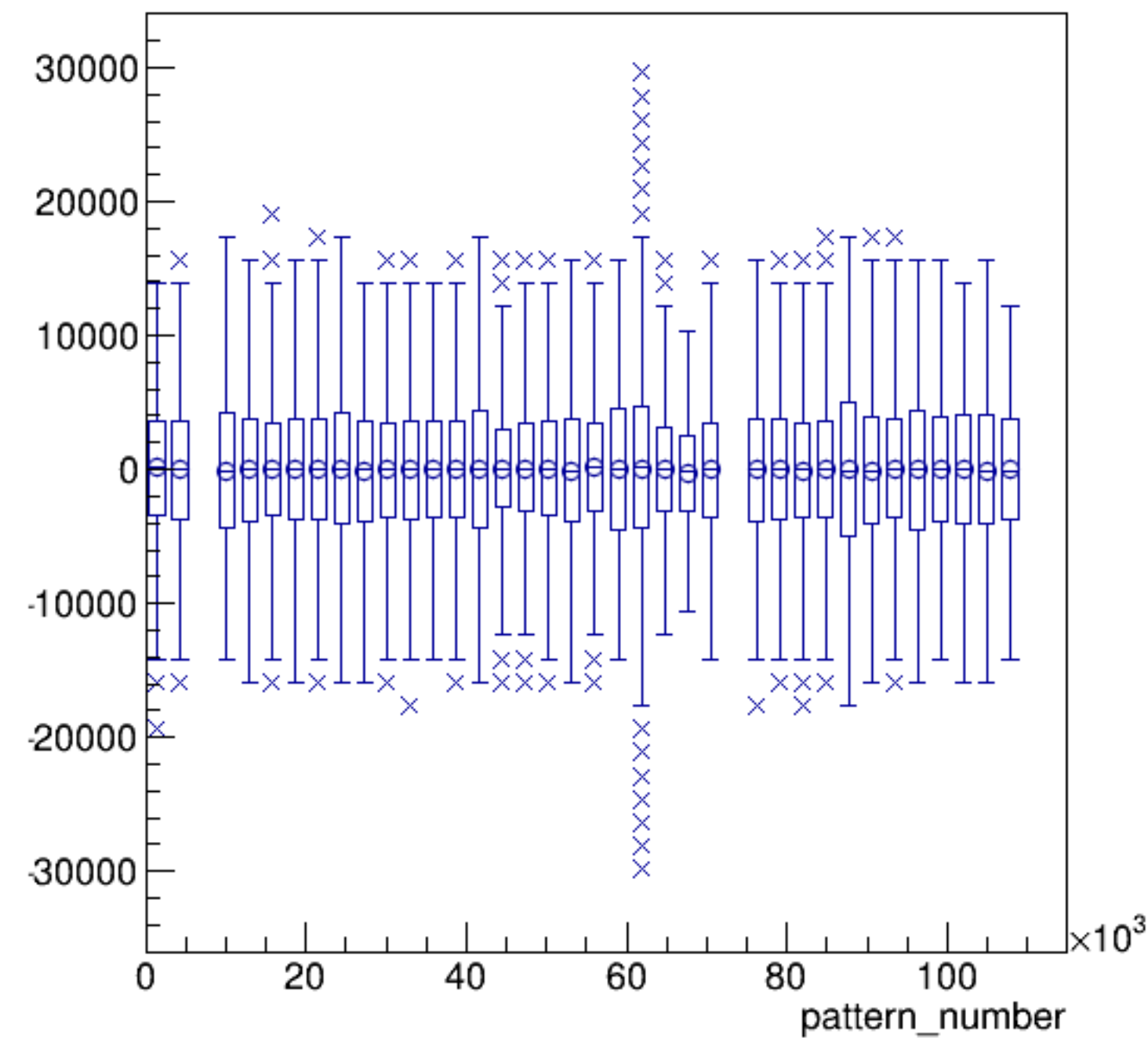
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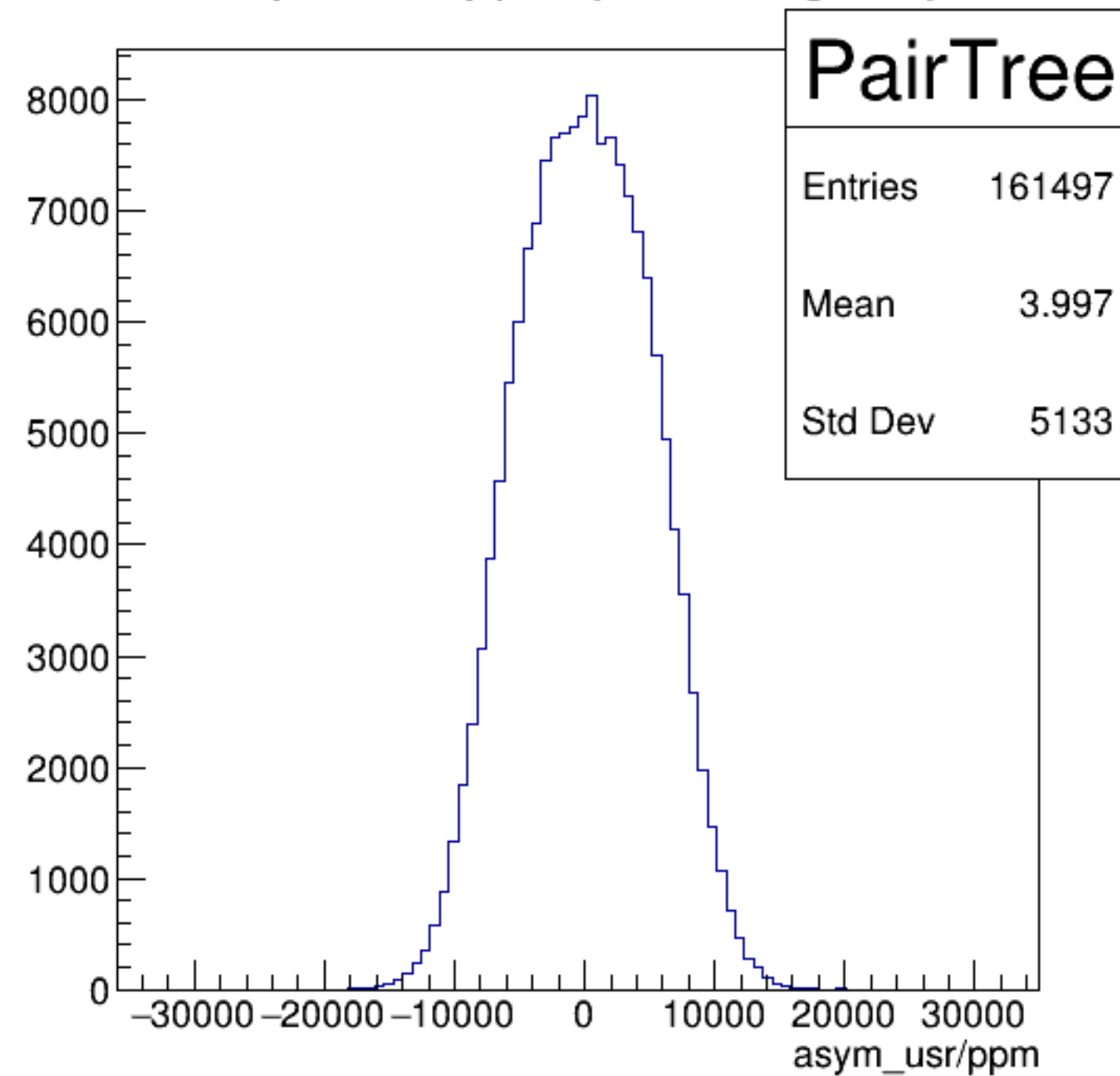
yield_usr {ErrorFlag==0}



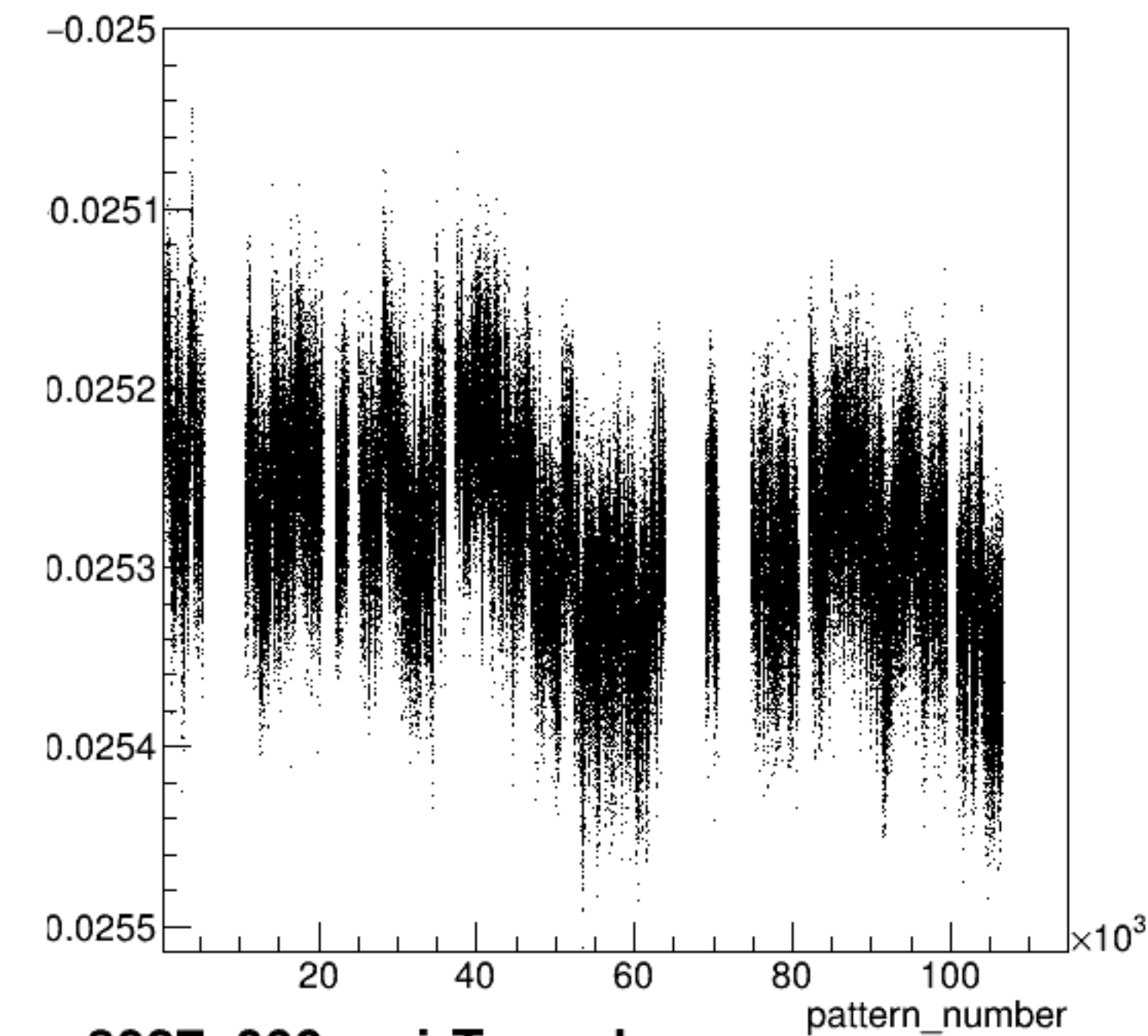
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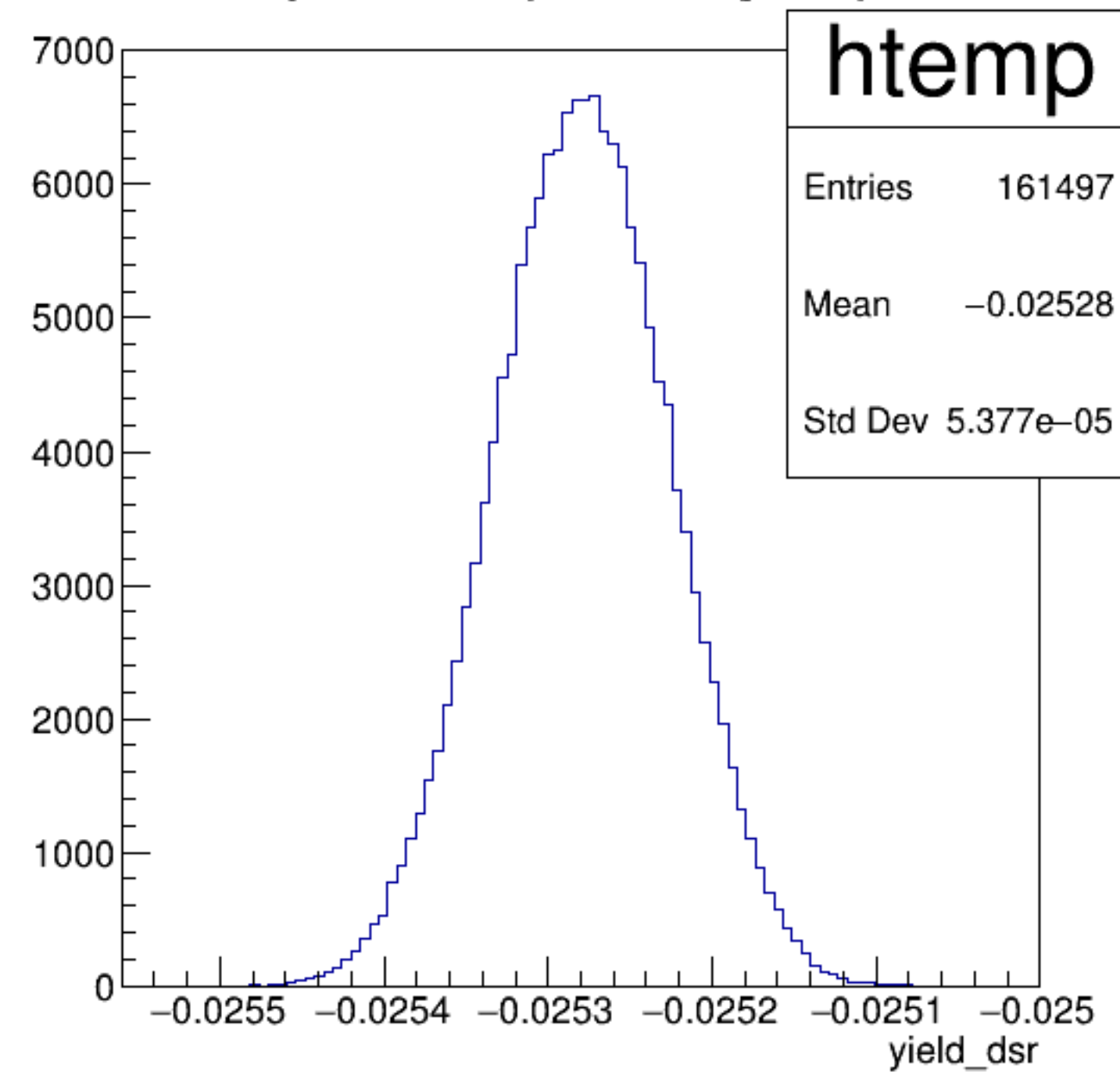


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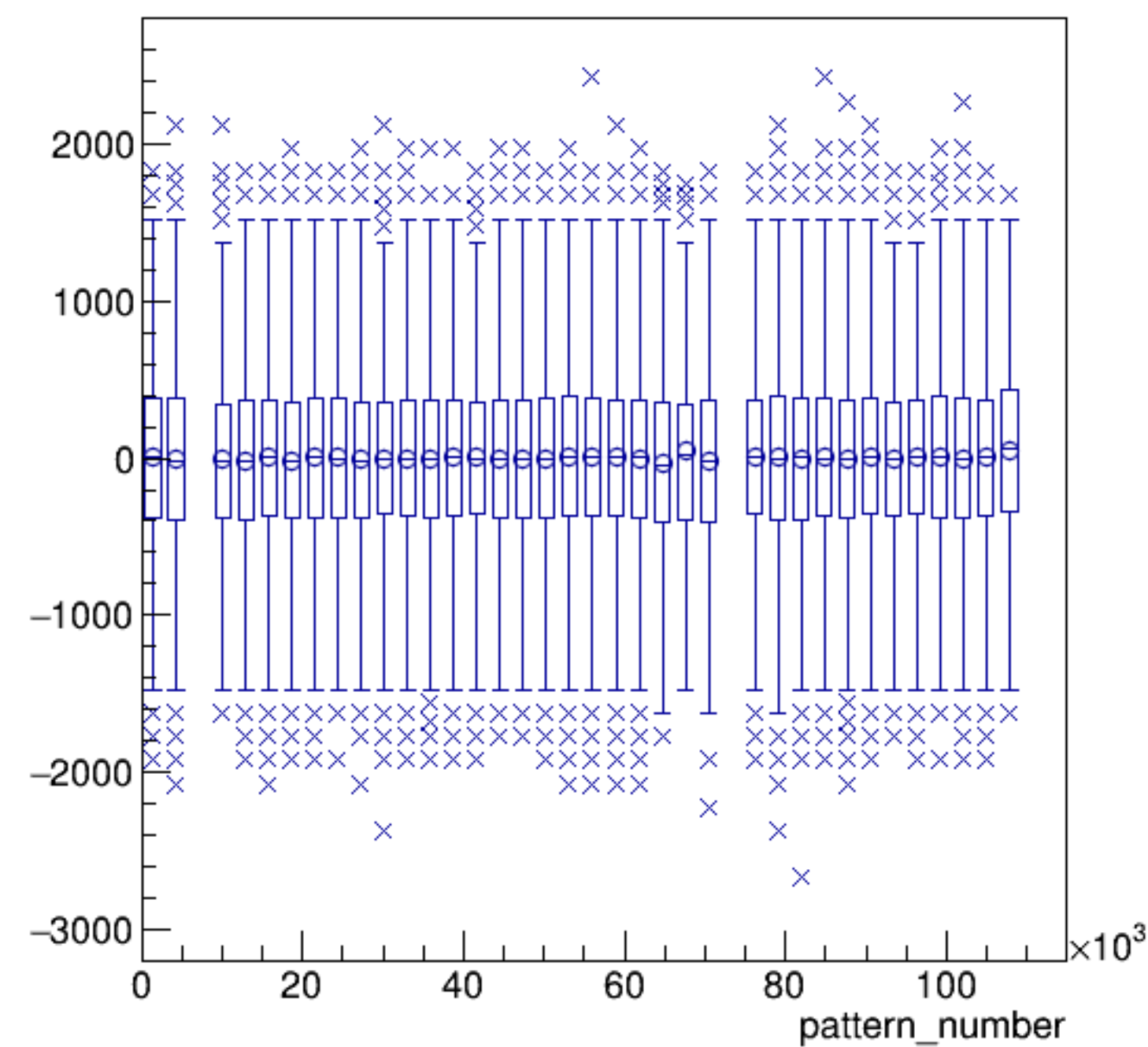


run8027_000_pairTree_dsr.png

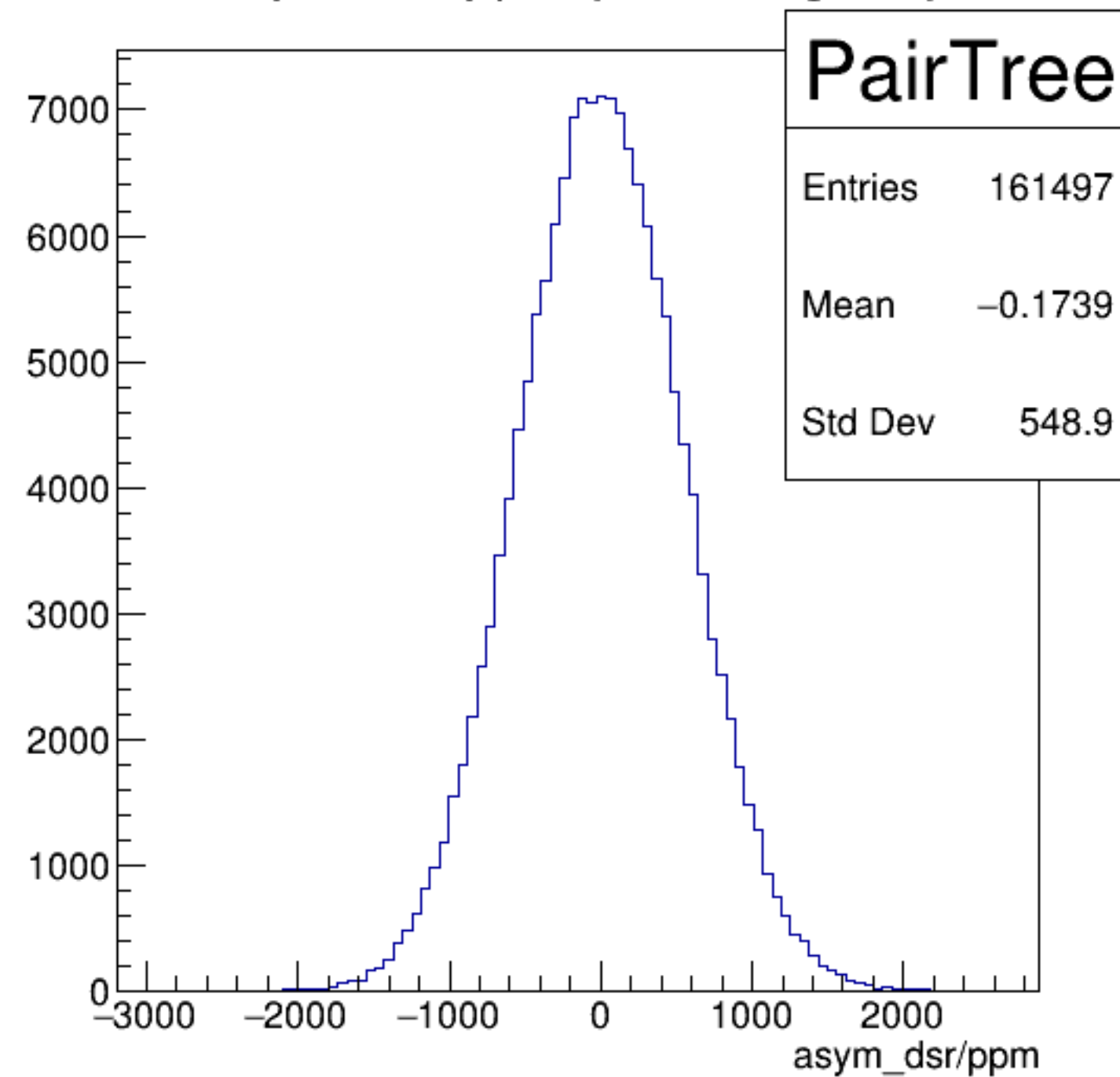
yield_dsr {ErrorFlag==0}



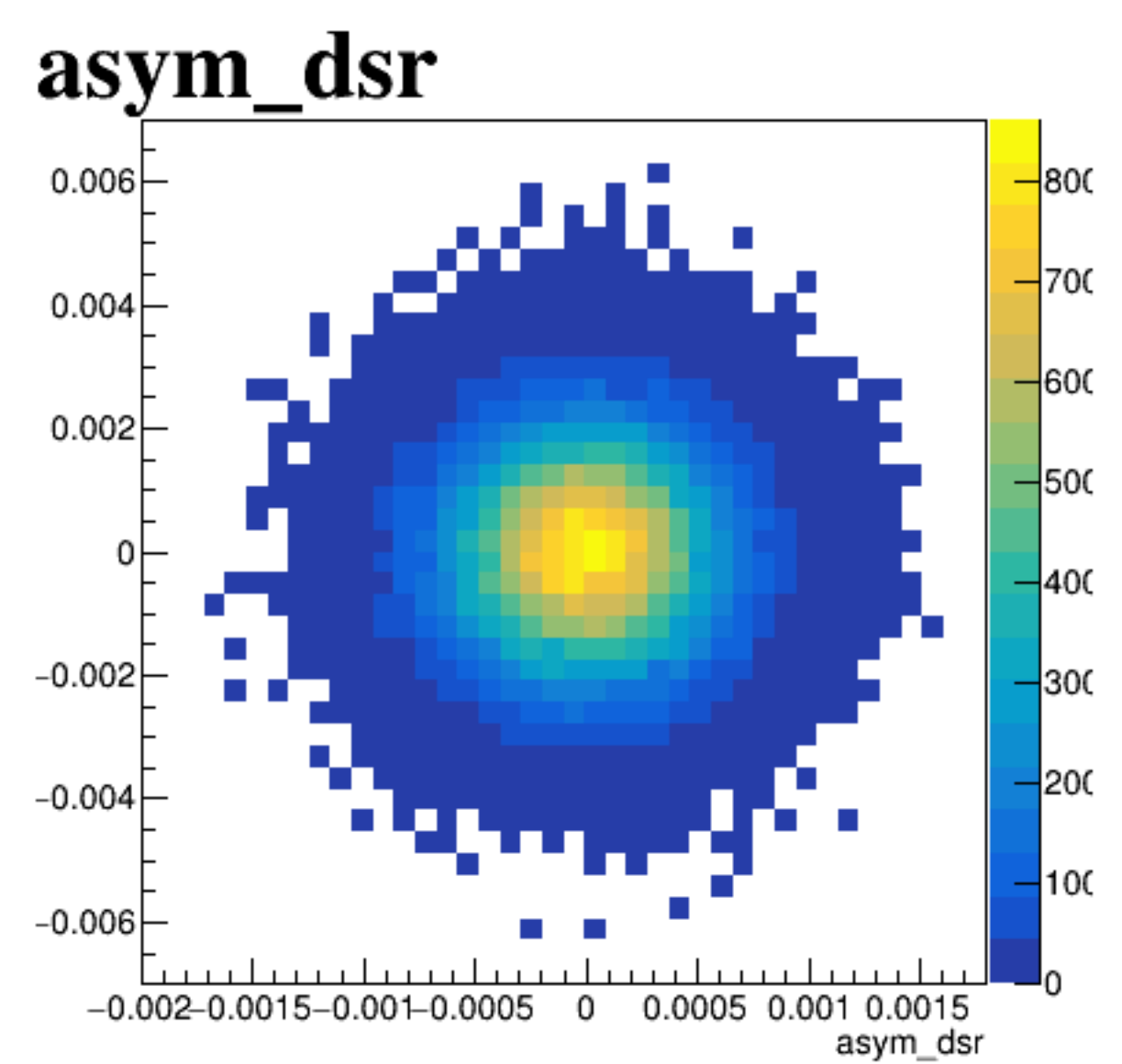
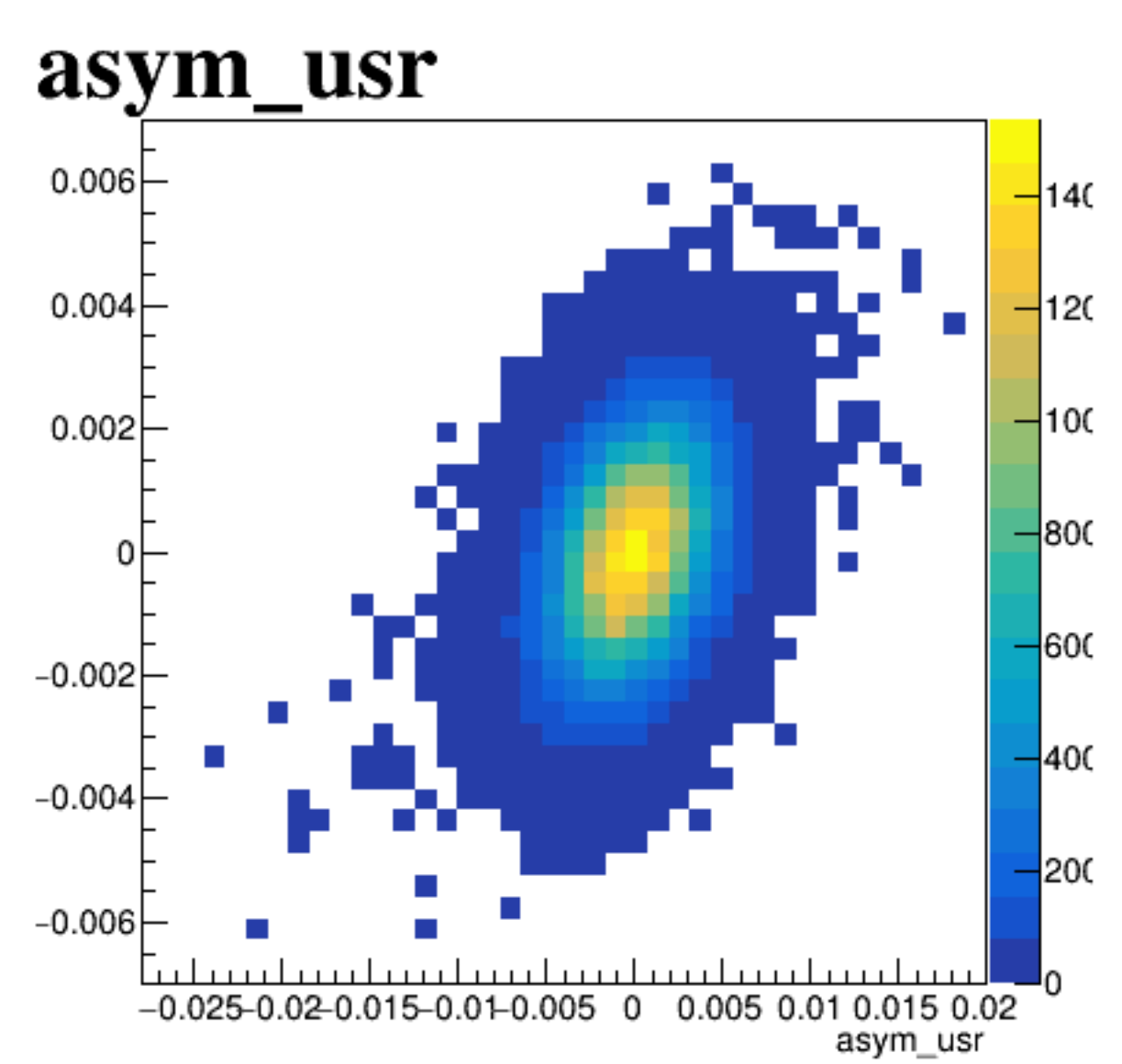
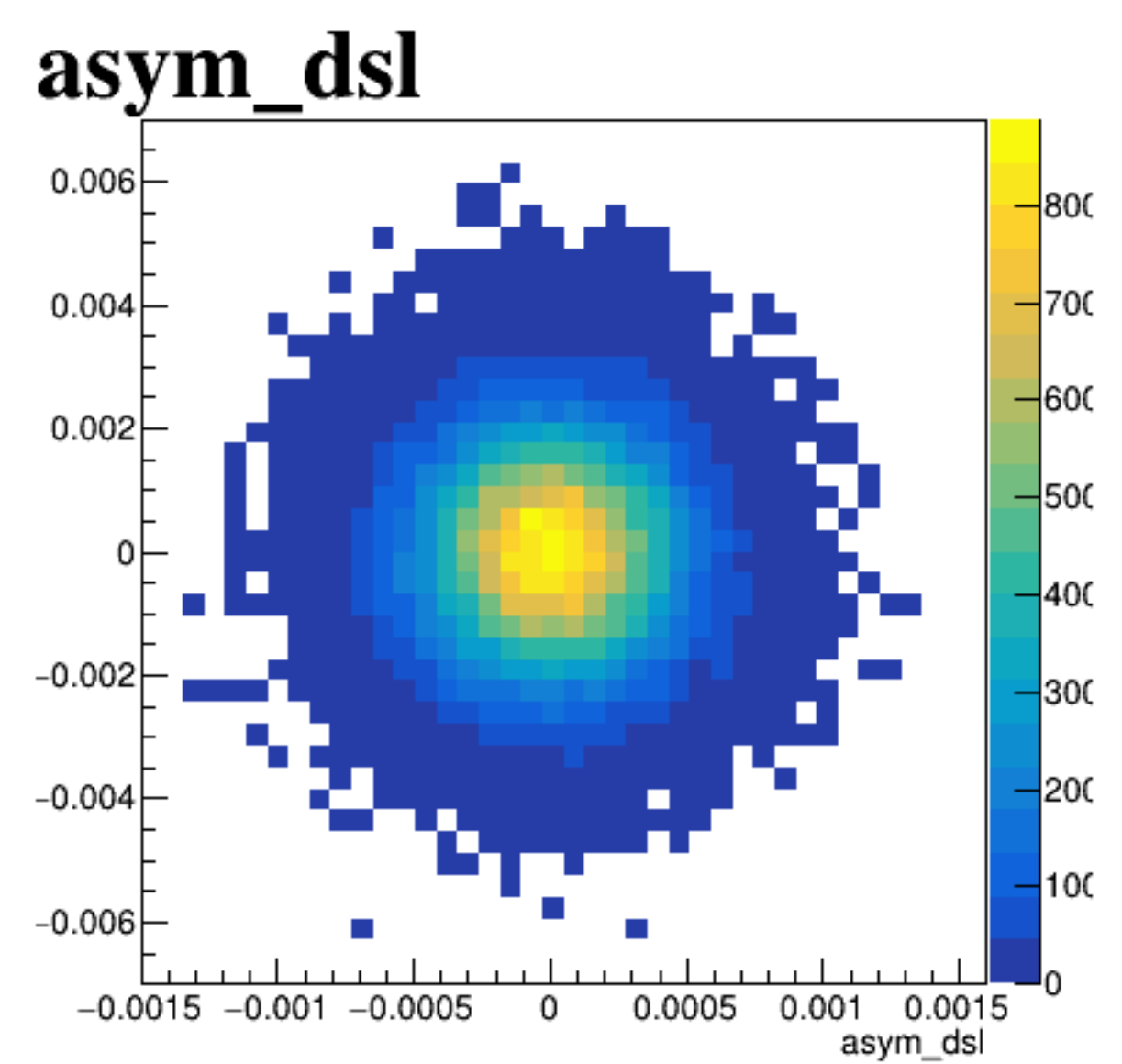
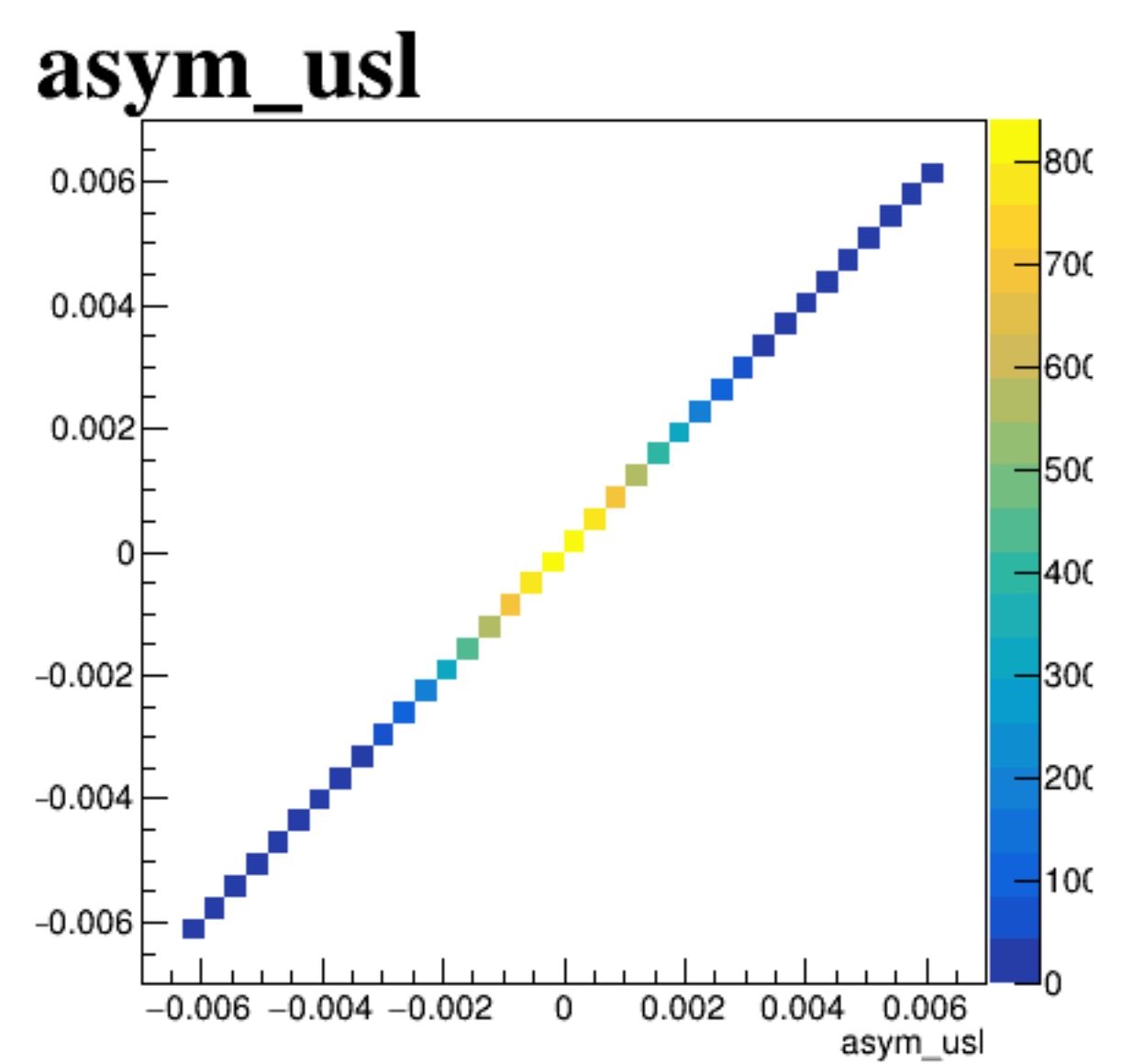
asym_dsr/ppm:pattern_number {ErrorFlag==0}



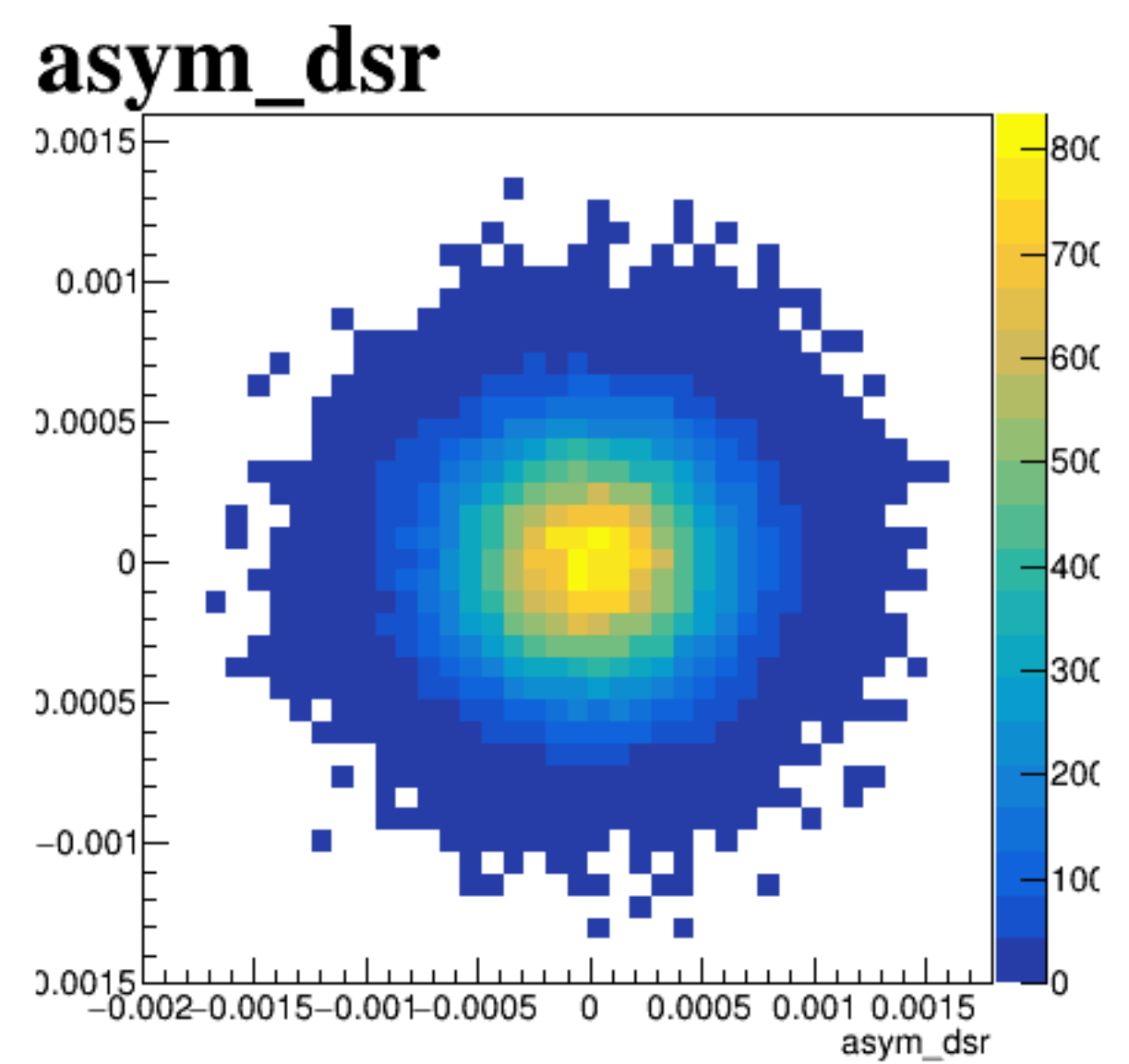
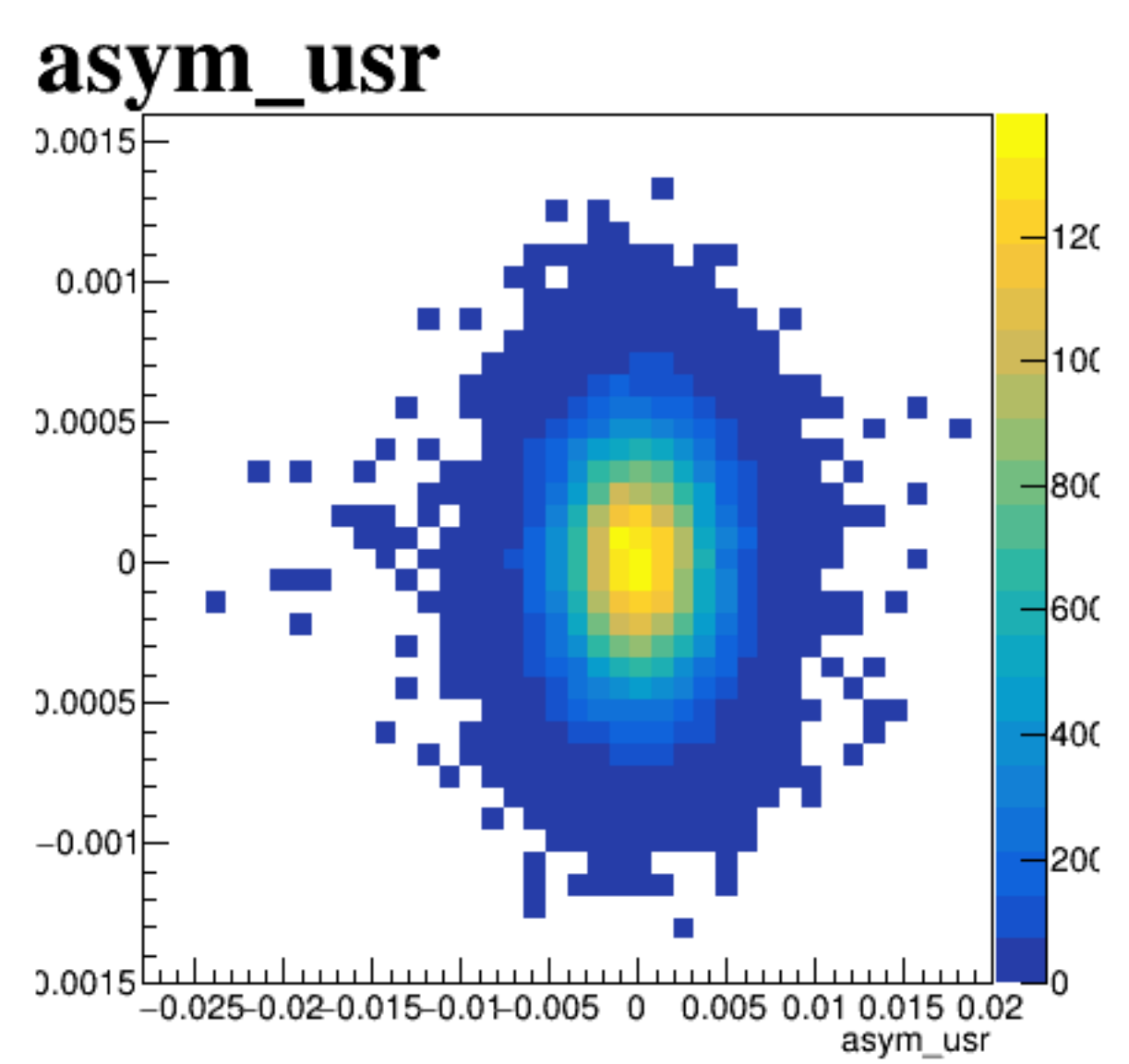
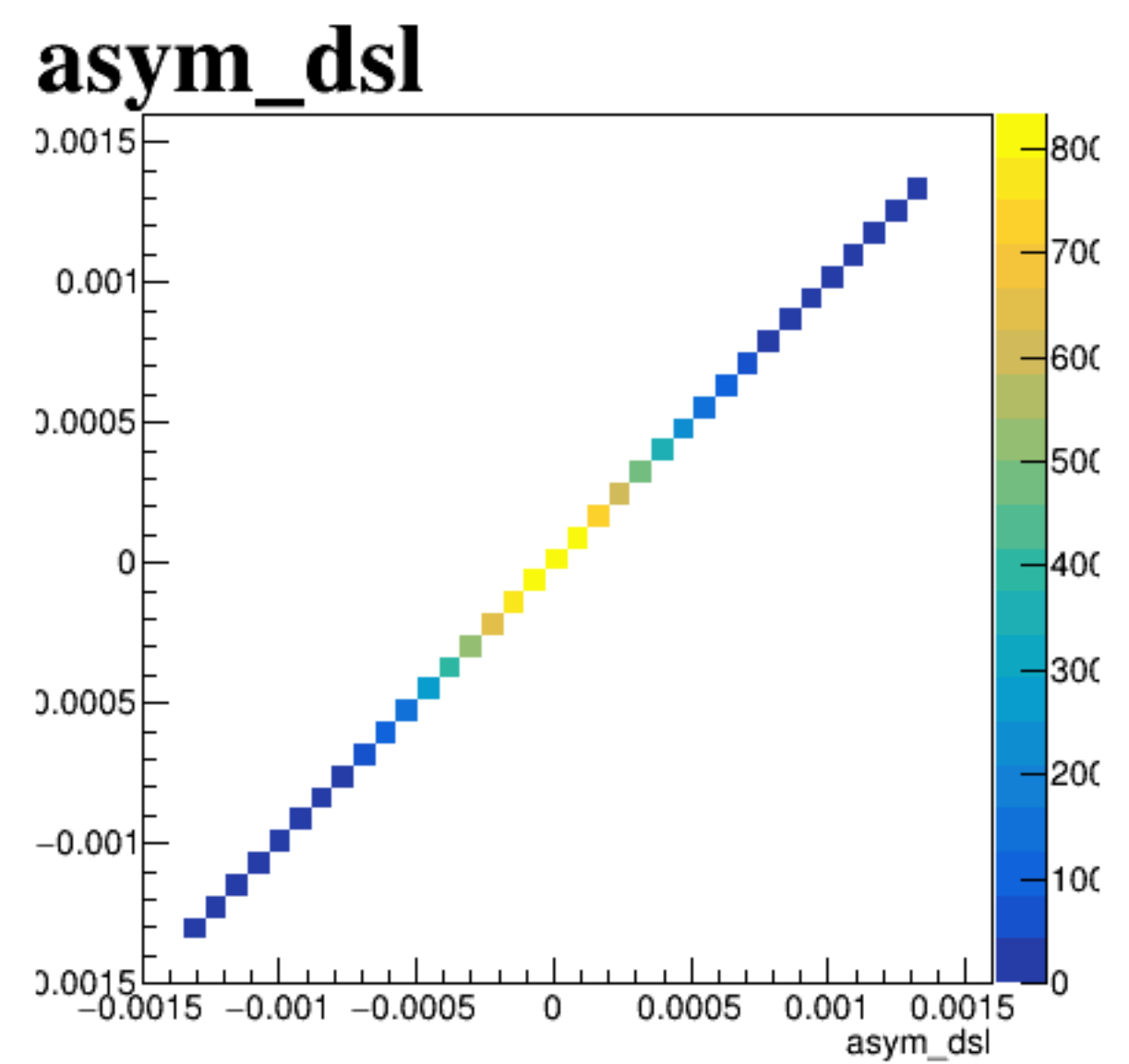
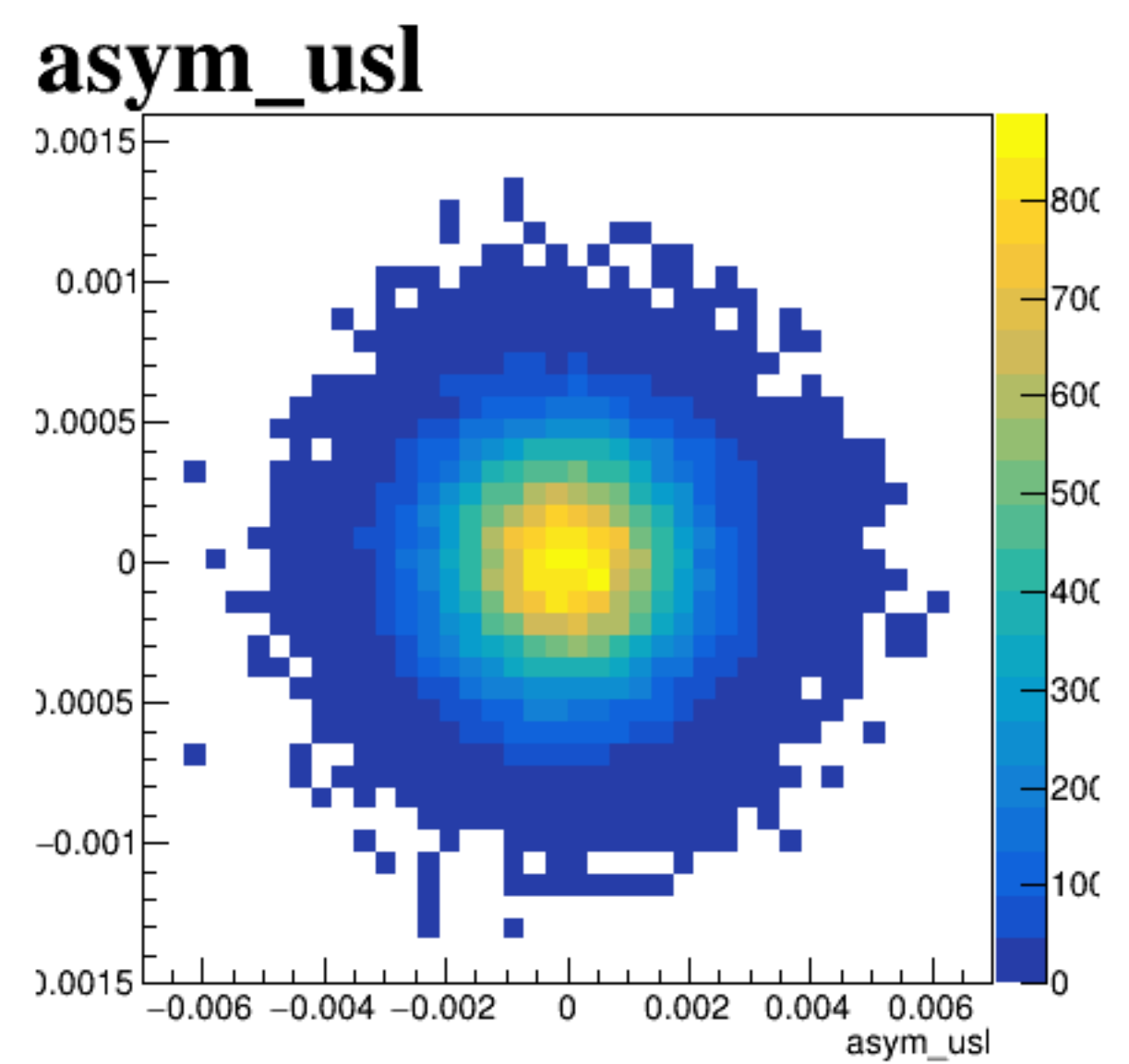
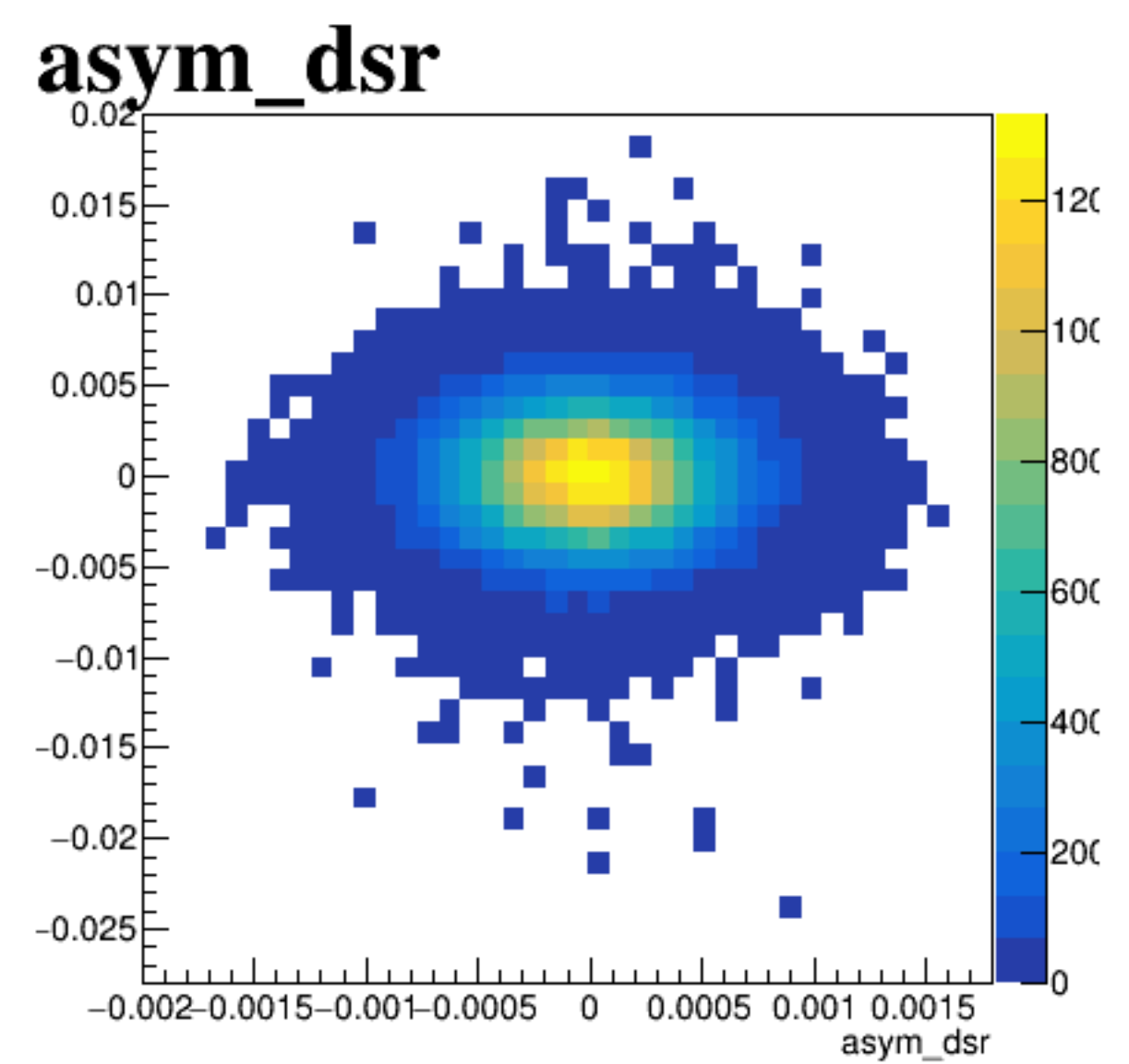
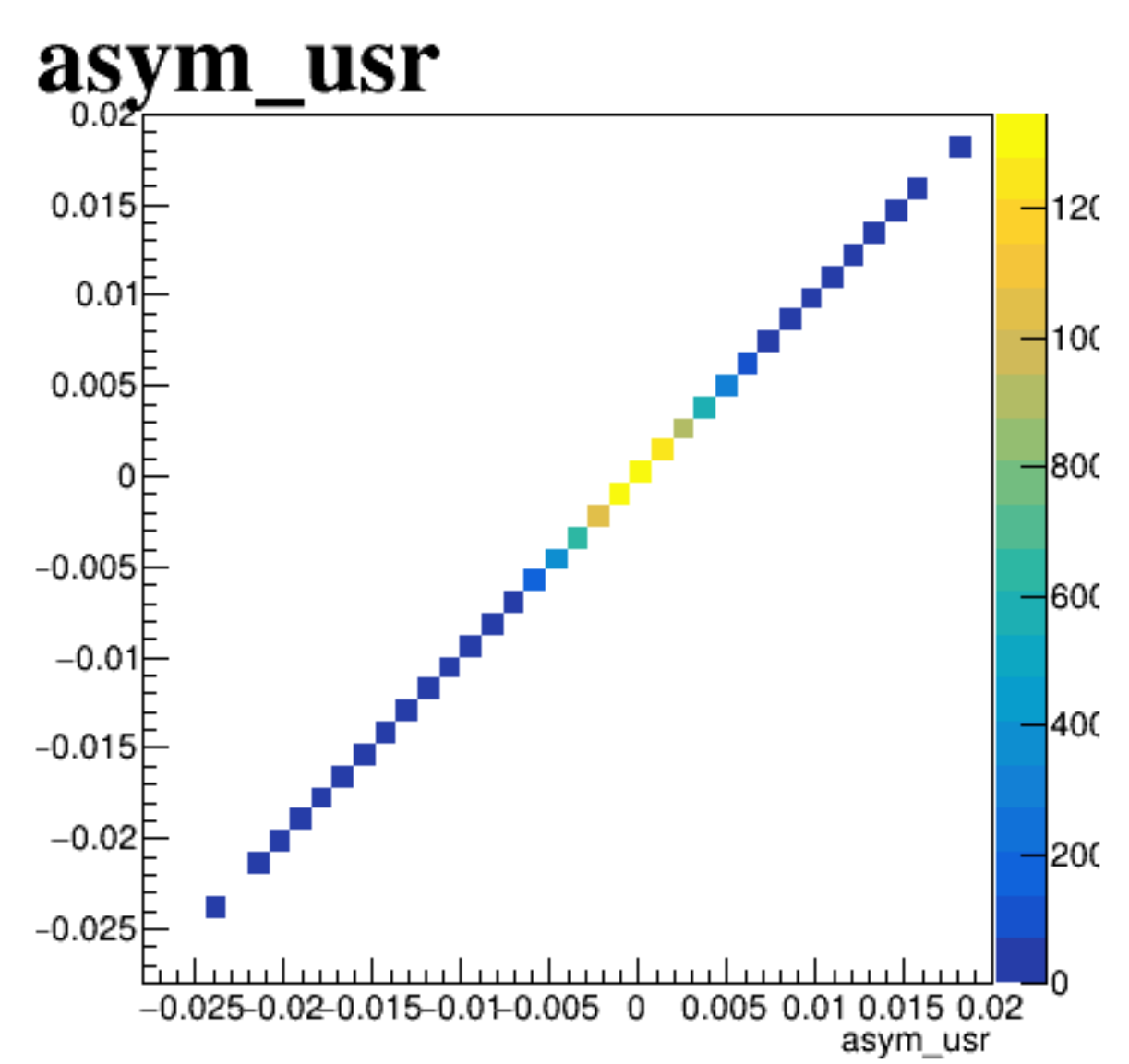
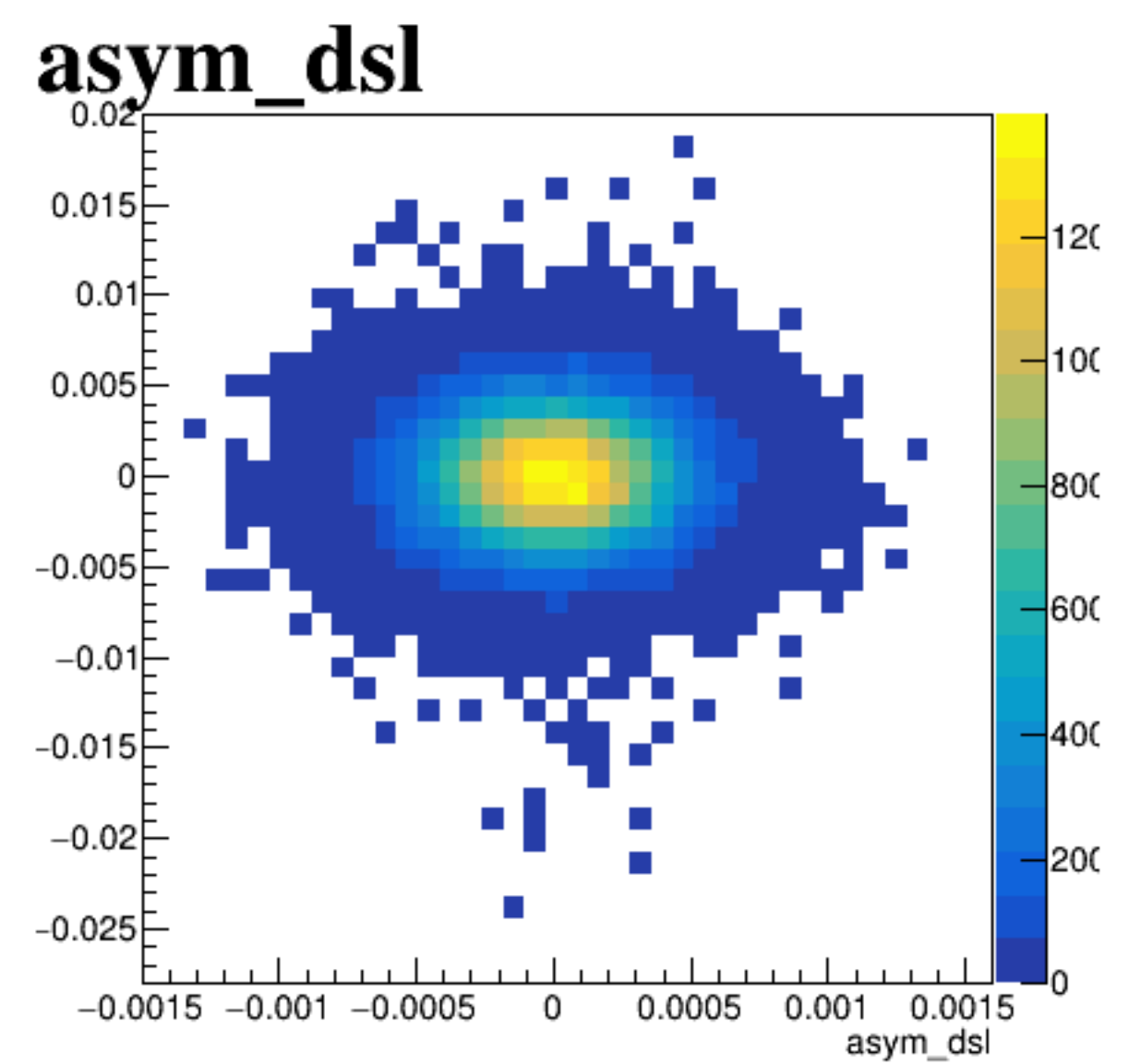
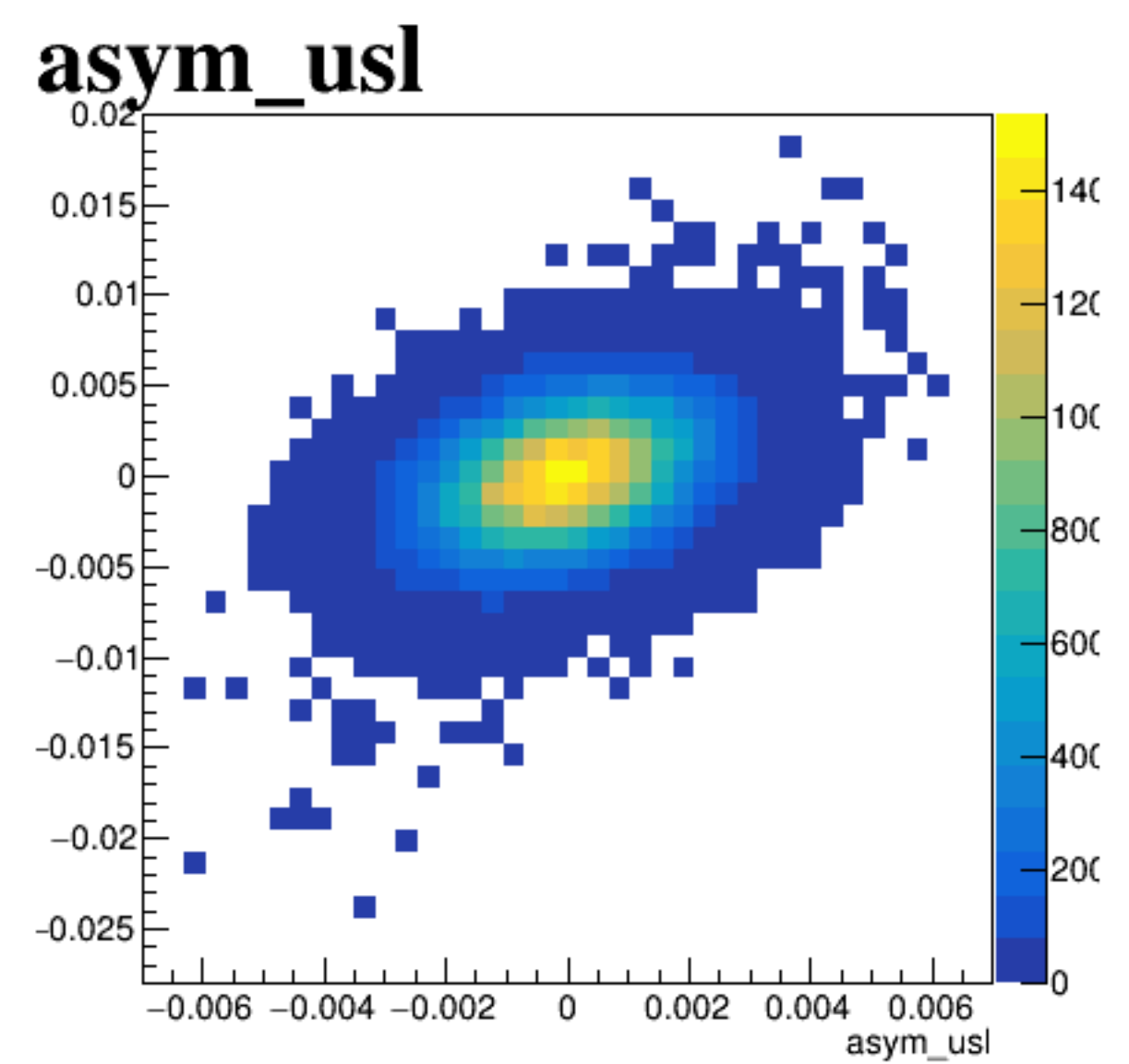
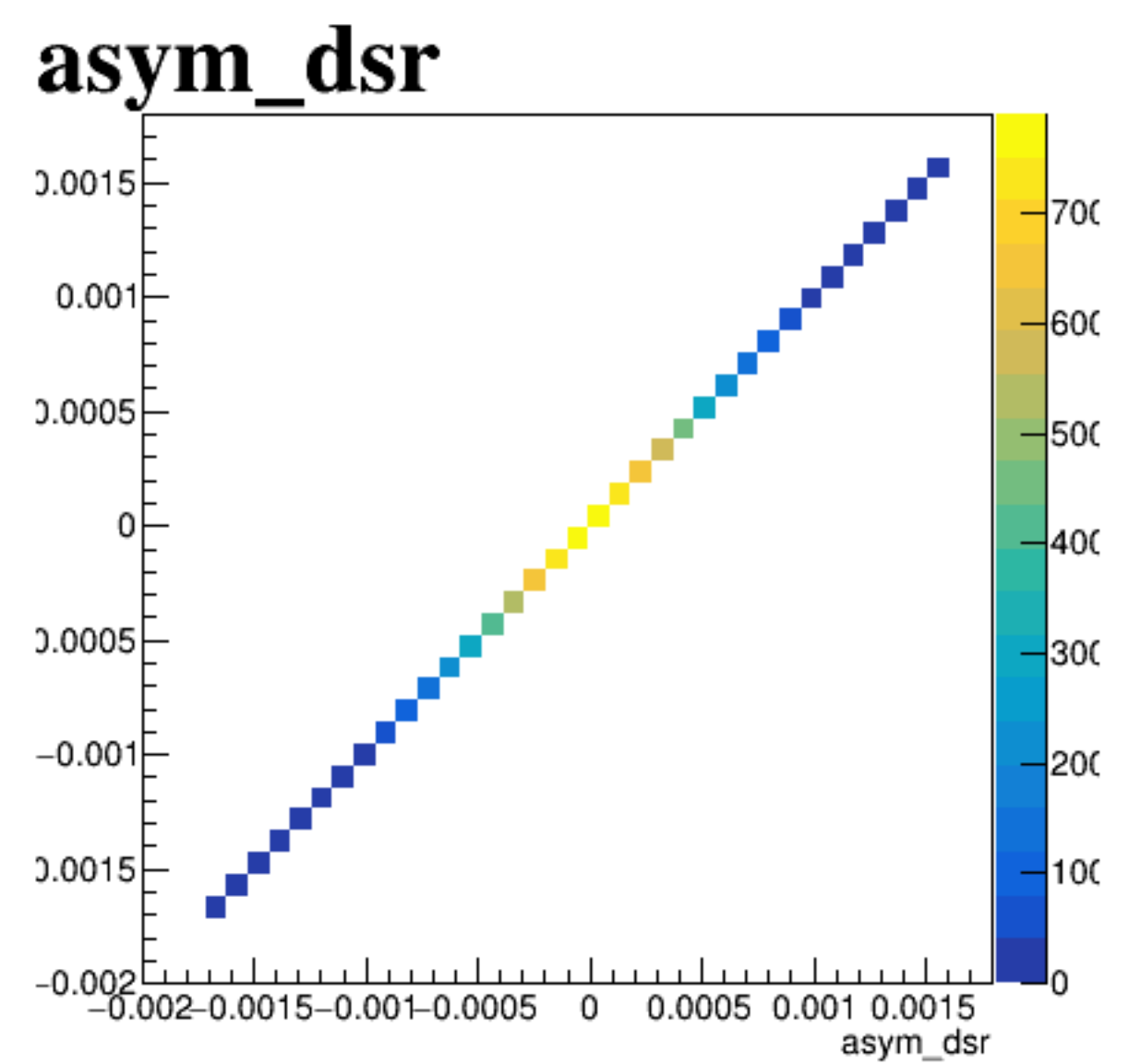
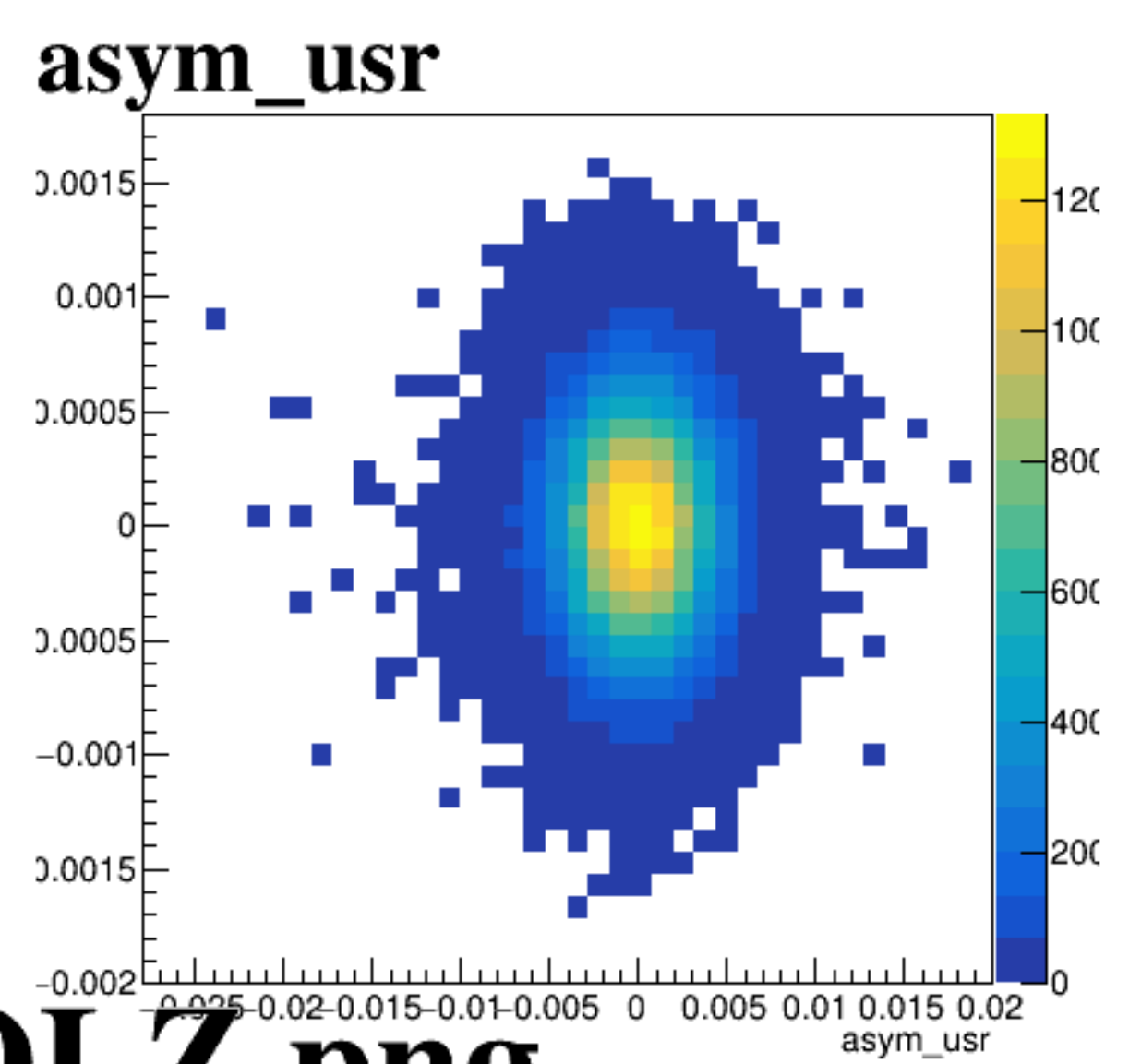
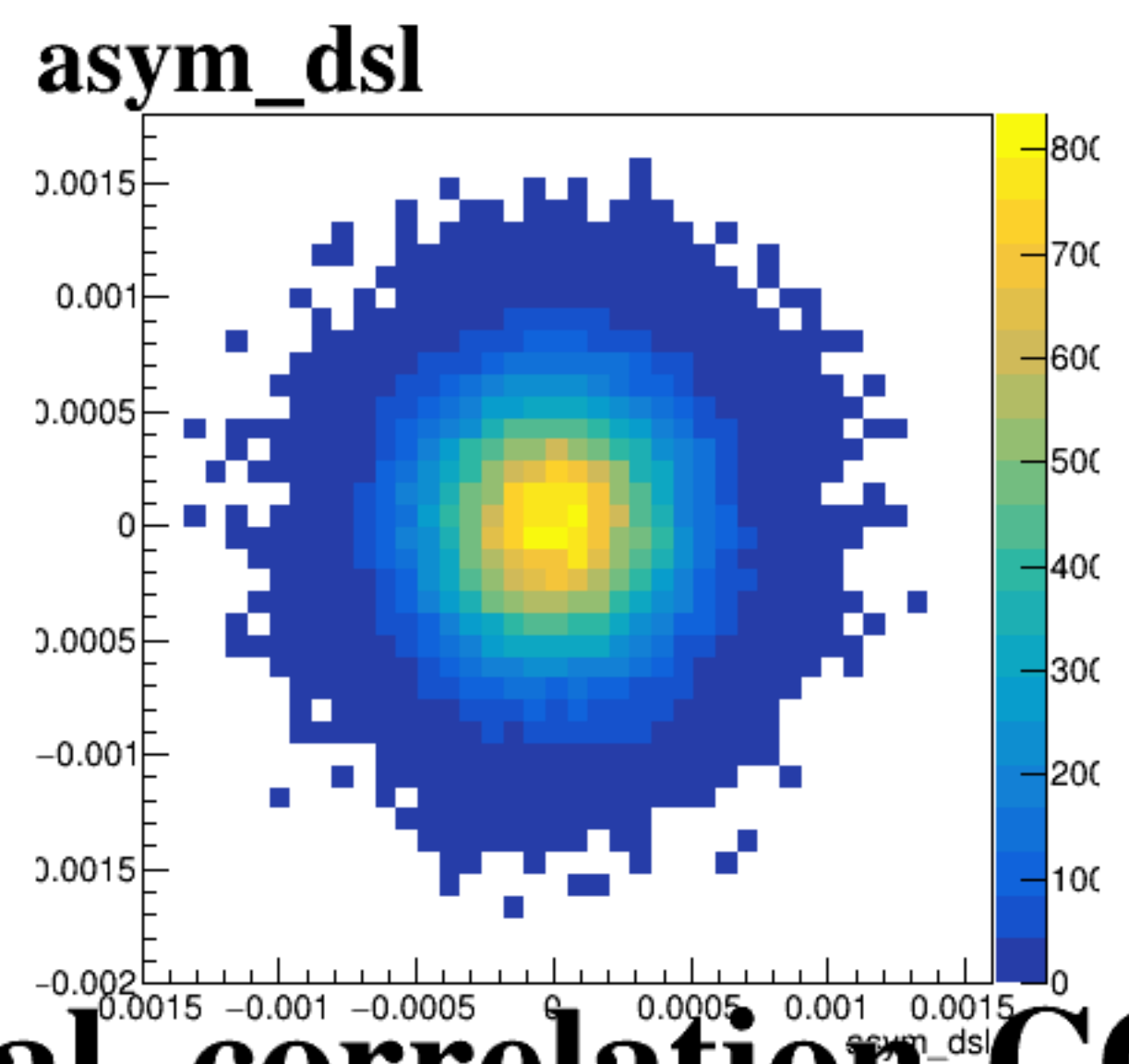
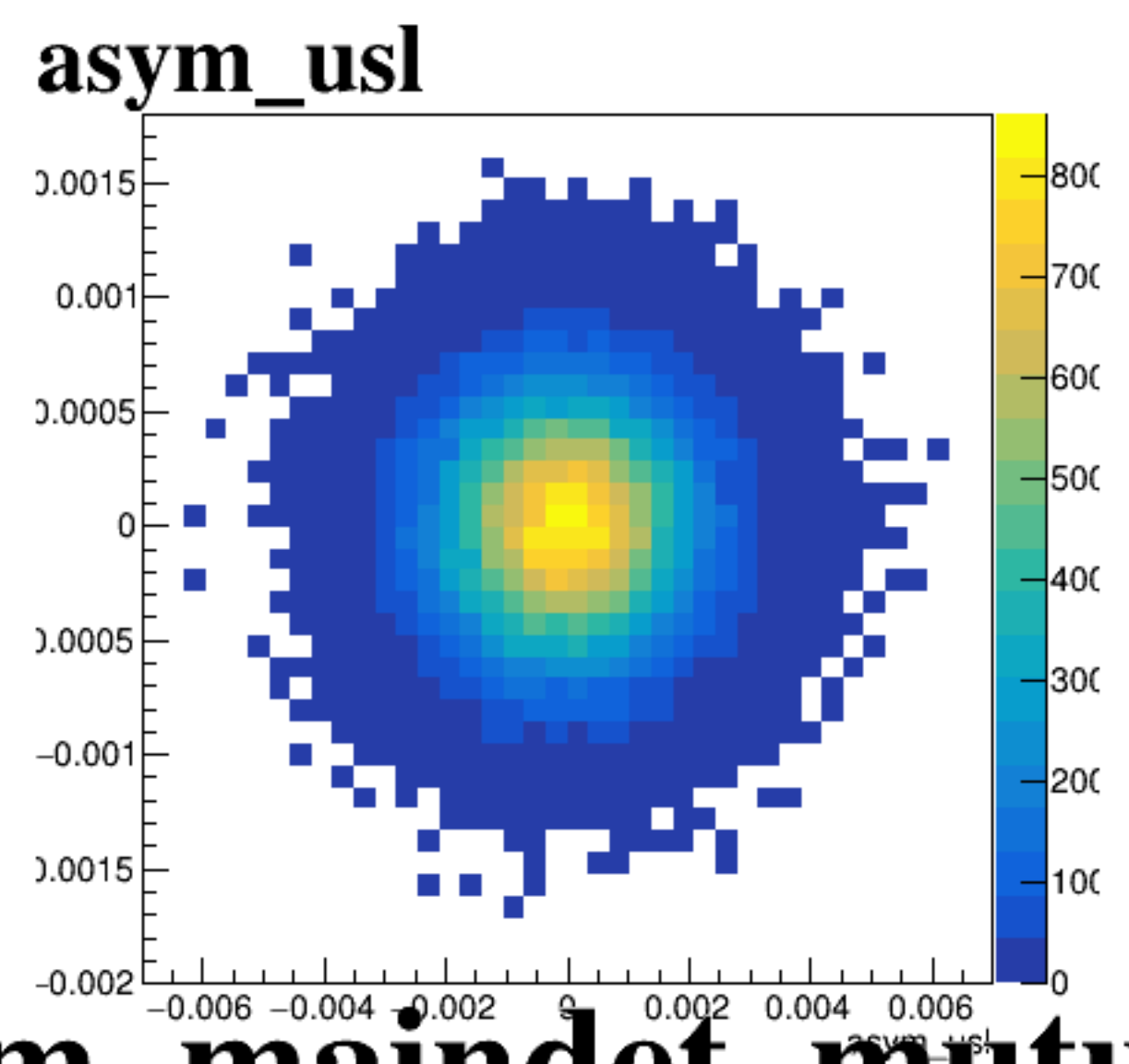
asym_dsr/ppm {ErrorFlag==0}



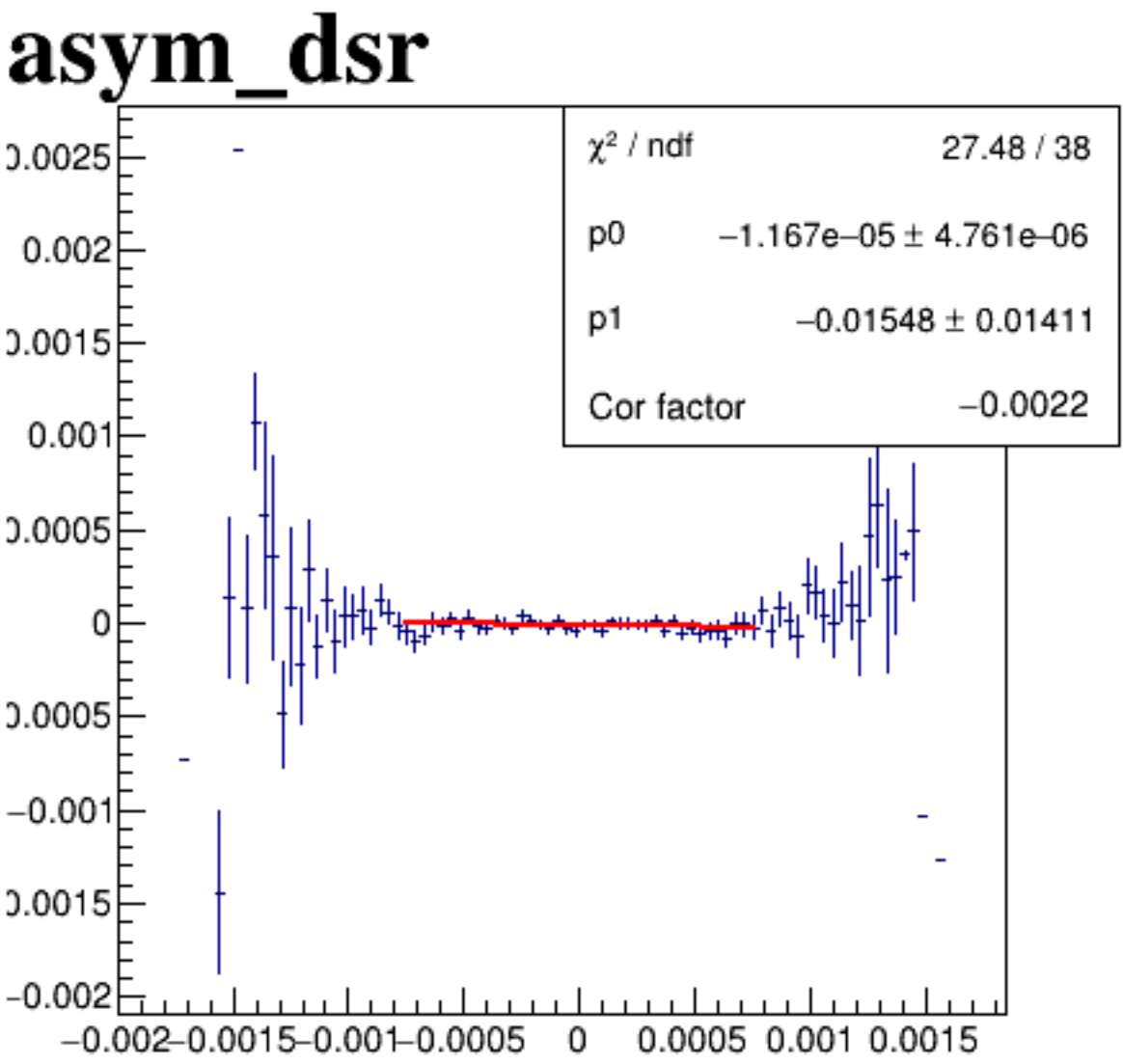
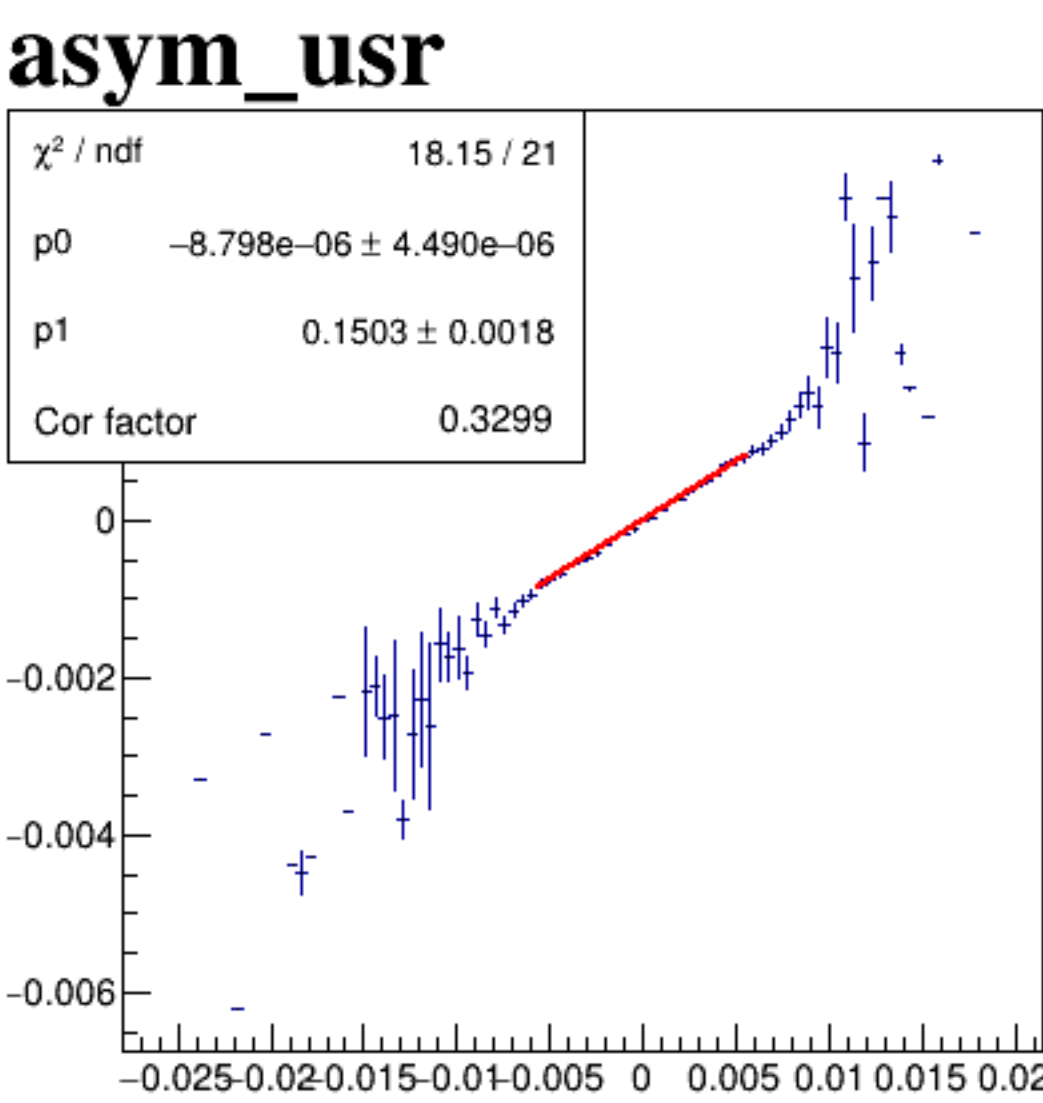
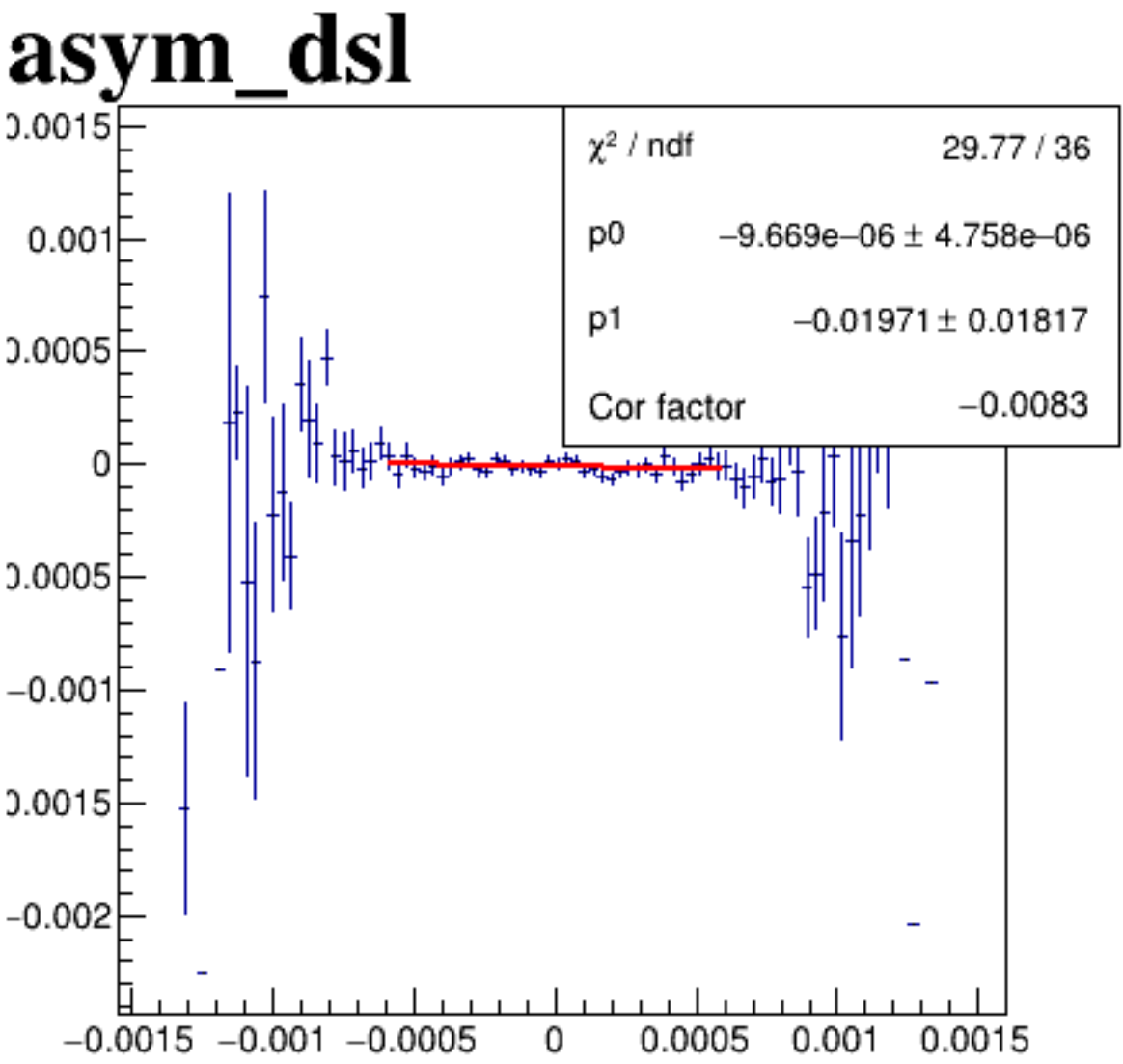
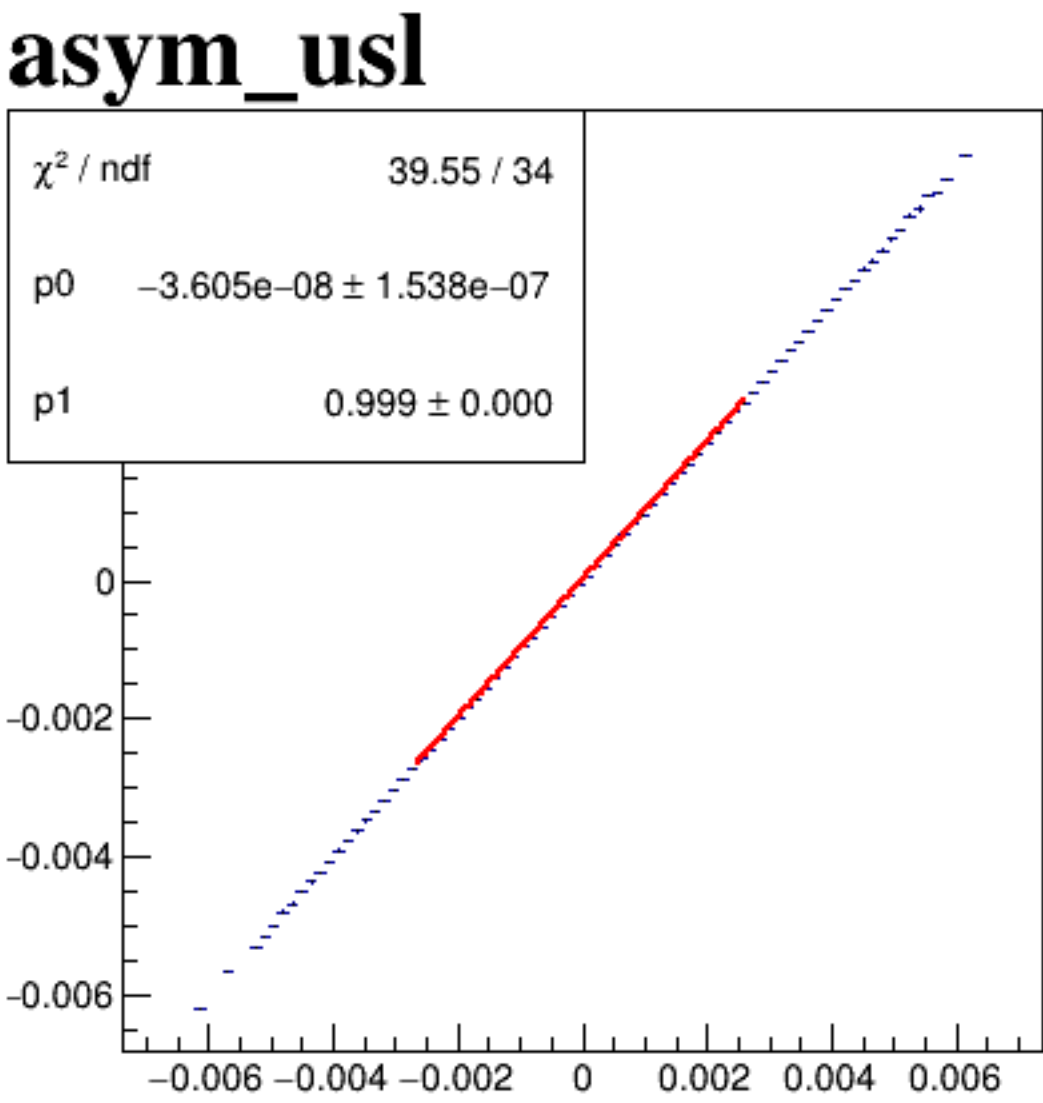
asym_usl



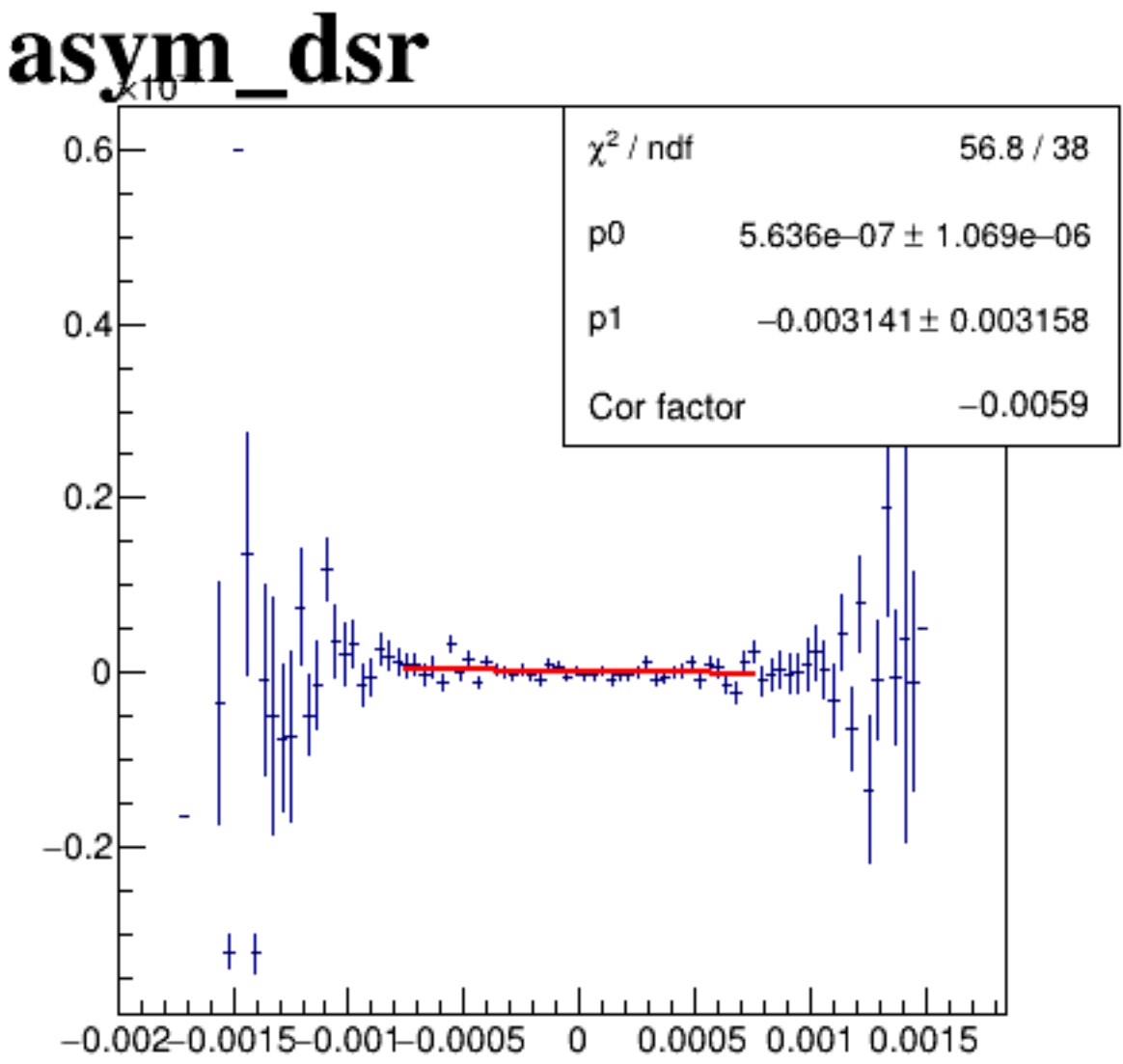
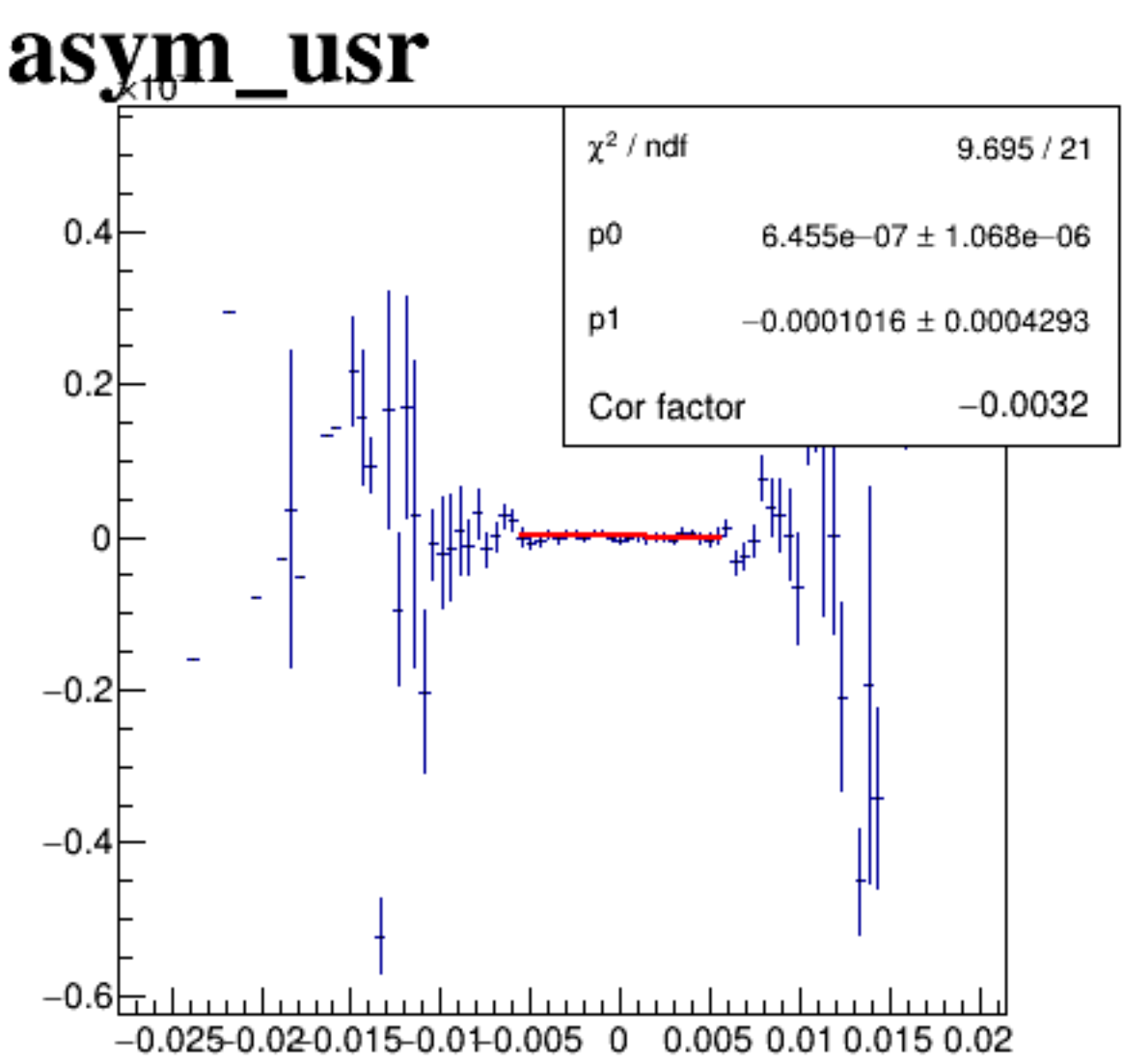
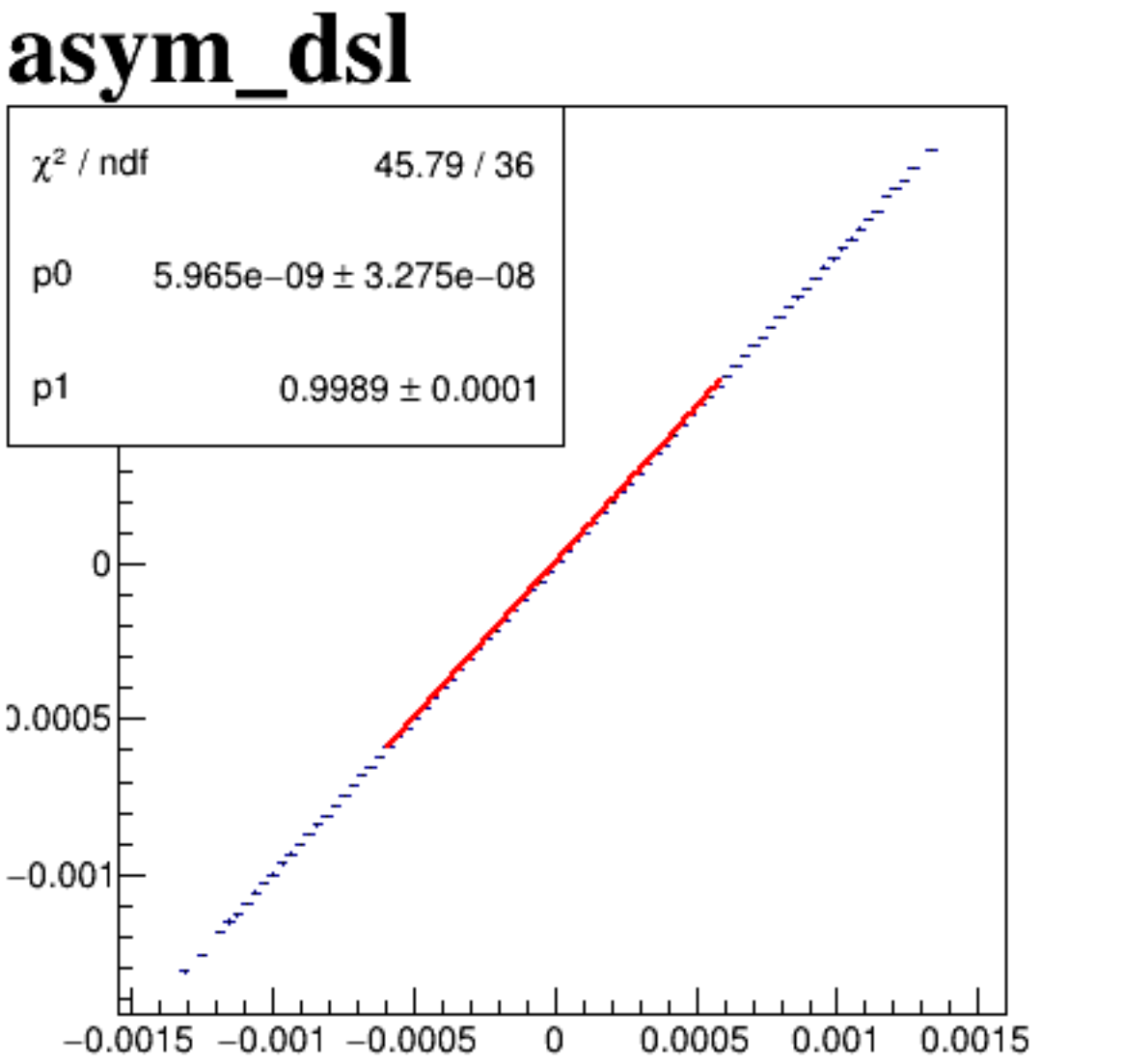
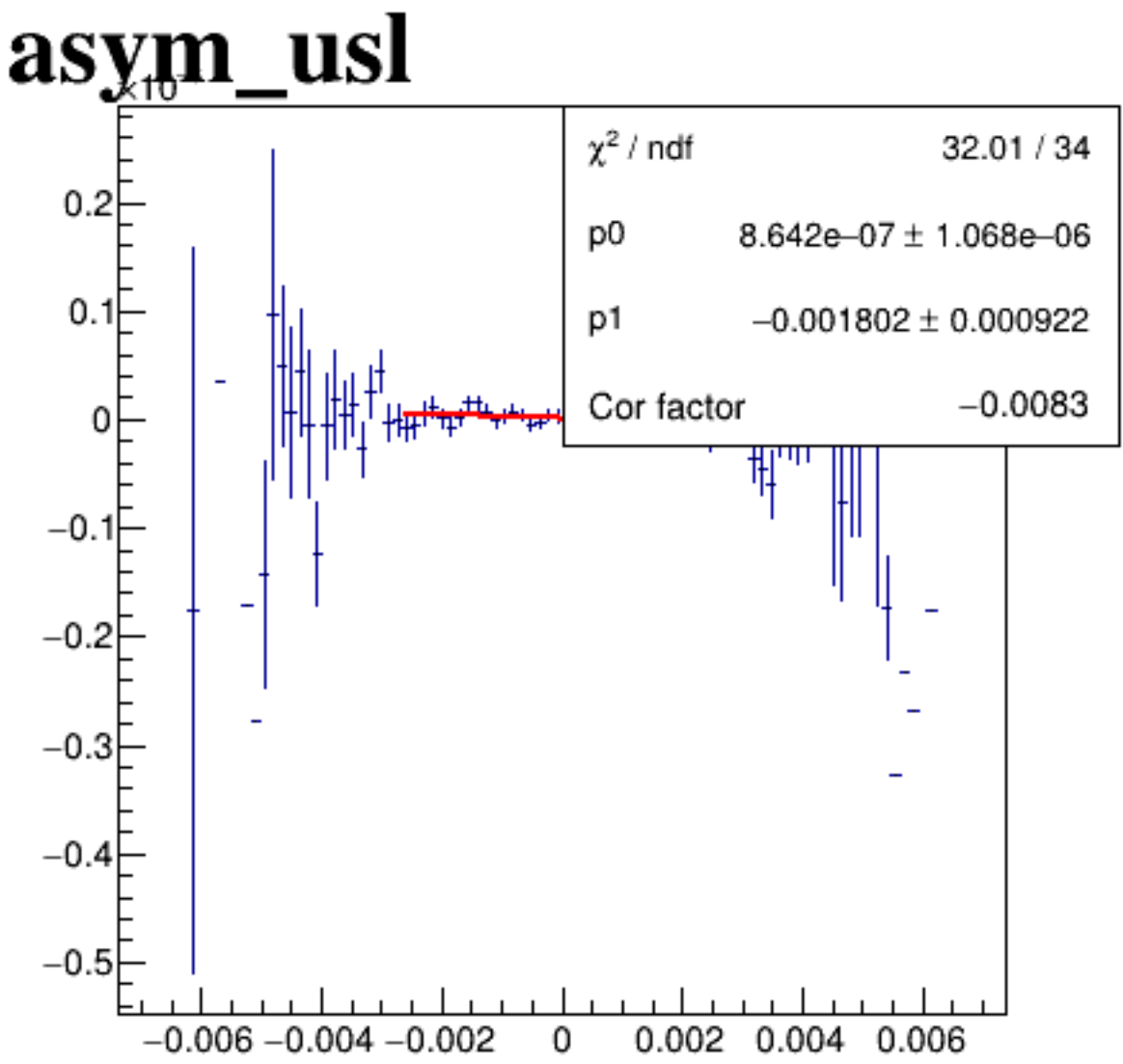
asym_dsl

**asym_usr****asym_dsr**

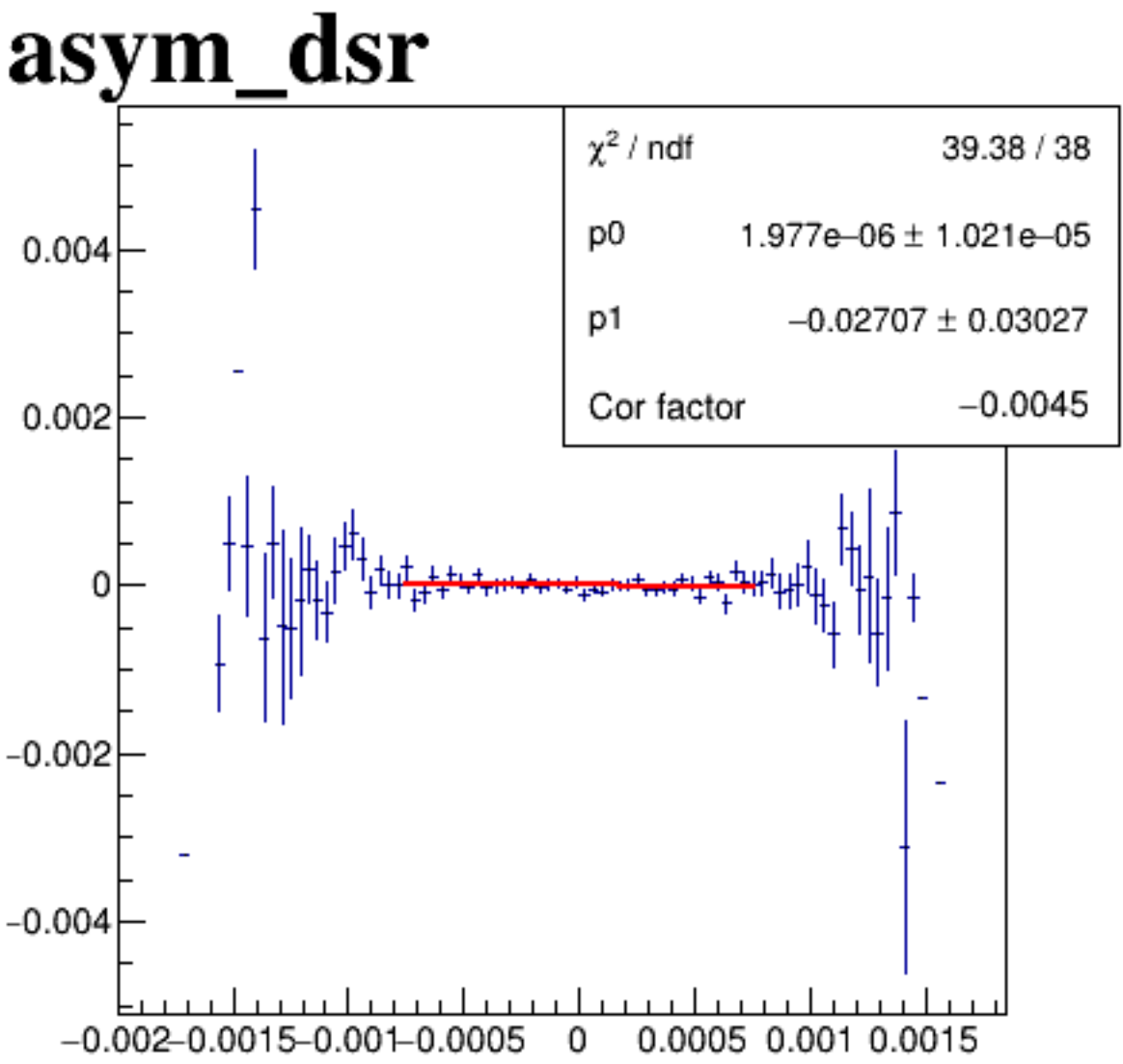
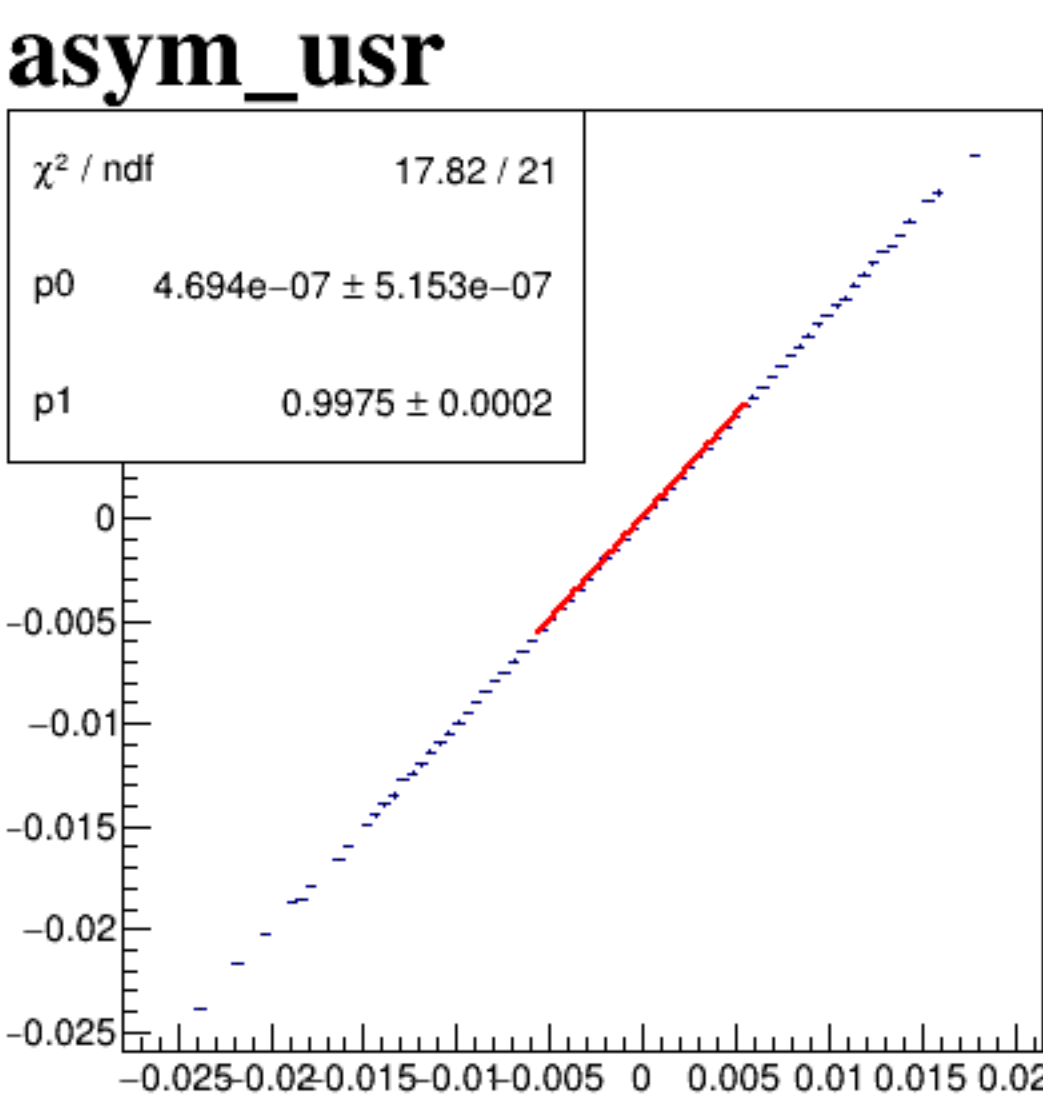
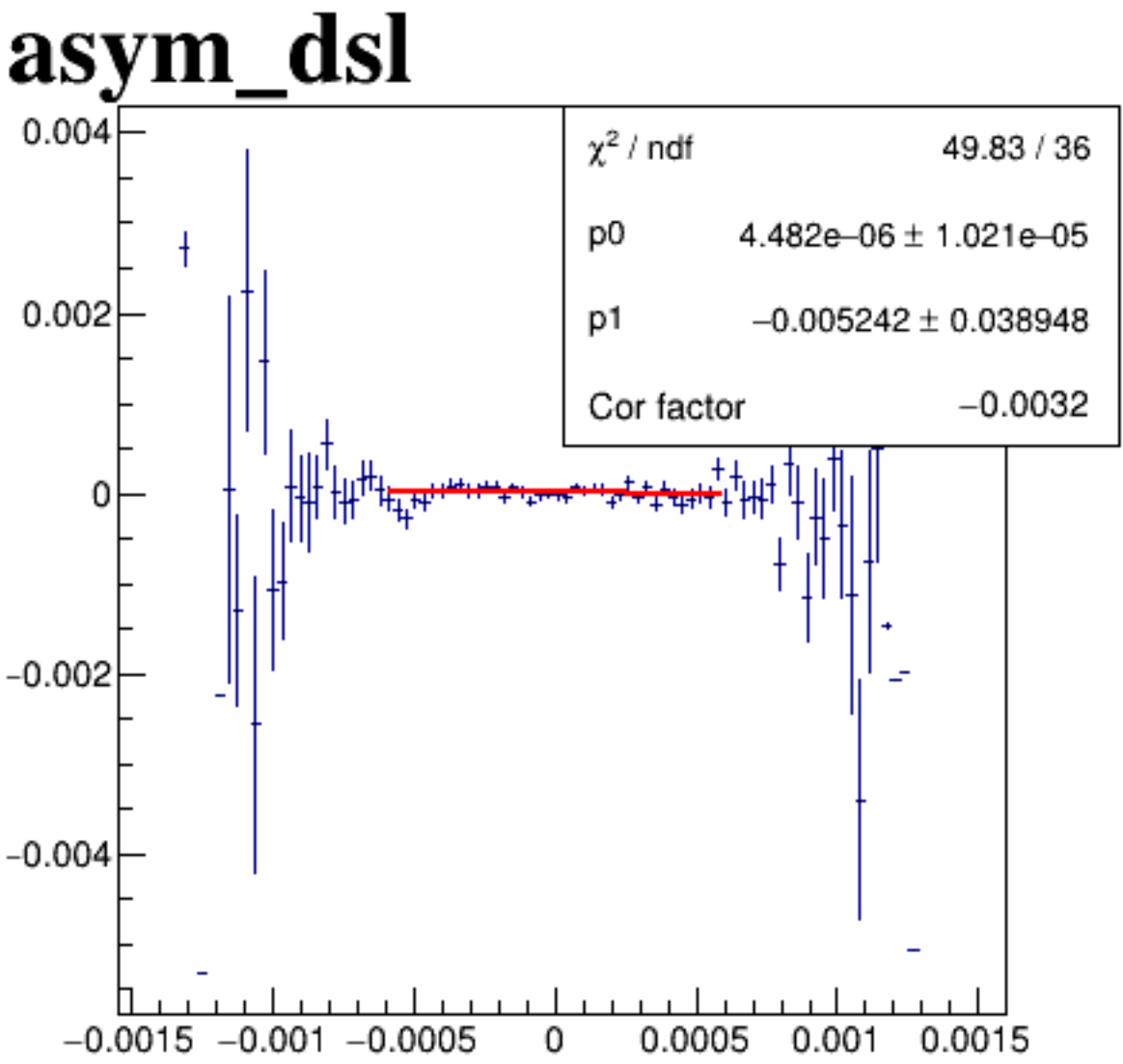
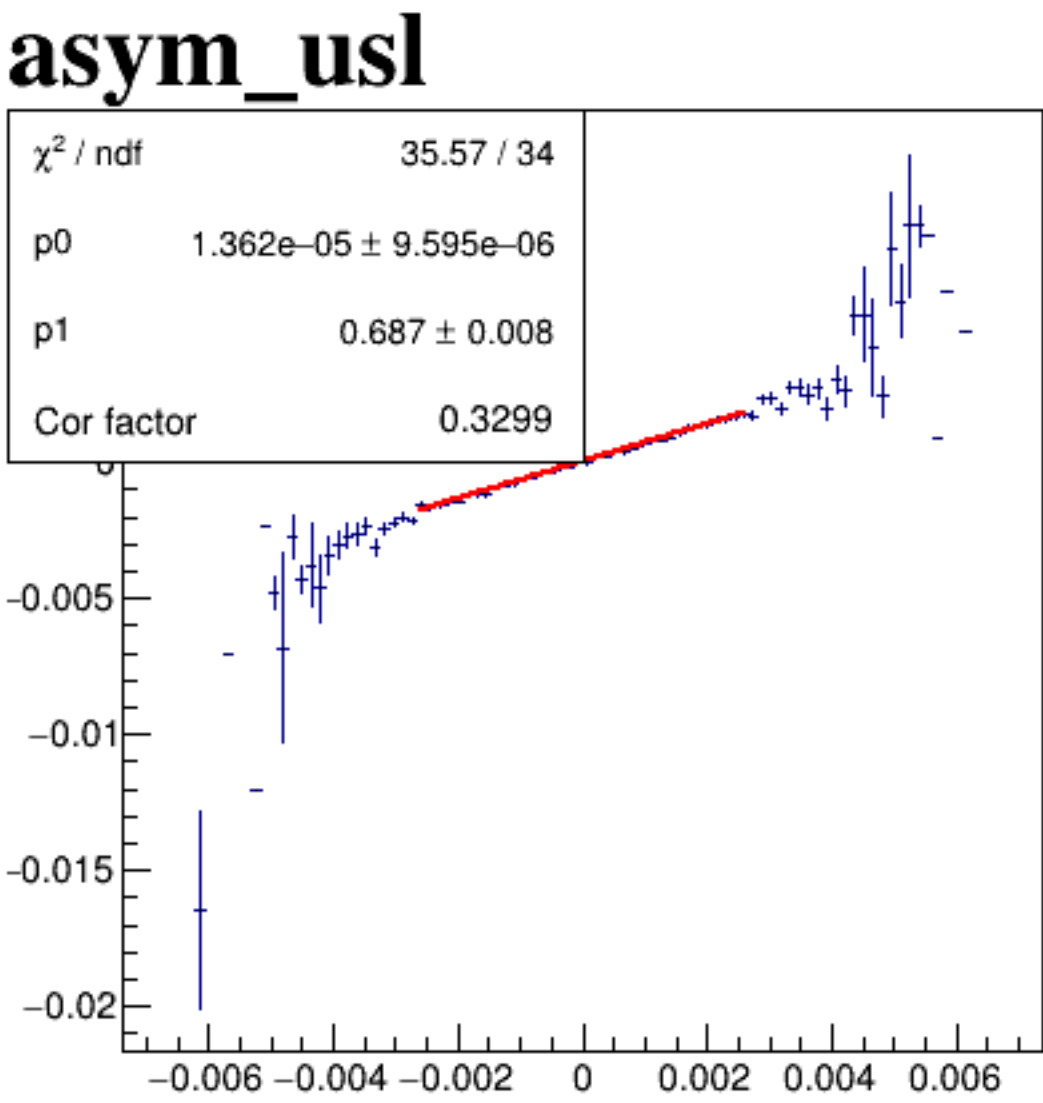
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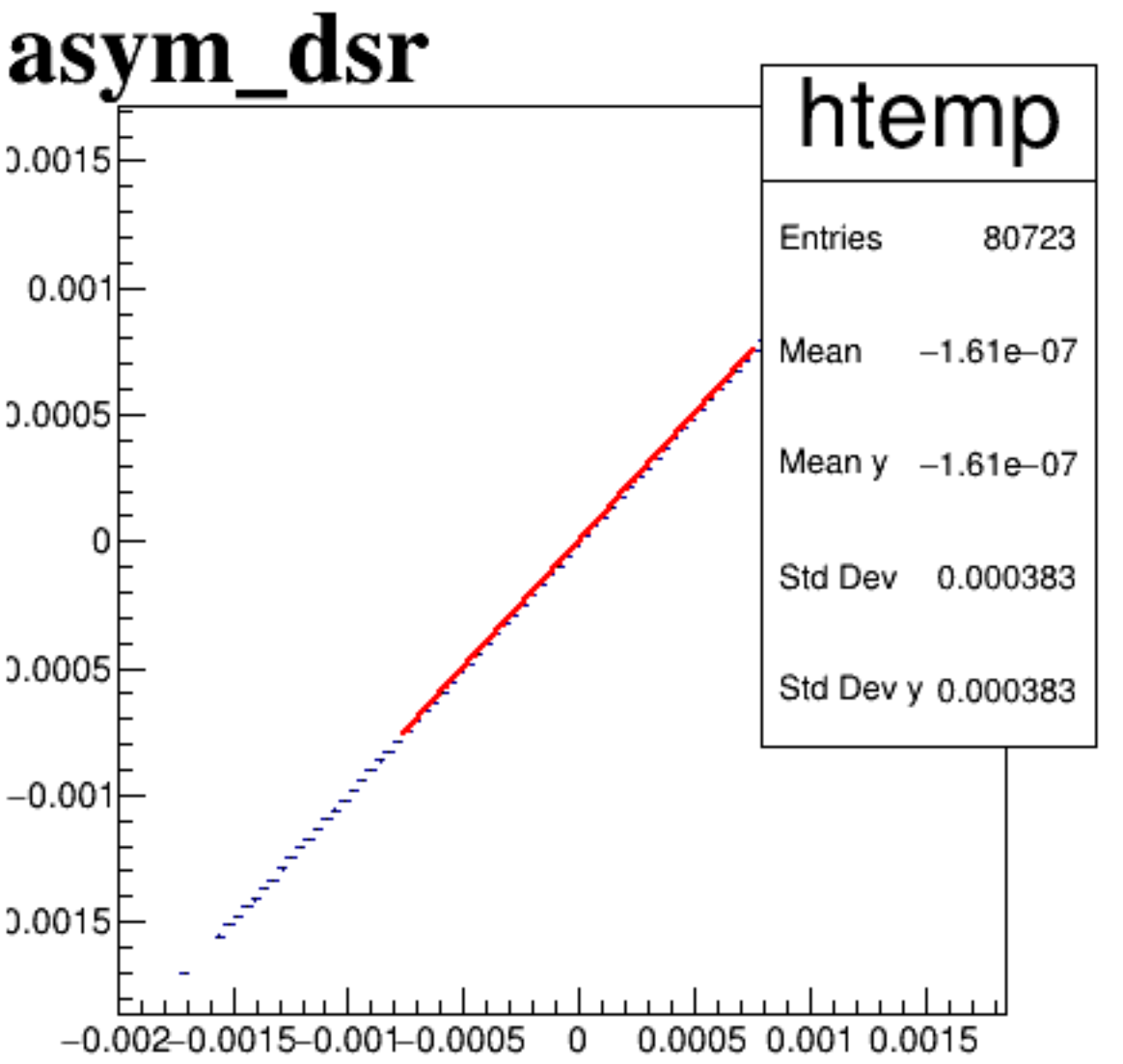
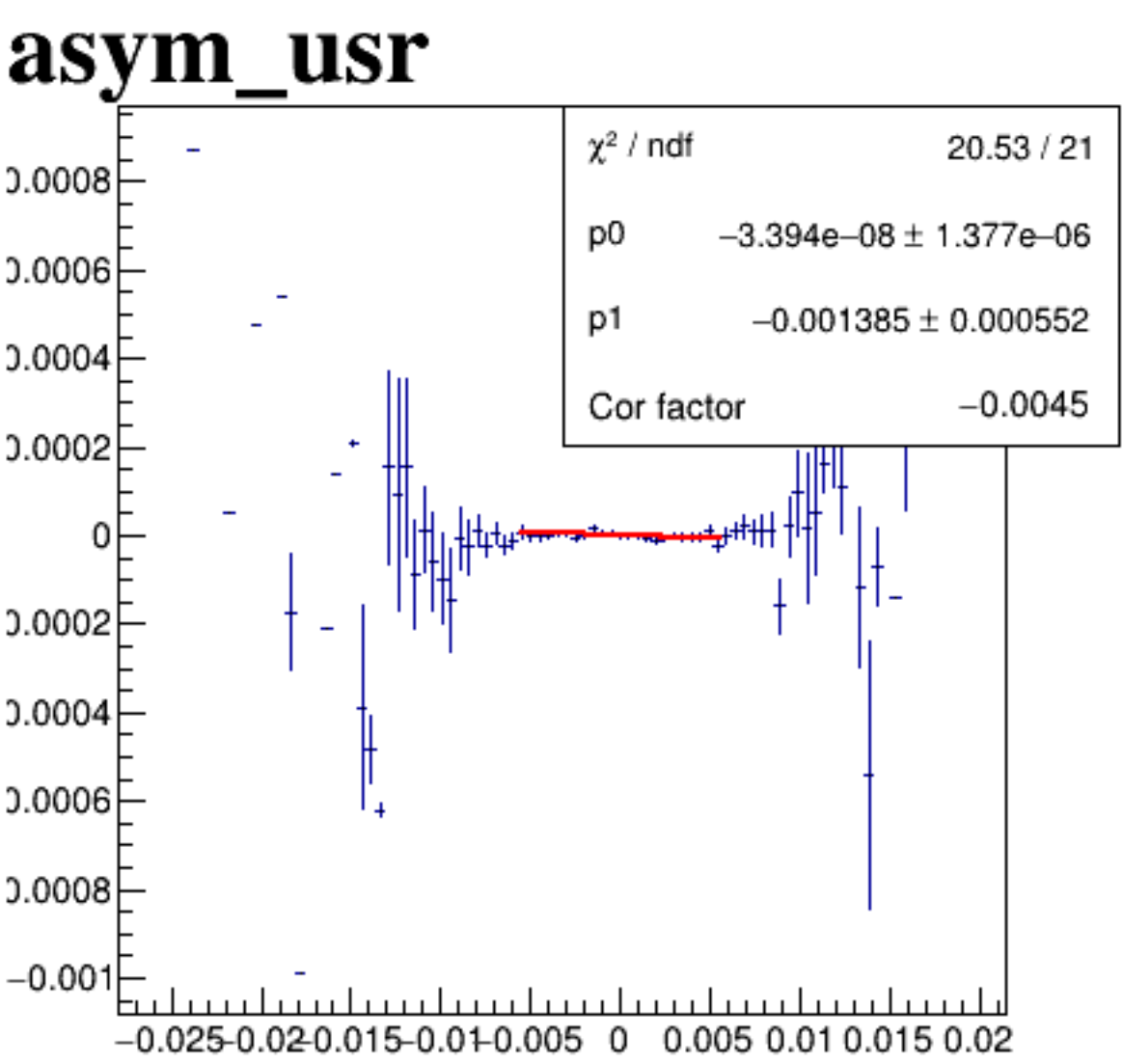
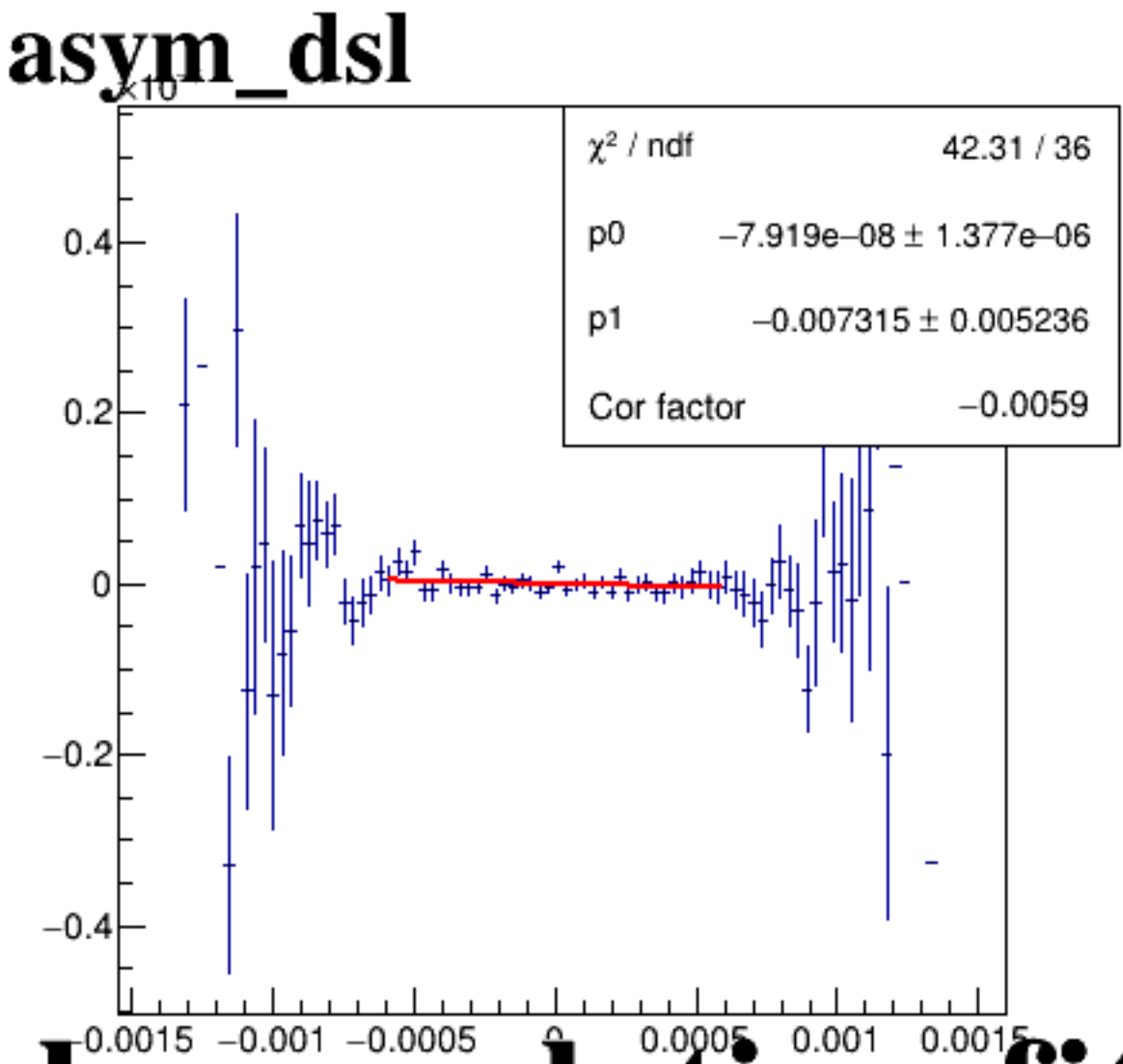
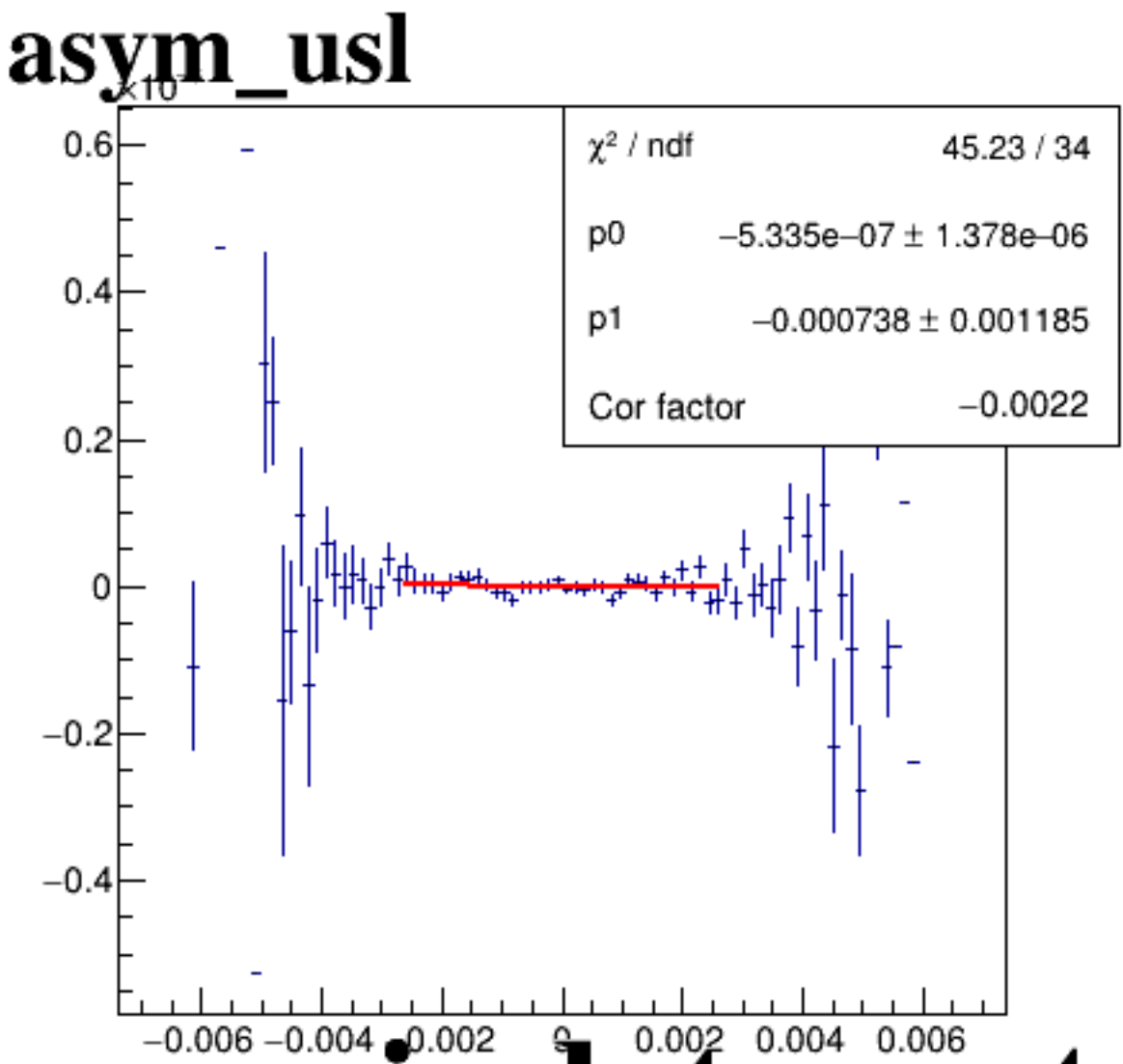
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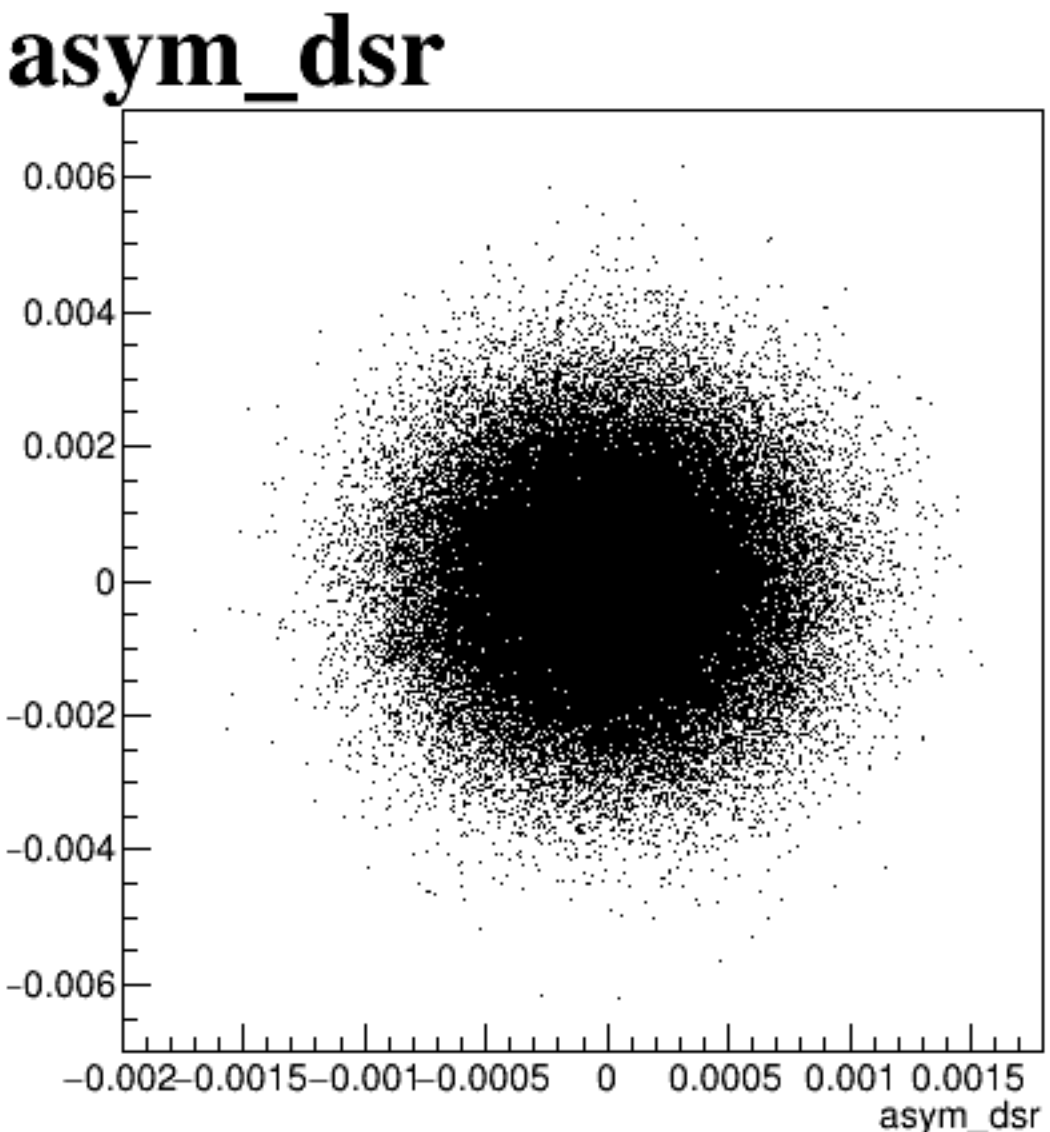
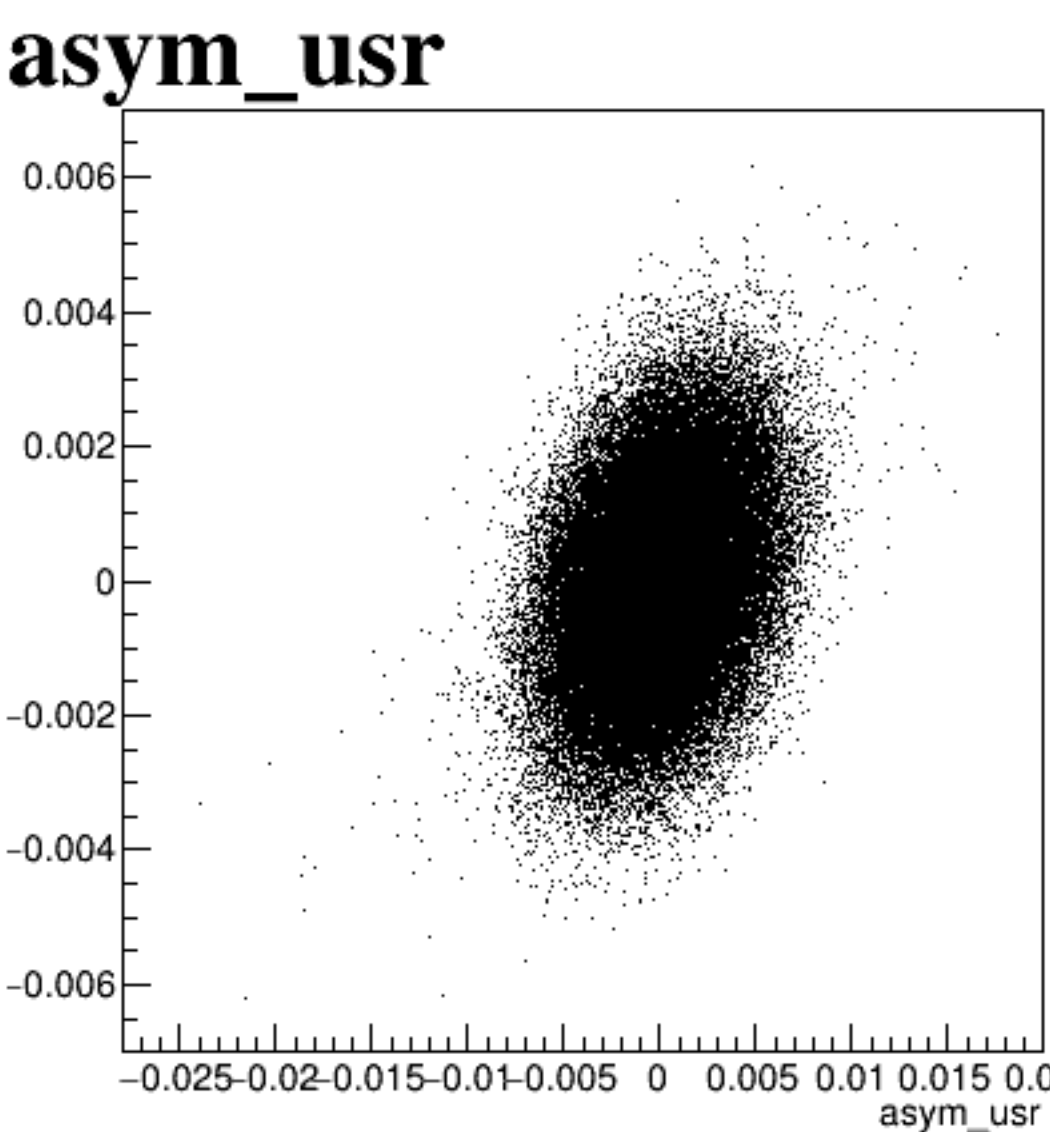
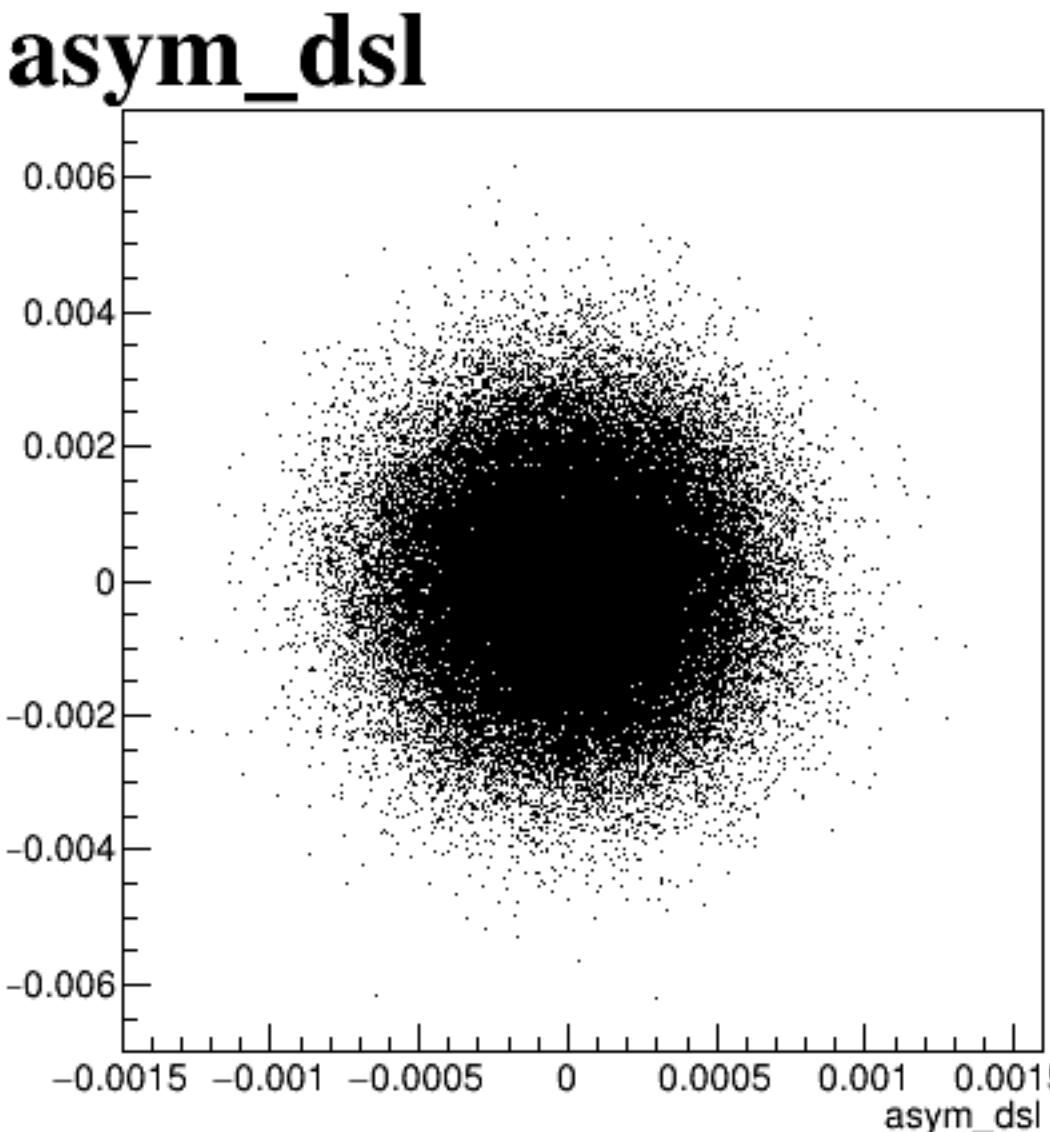
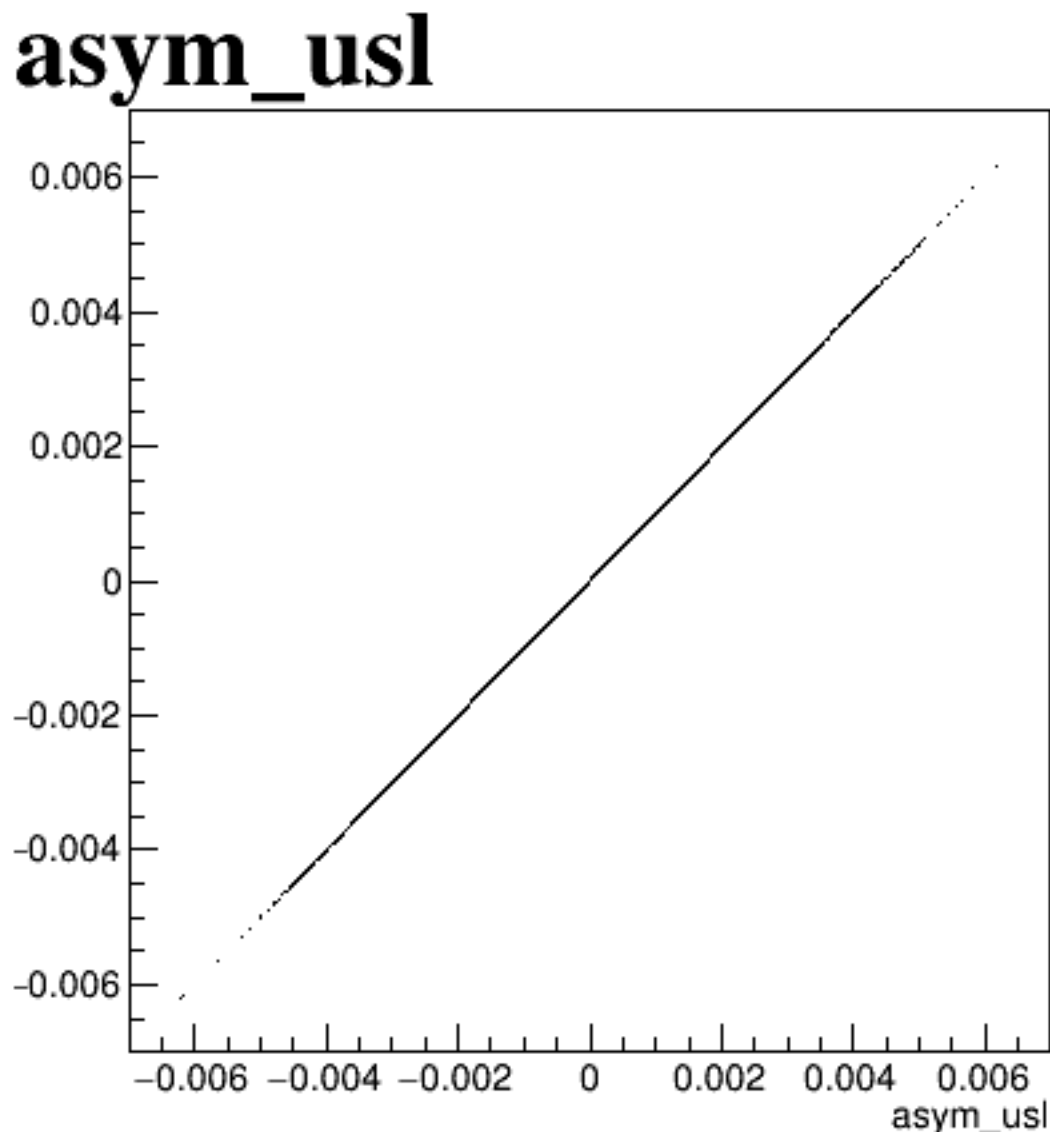
asym_usr



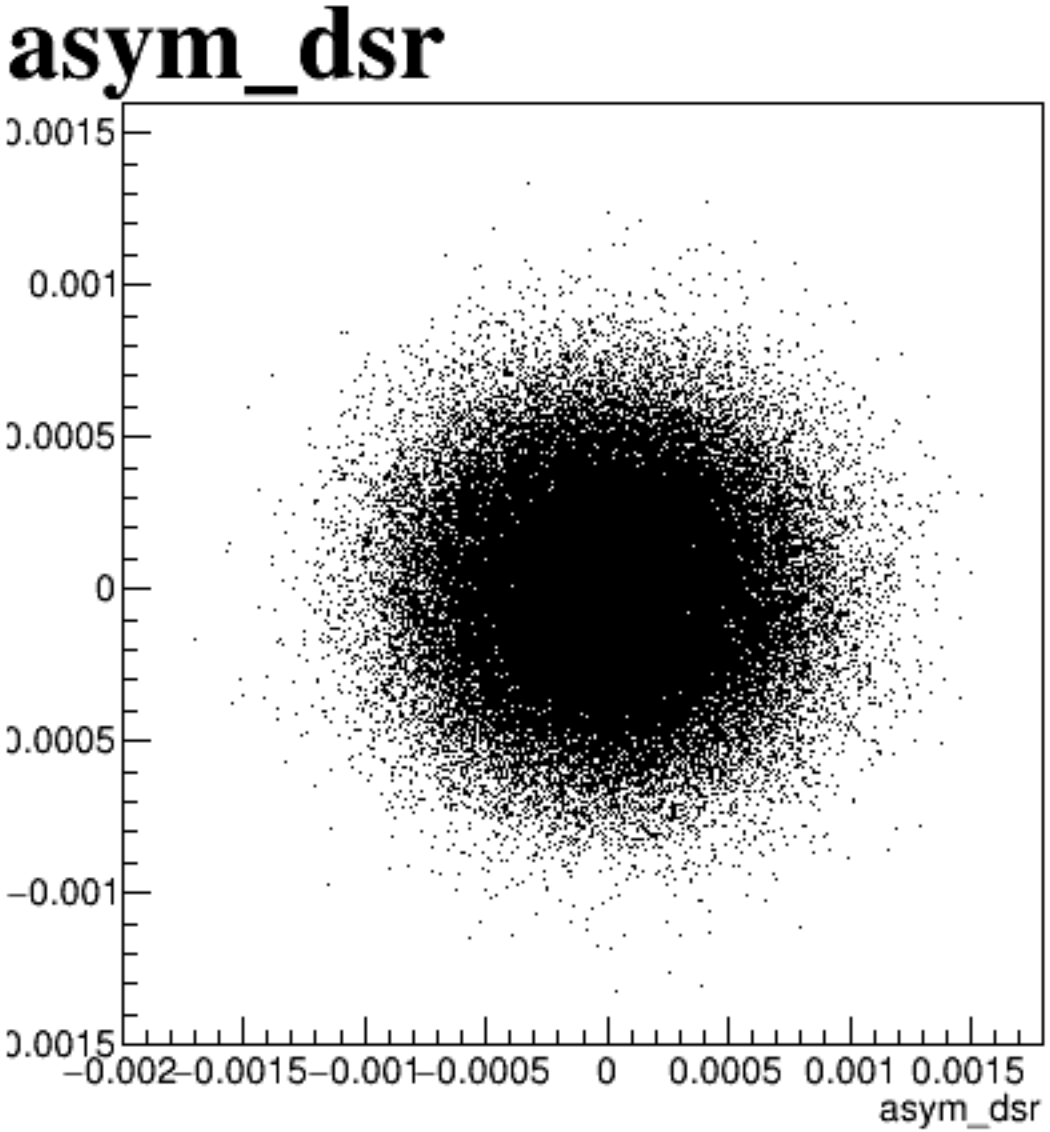
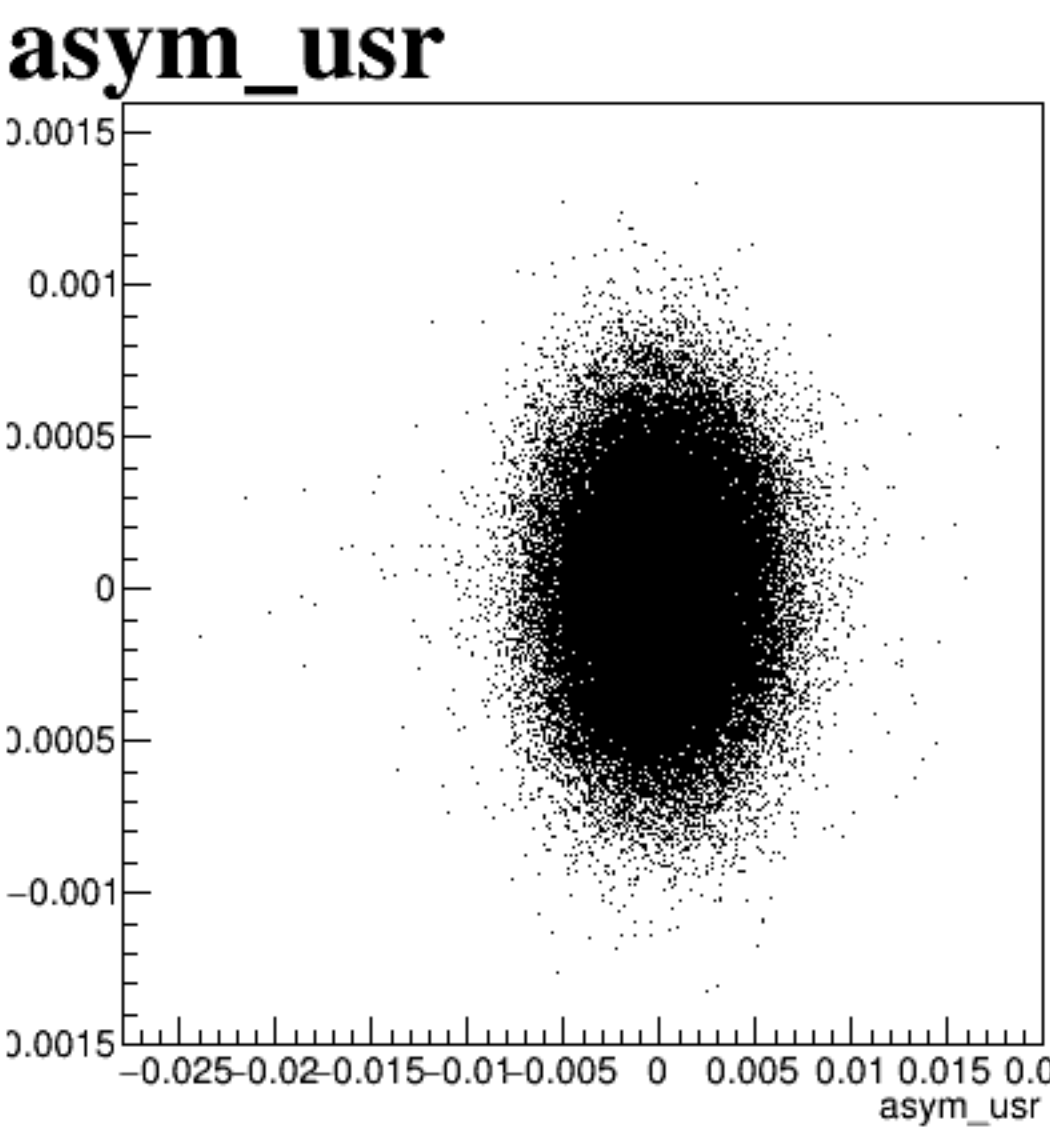
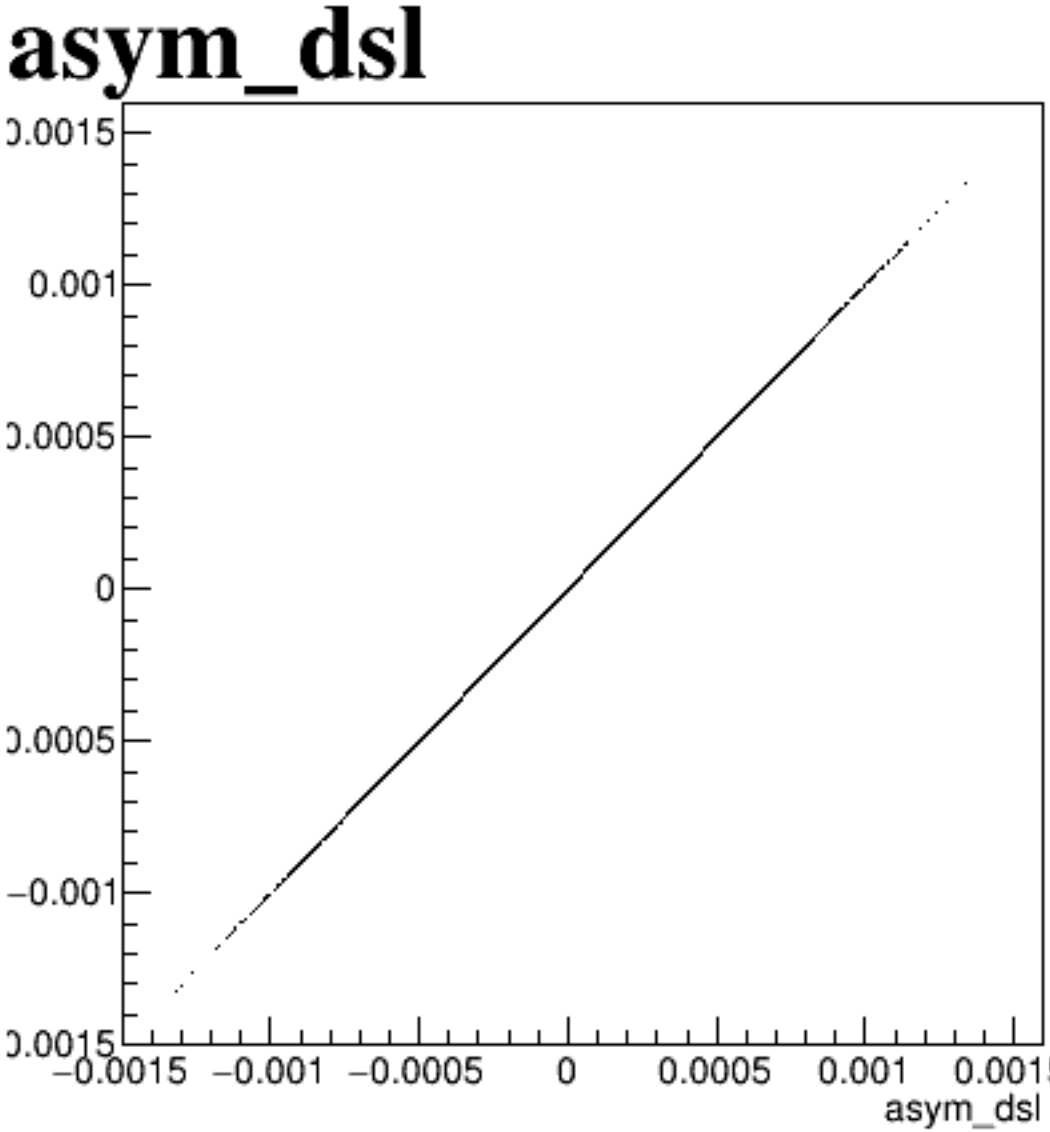
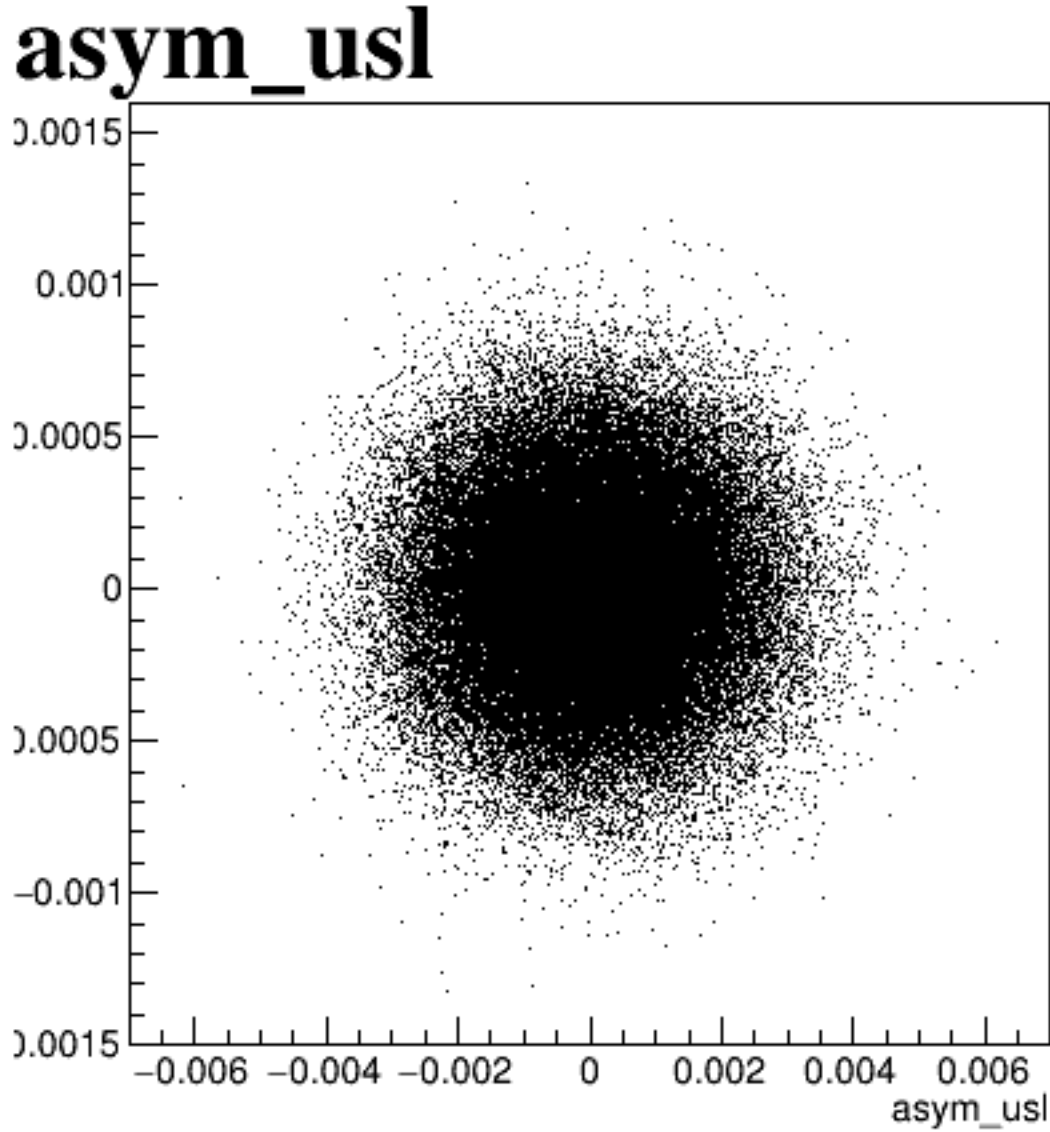
asym_dsr



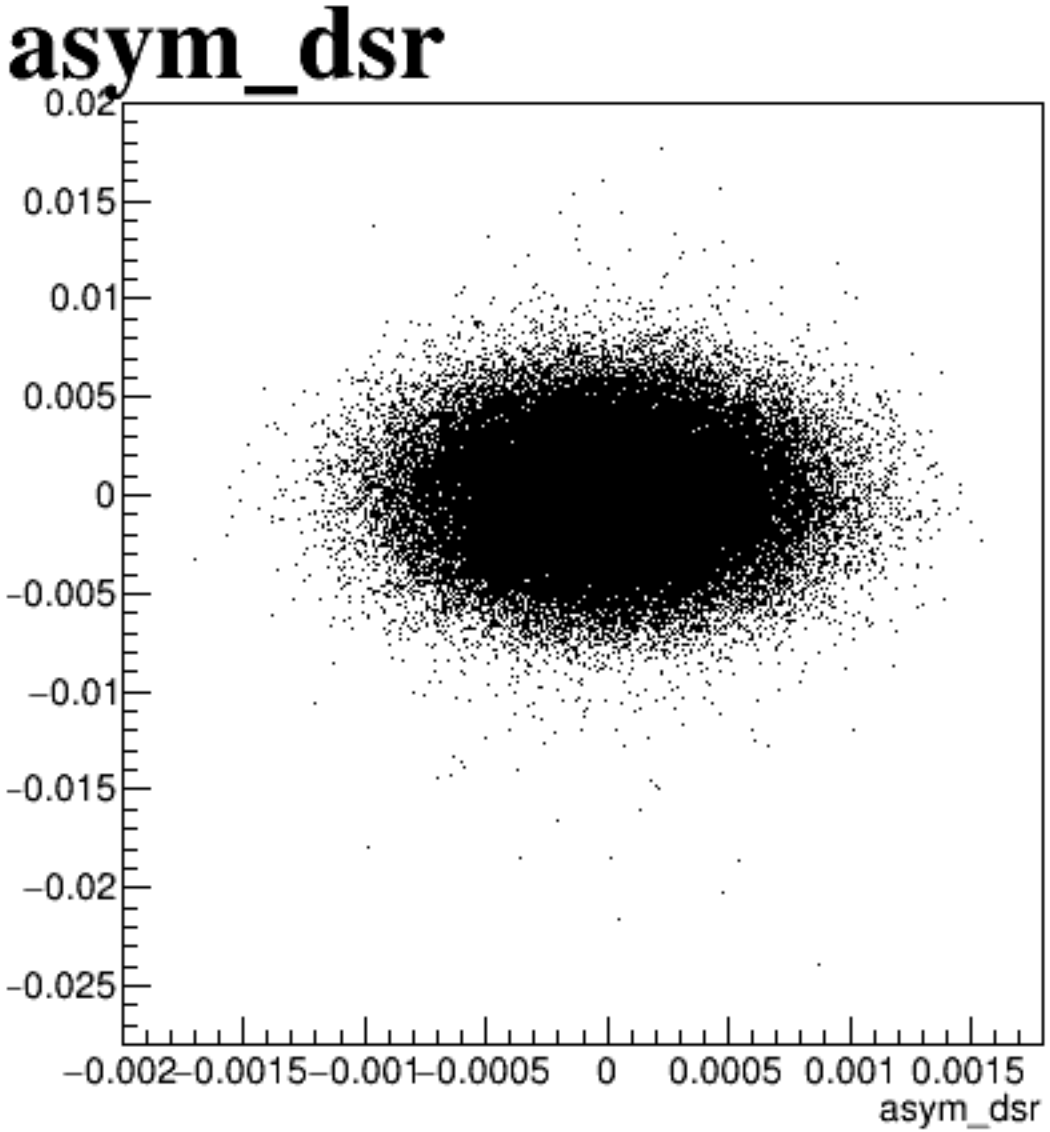
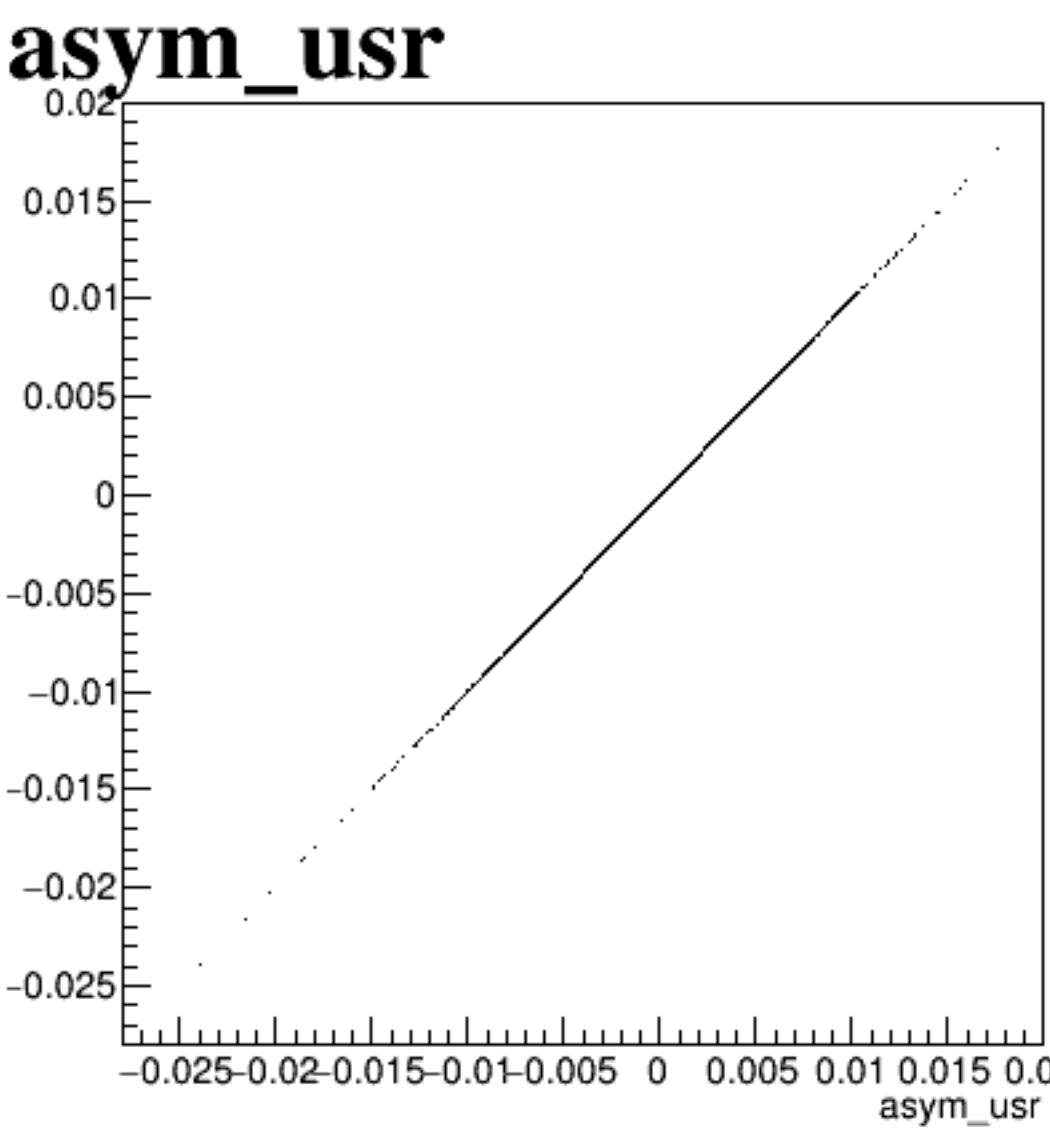
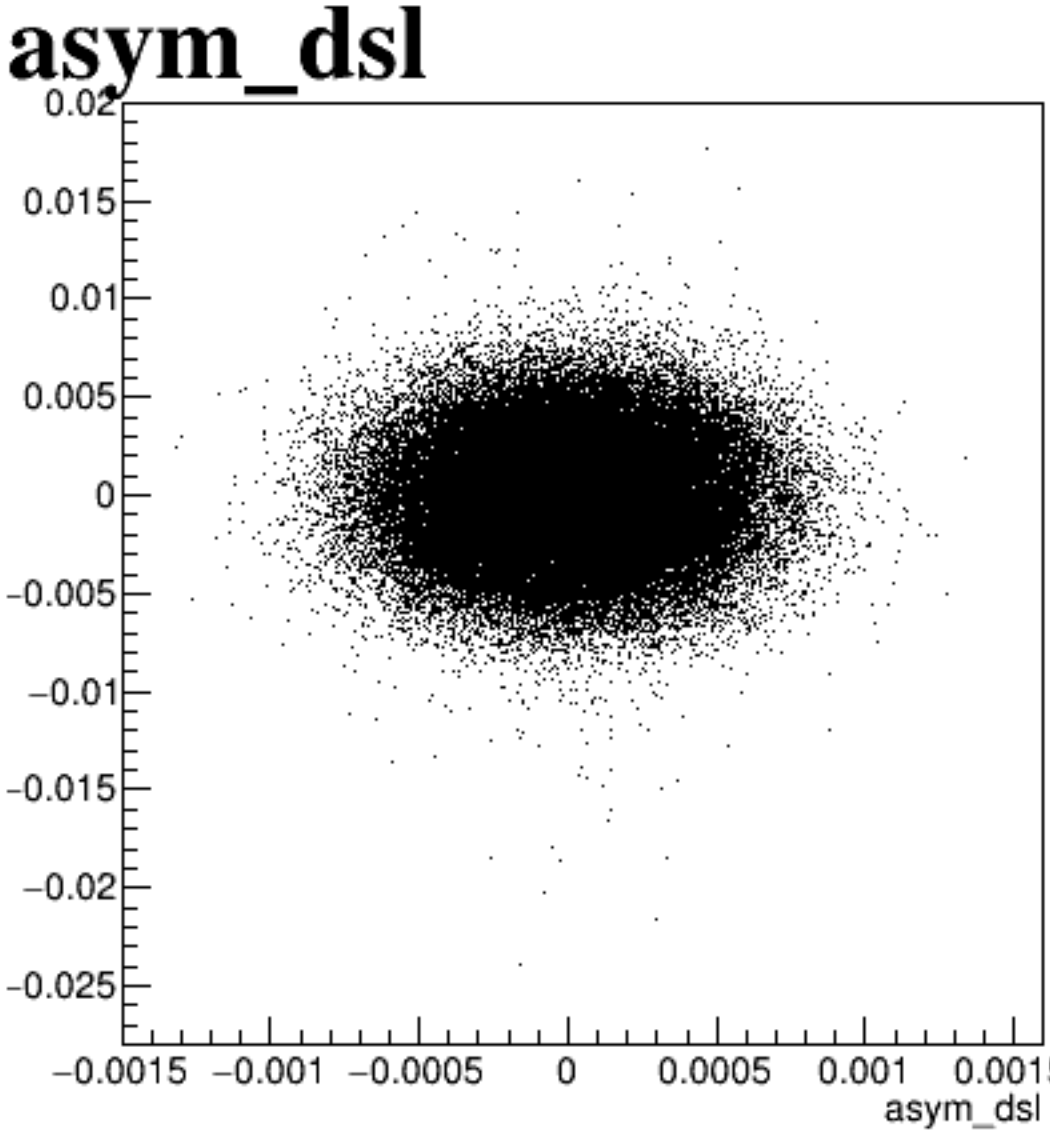
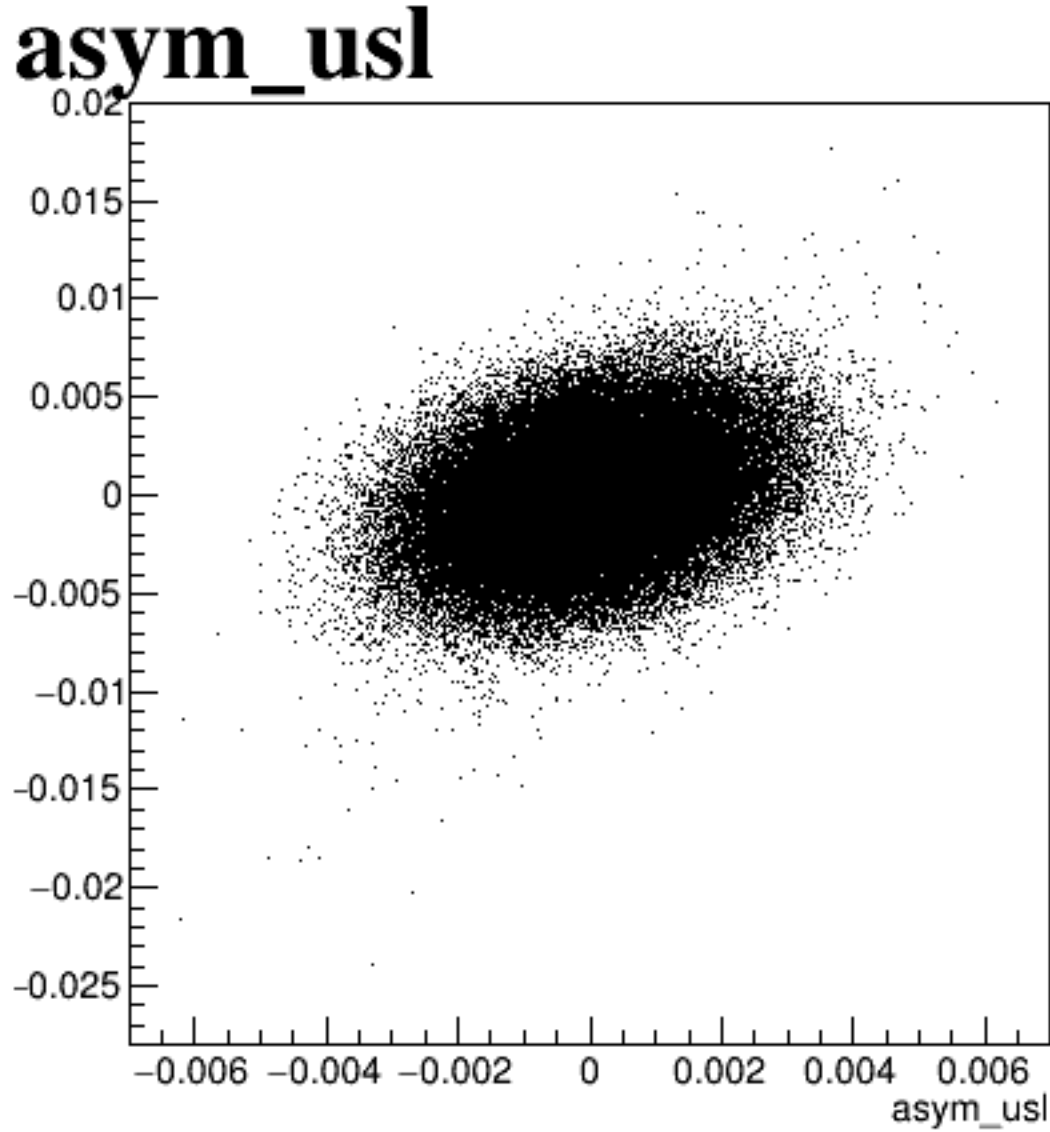
asym_usl



asym_dsl



asym_usr



asym_dsr

