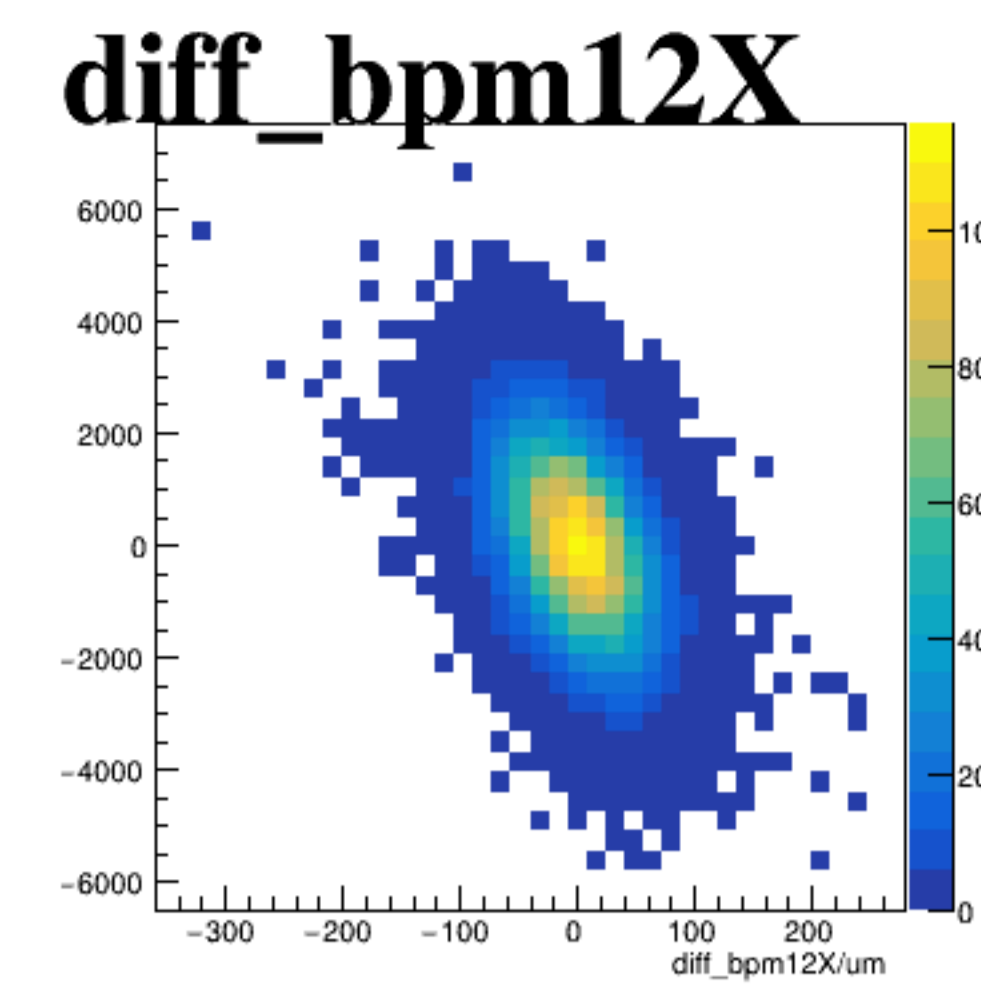
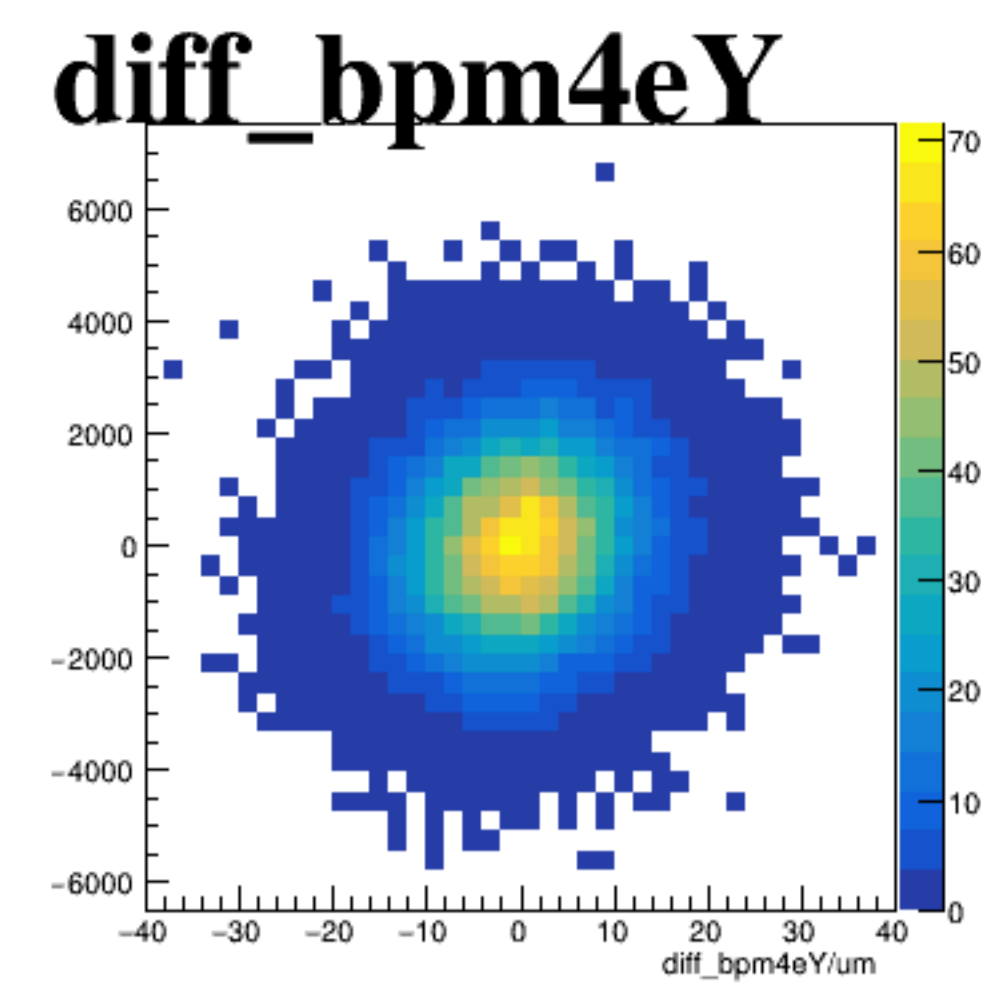
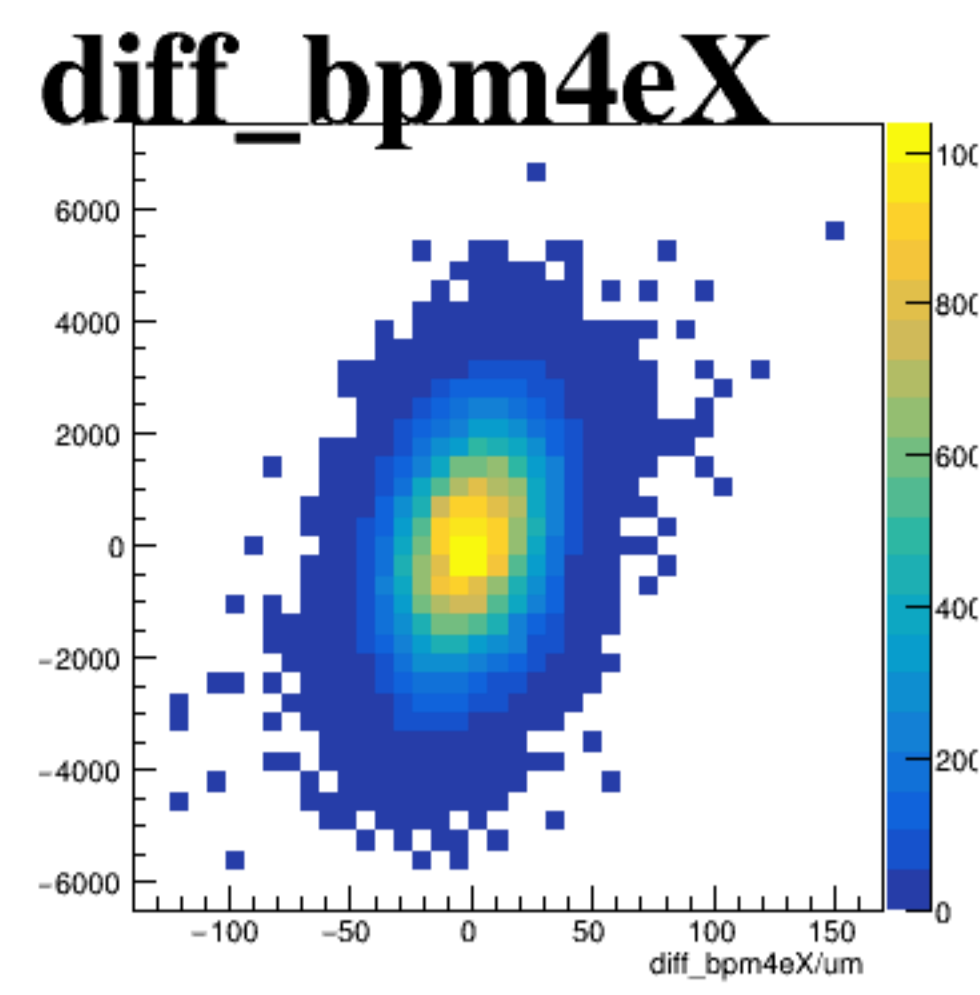
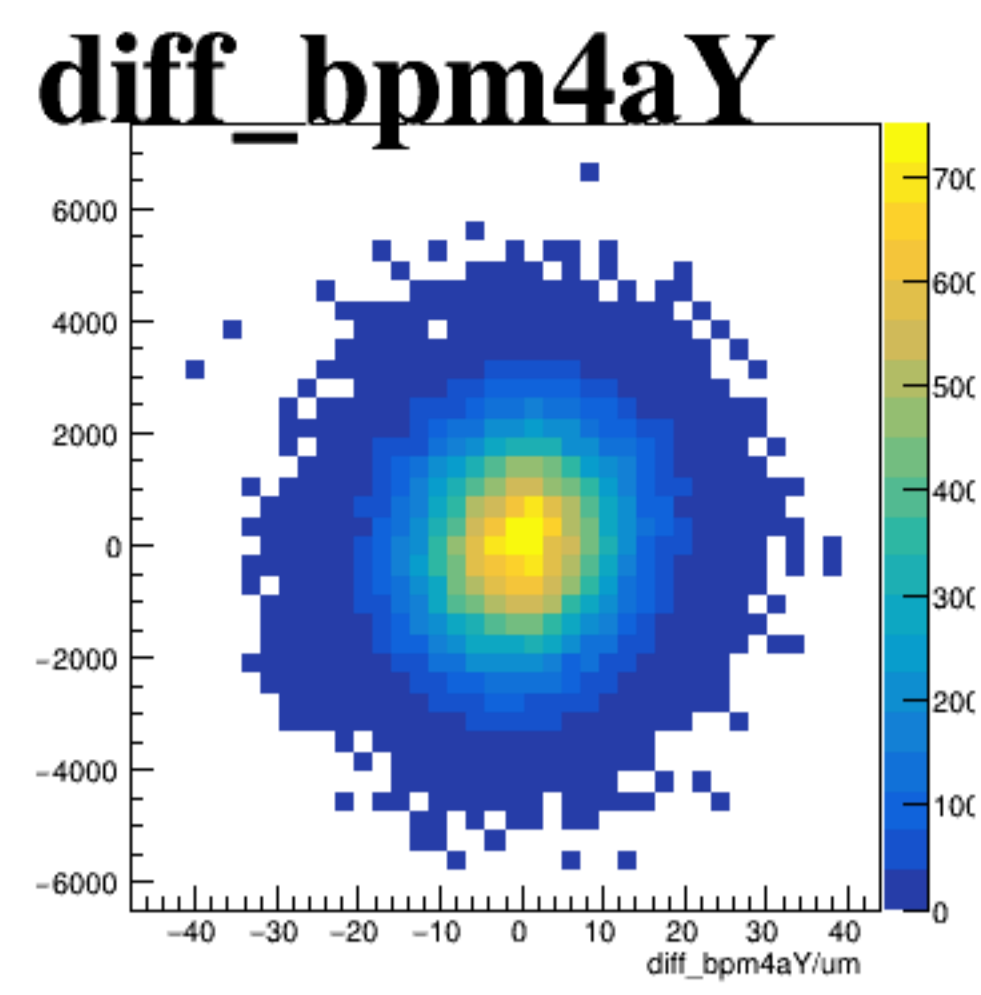
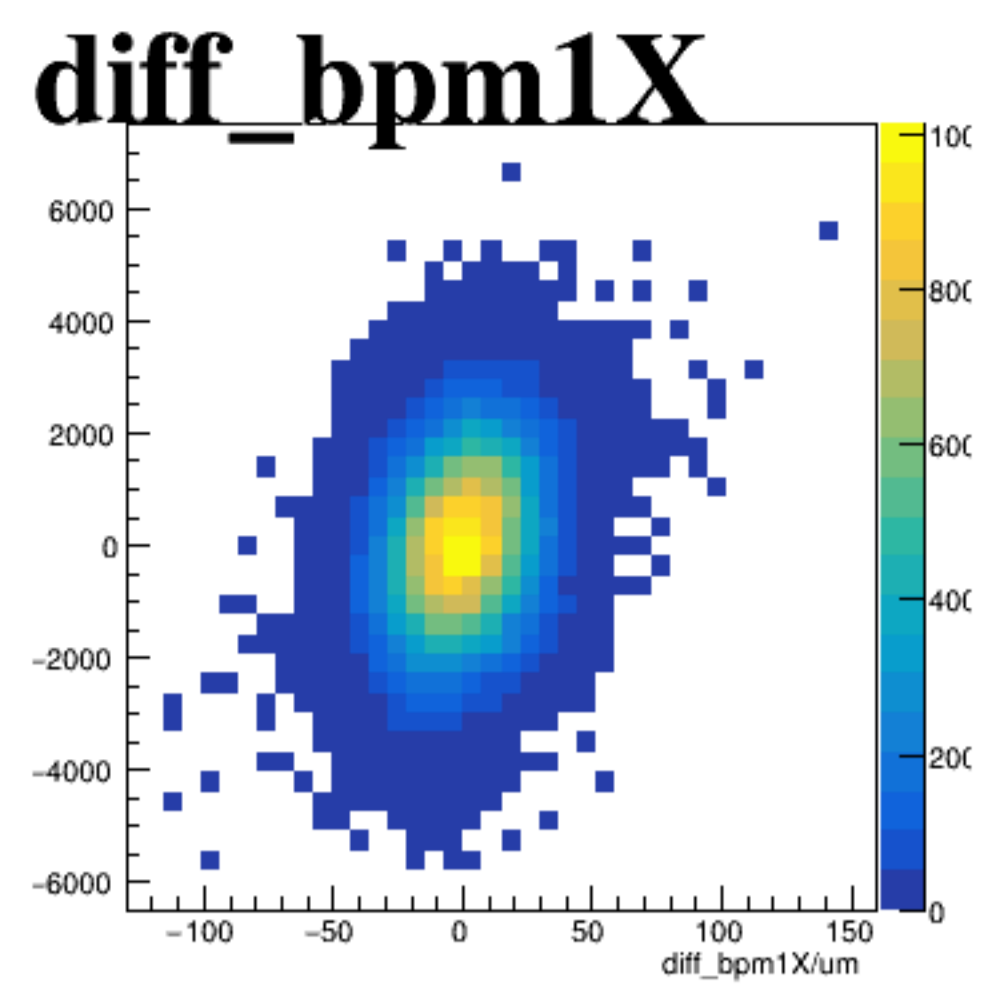
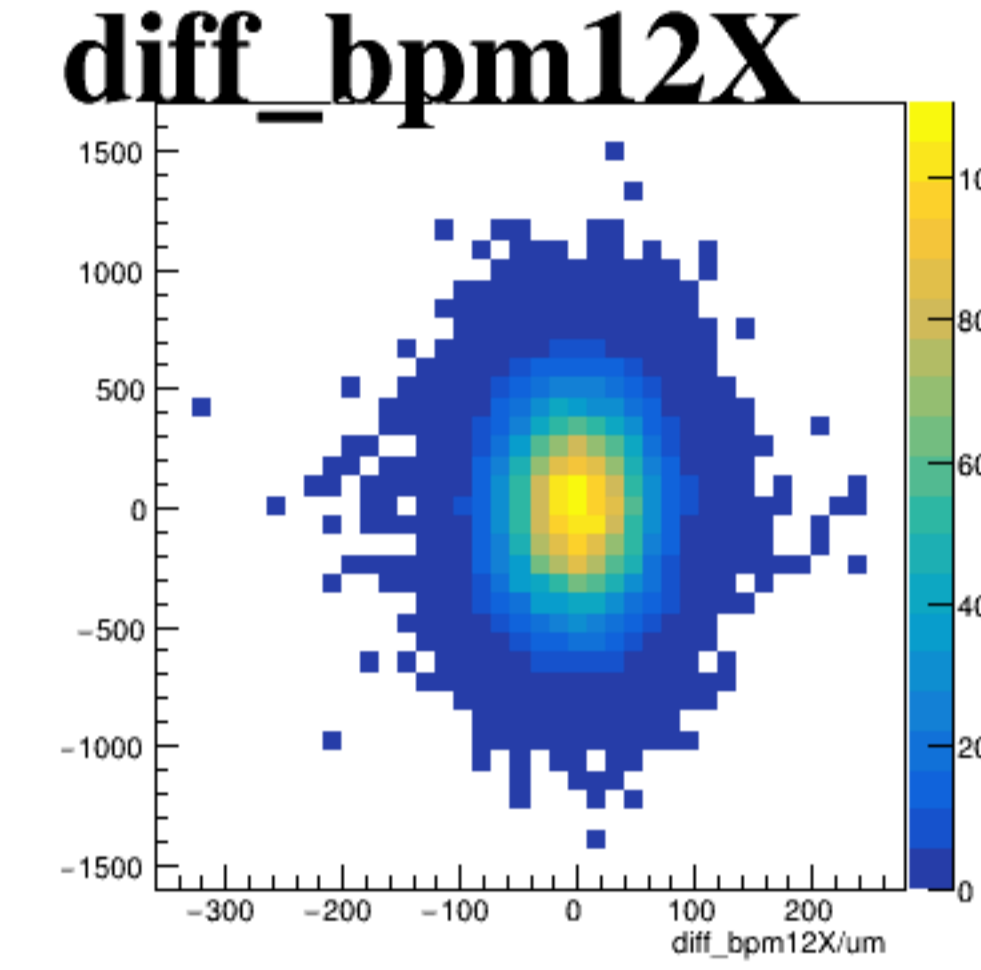
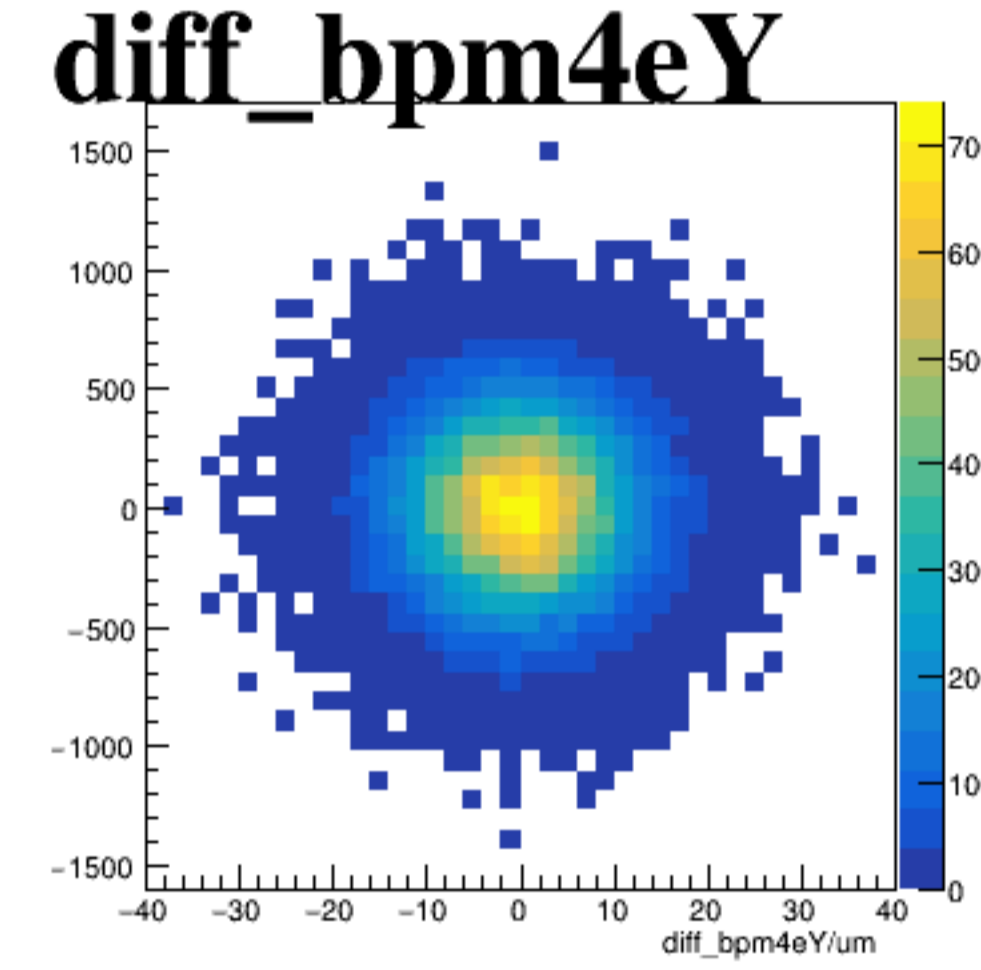
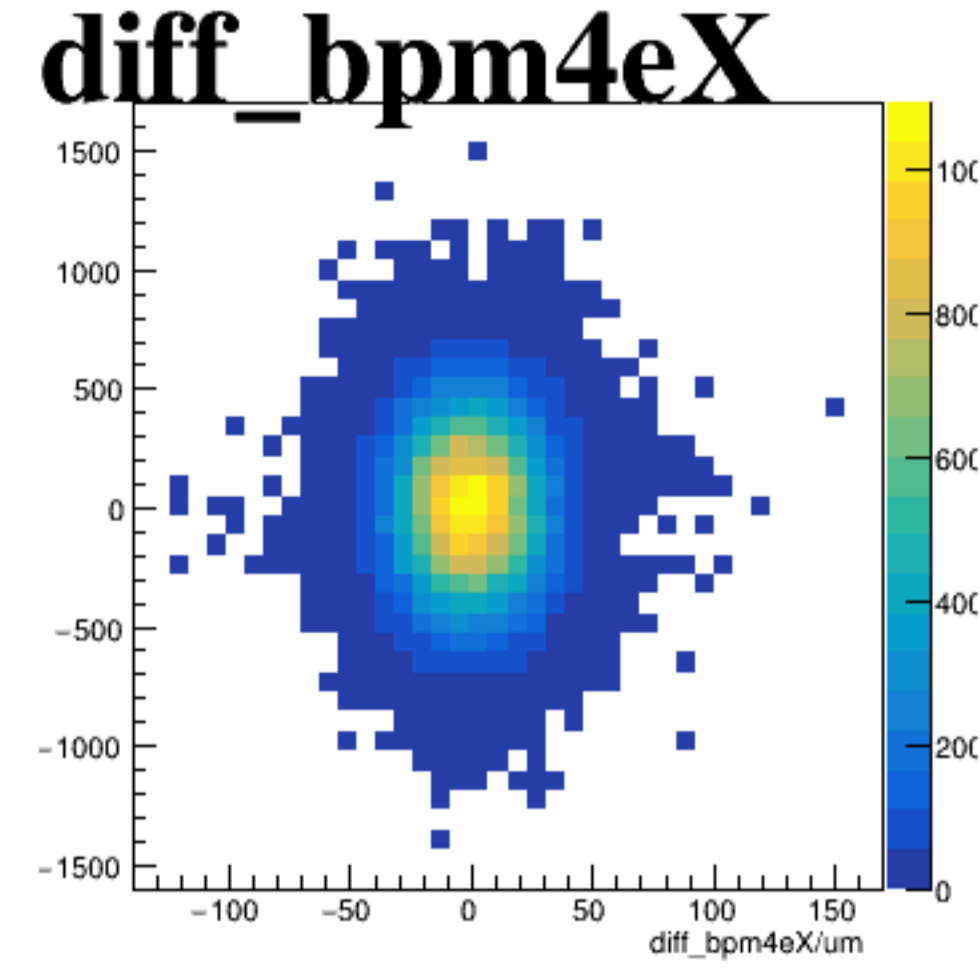
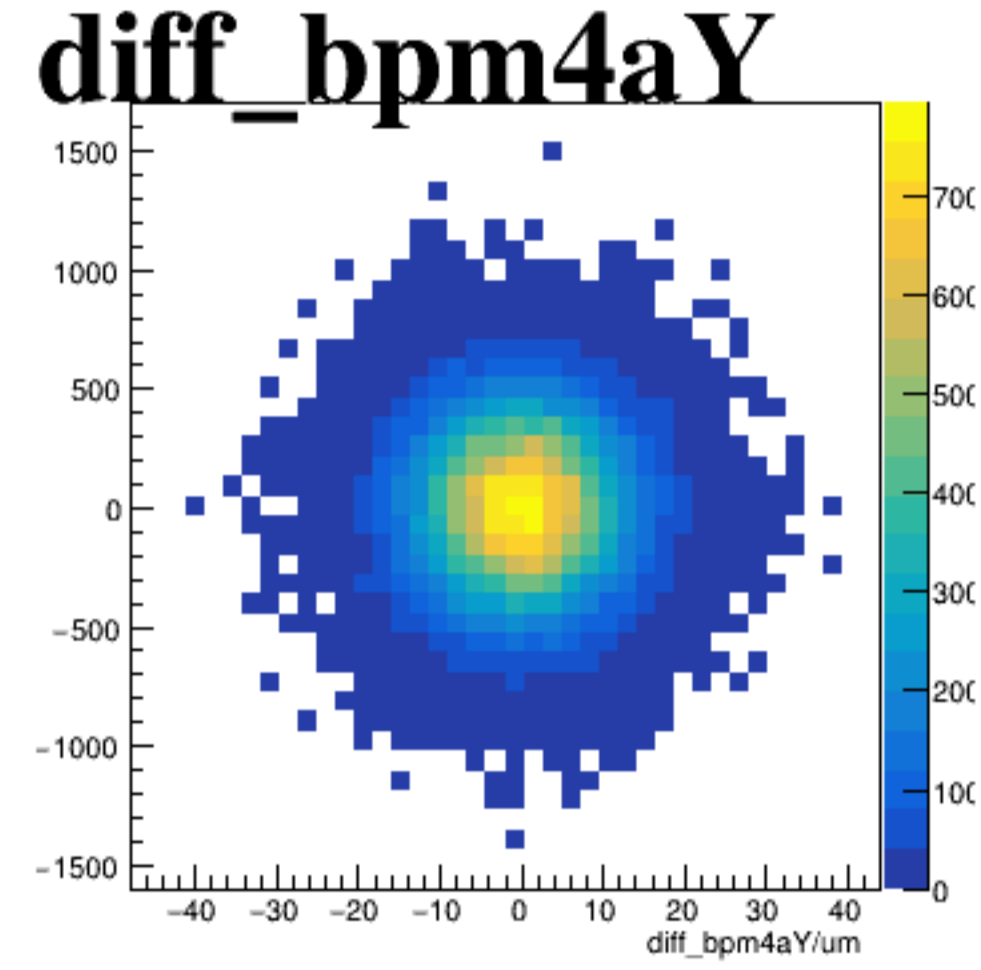
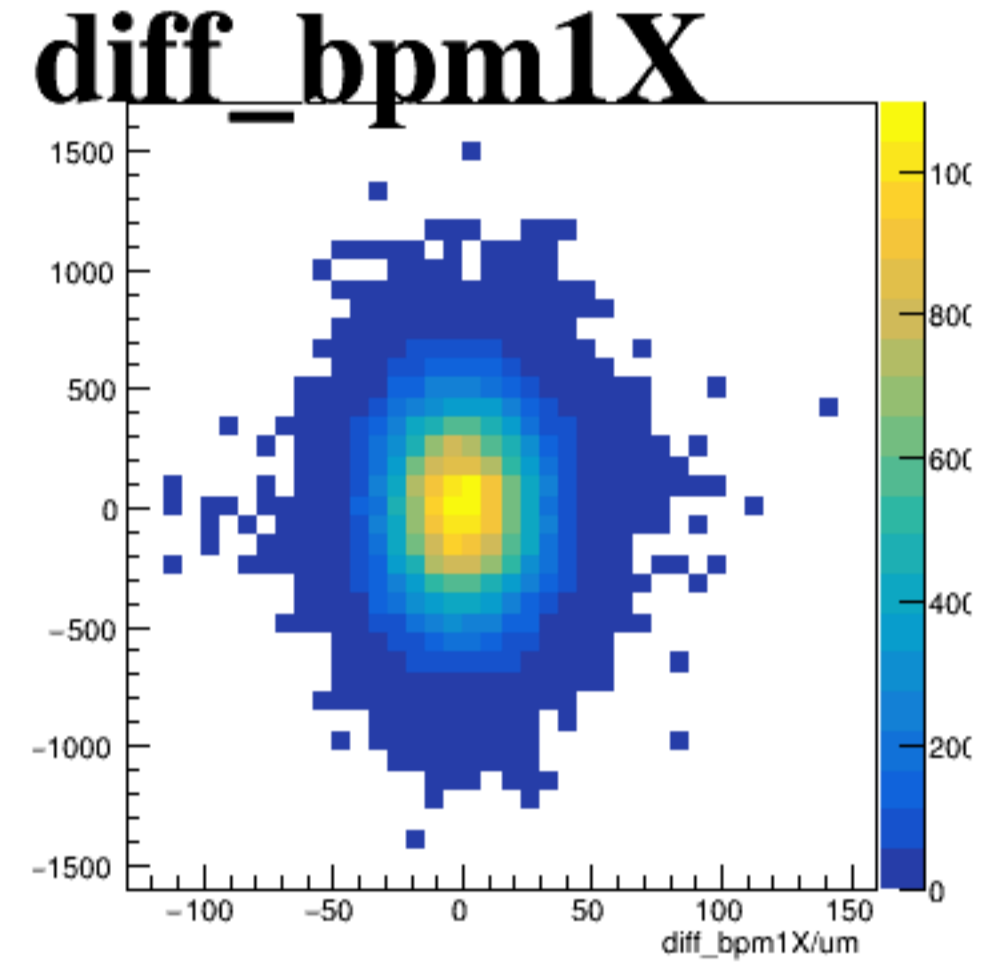


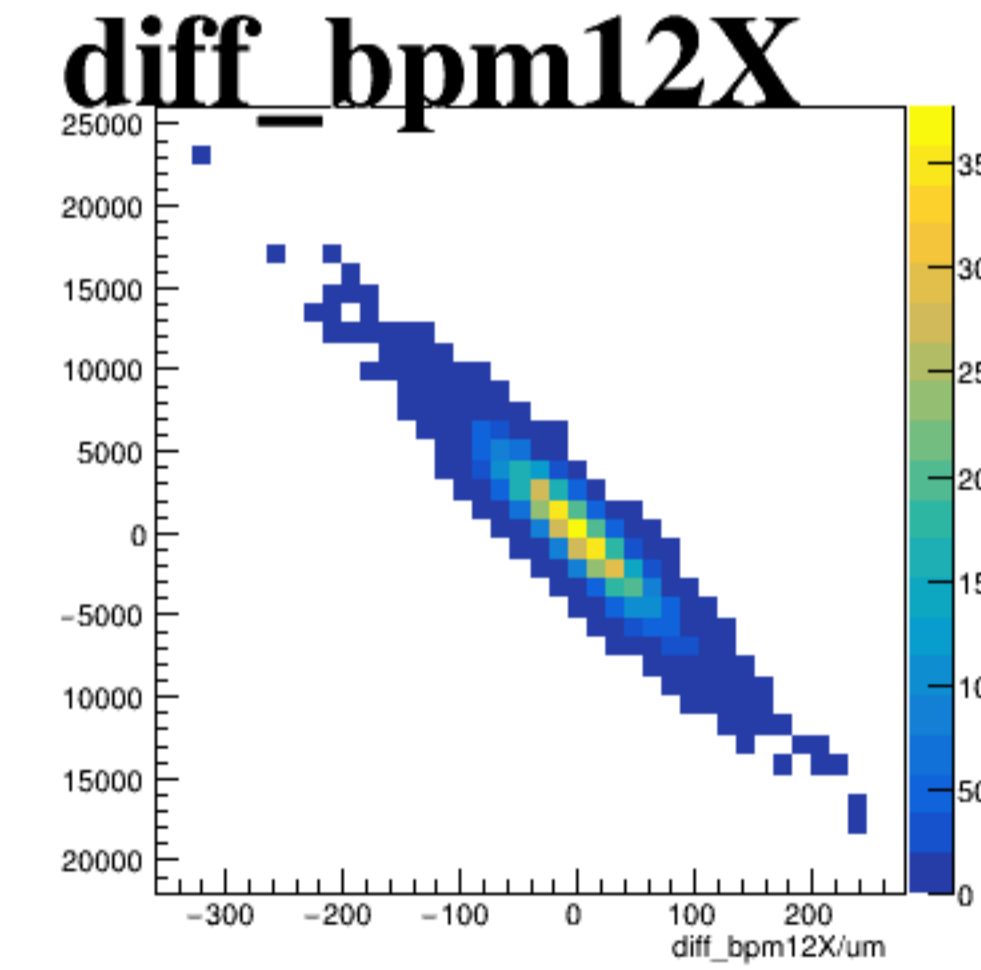
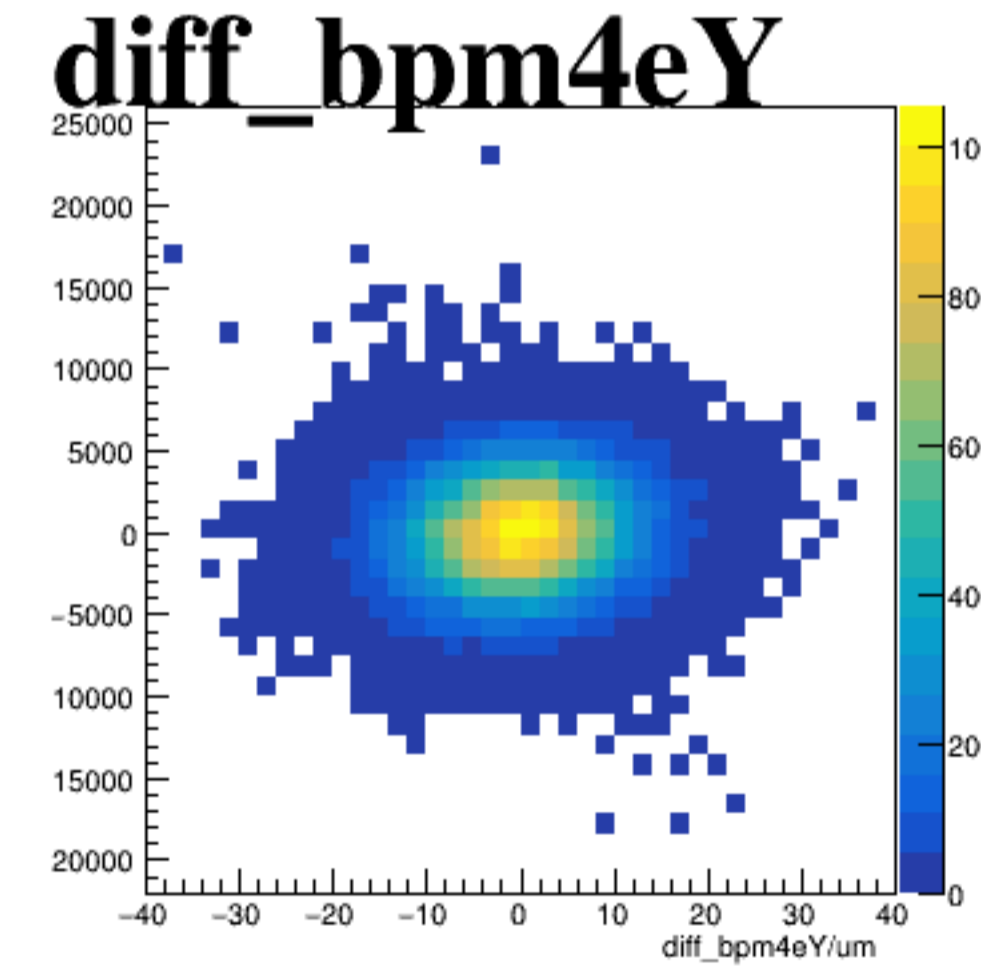
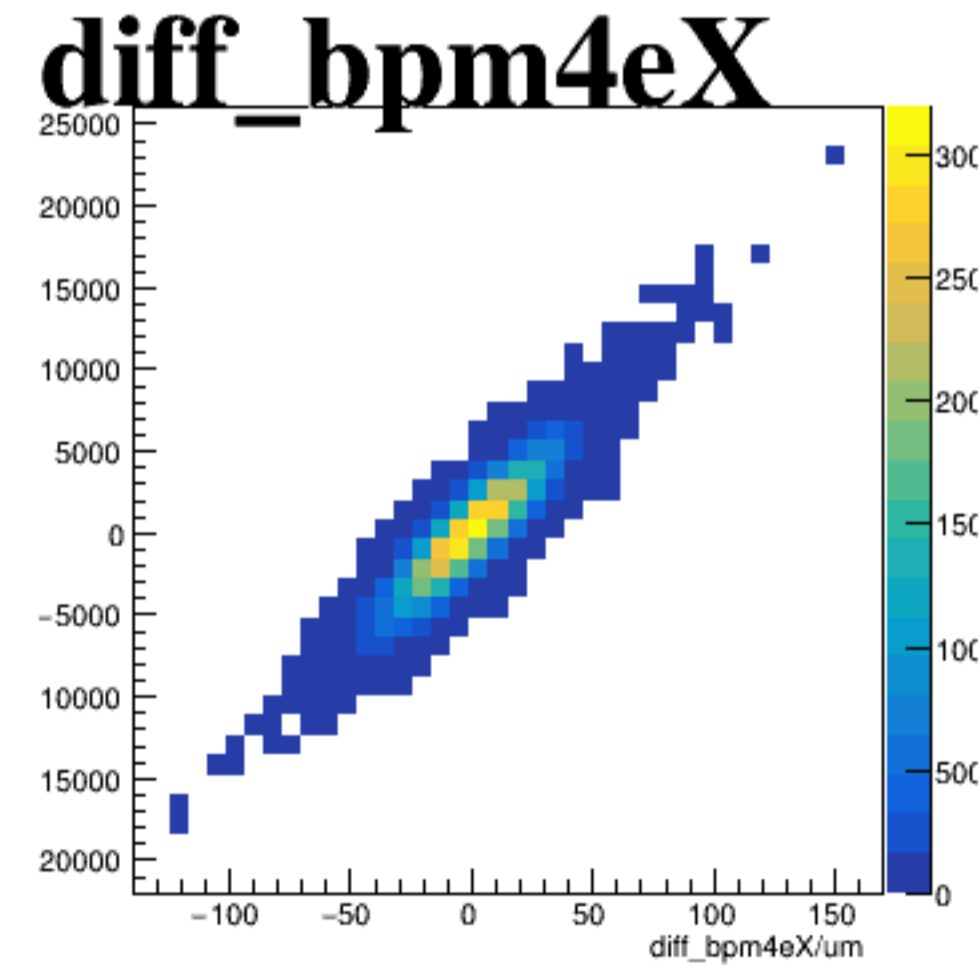
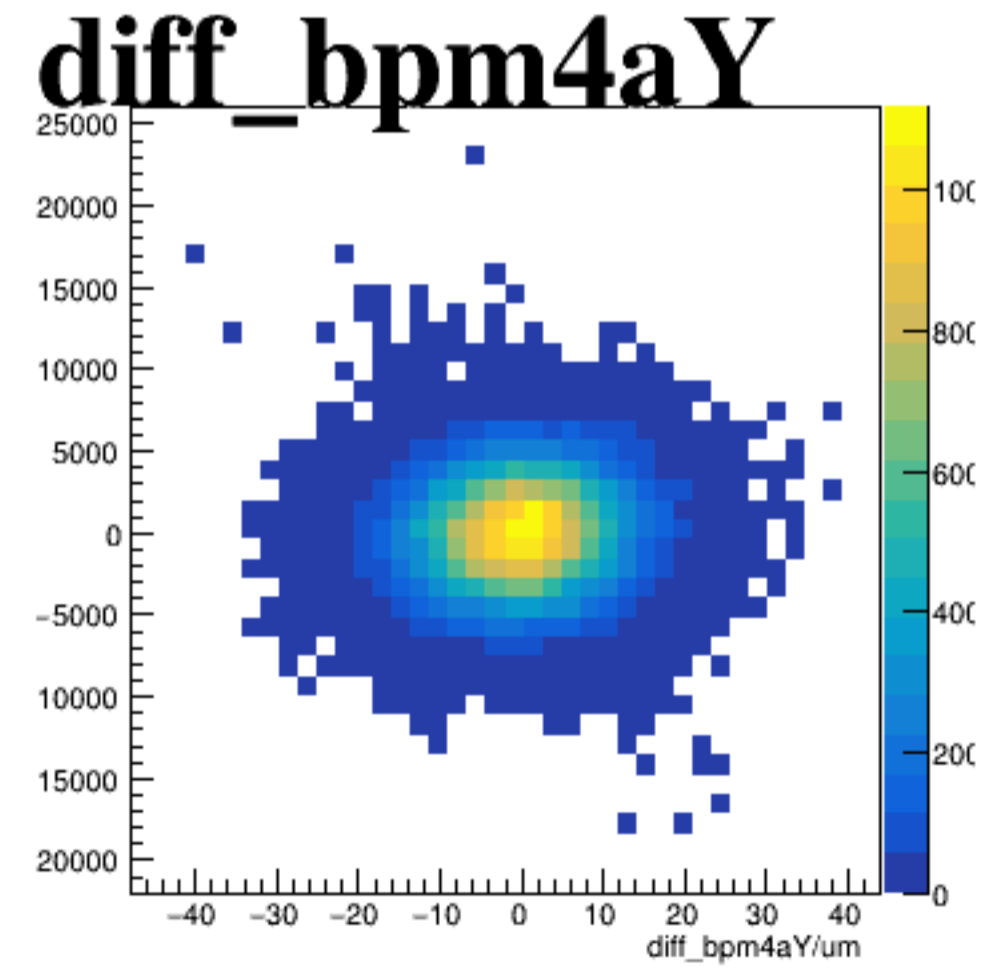
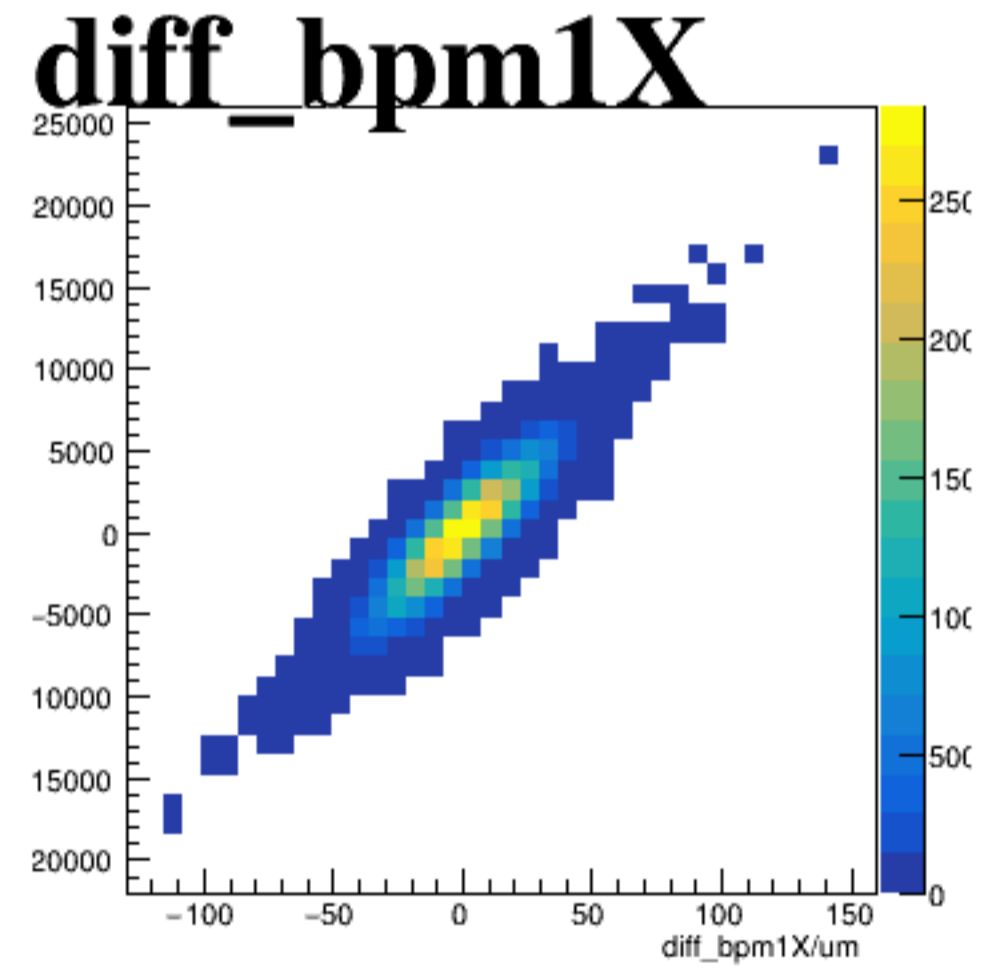
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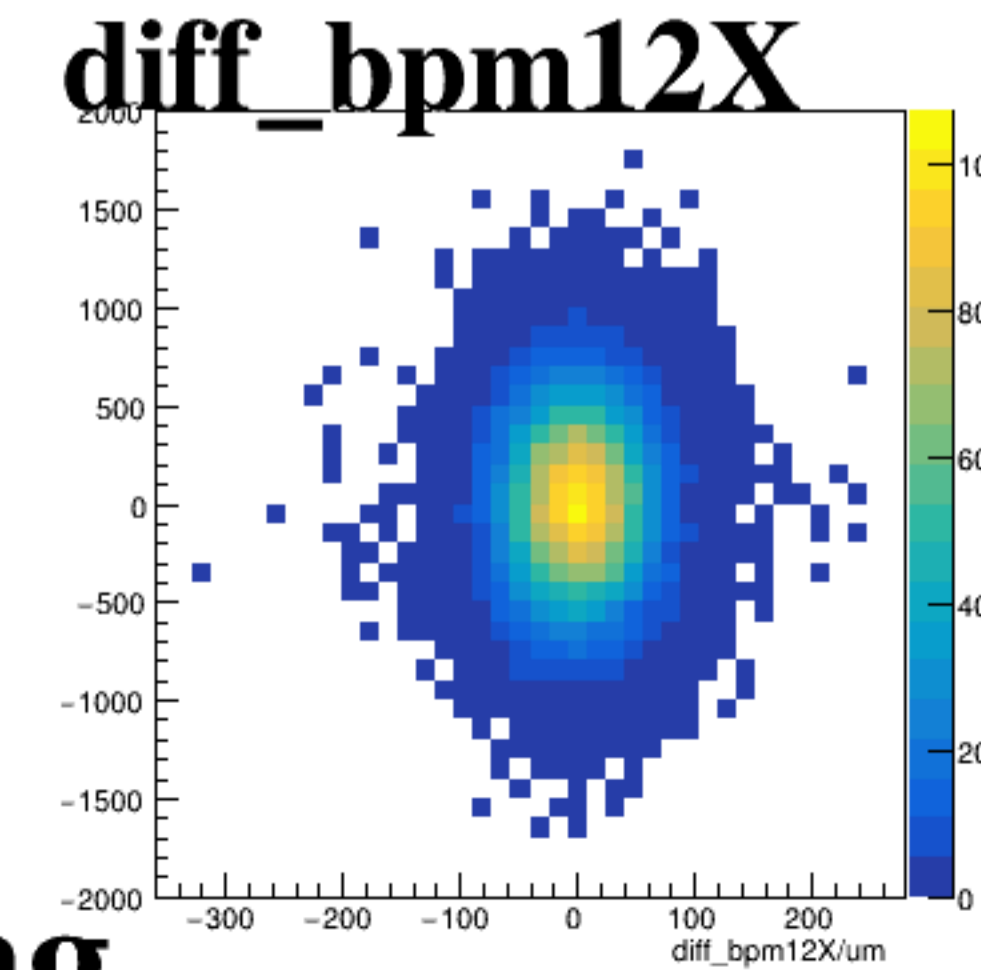
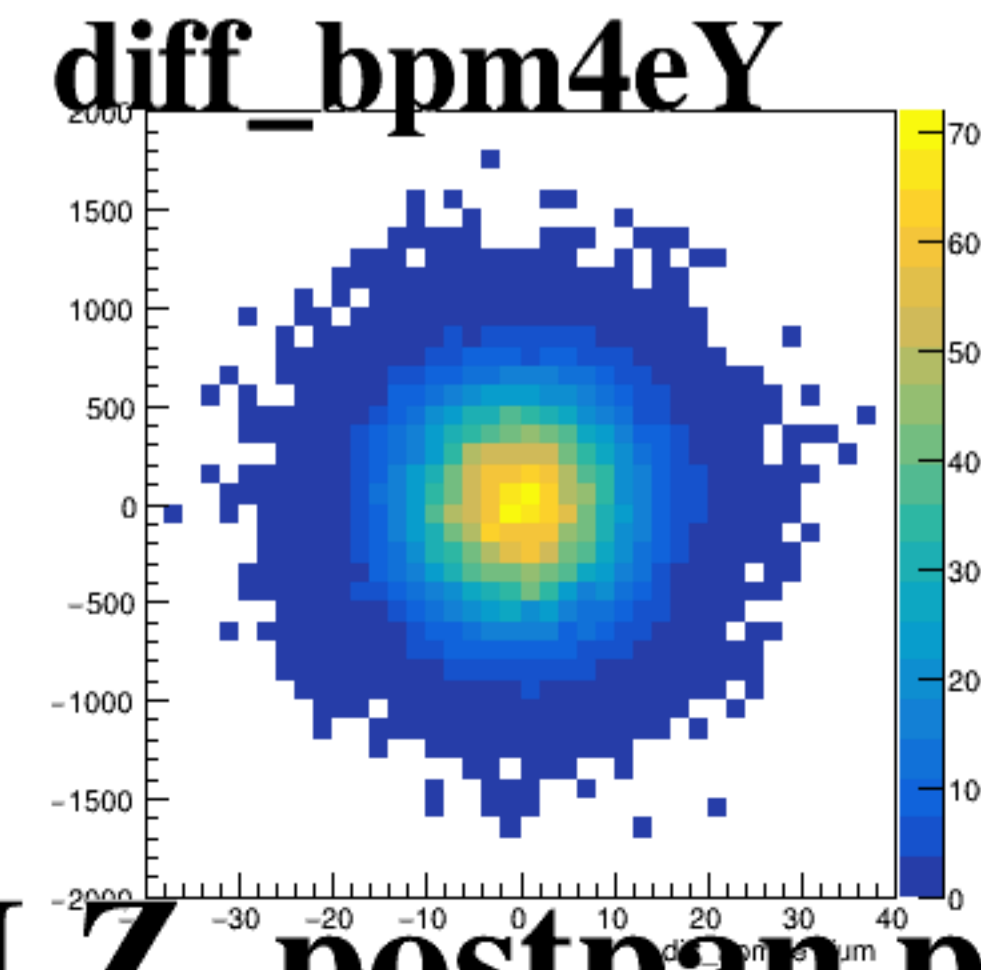
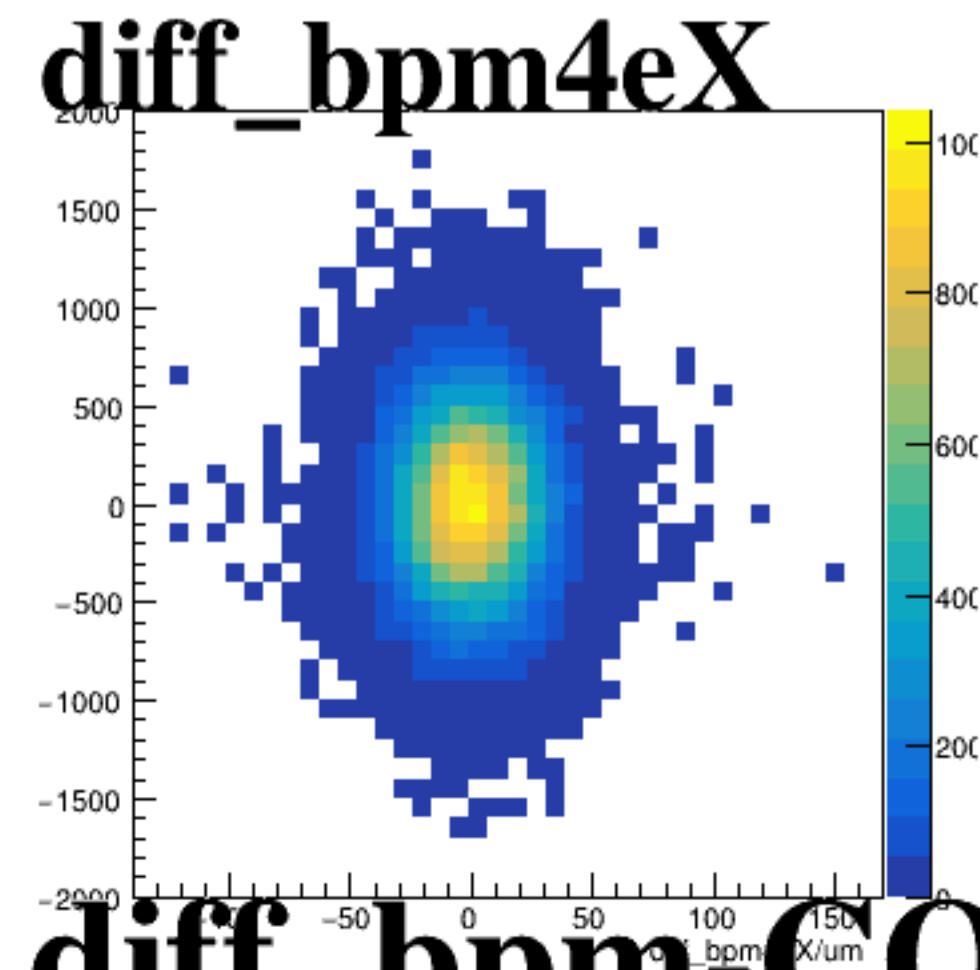
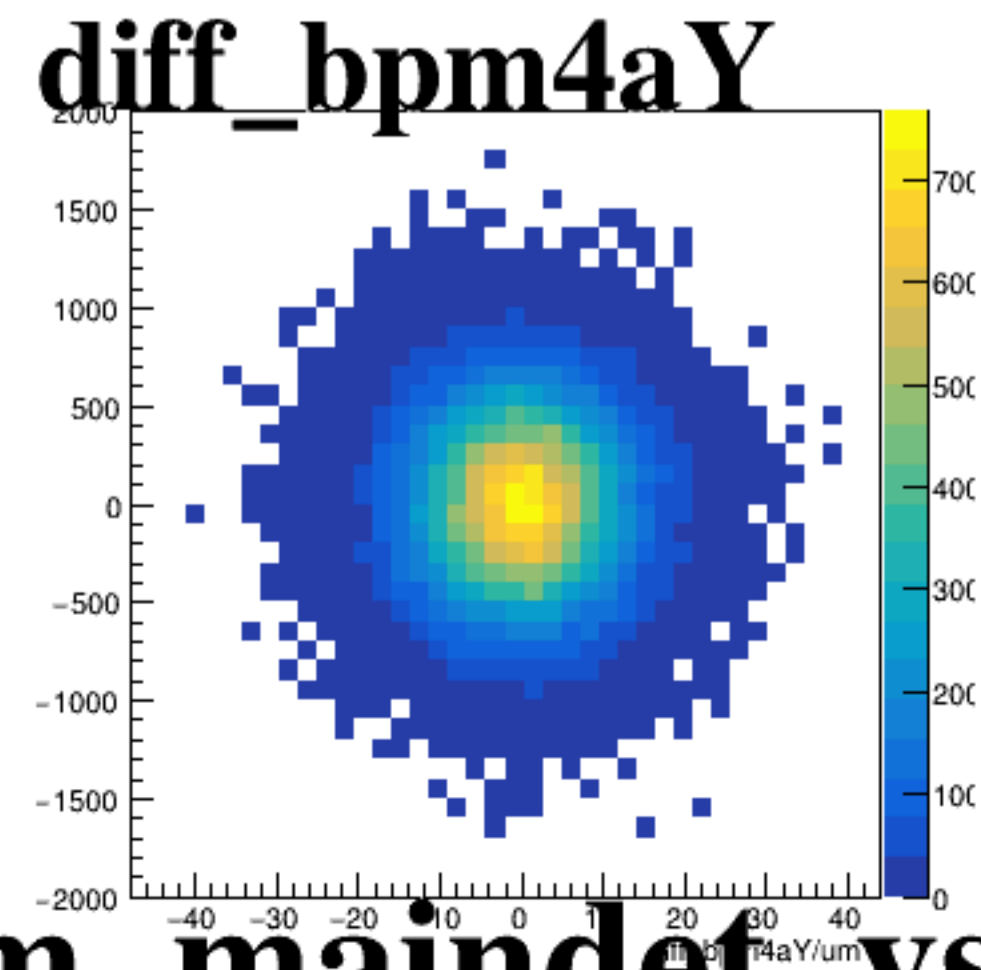
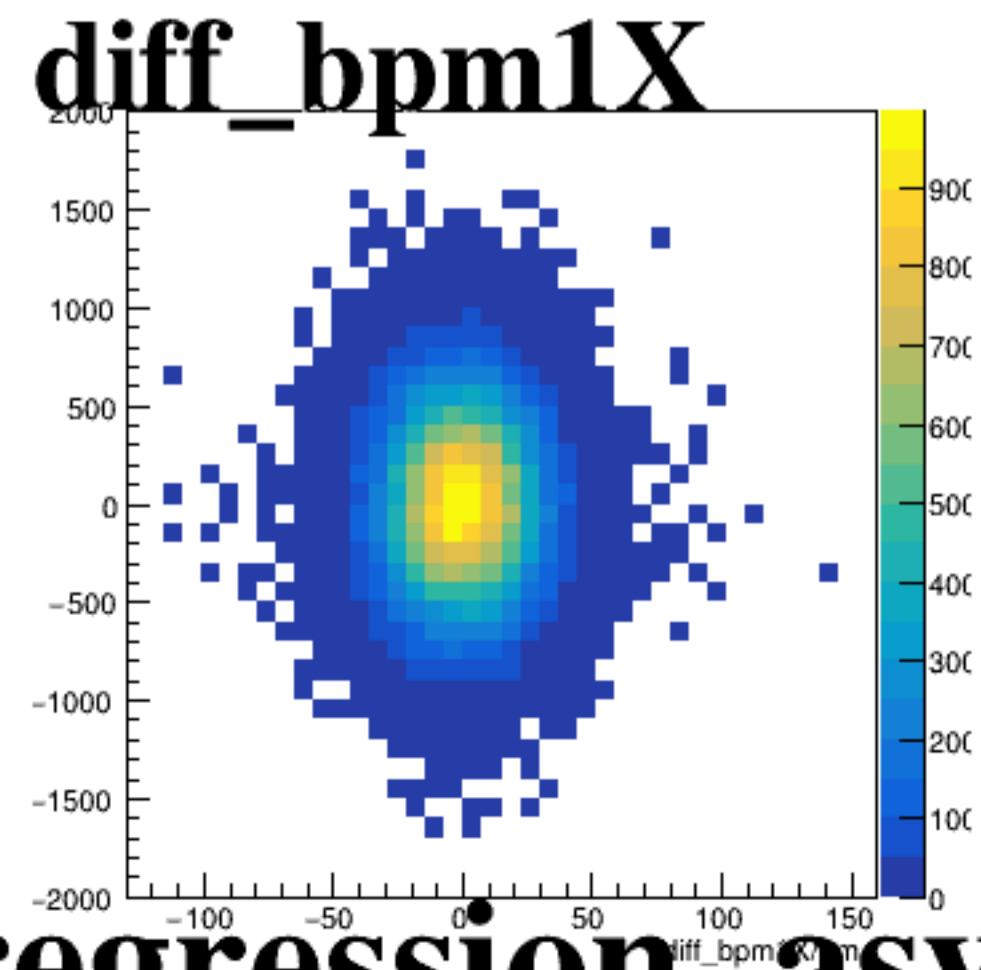
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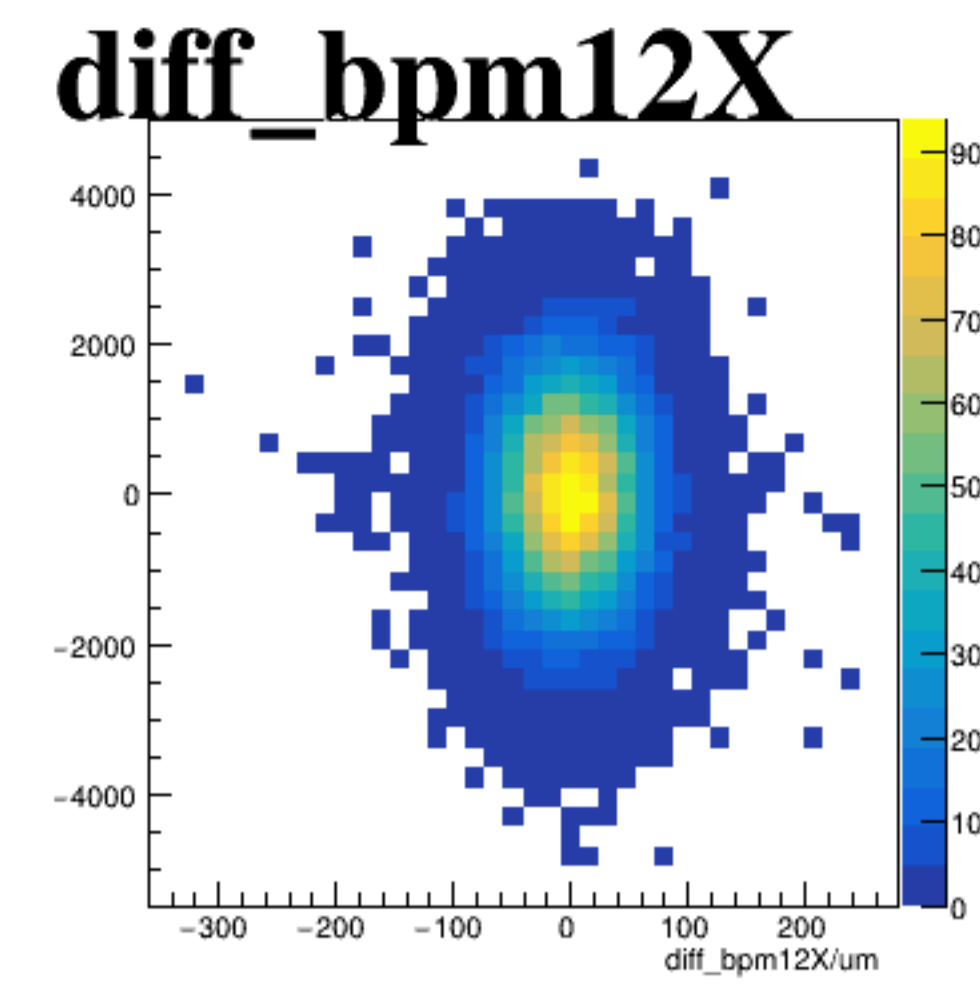
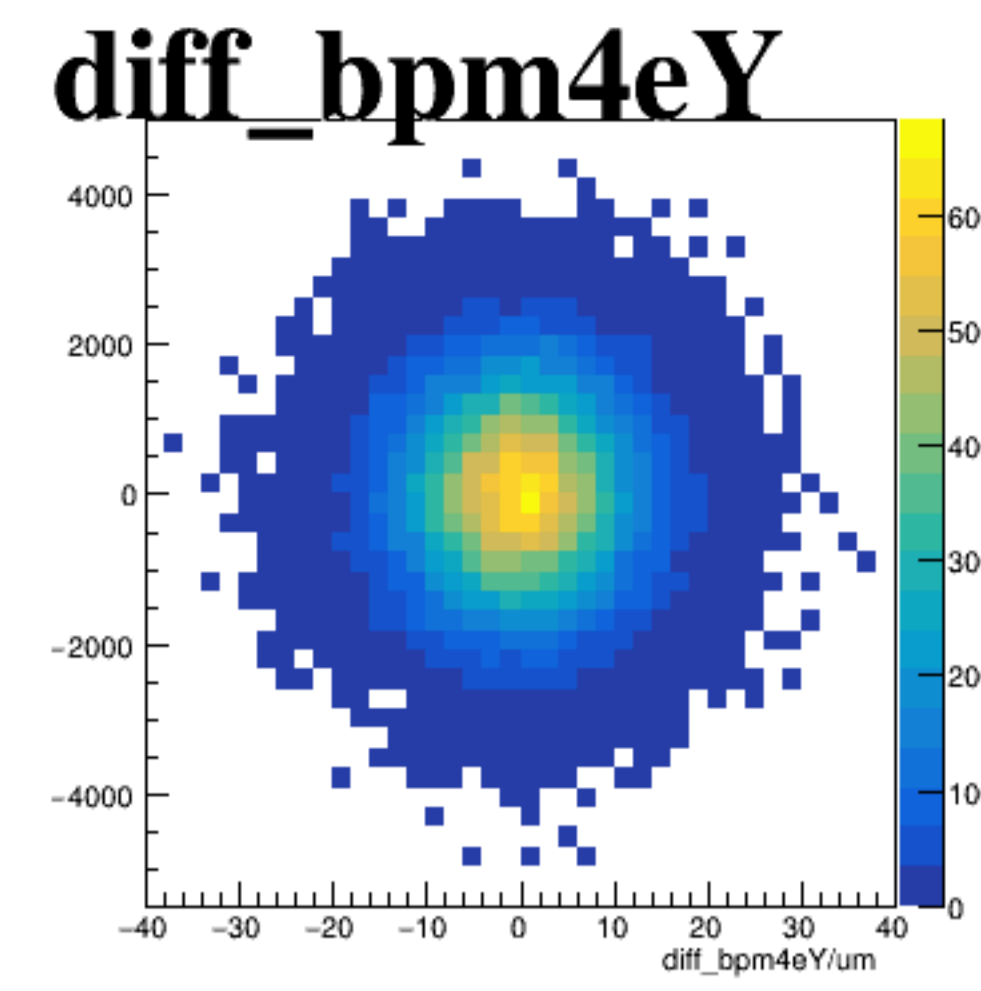
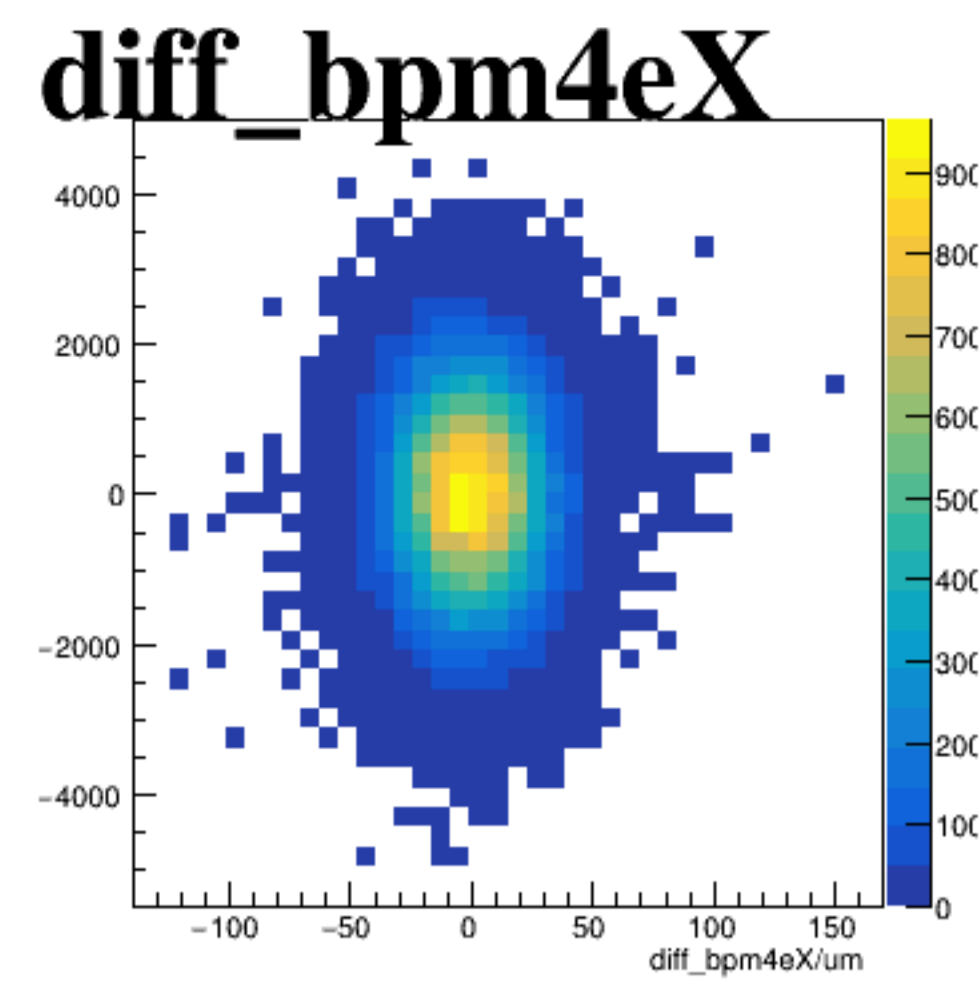
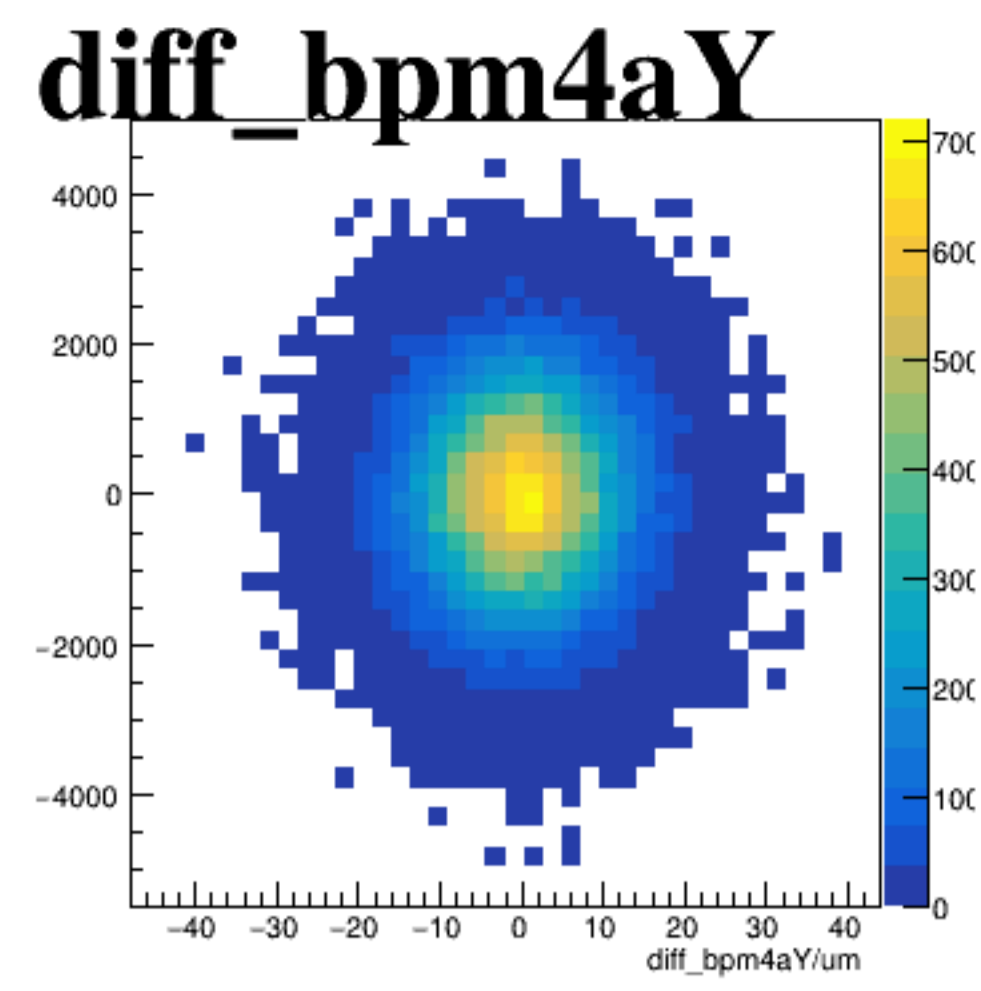
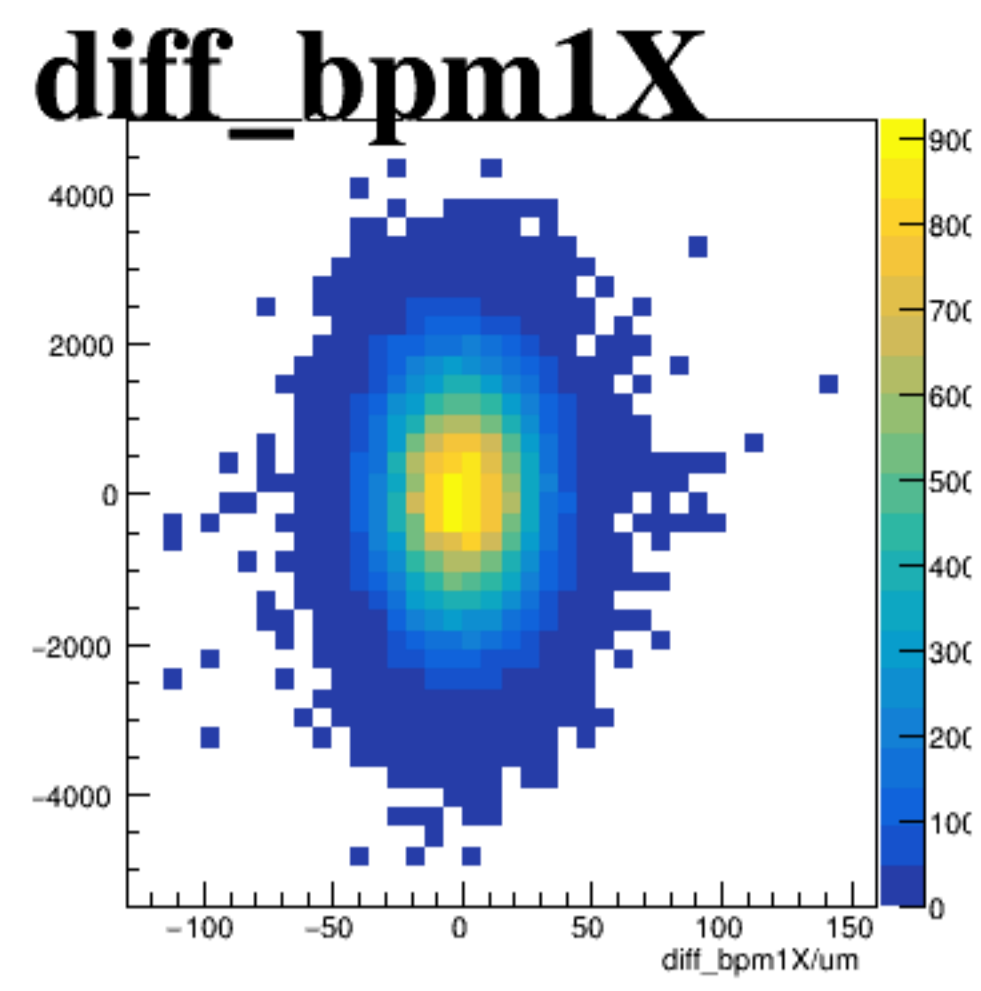
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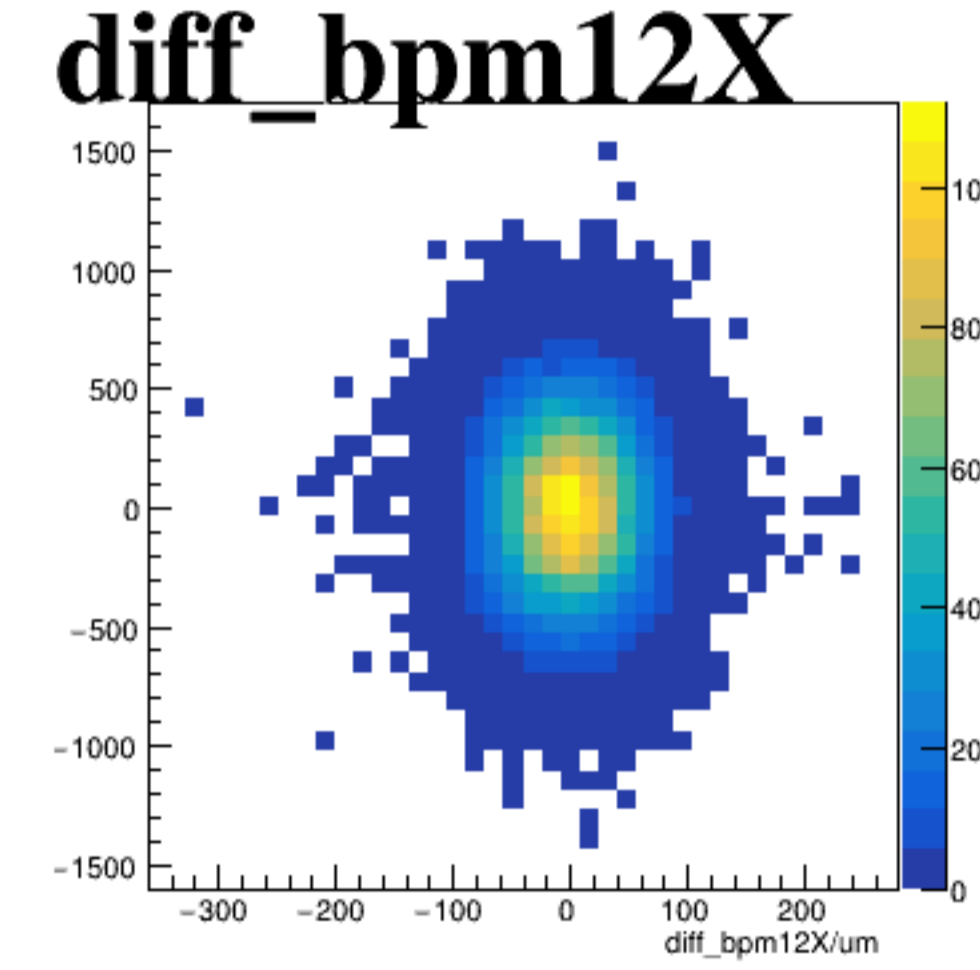
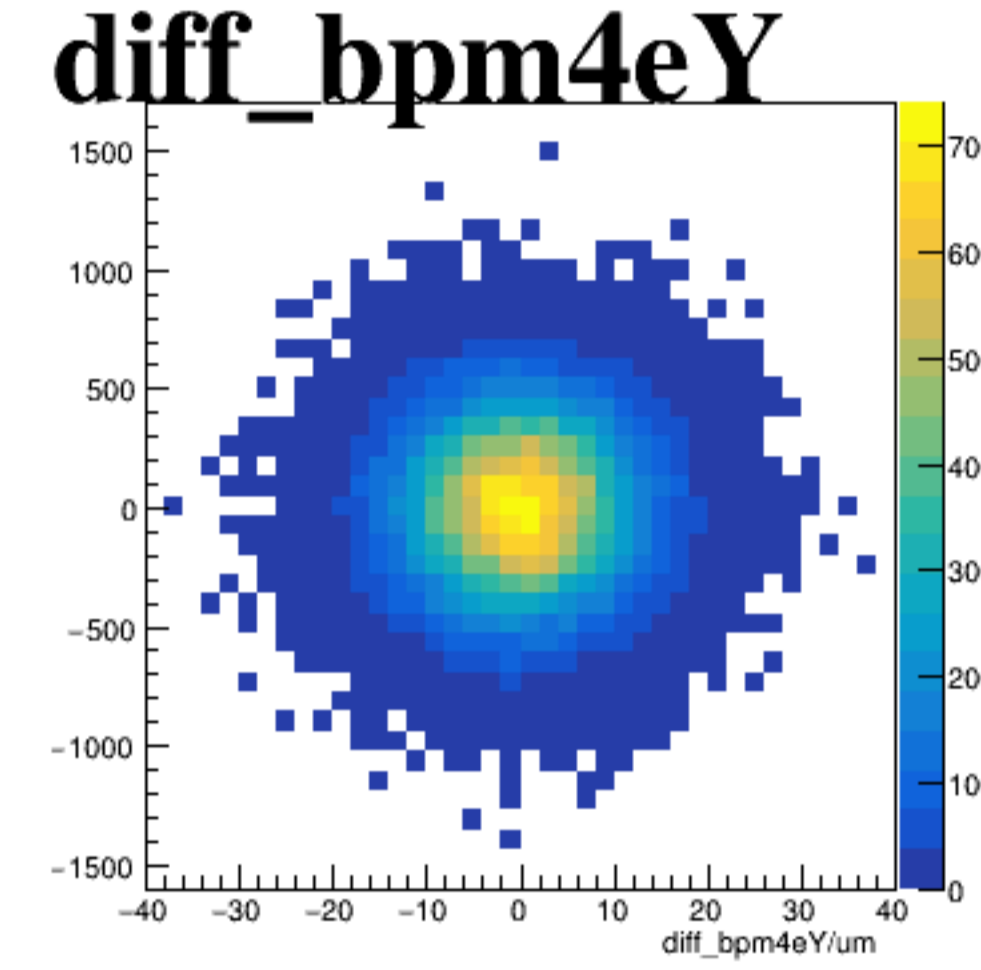
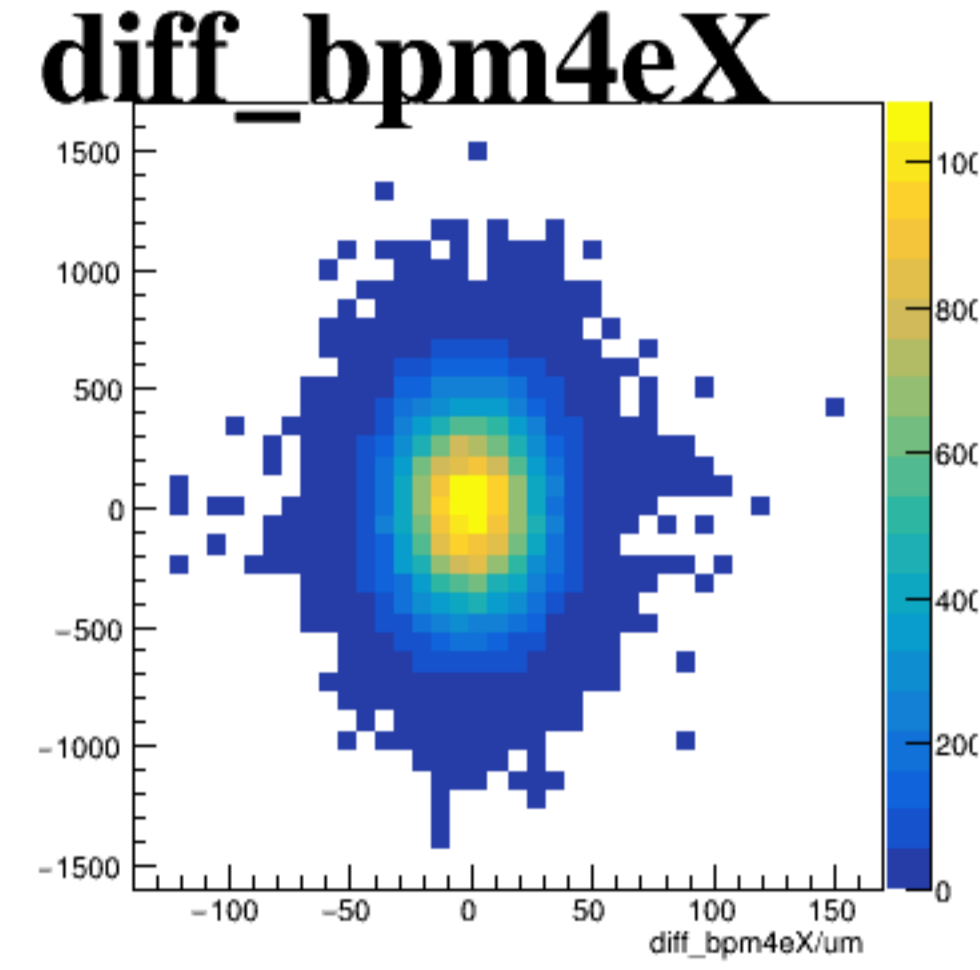
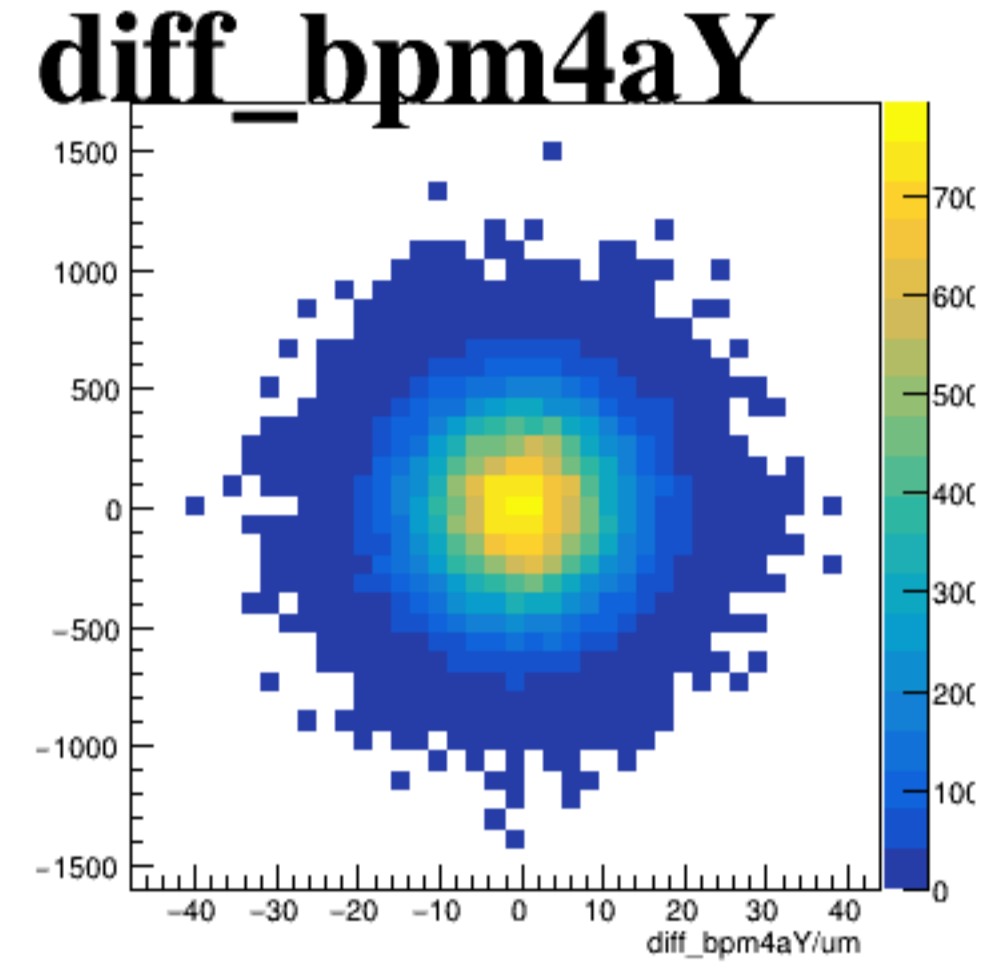
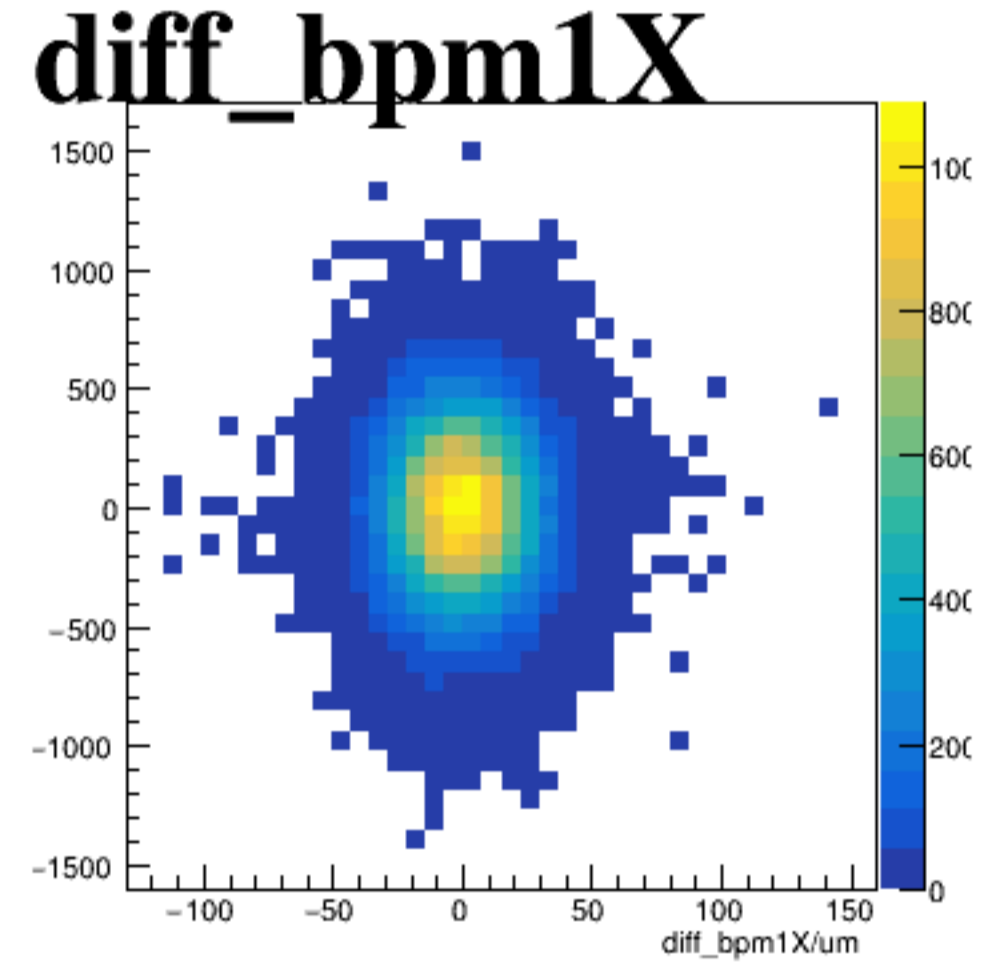
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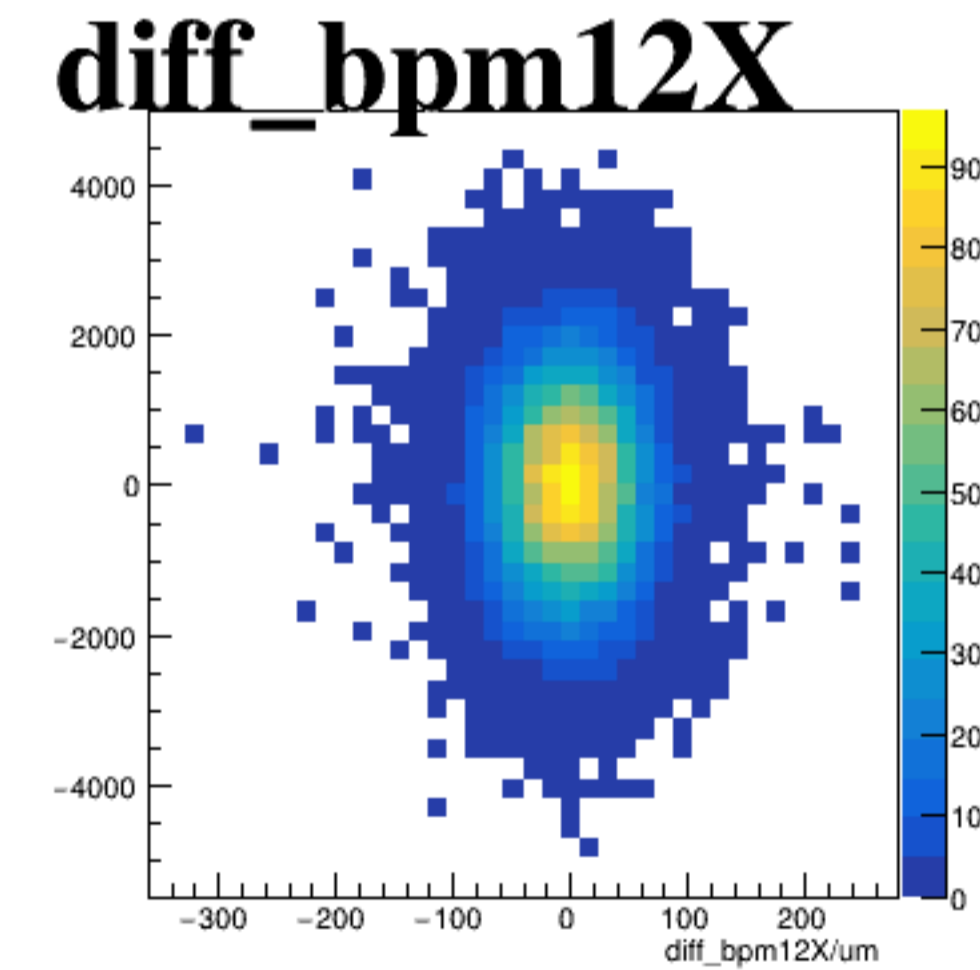
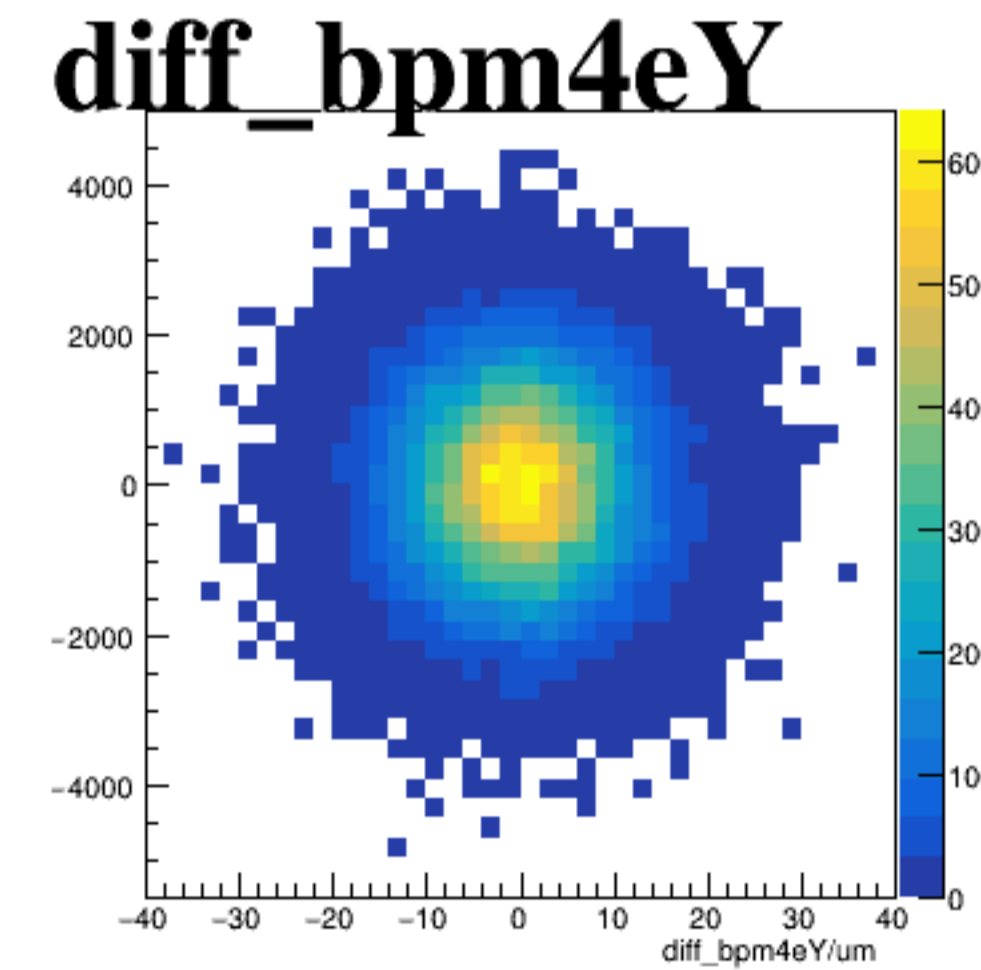
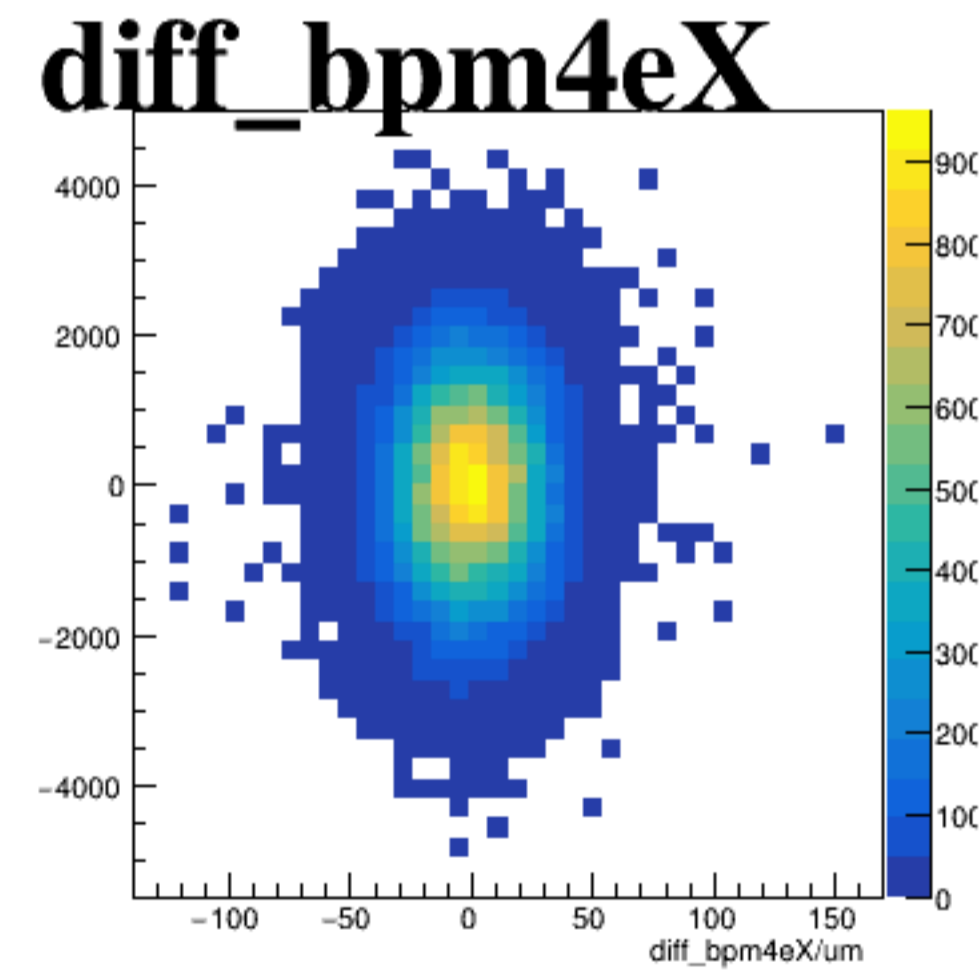
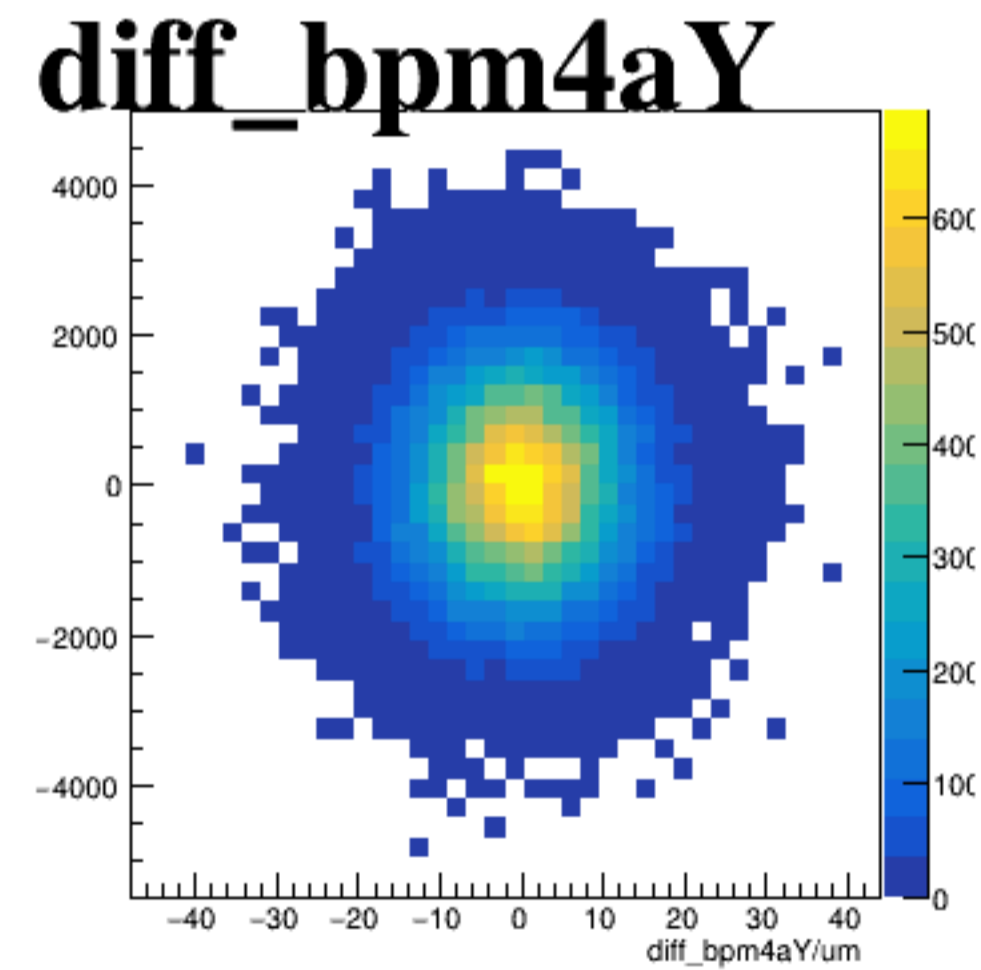
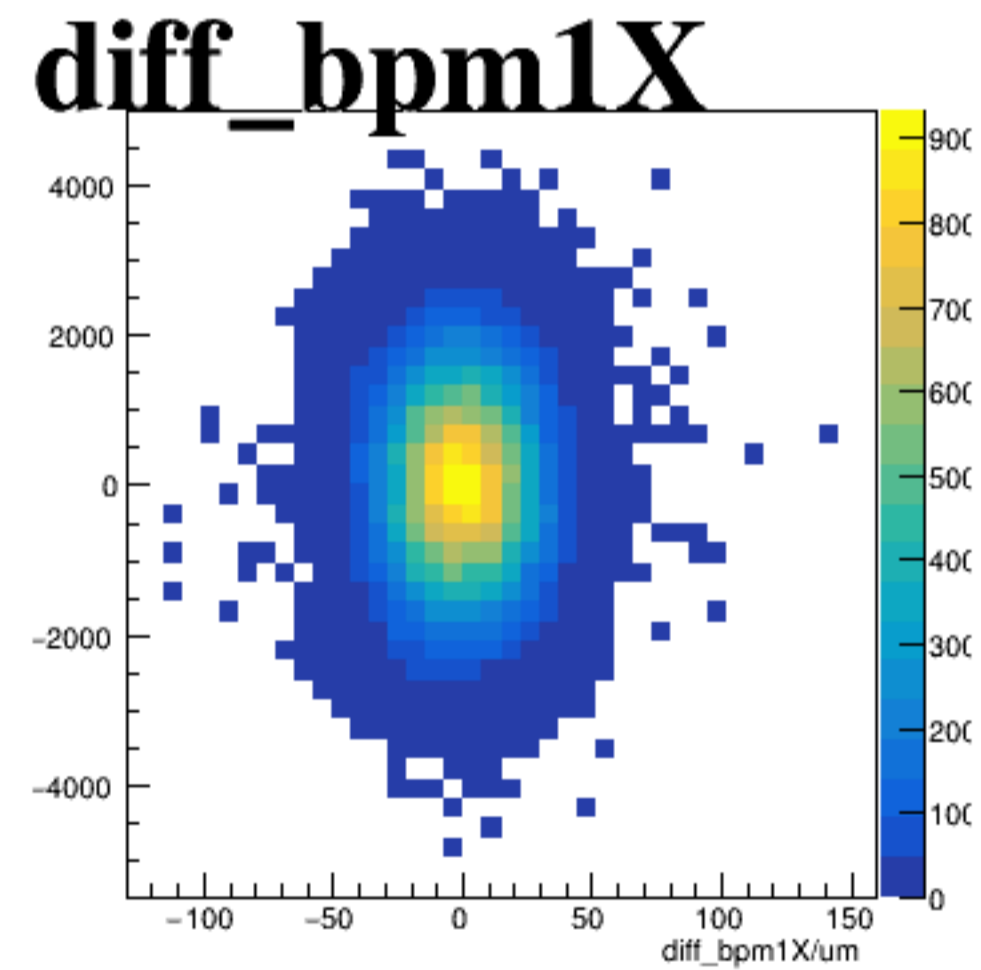
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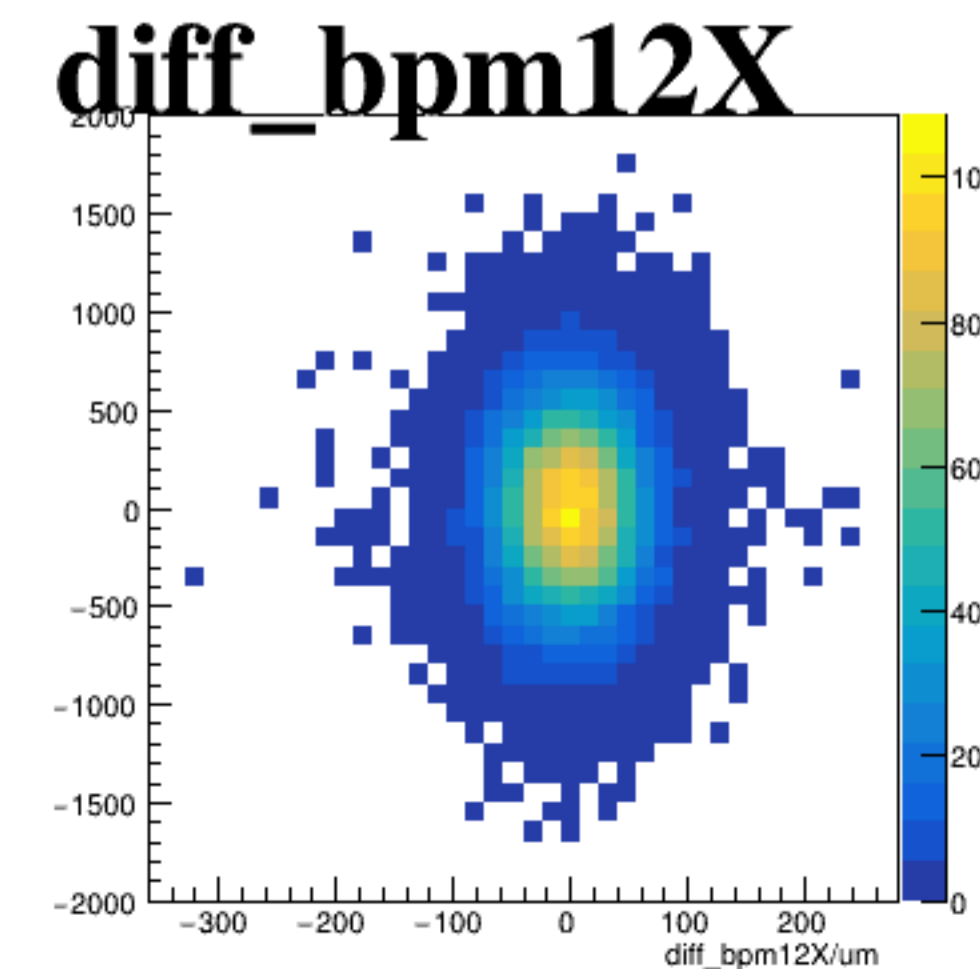
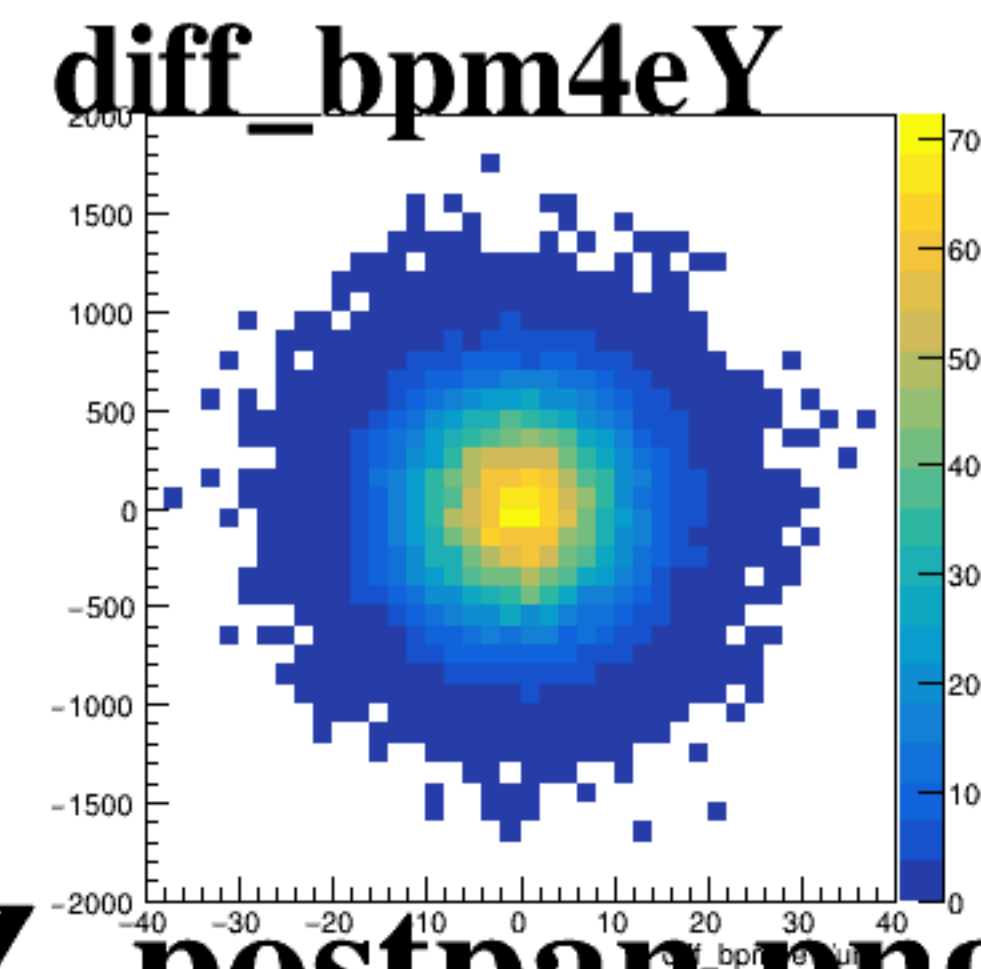
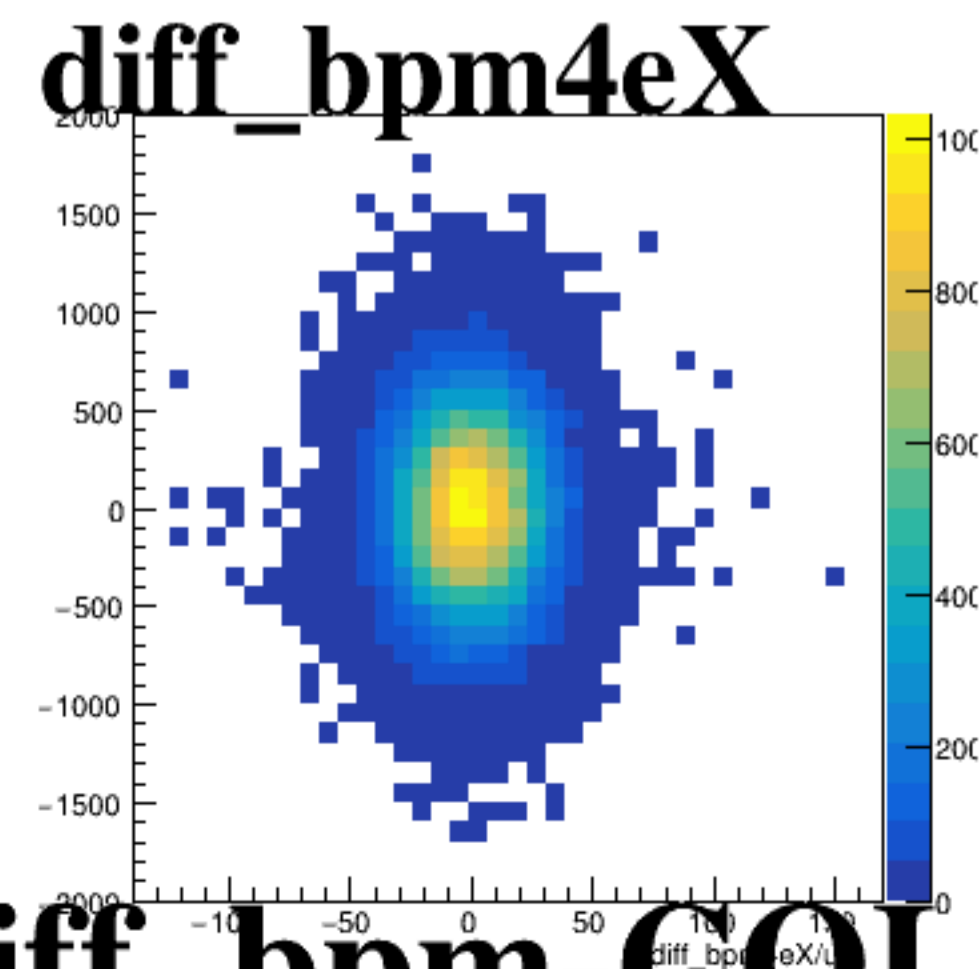
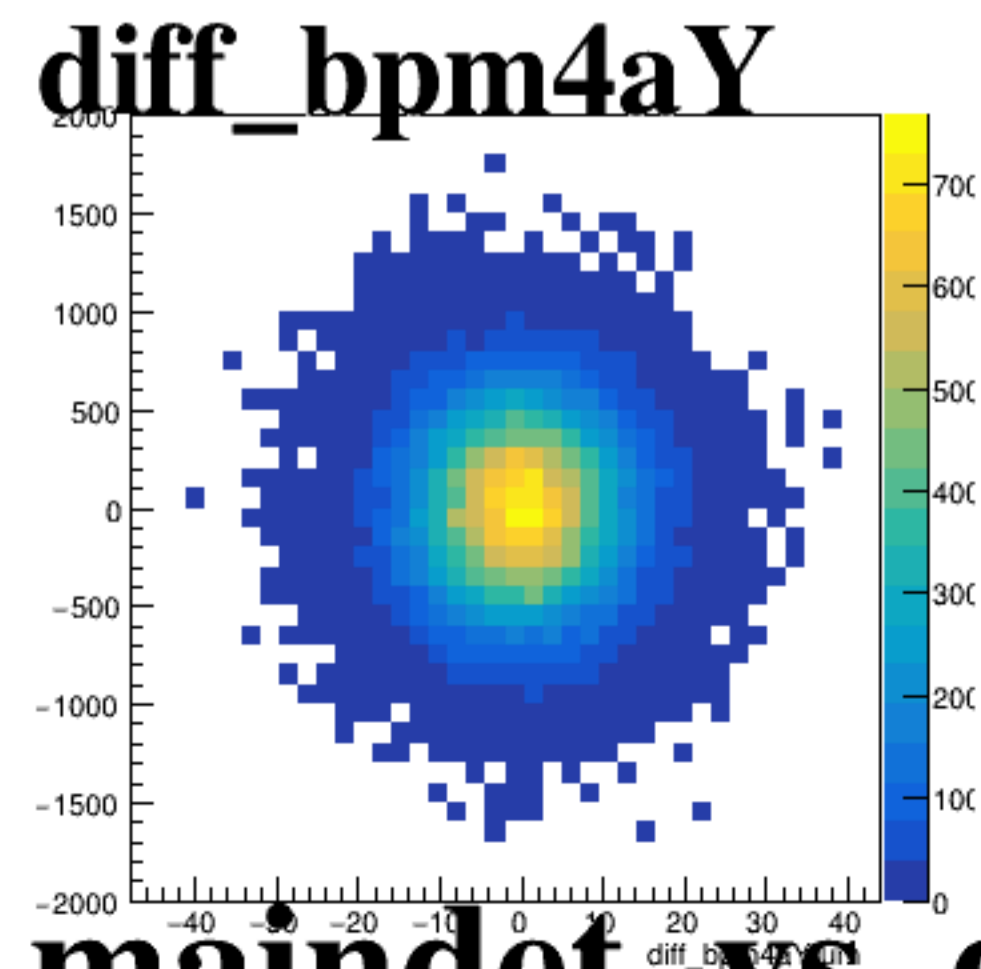
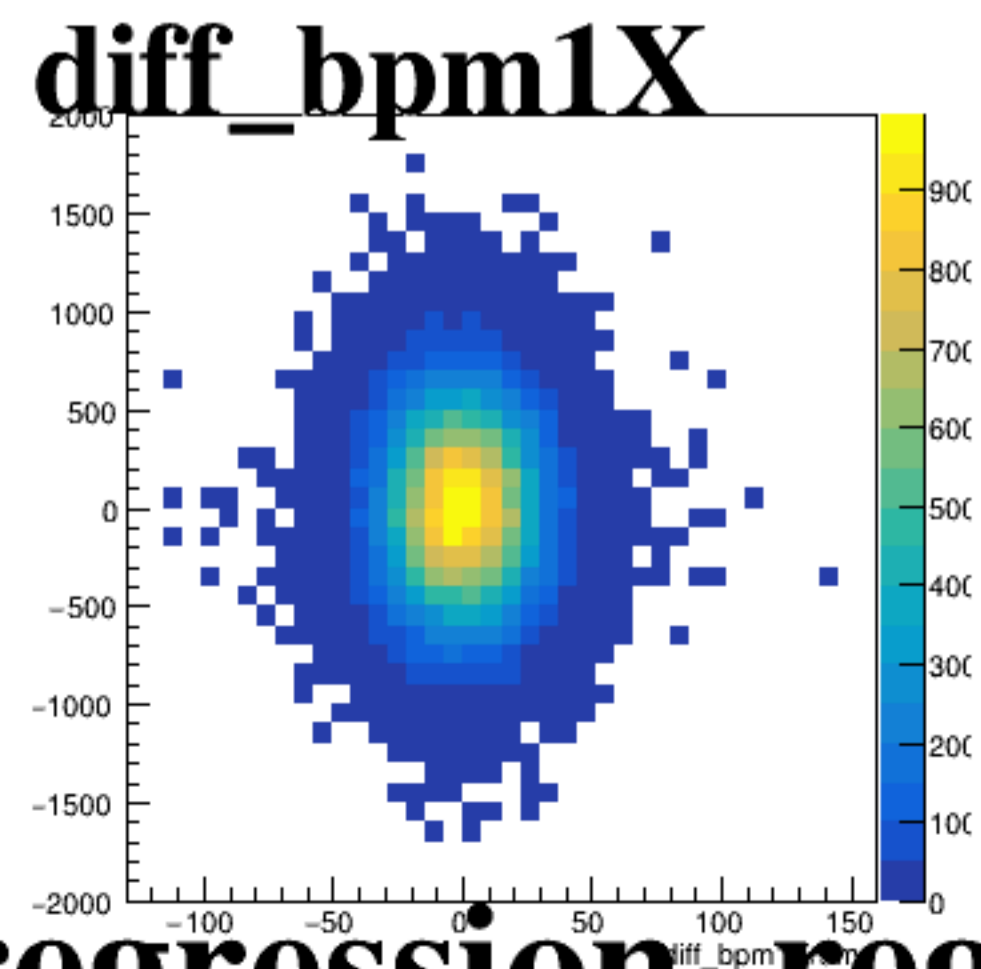
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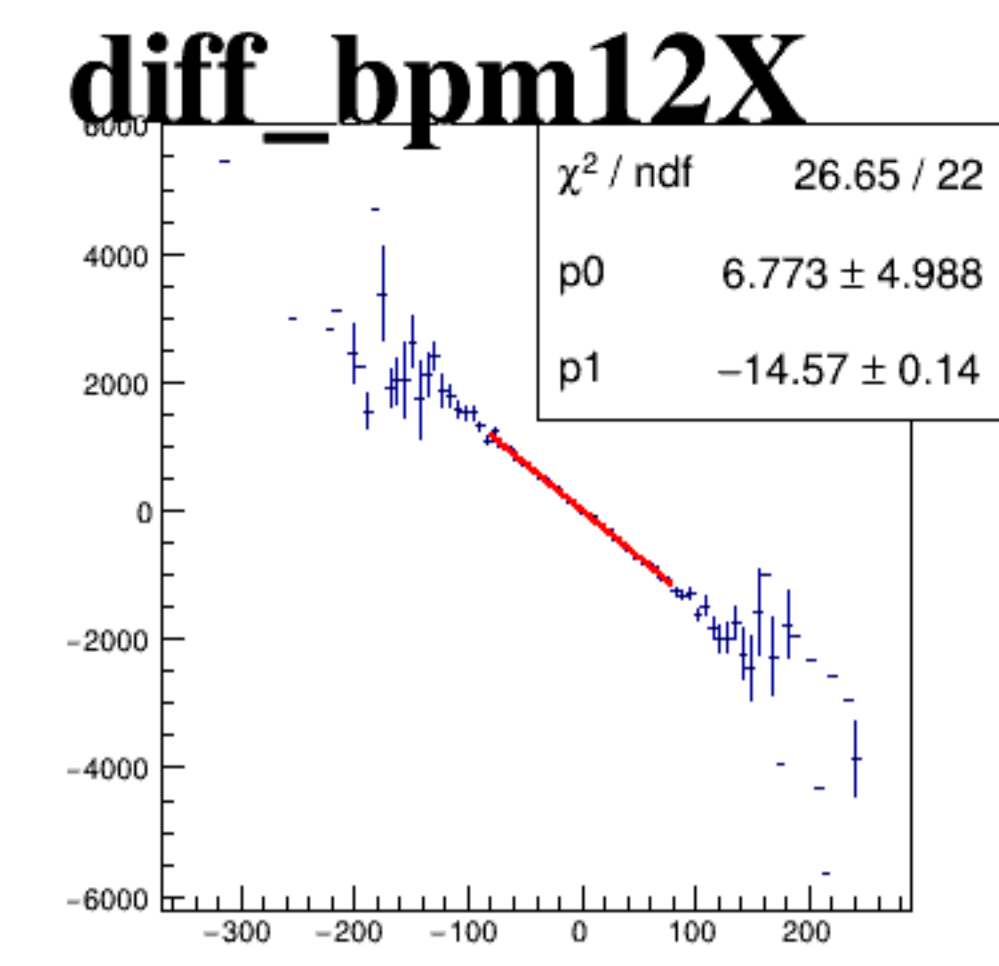
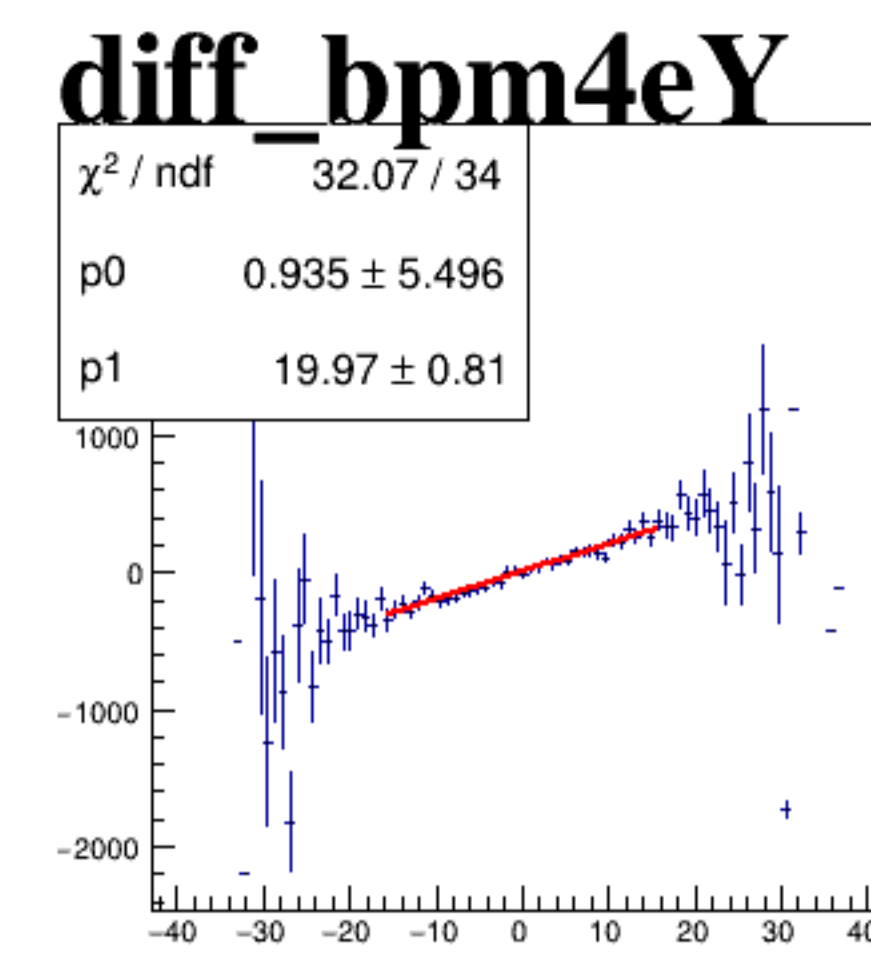
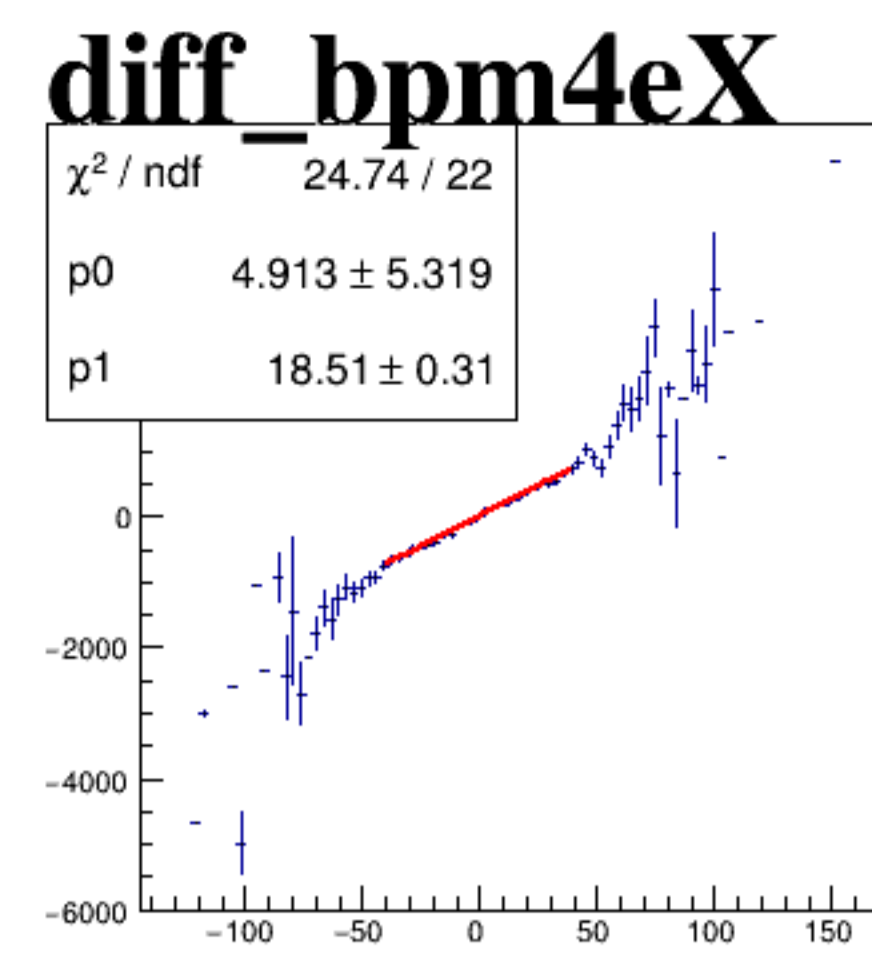
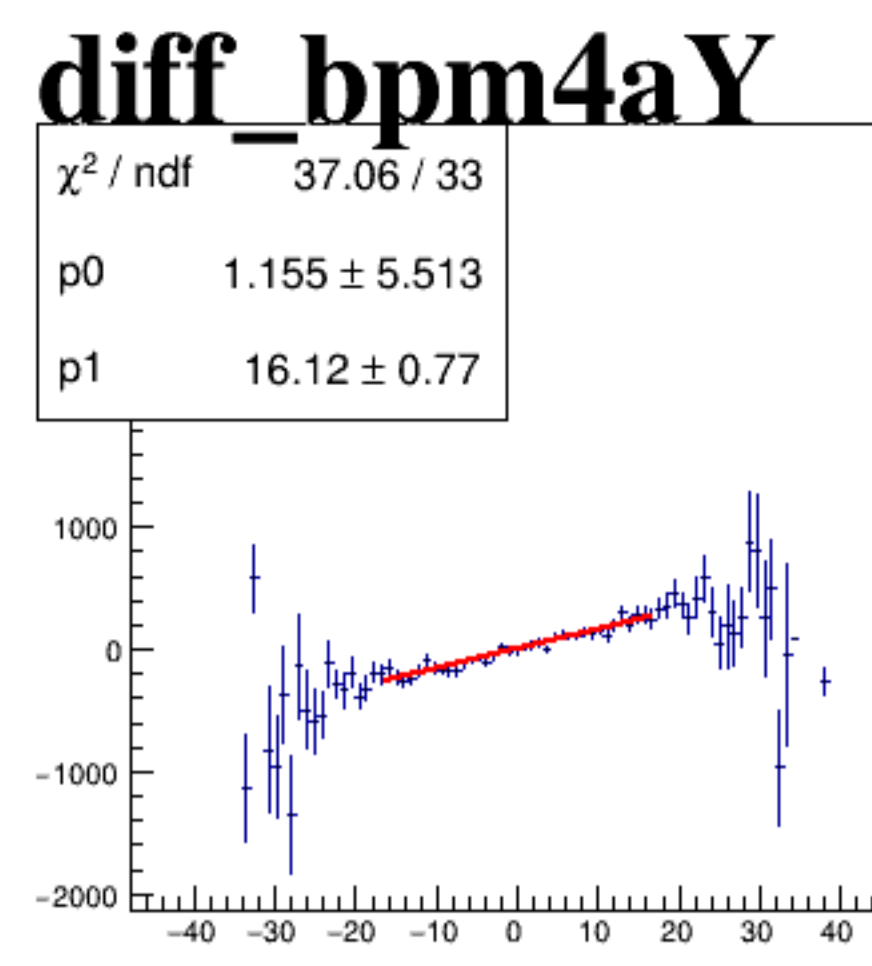
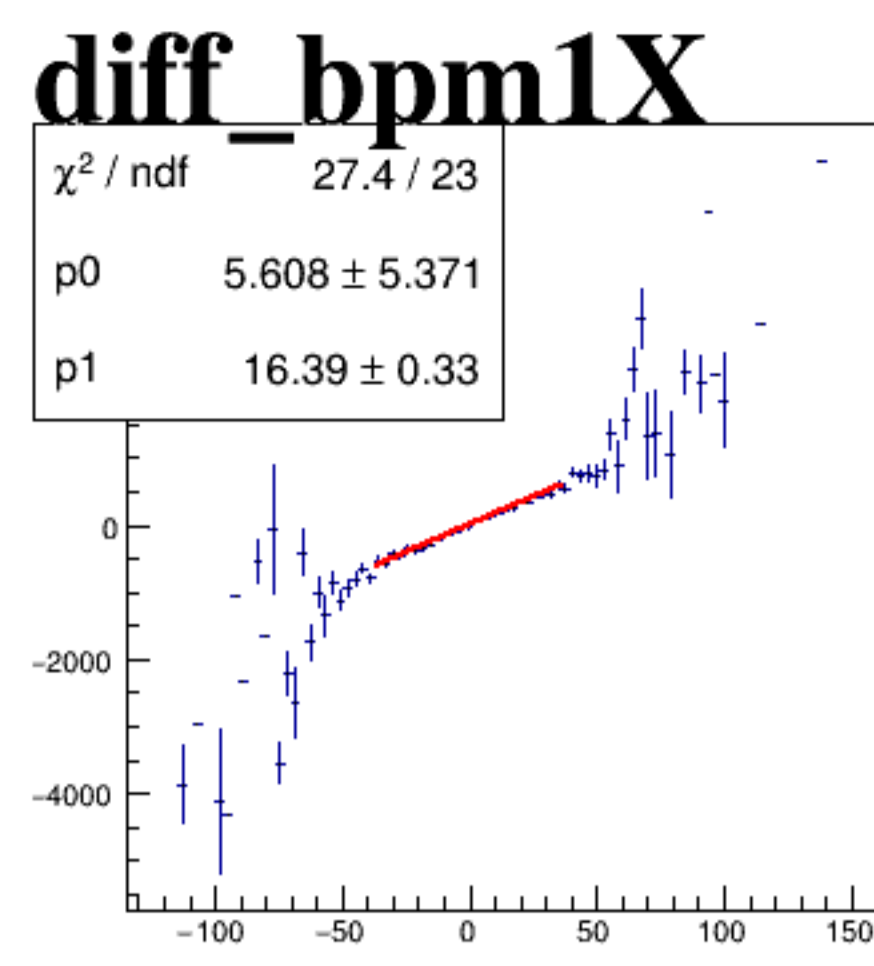
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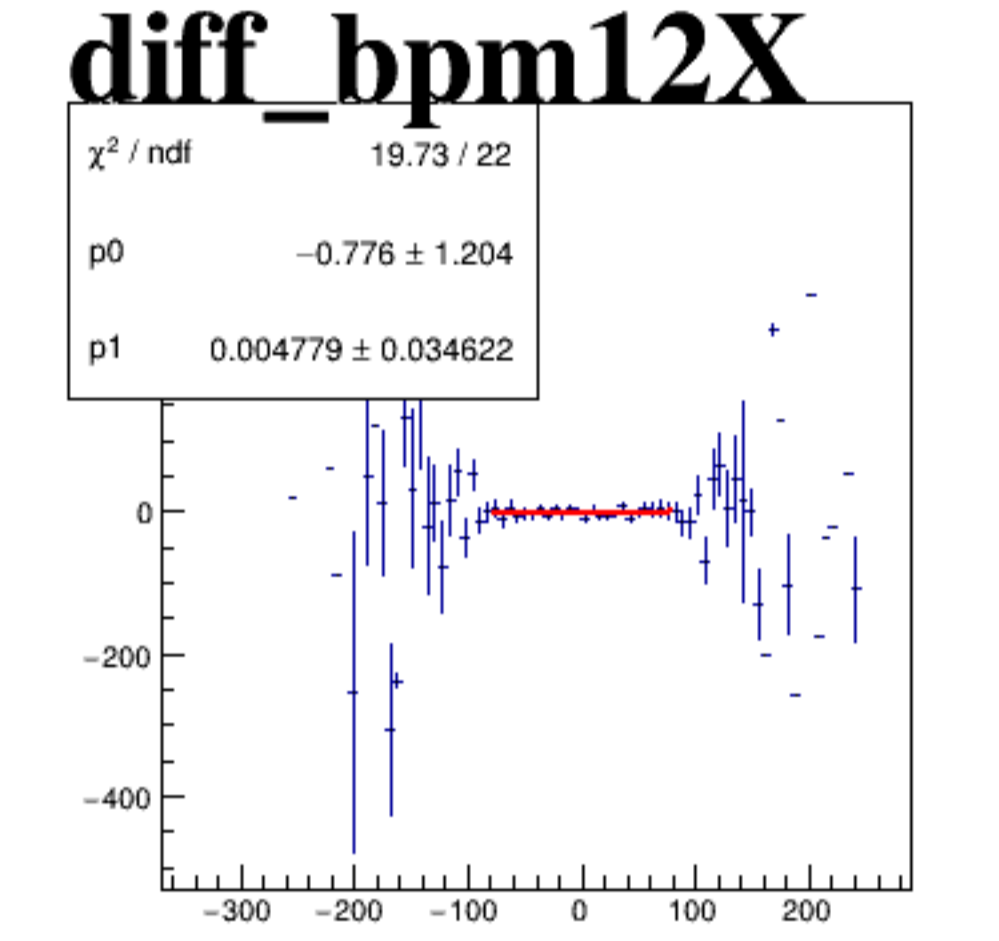
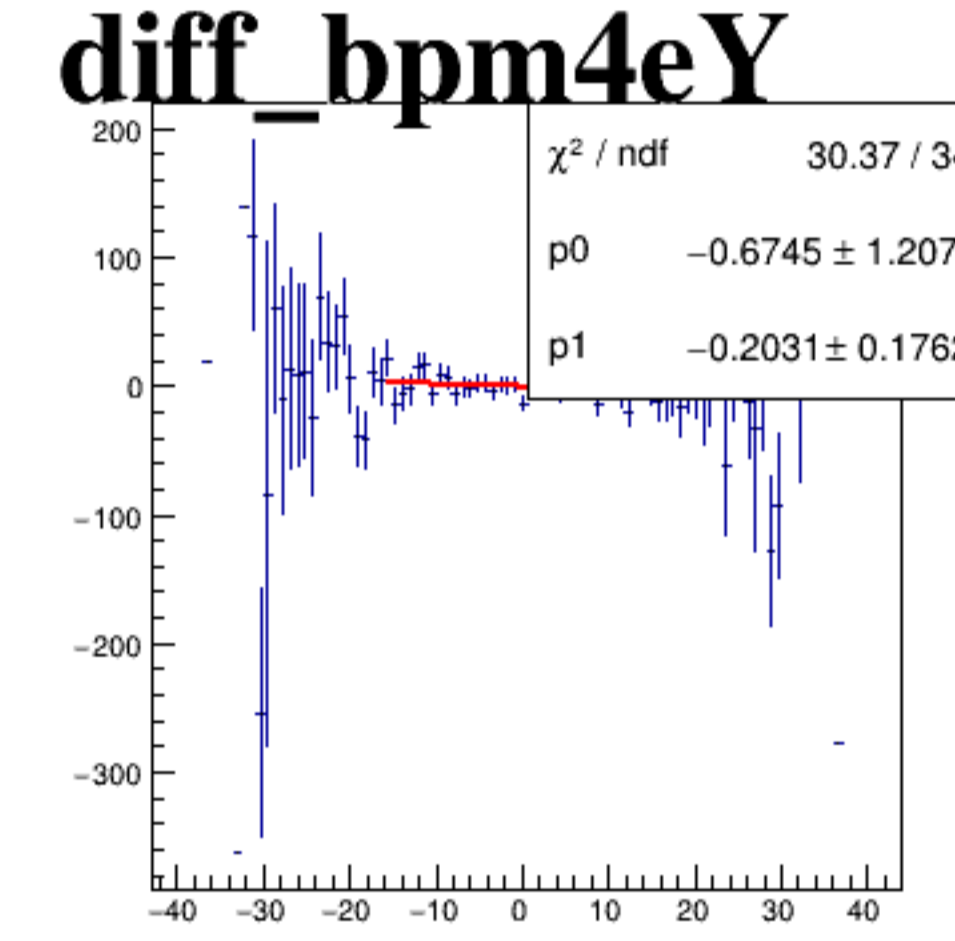
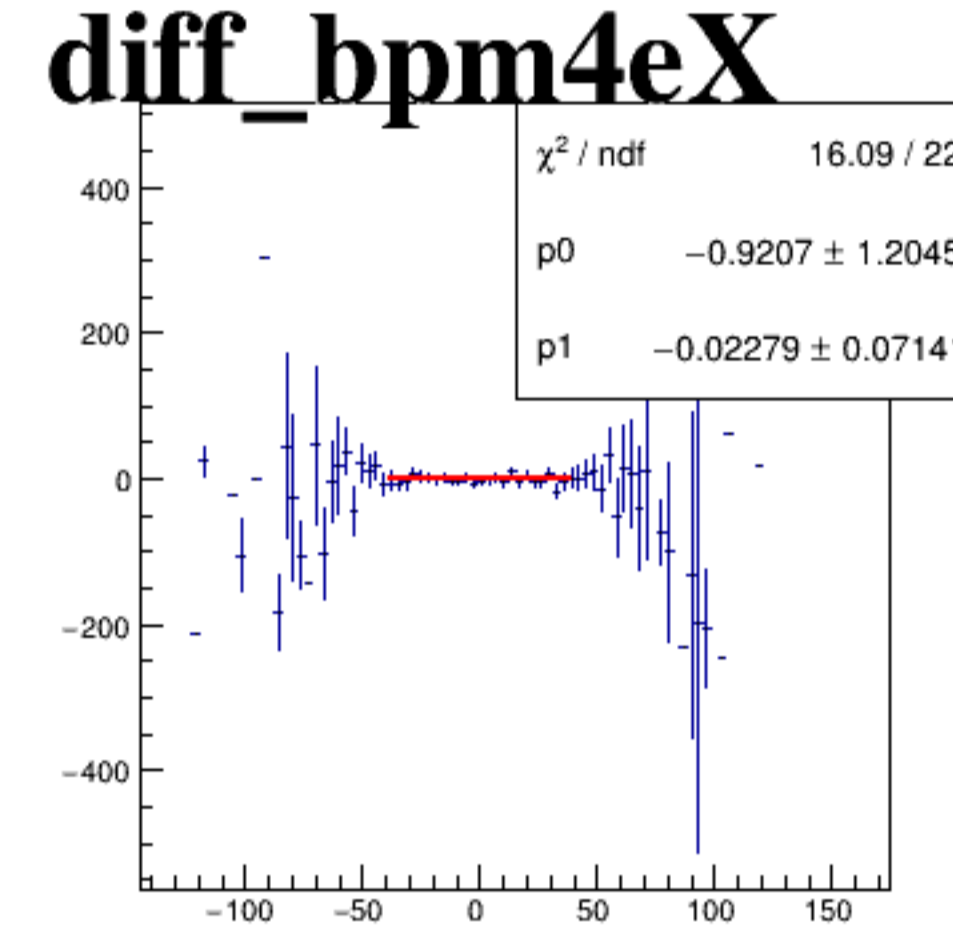
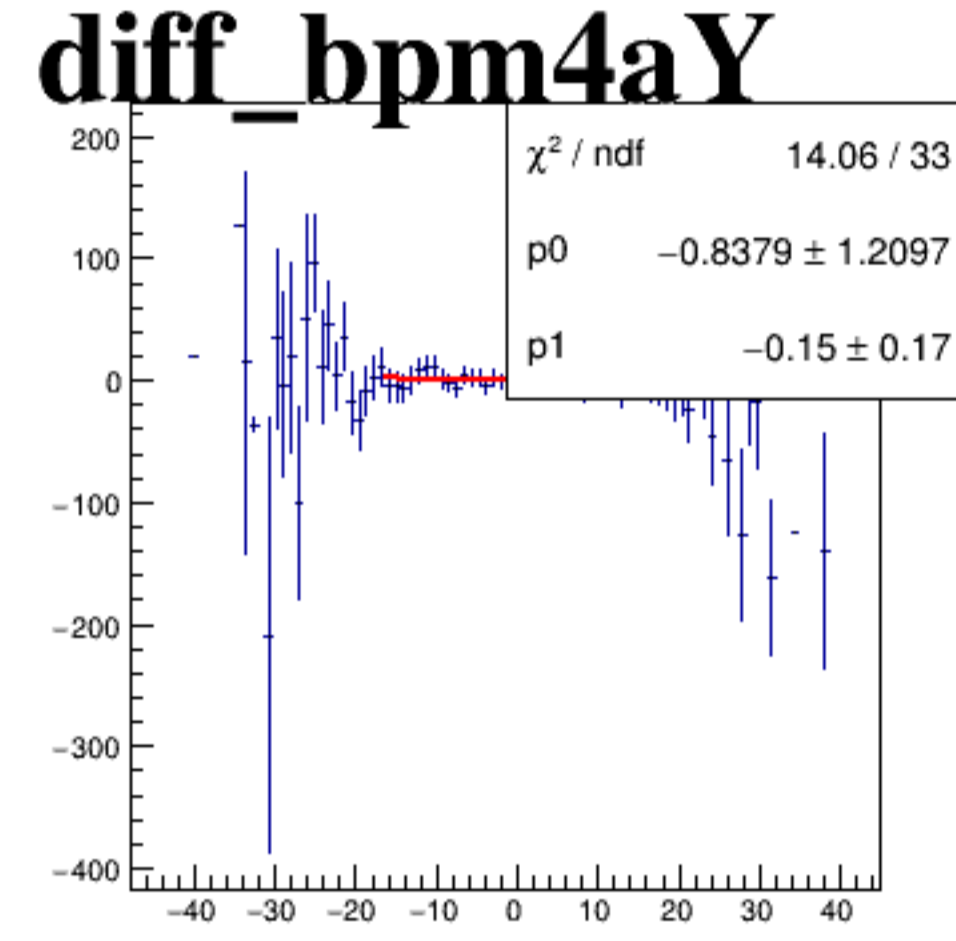
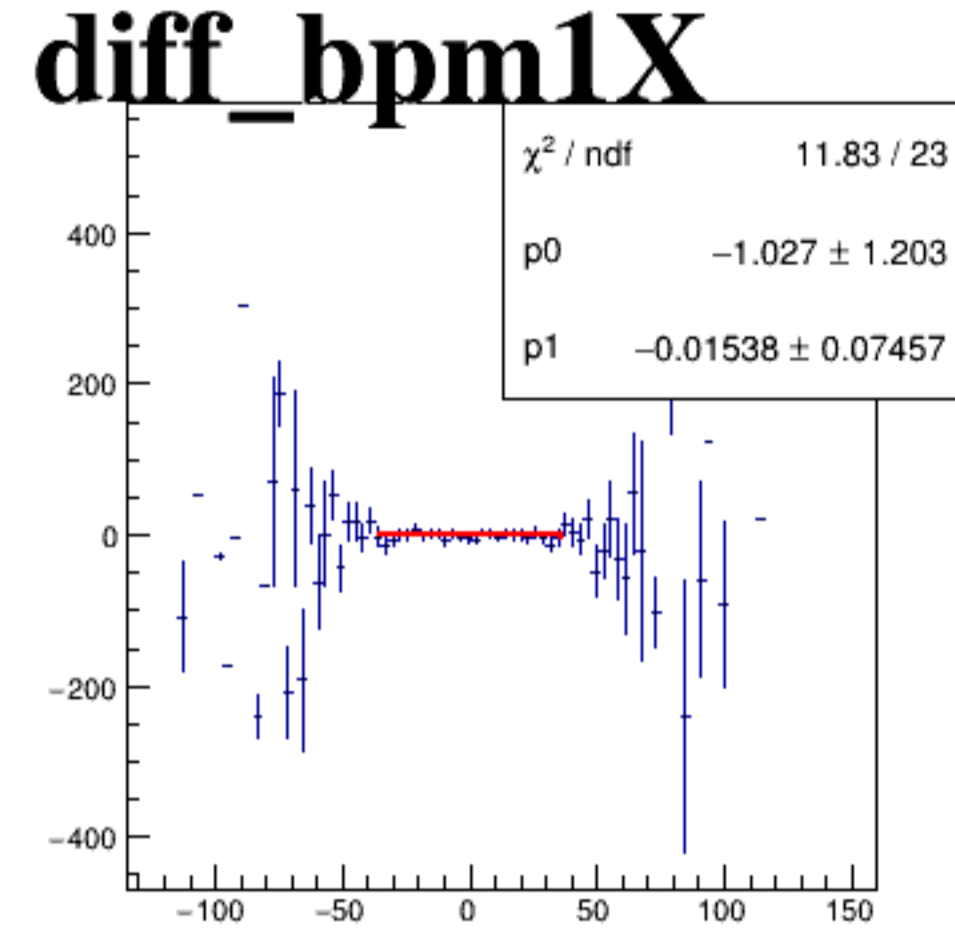
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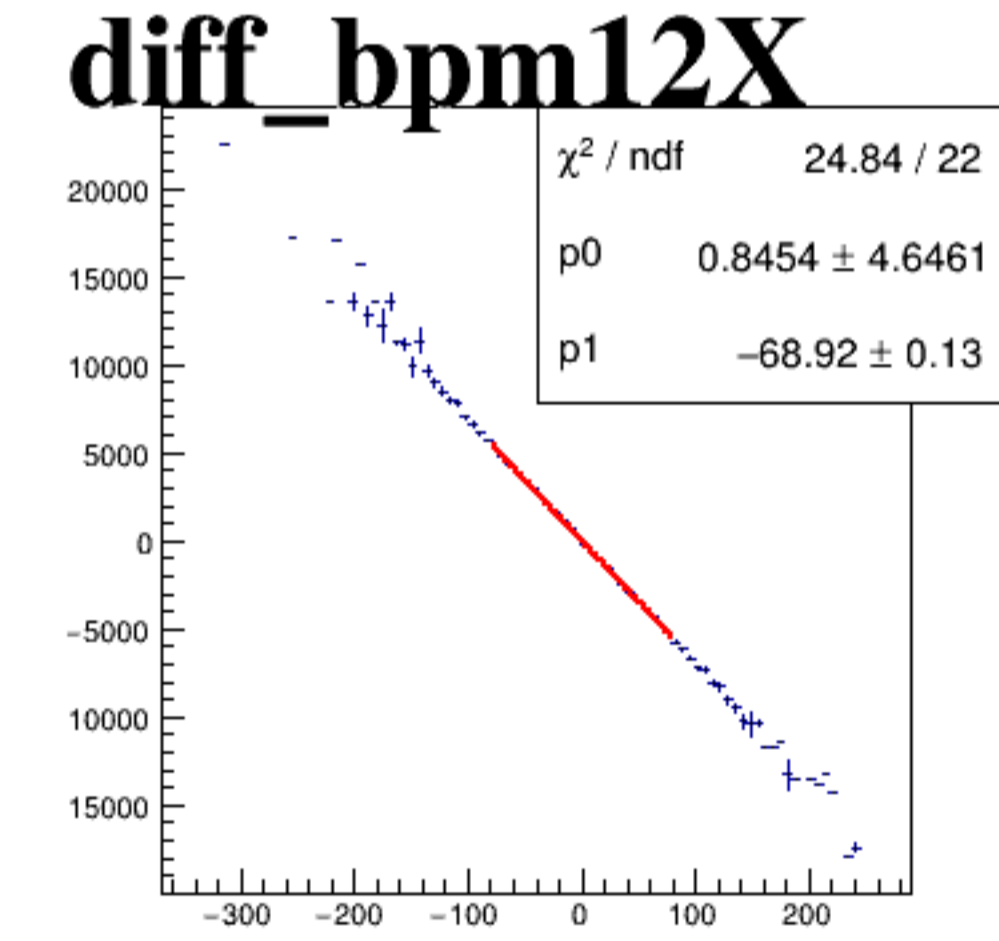
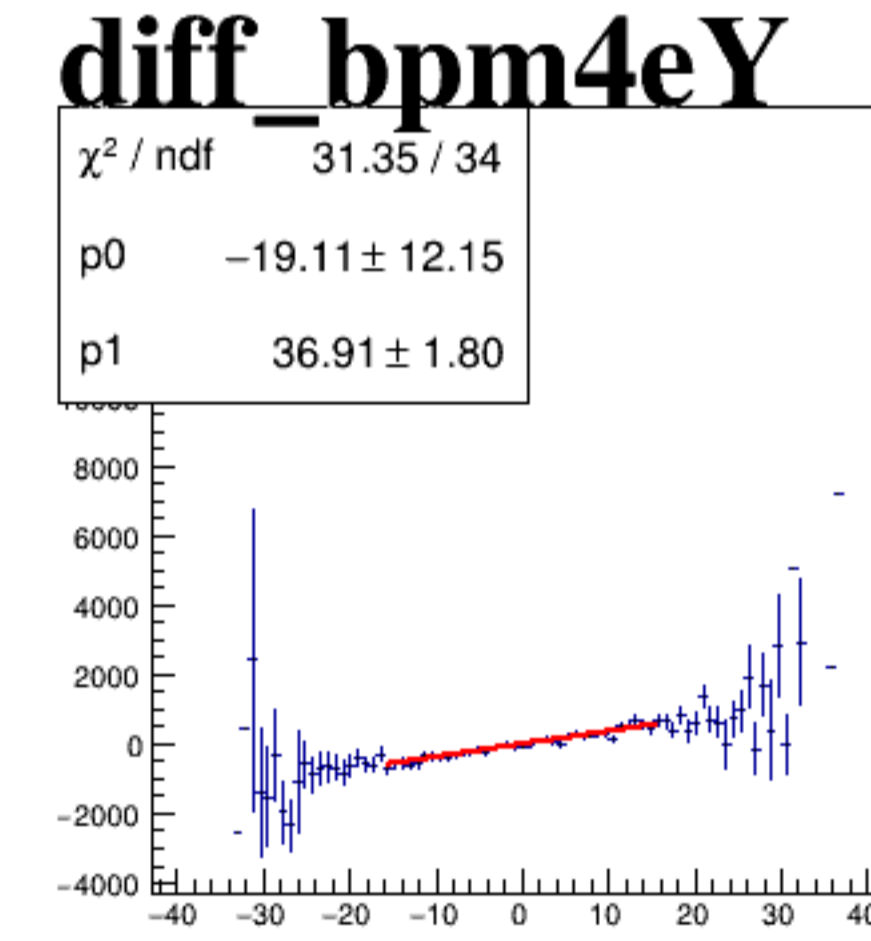
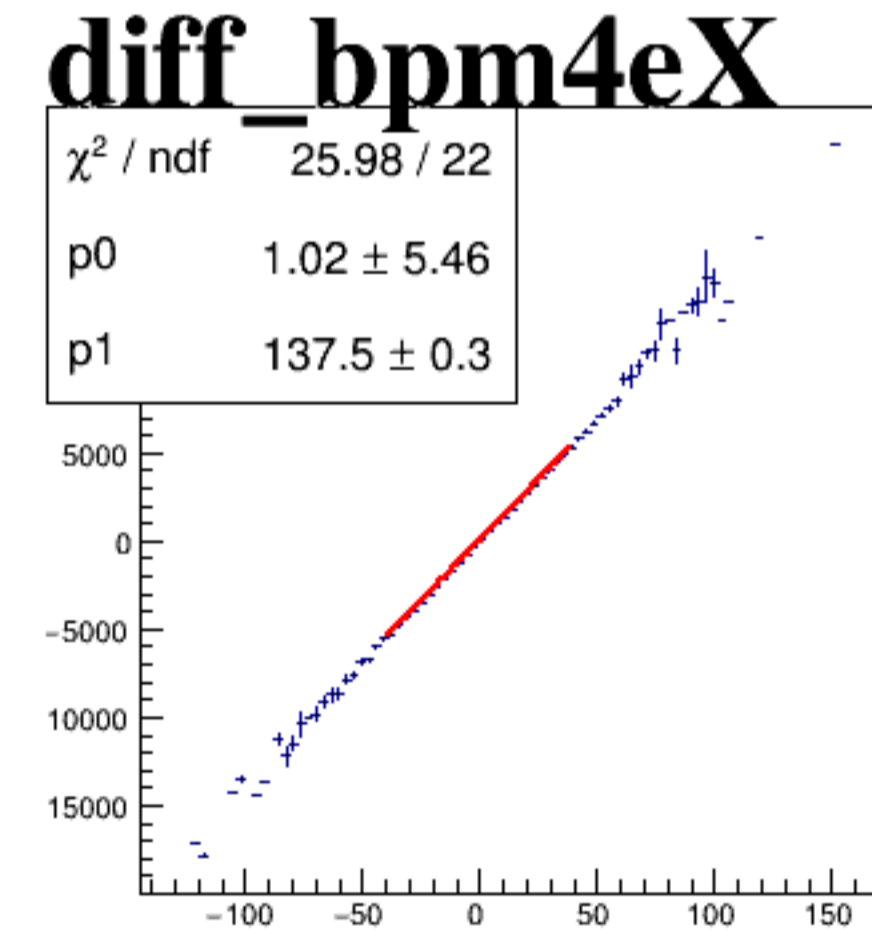
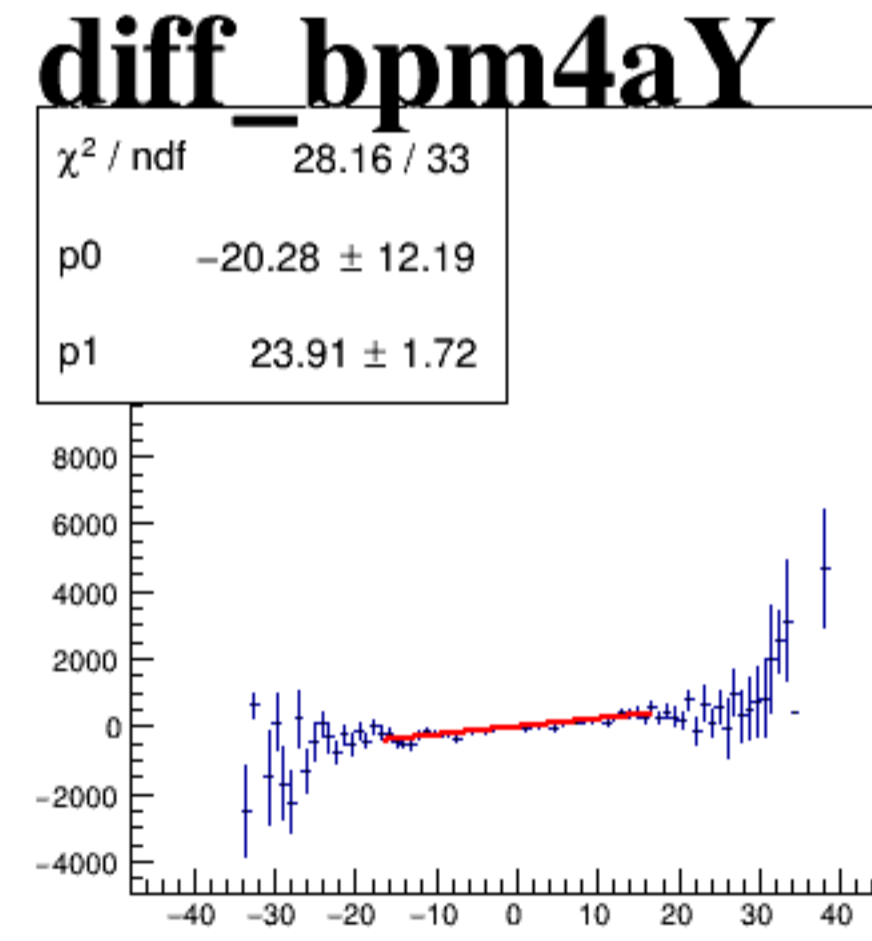
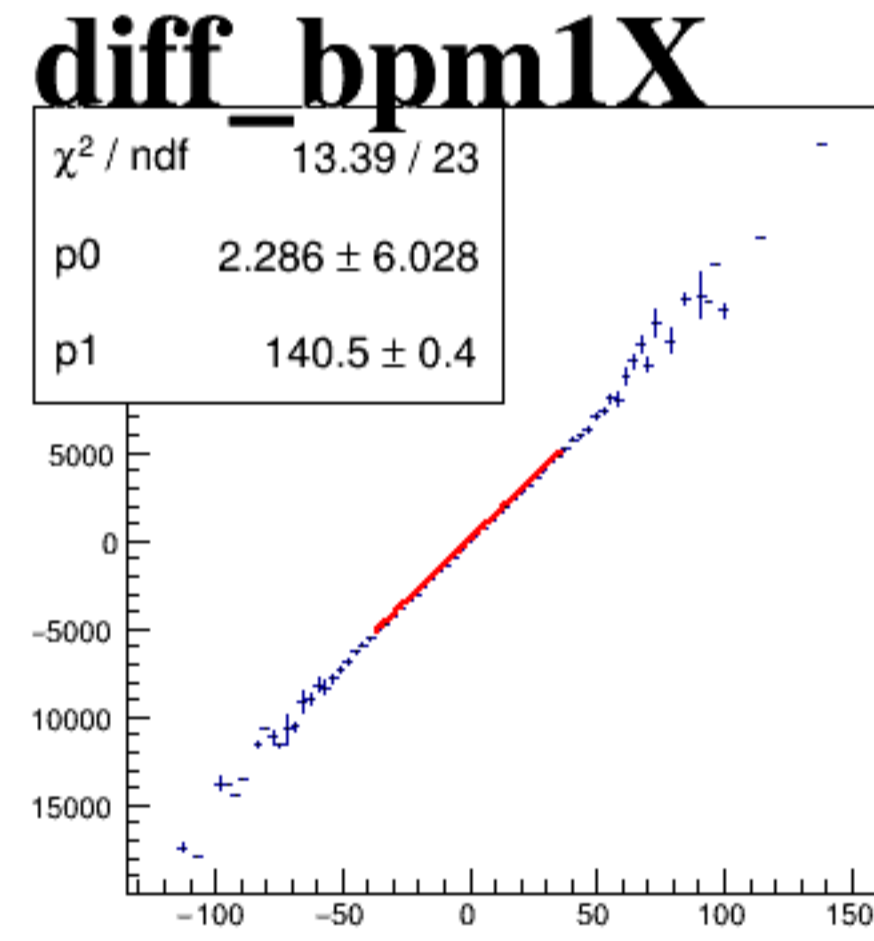
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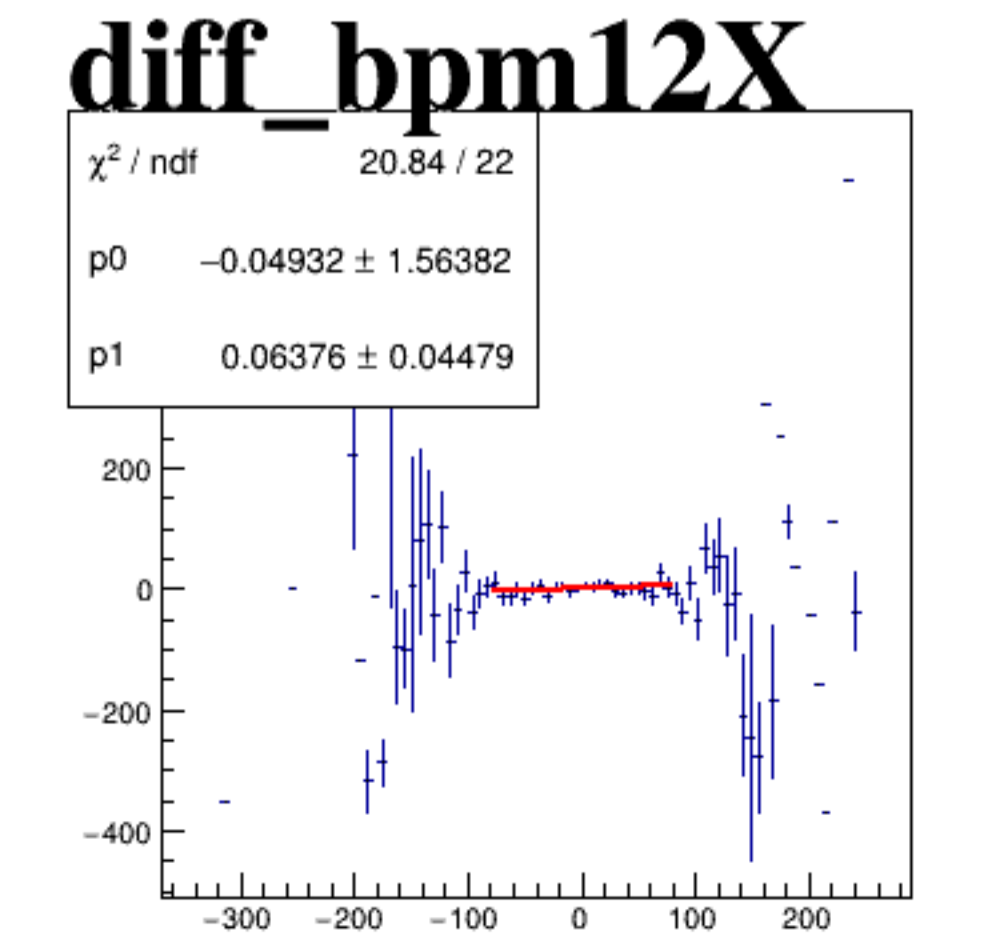
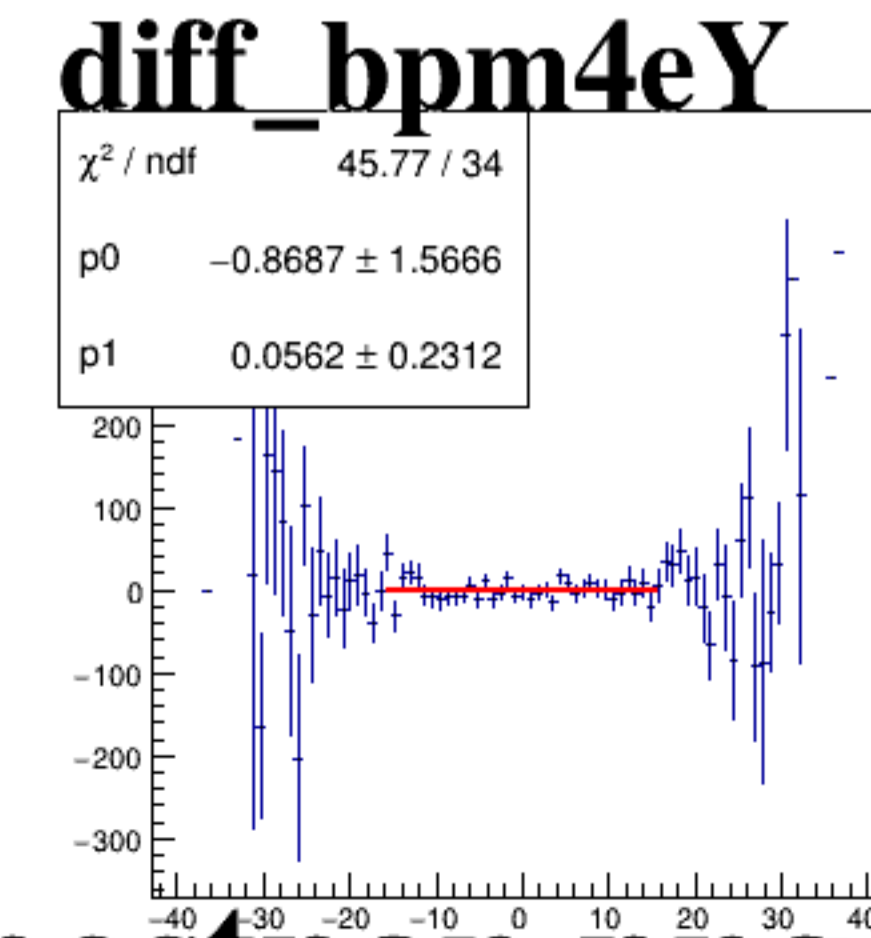
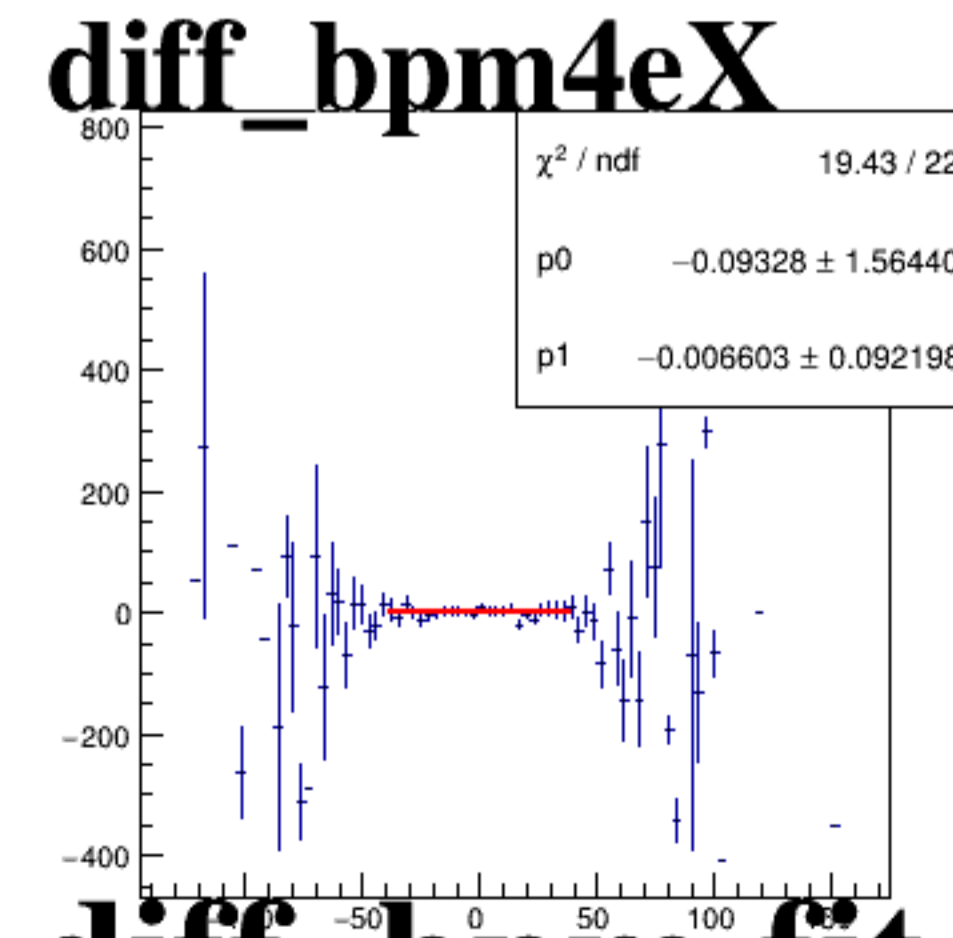
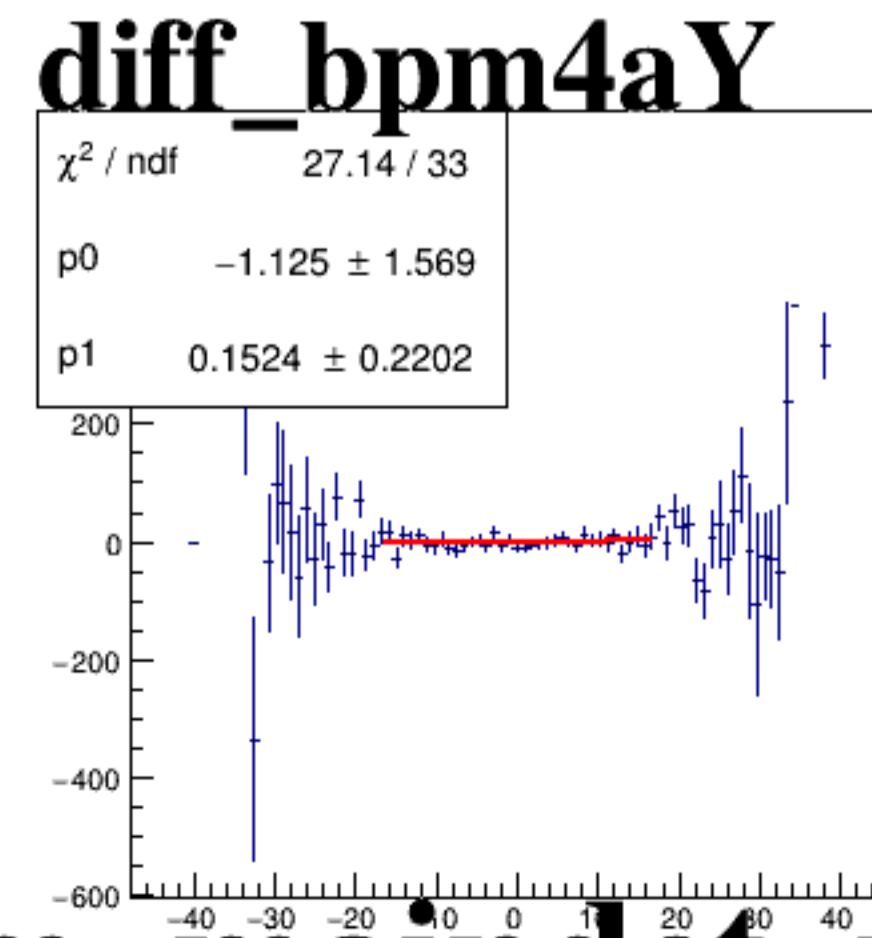
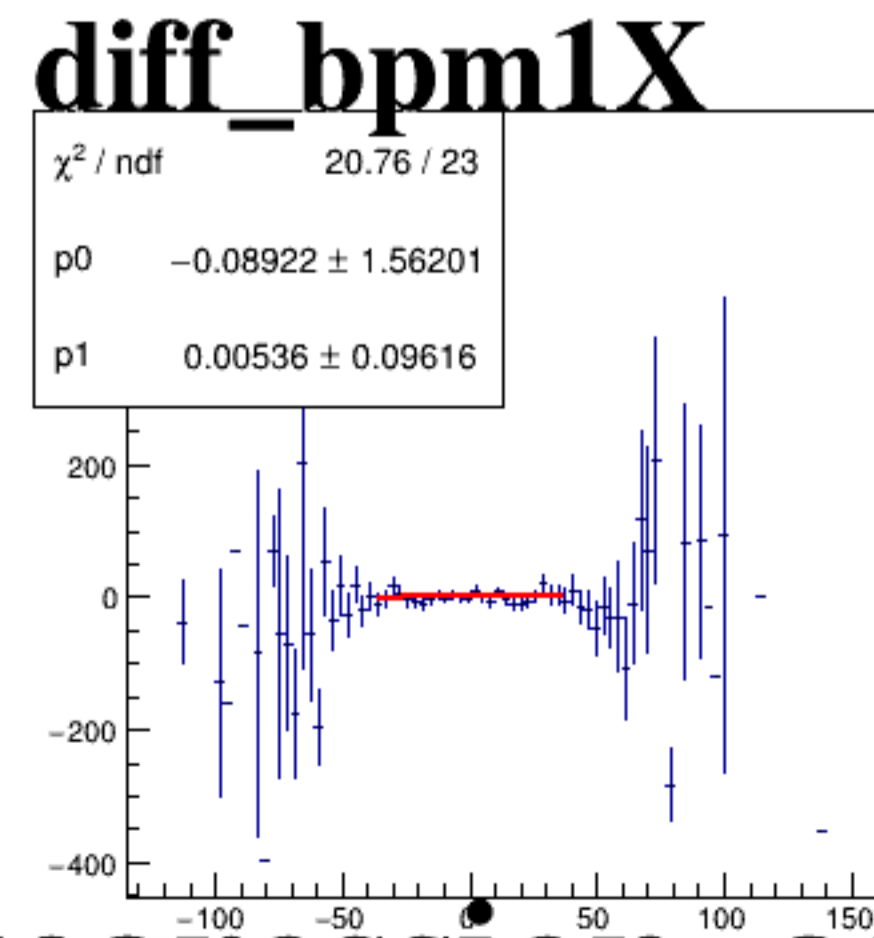
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asym_usr

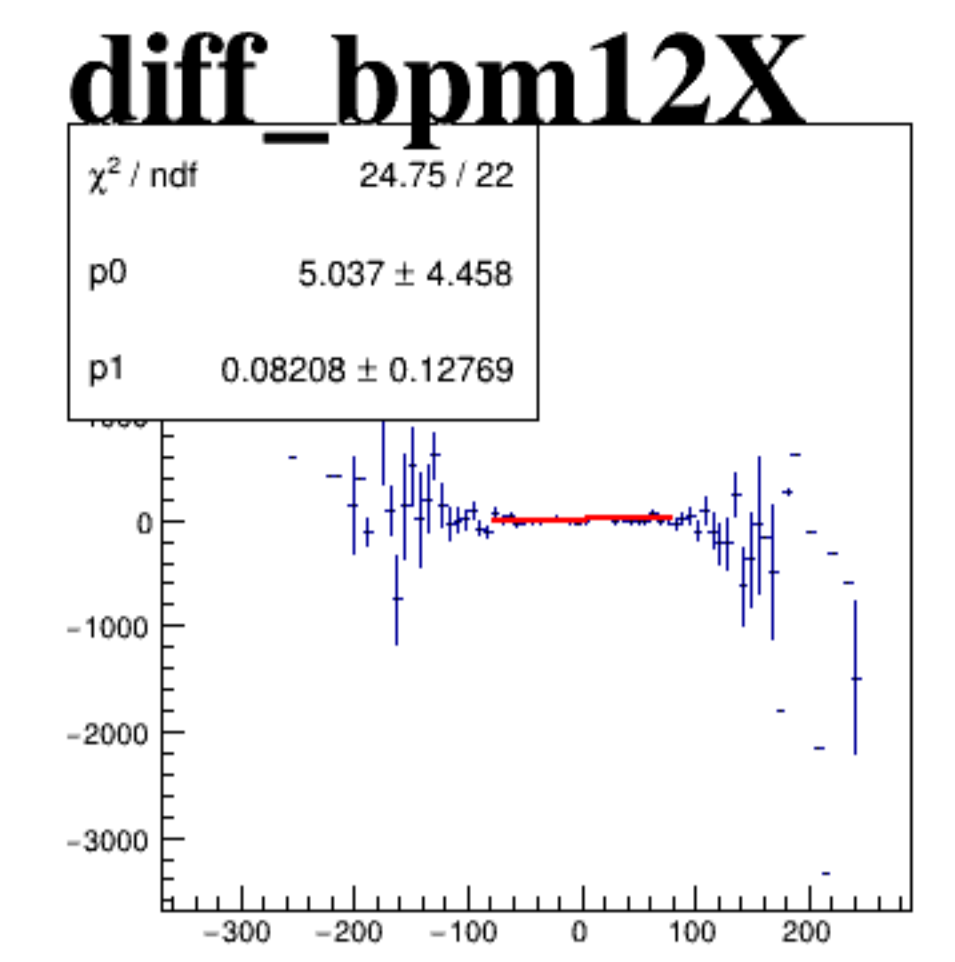
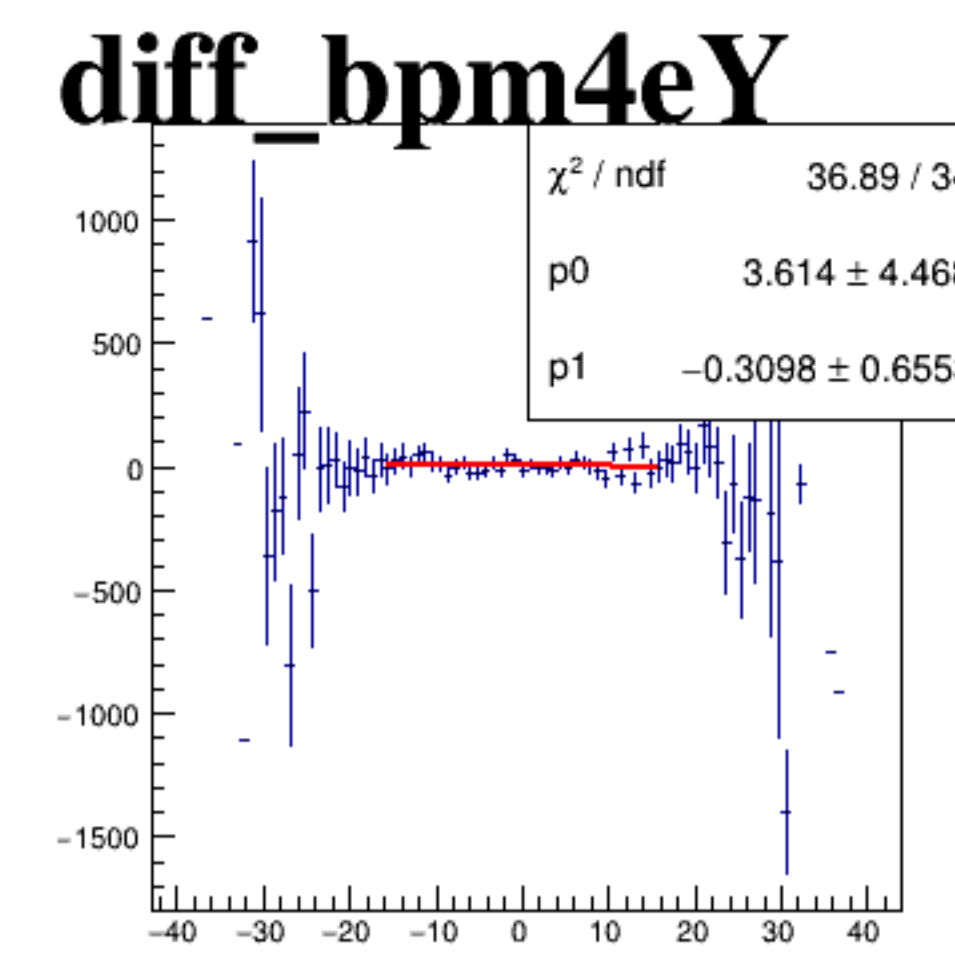
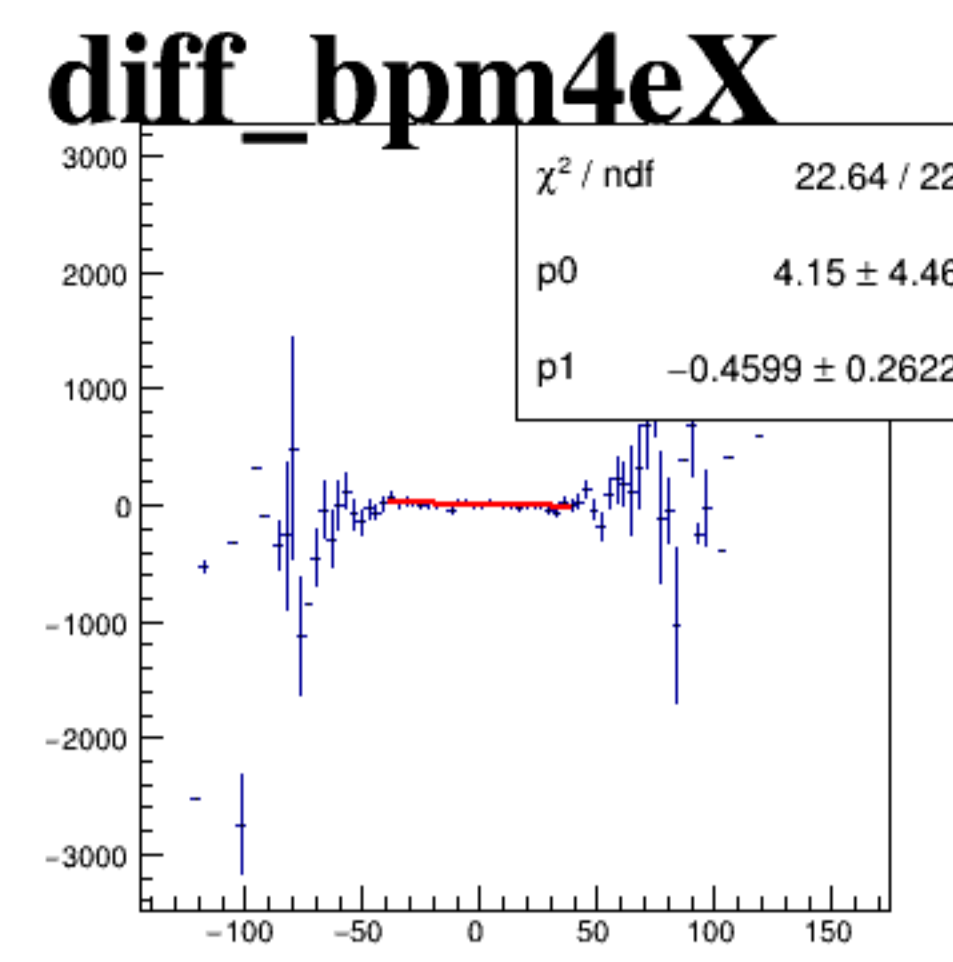
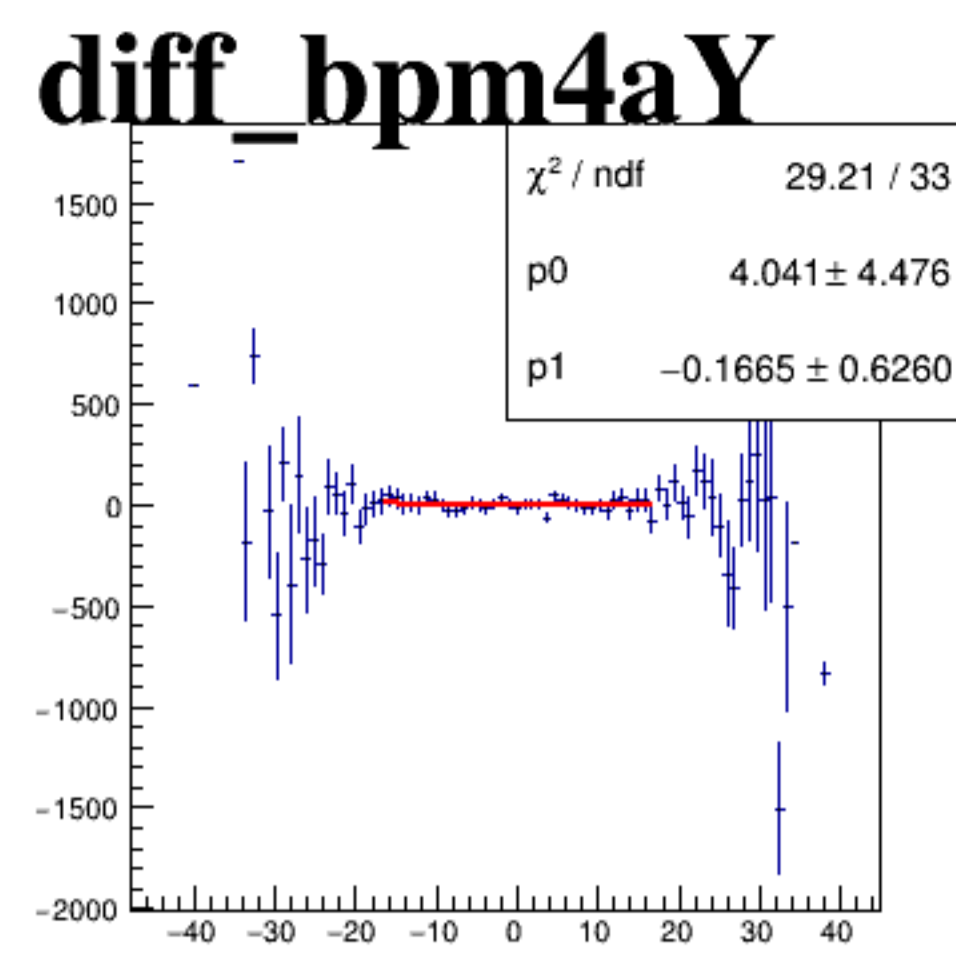
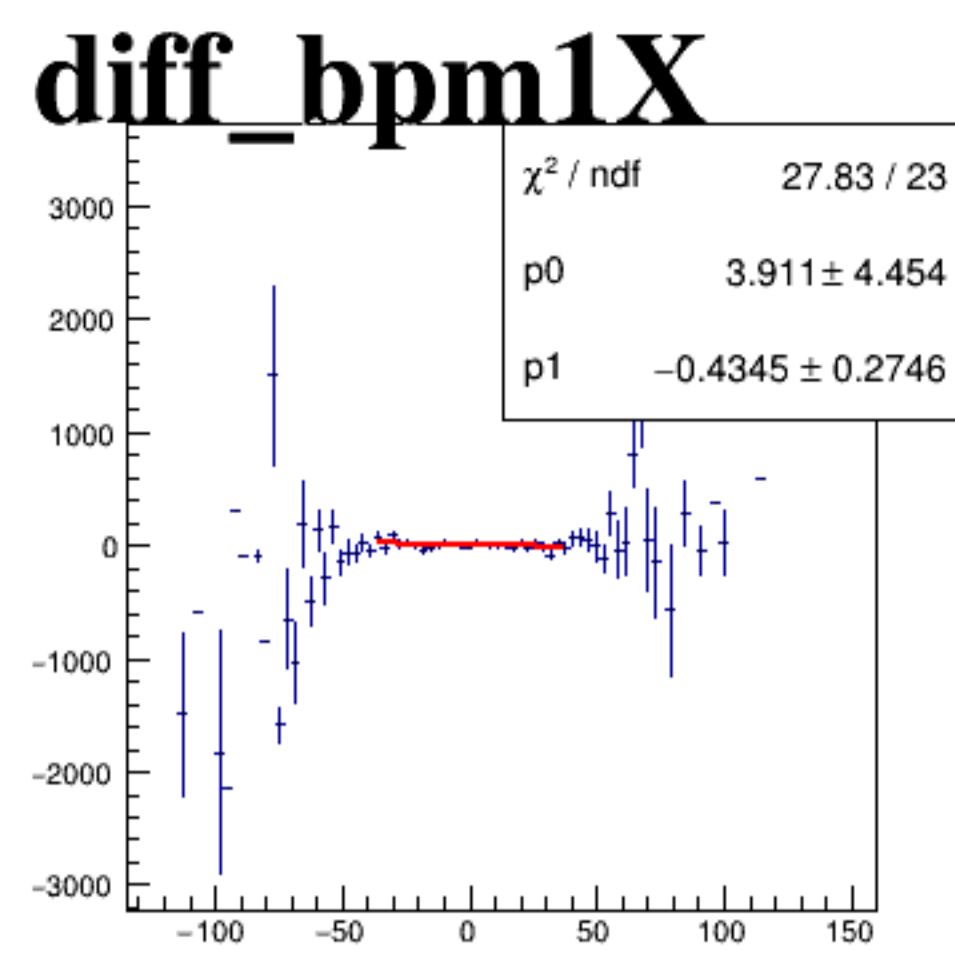


asym_dsr

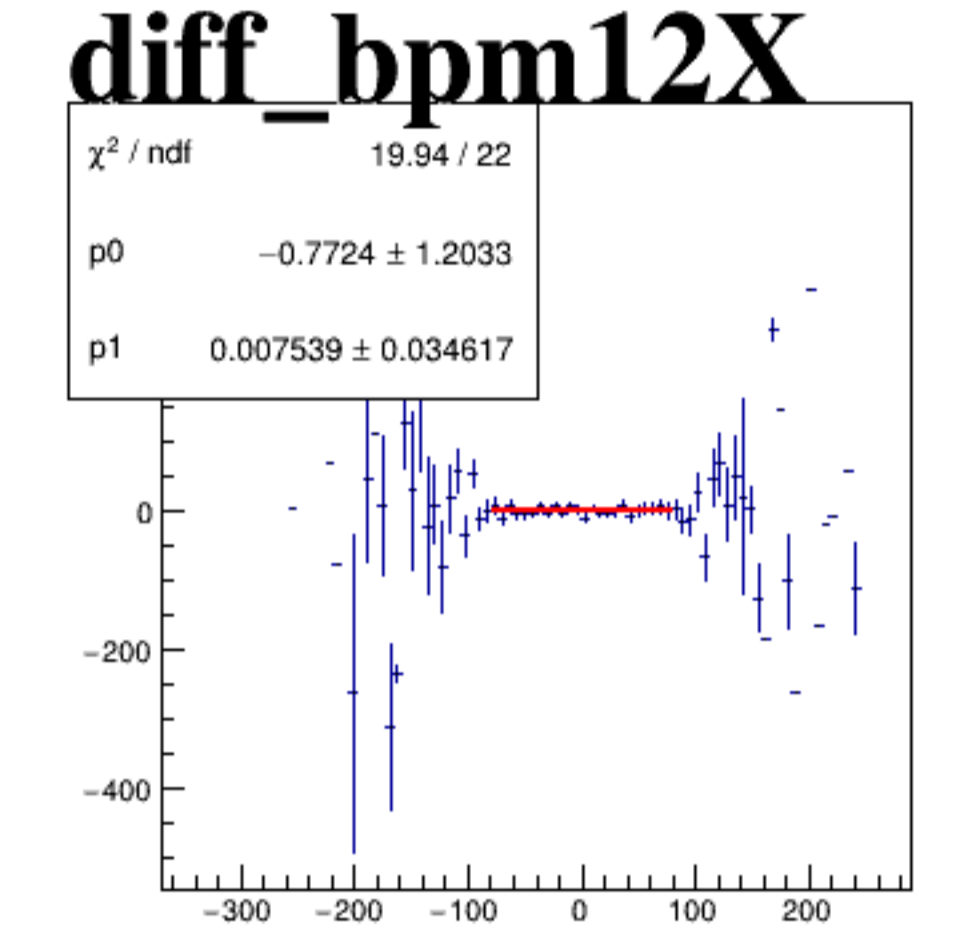
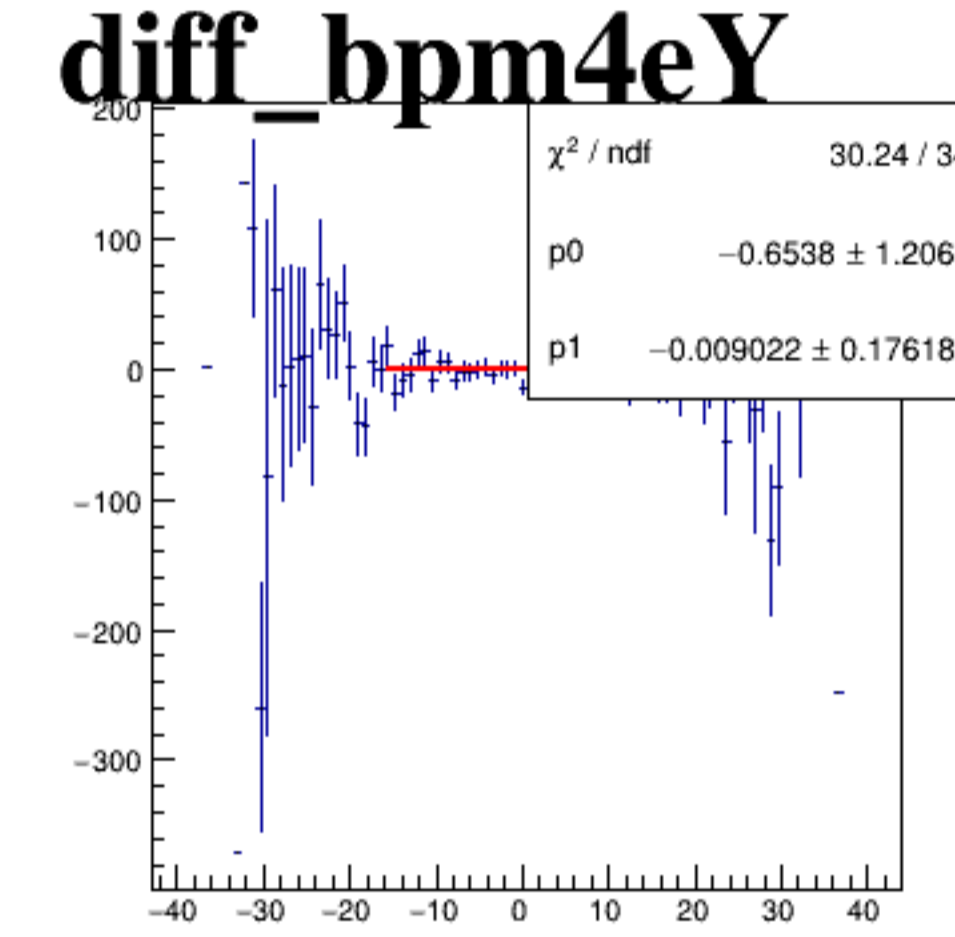
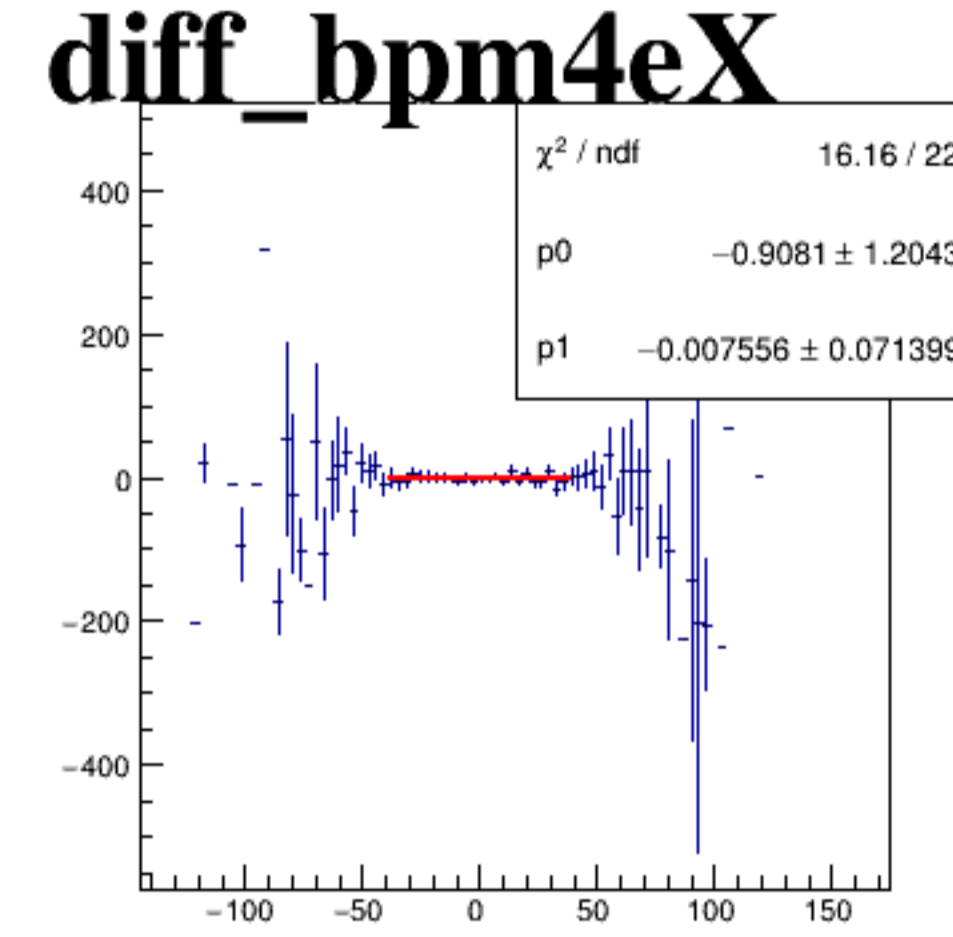
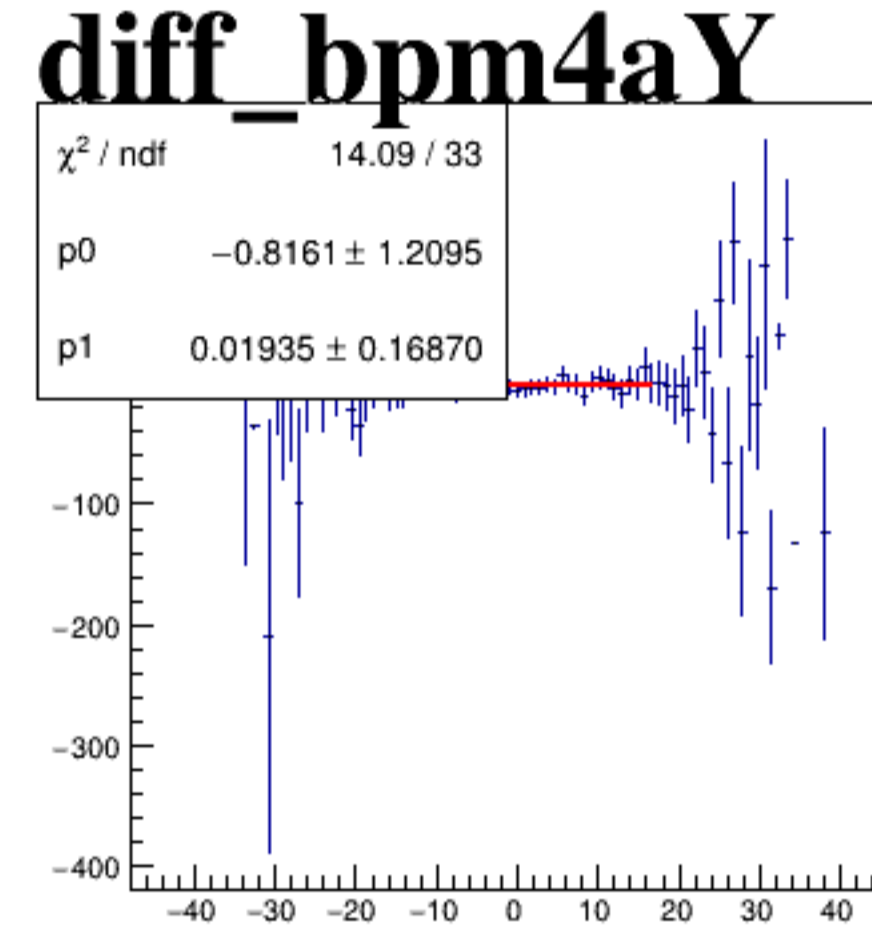
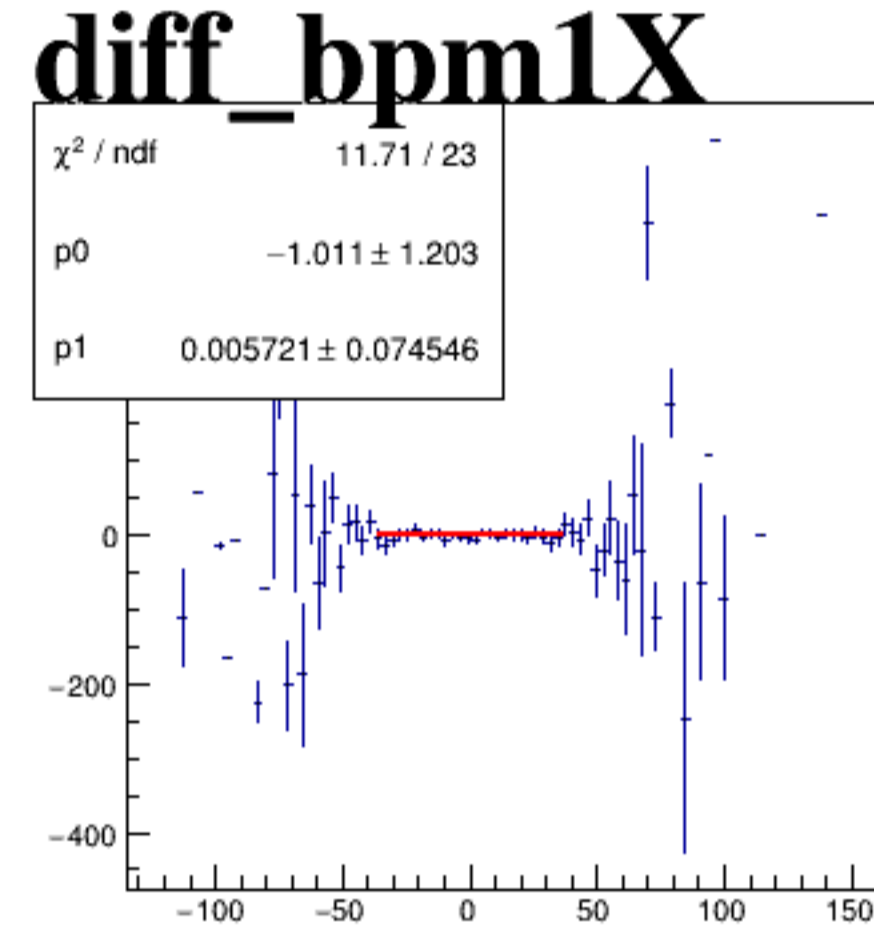


run8037_000_regression_asym_maintet_vs_diff_bpm-fit_postpan.png

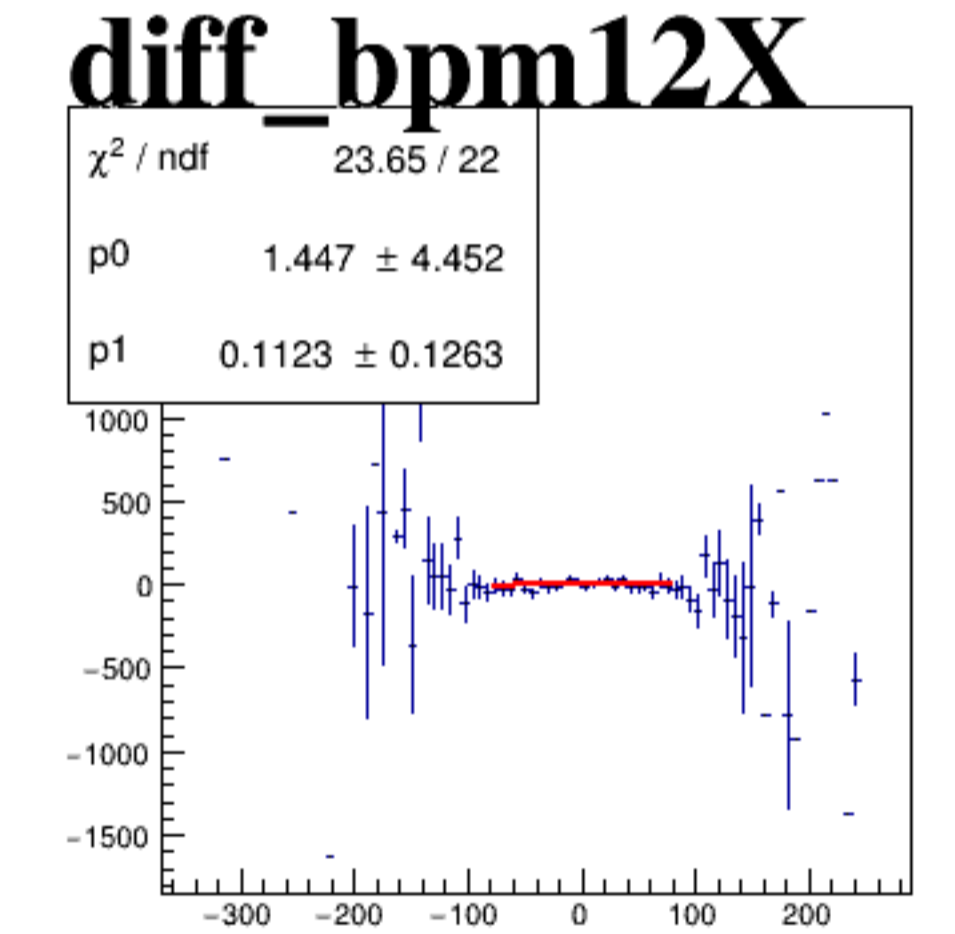
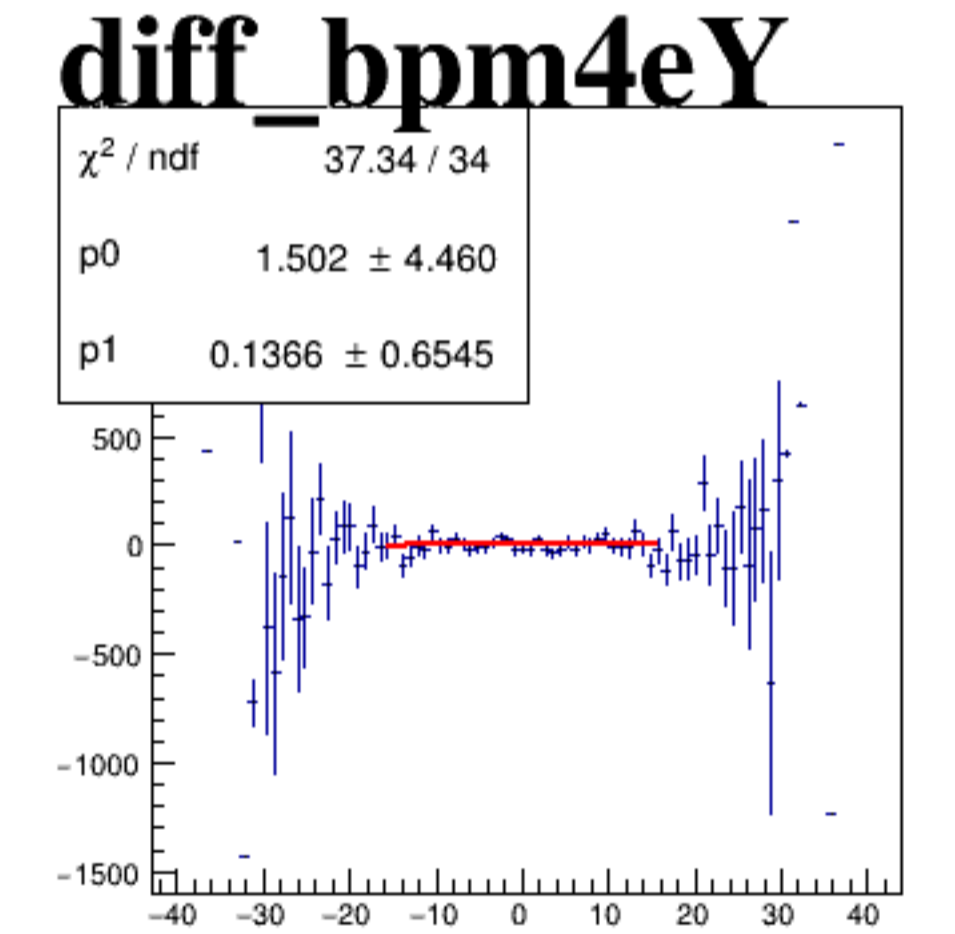
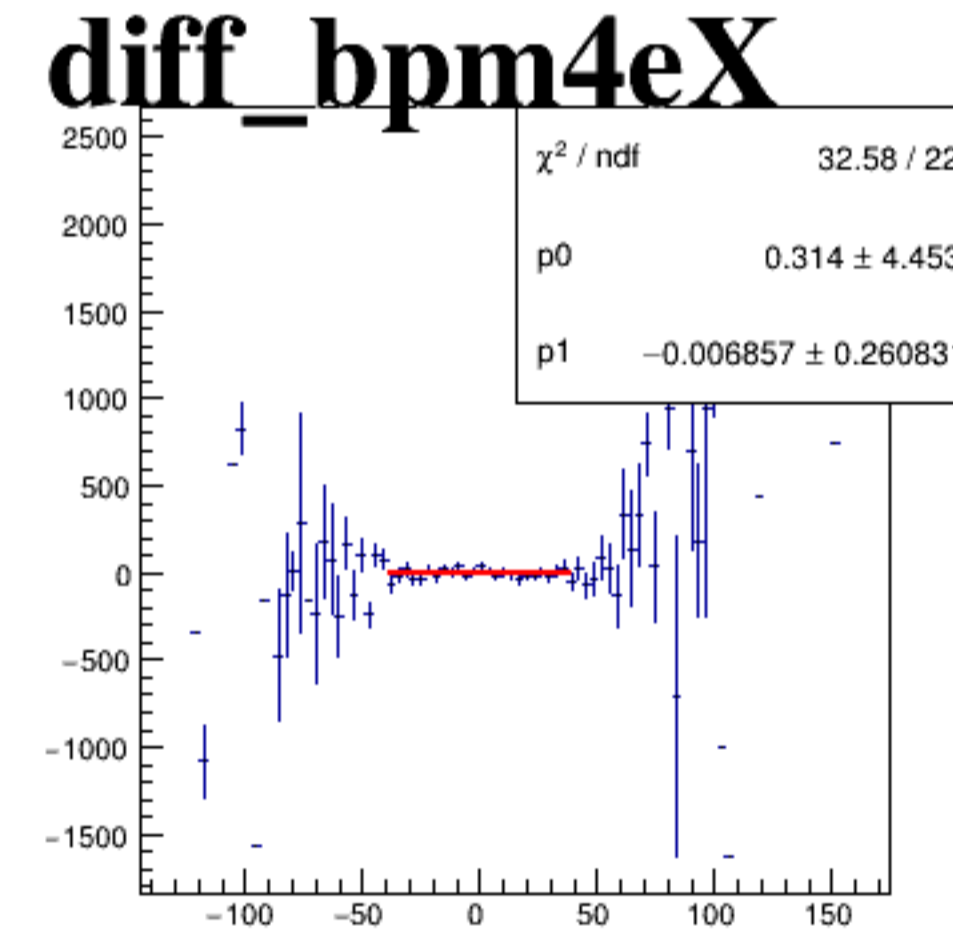
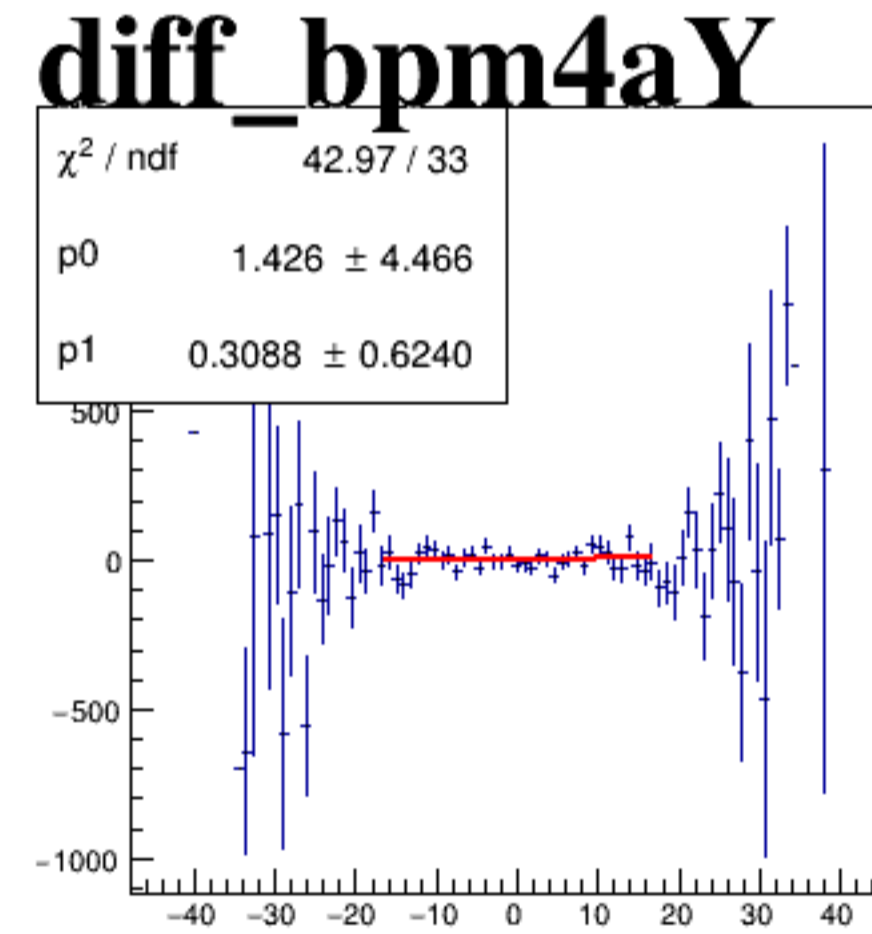
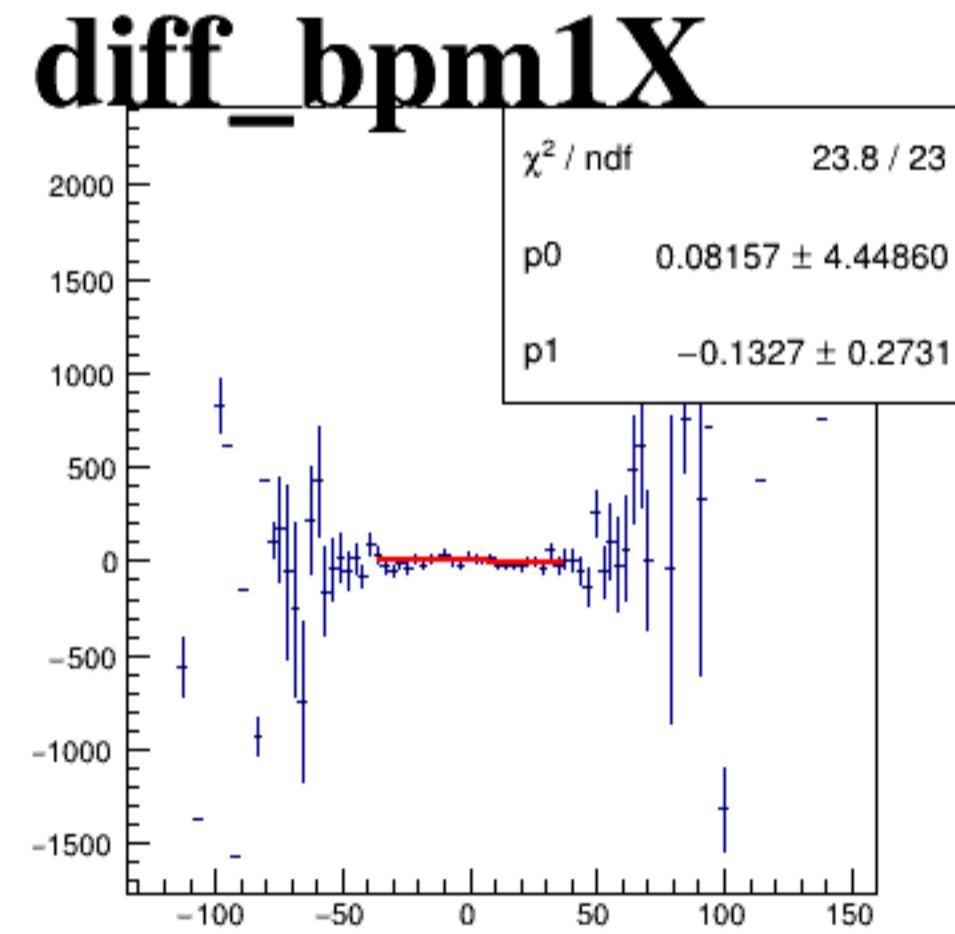
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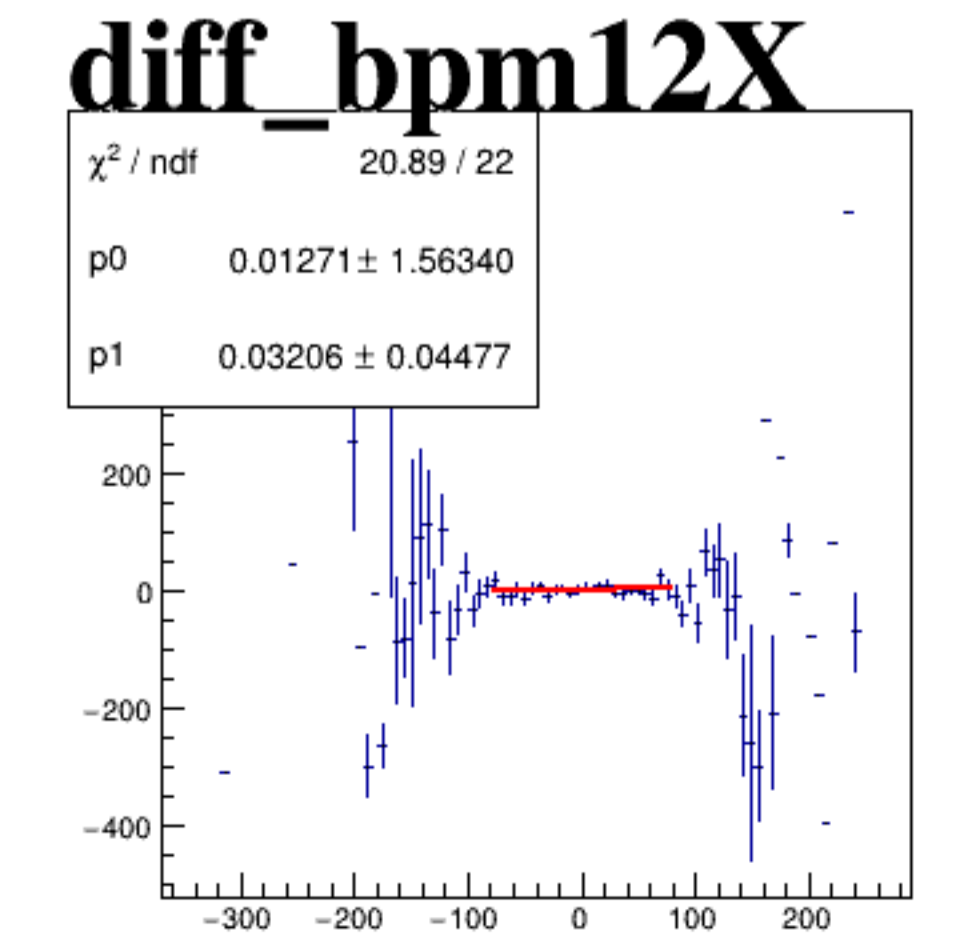
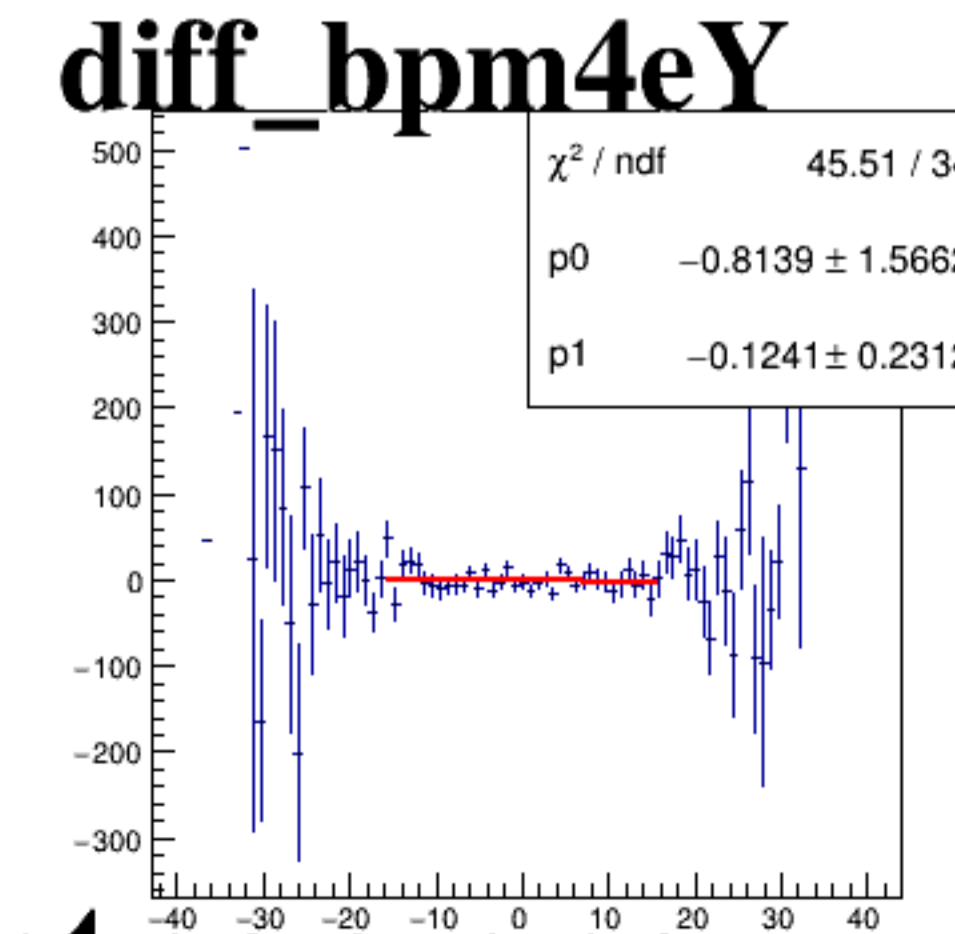
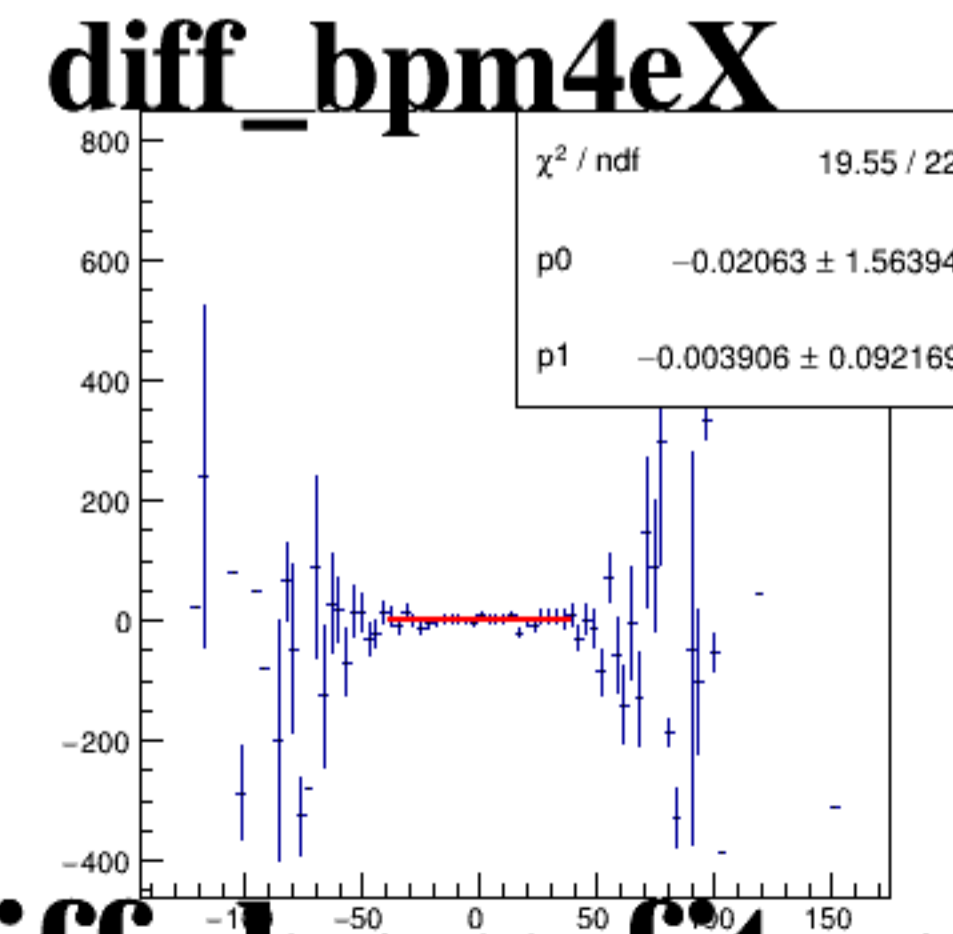
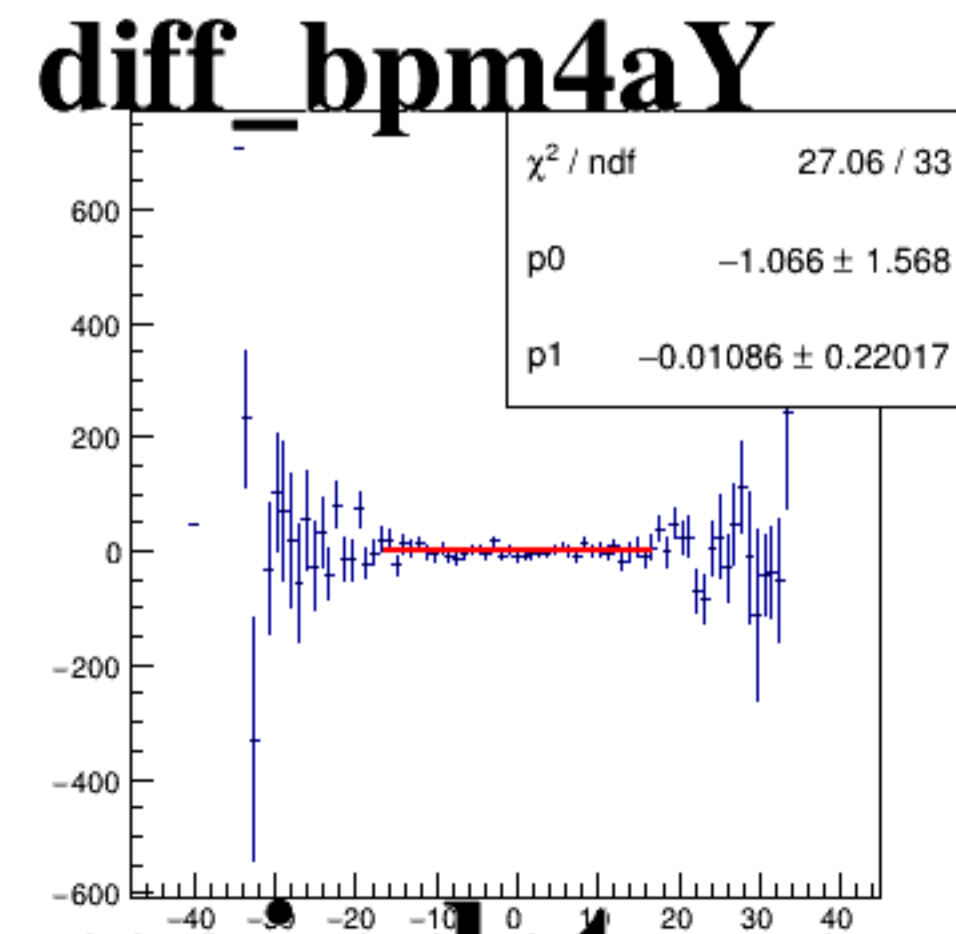
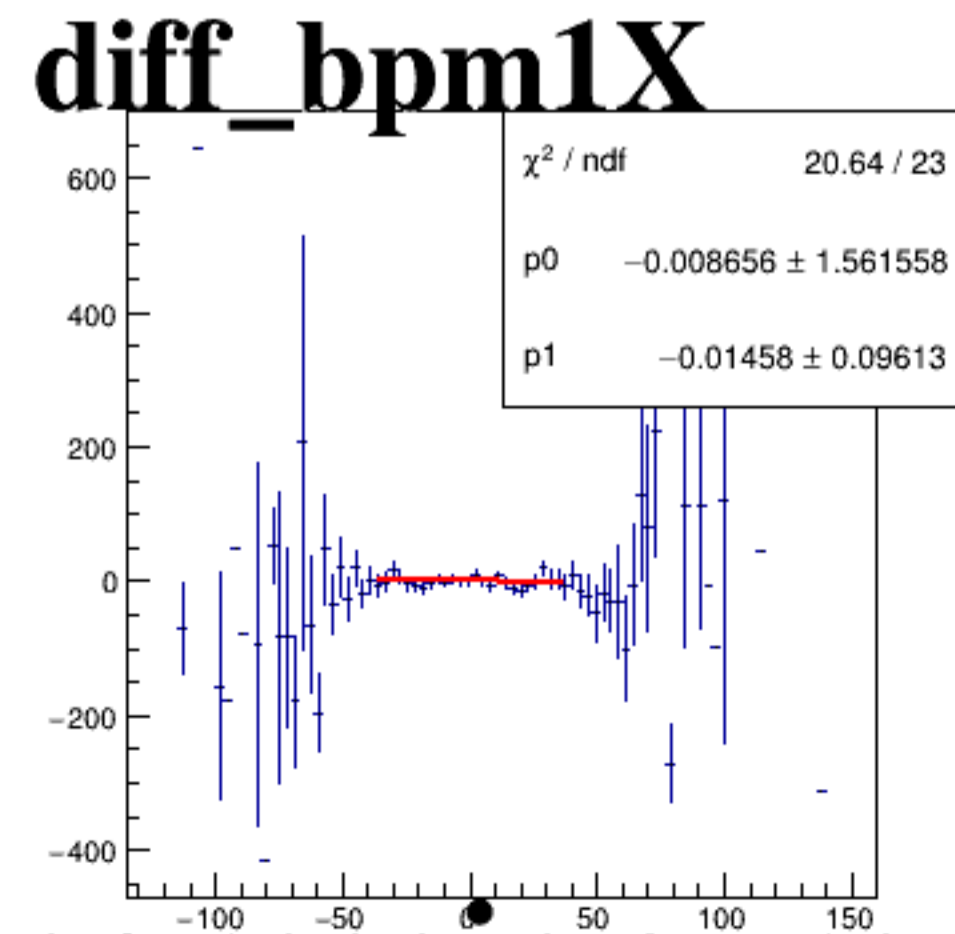
reg_asym_dsl



reg_asym_usr

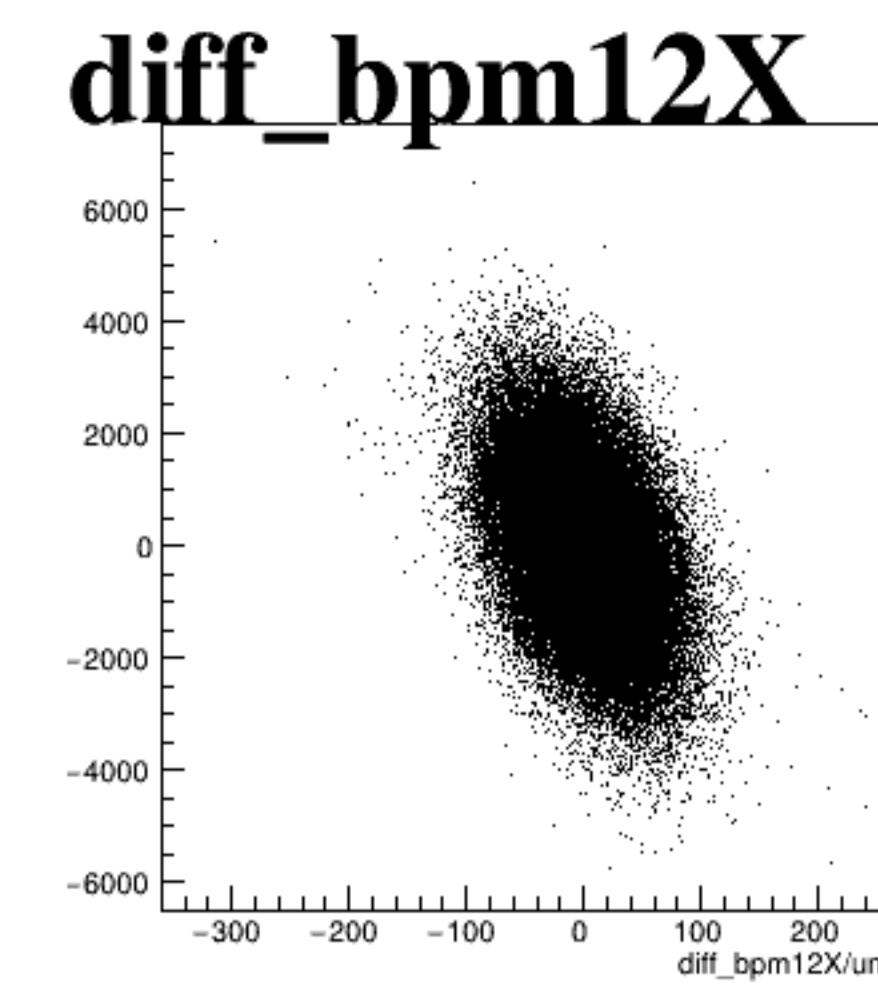
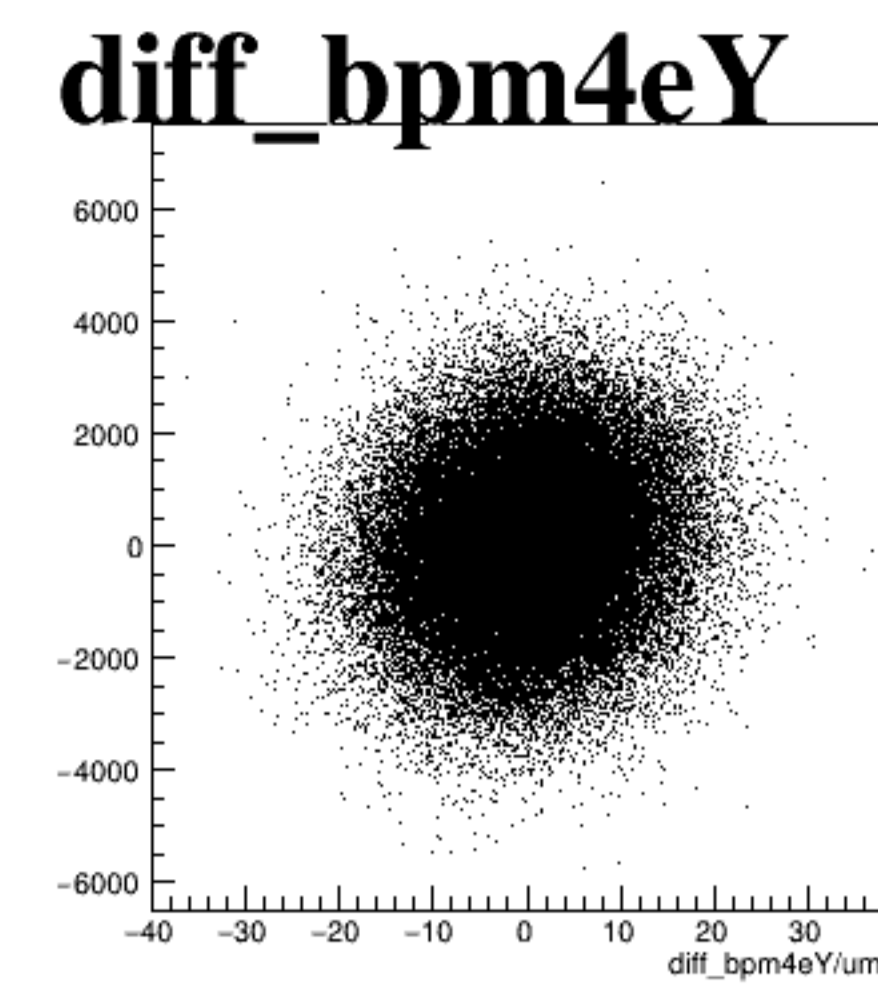
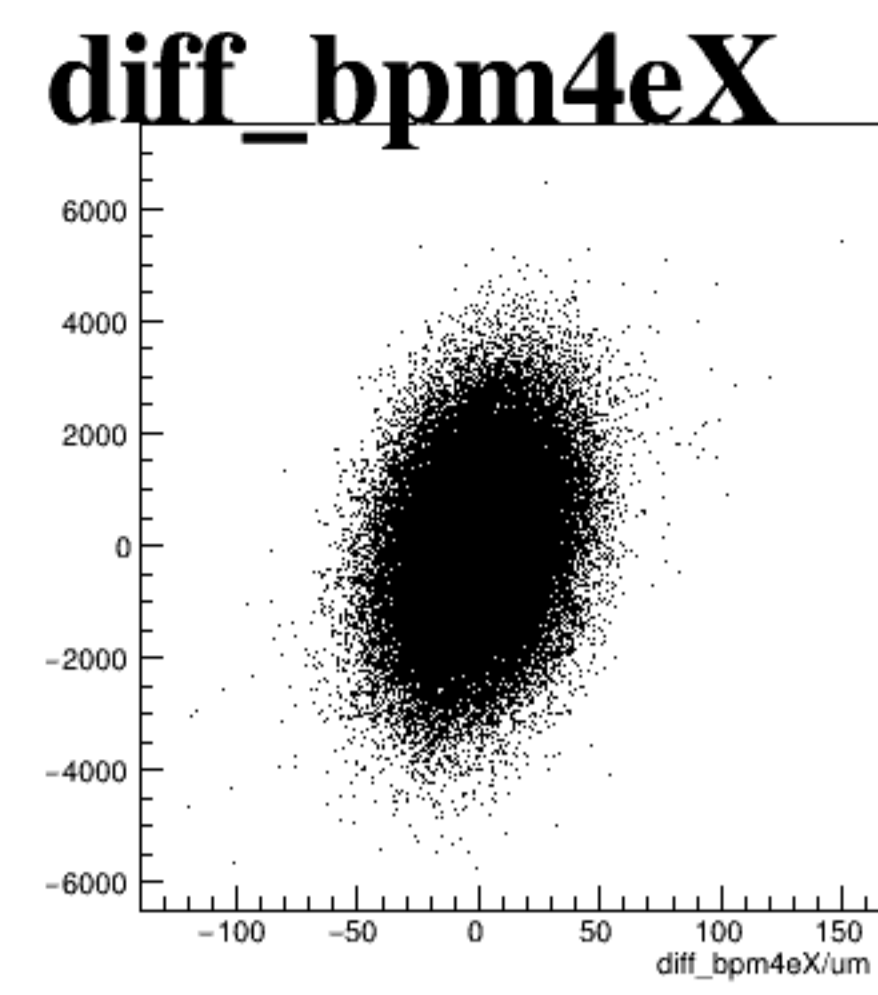
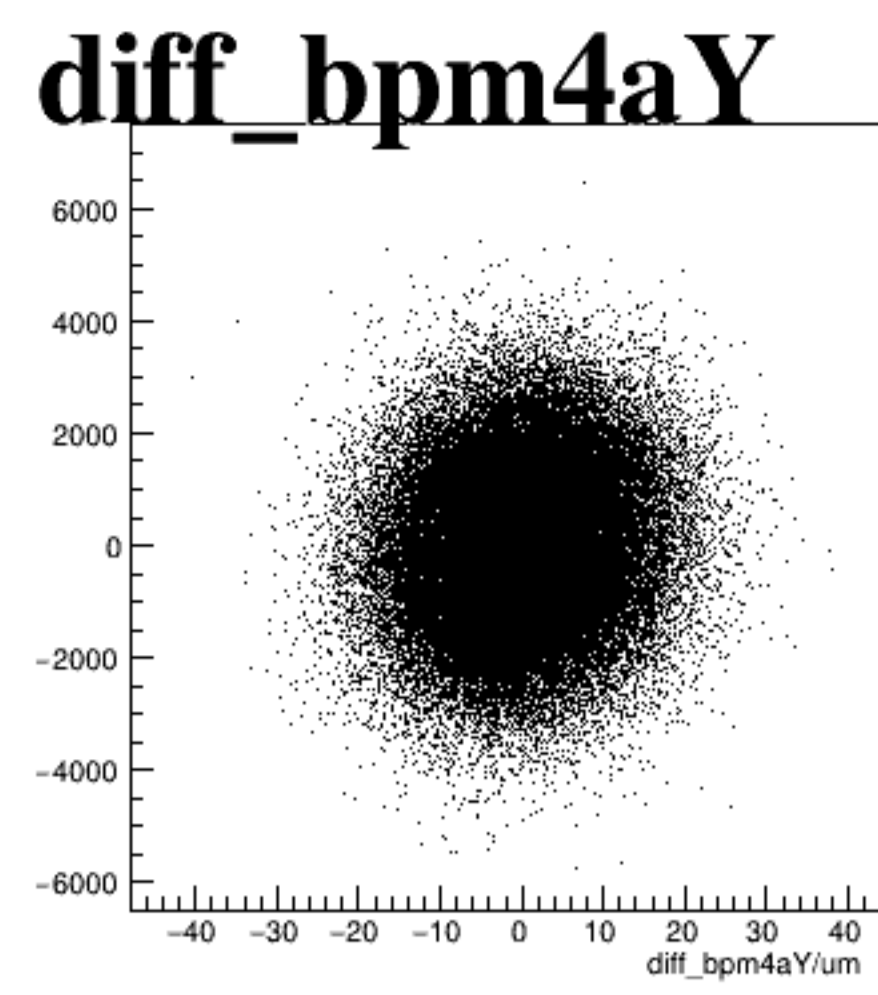
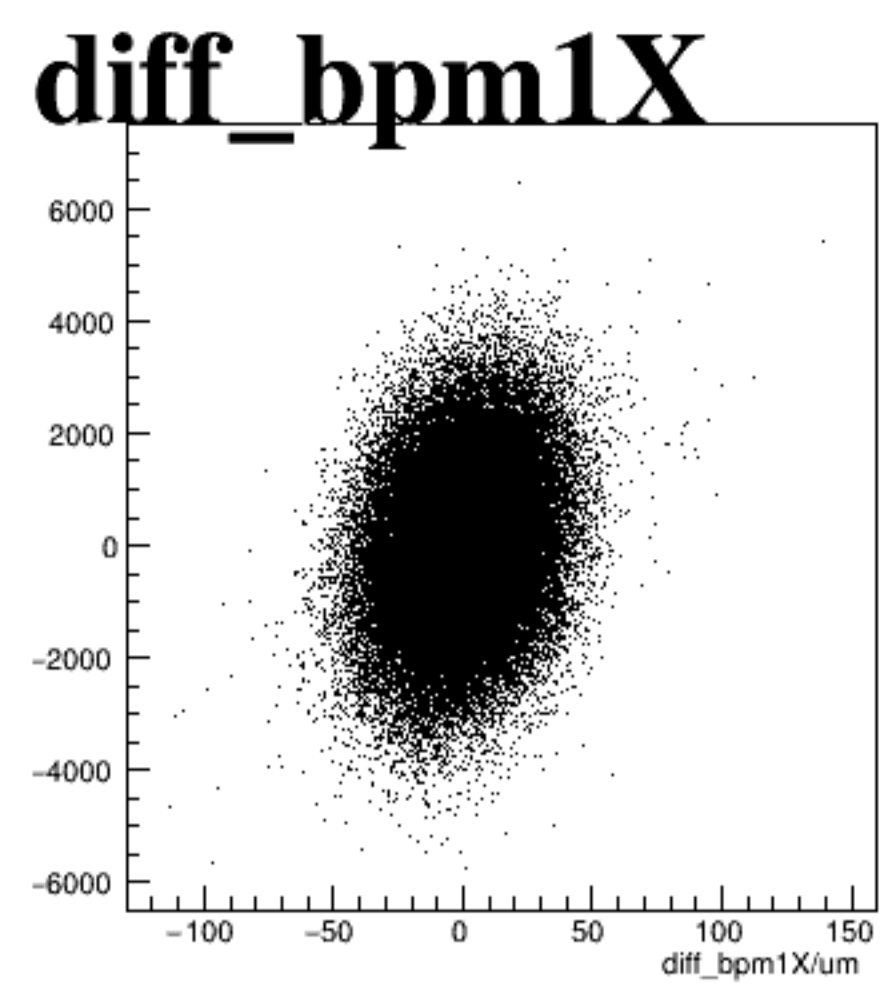


reg_asym_dsr

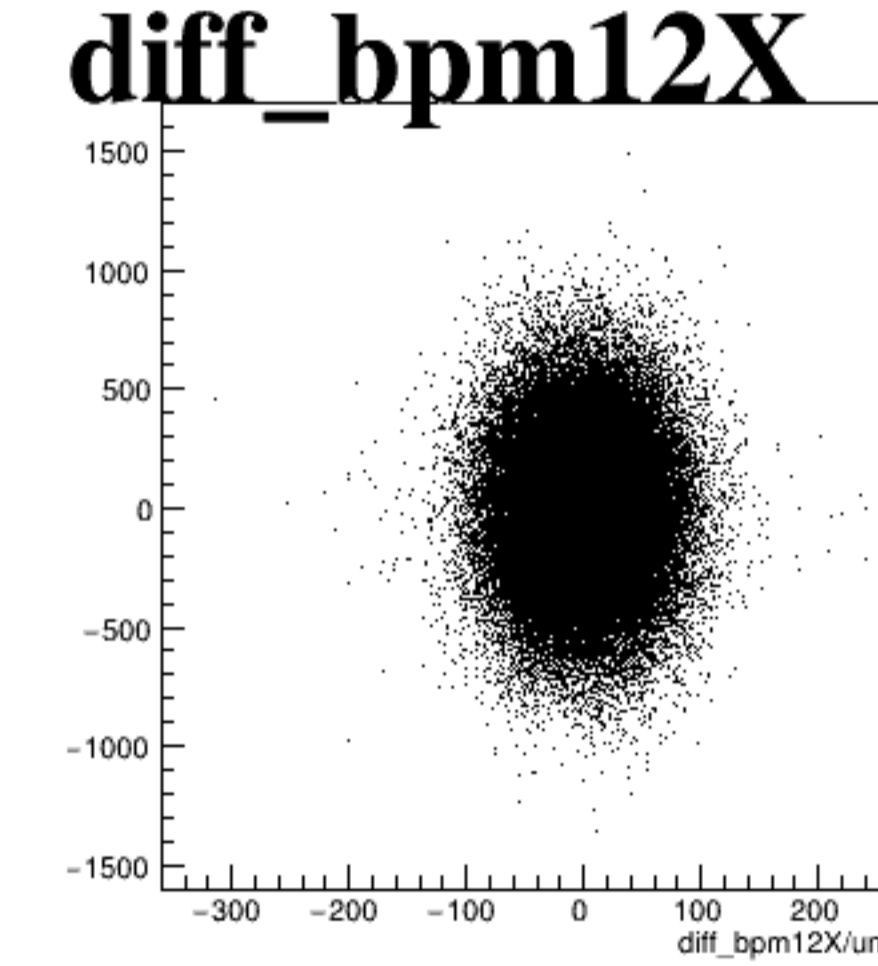
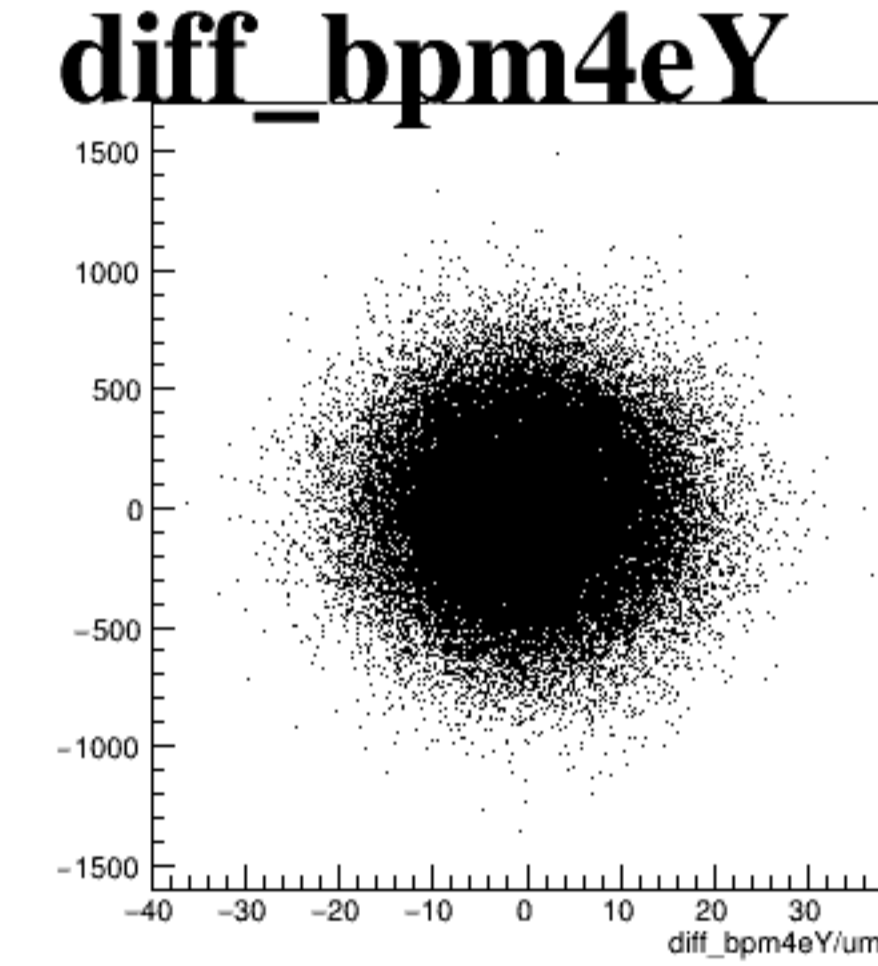
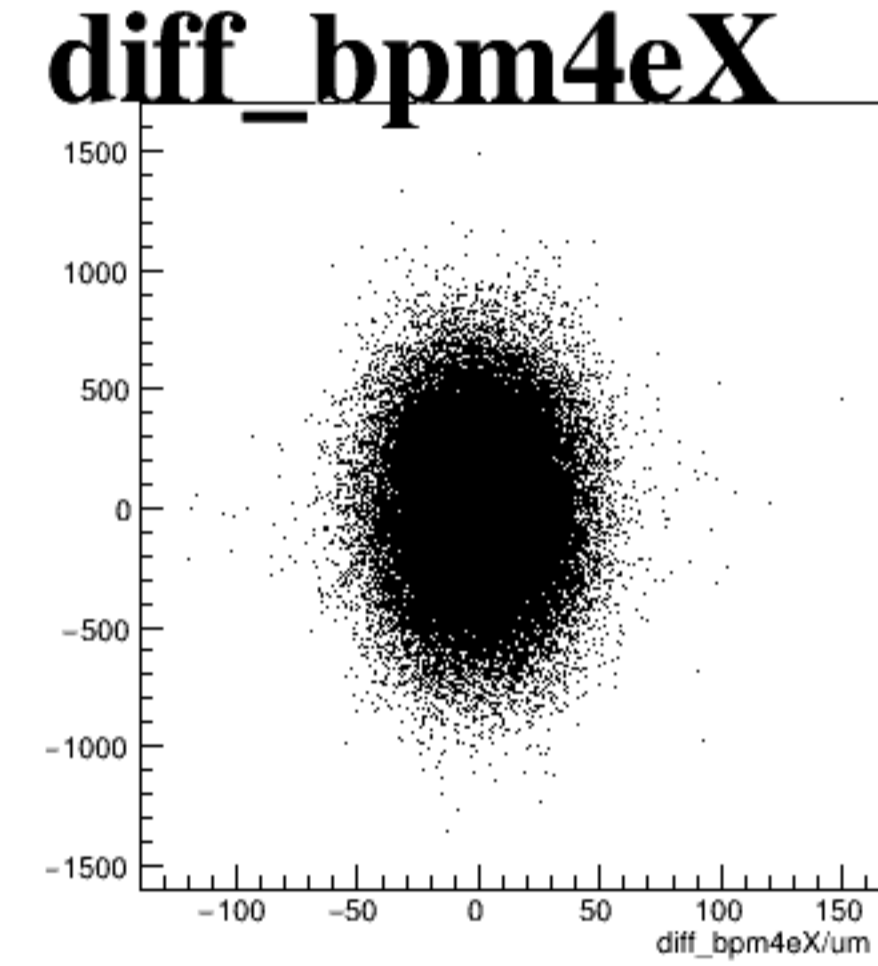
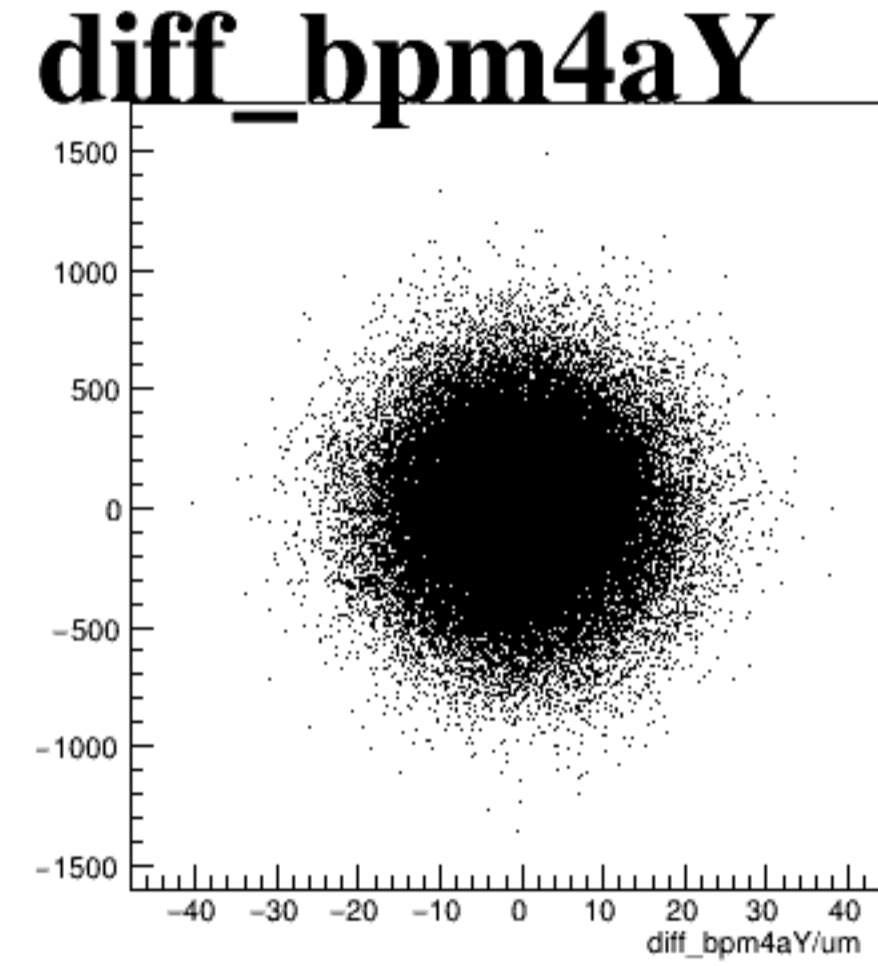
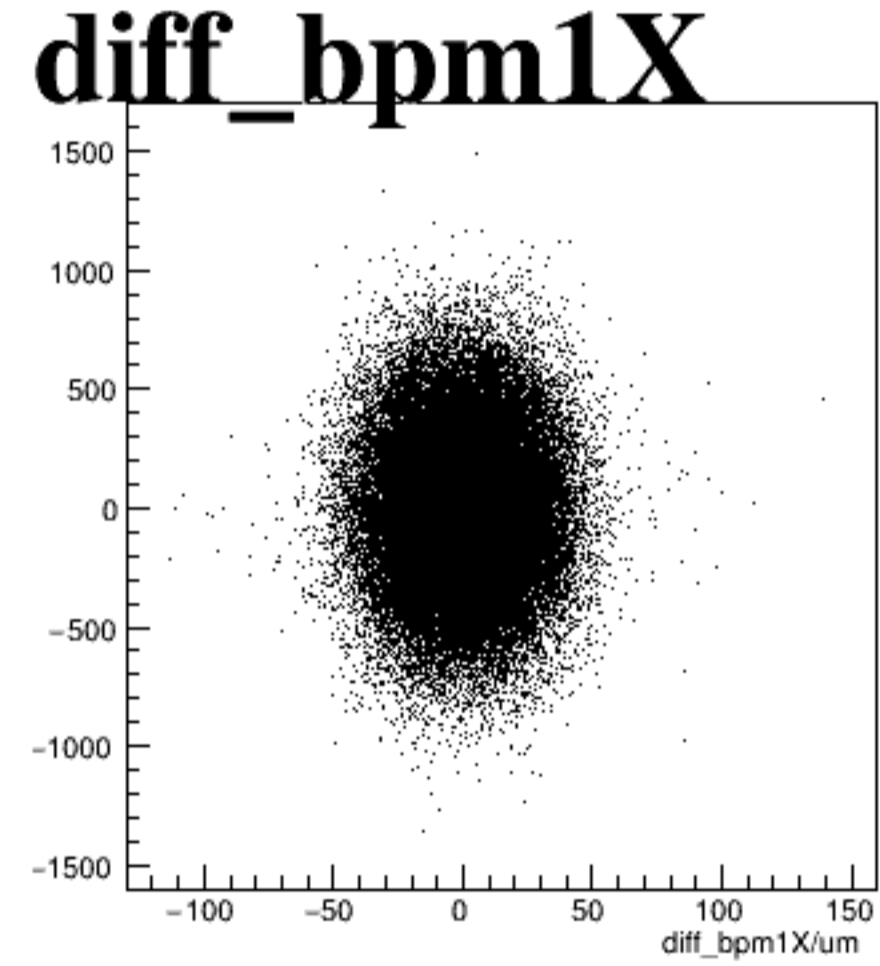


run8037_000_regression_reg_maintet_vs_diff_bpm-fit_postpan.png

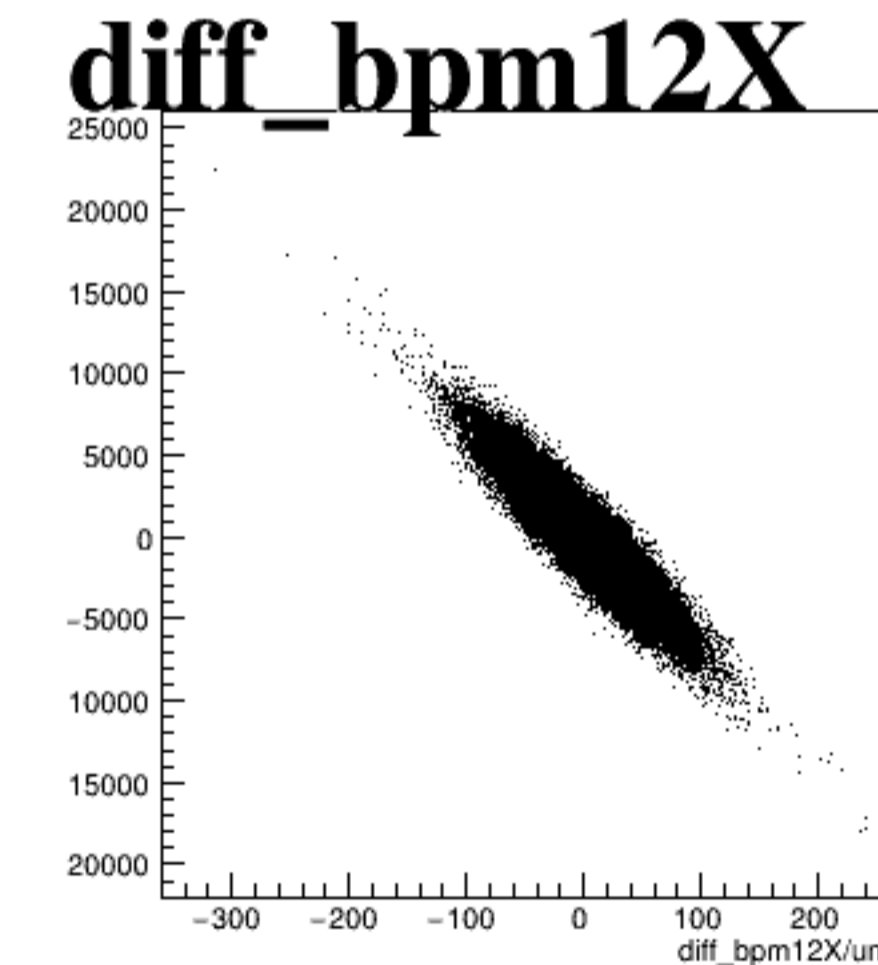
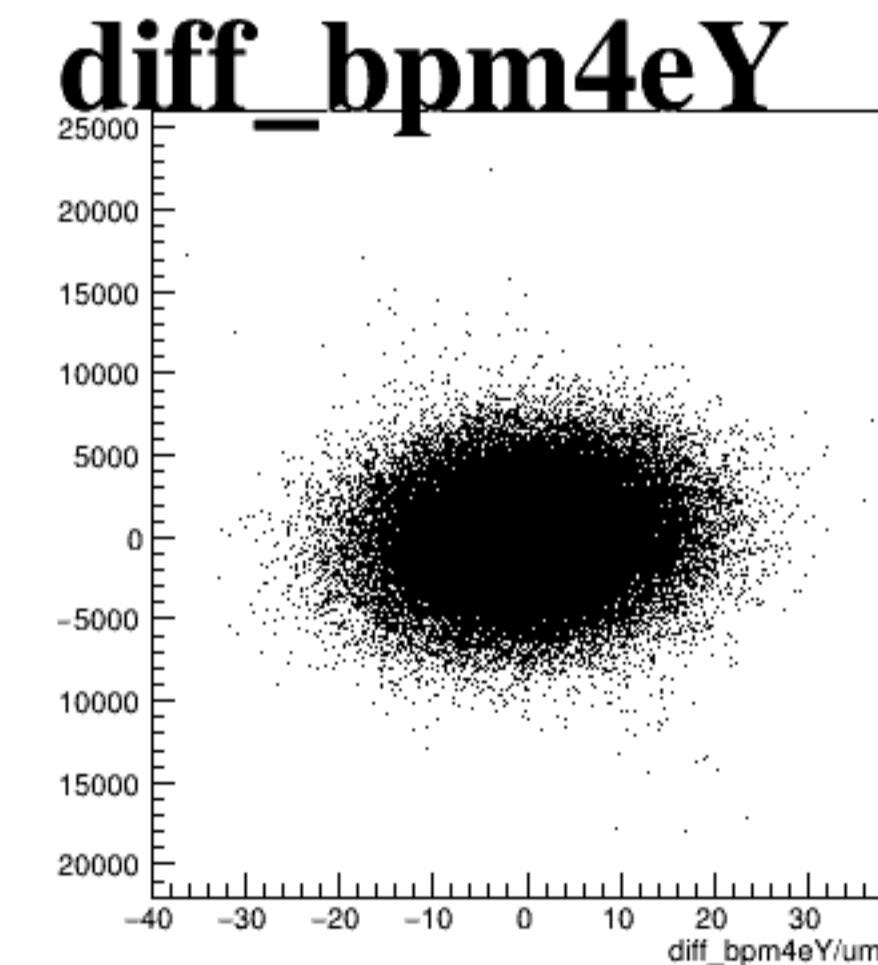
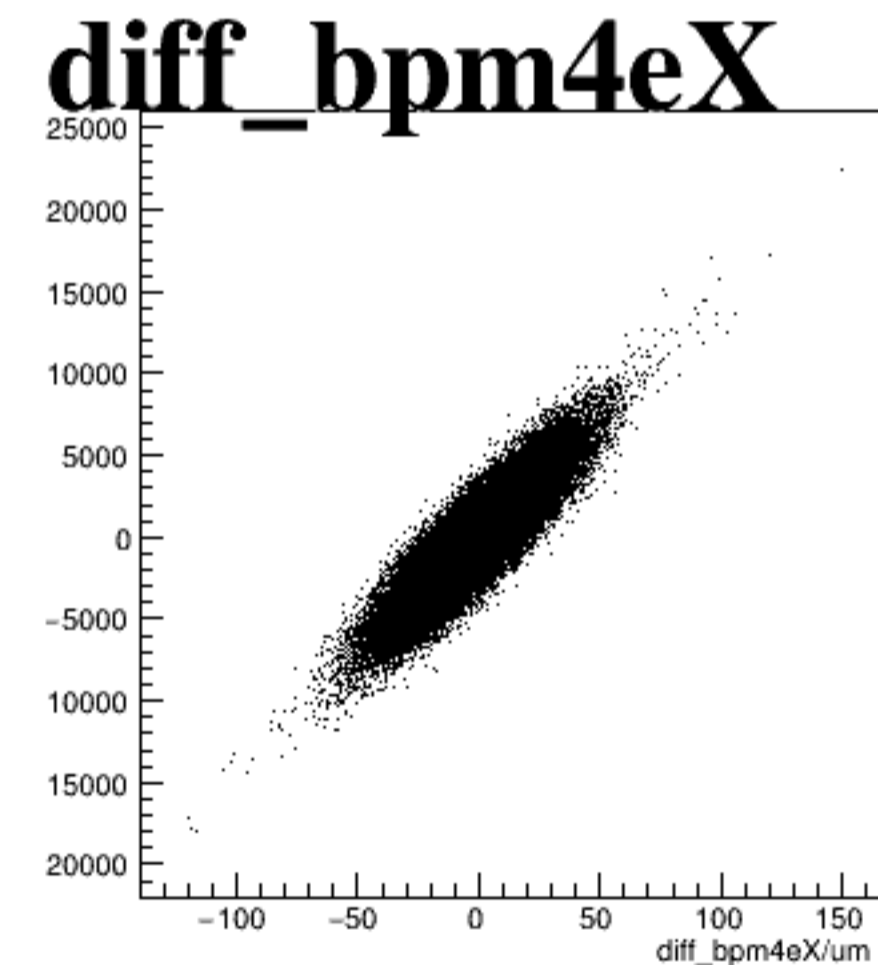
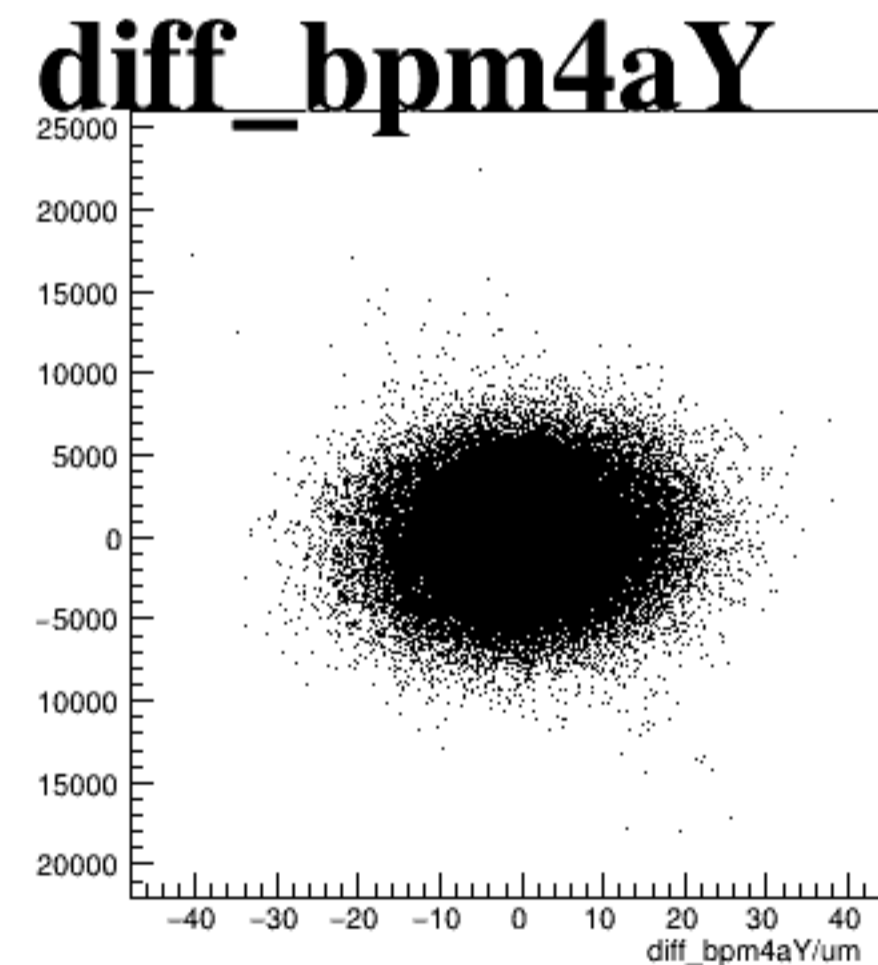
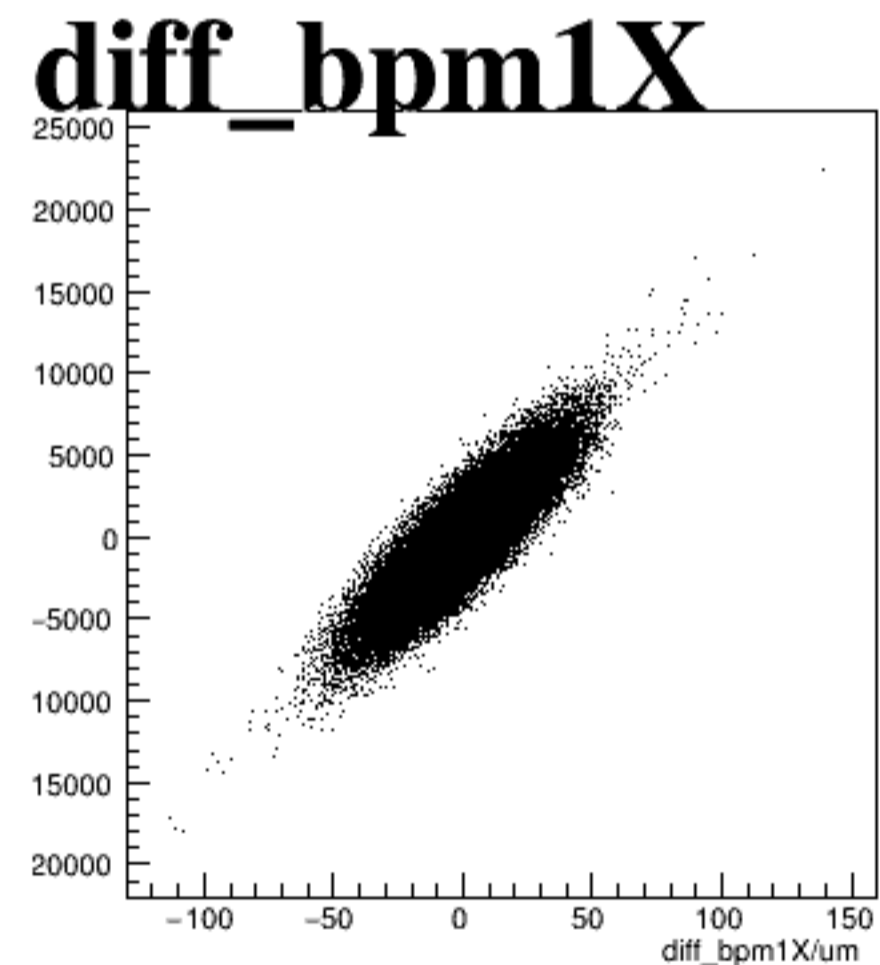
asym_usl



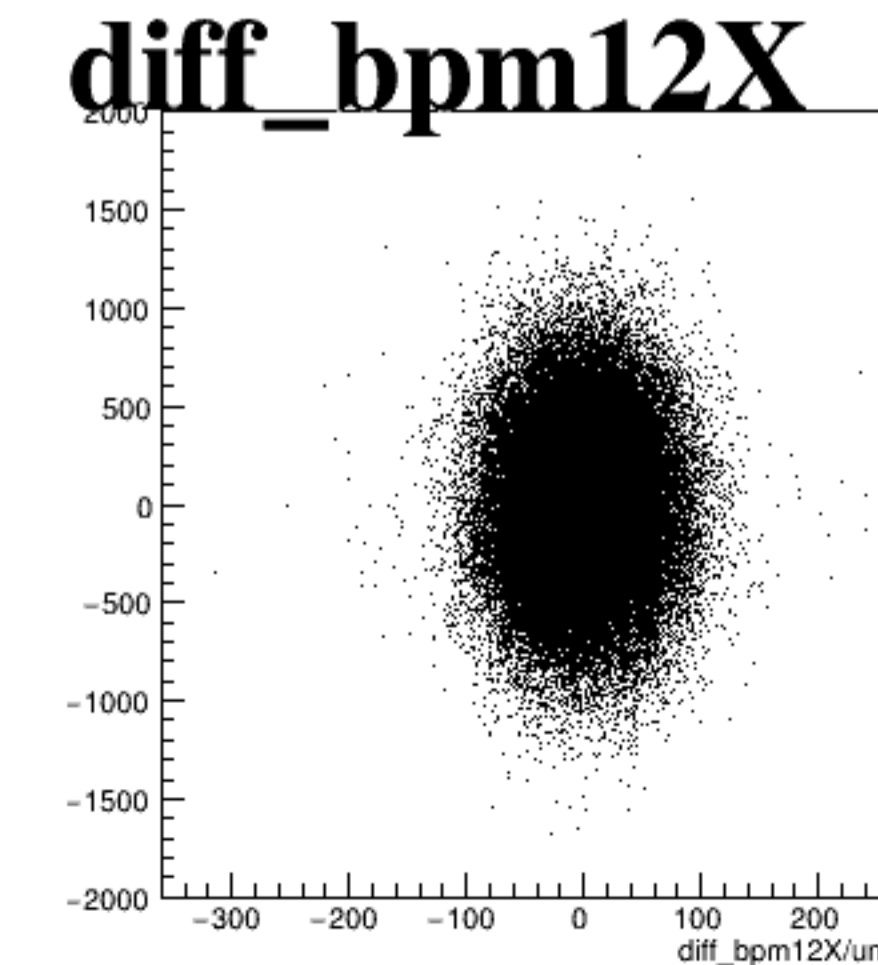
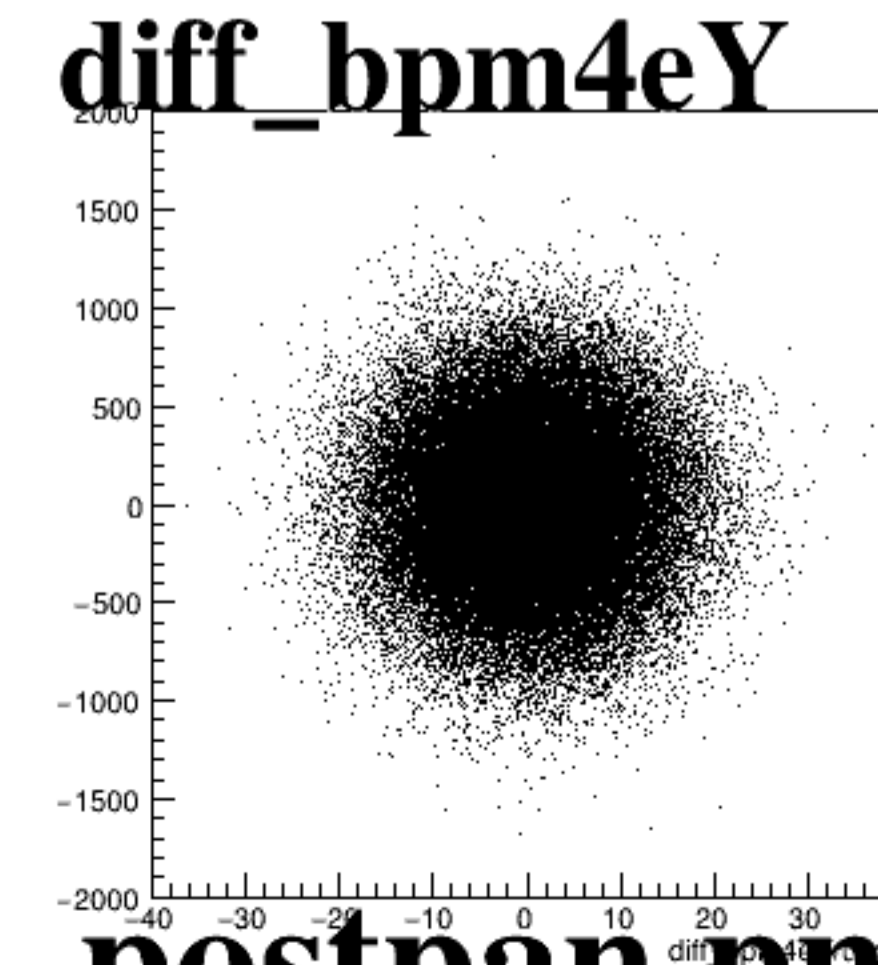
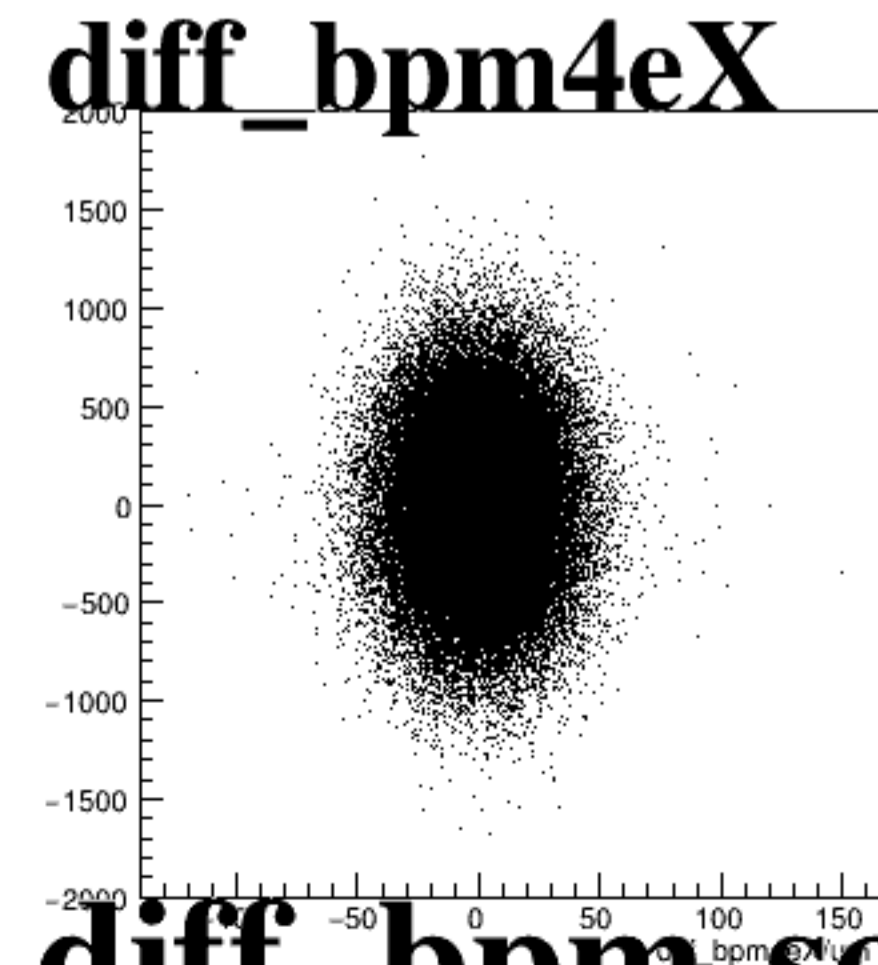
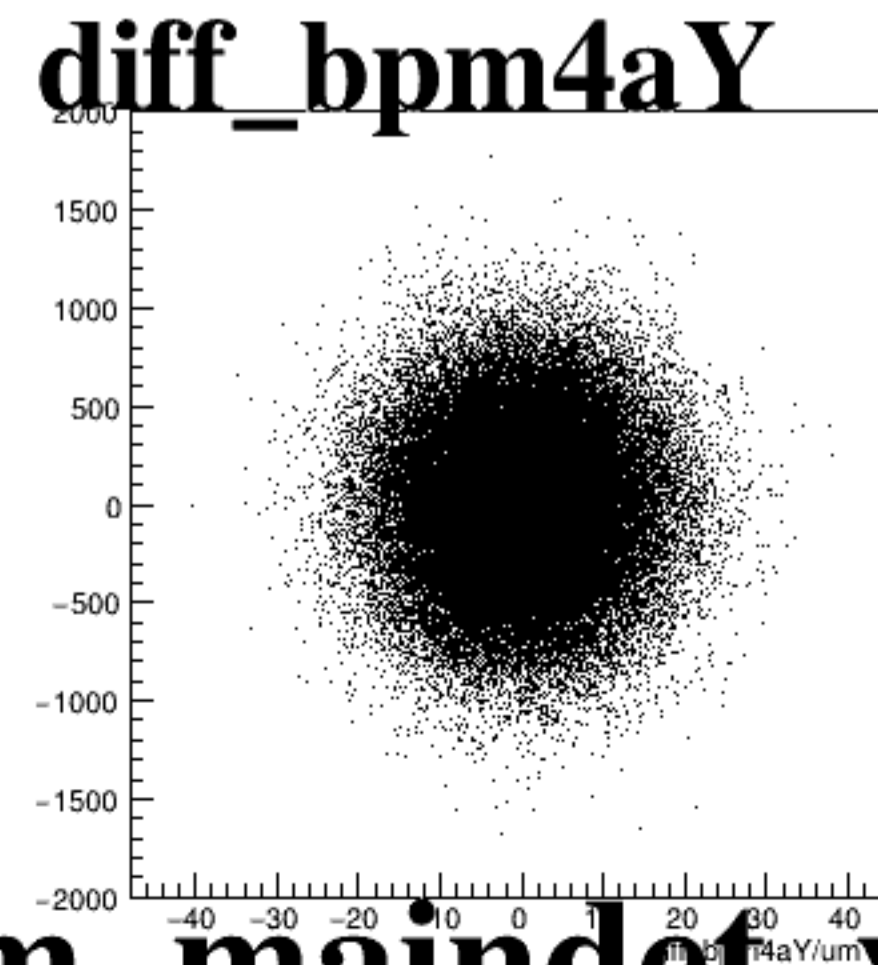
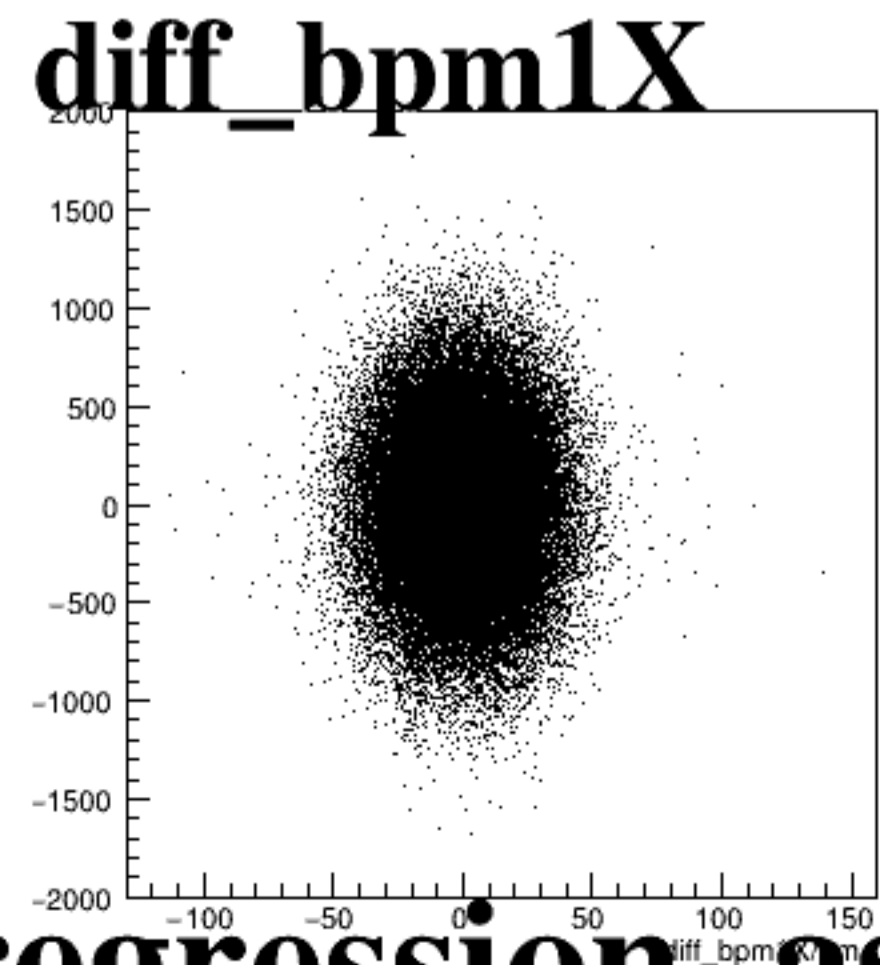
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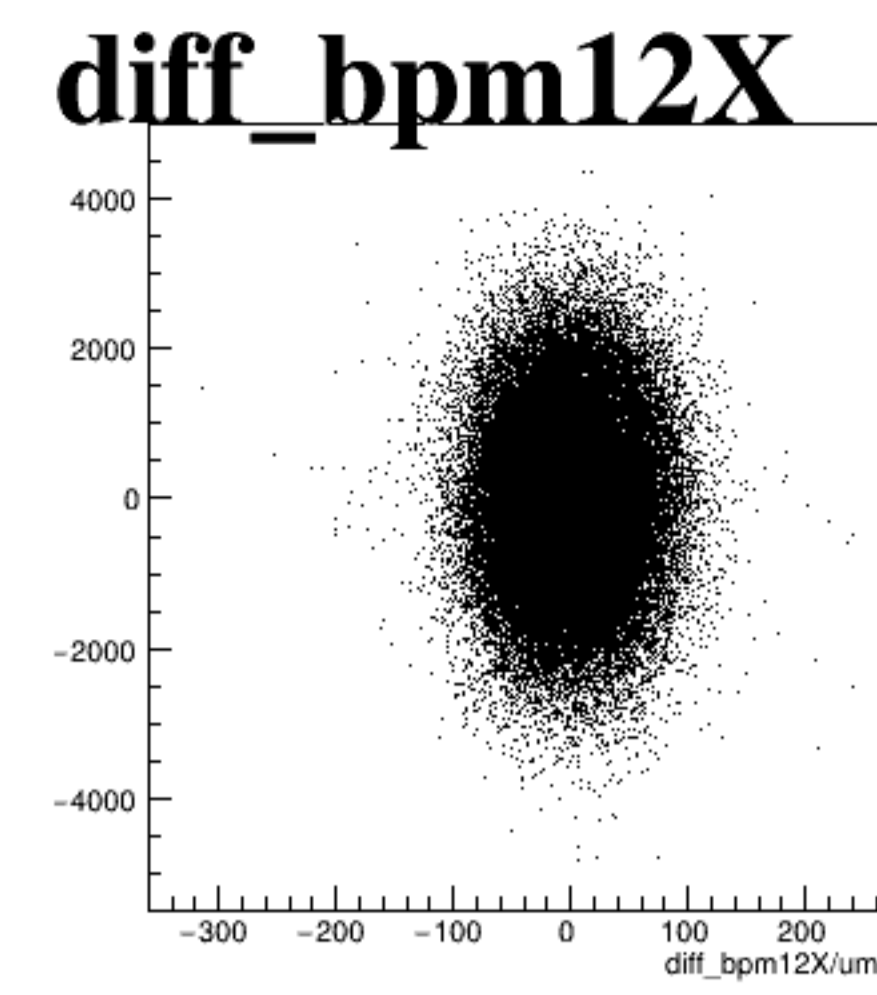
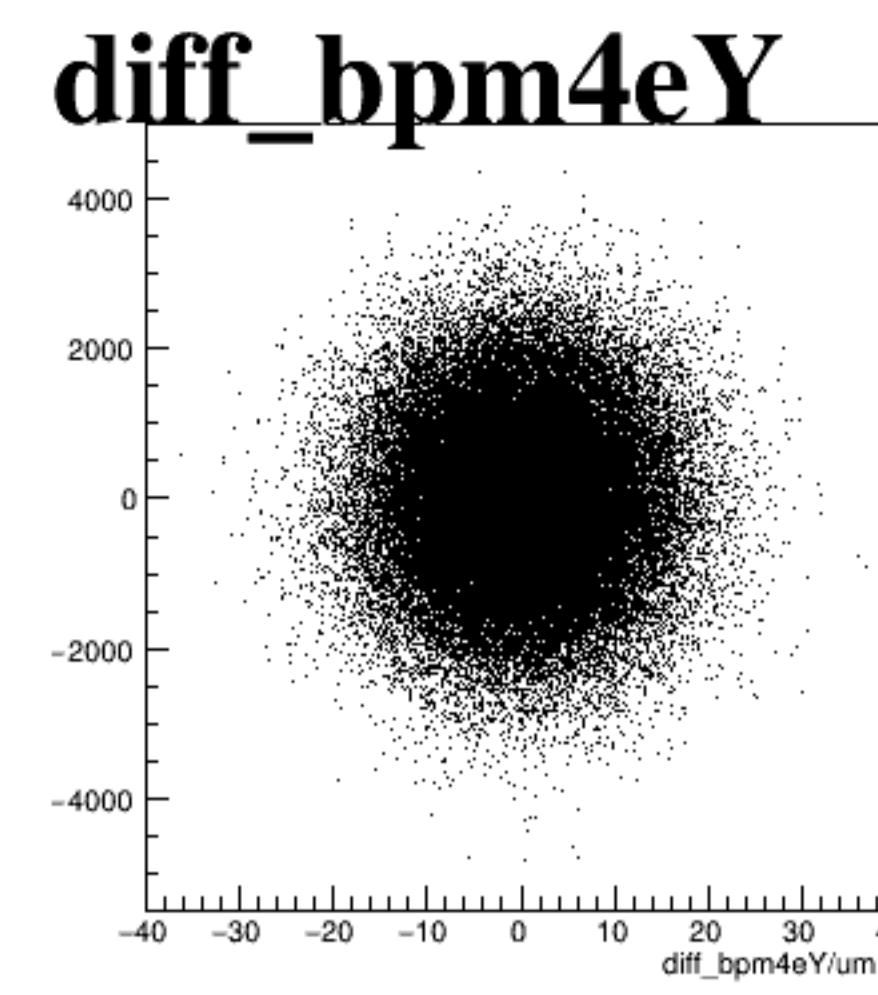
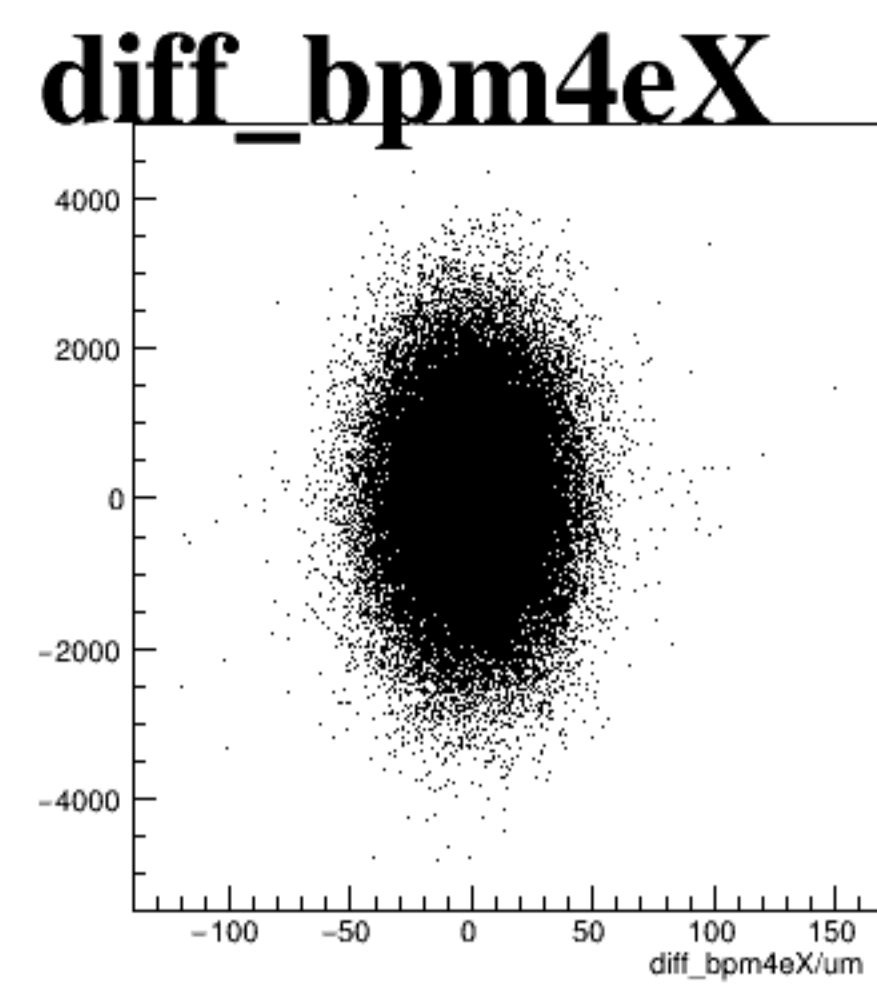
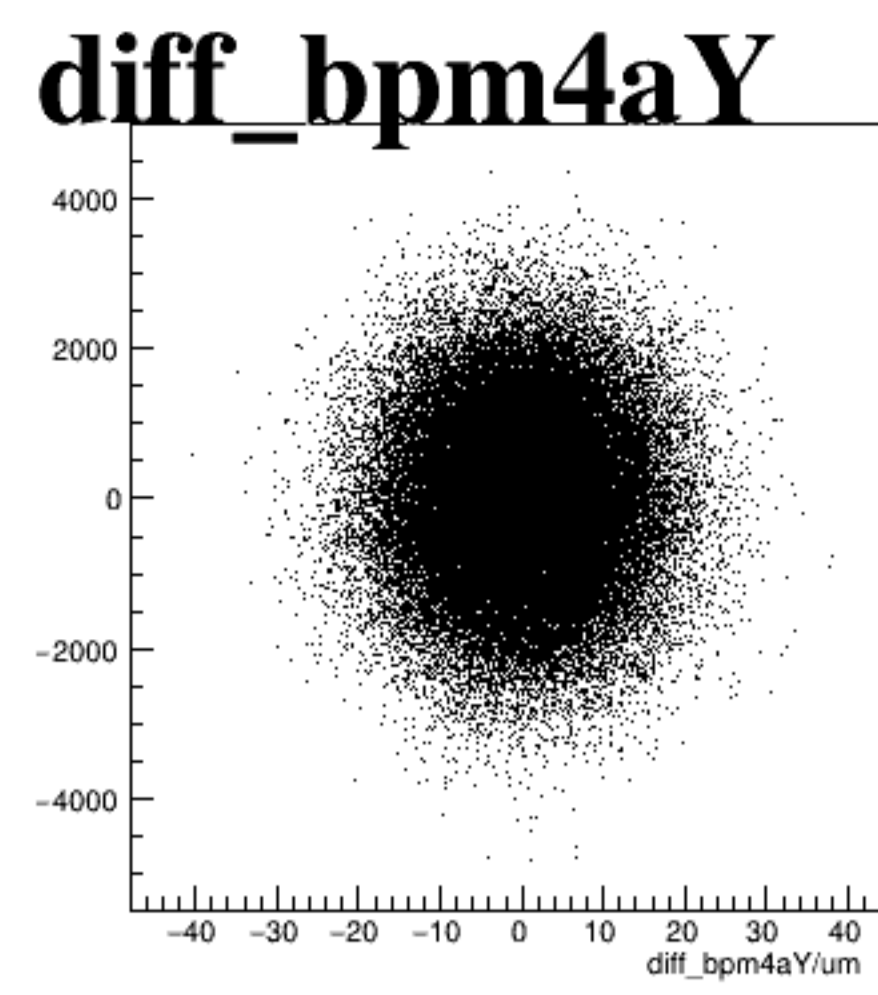
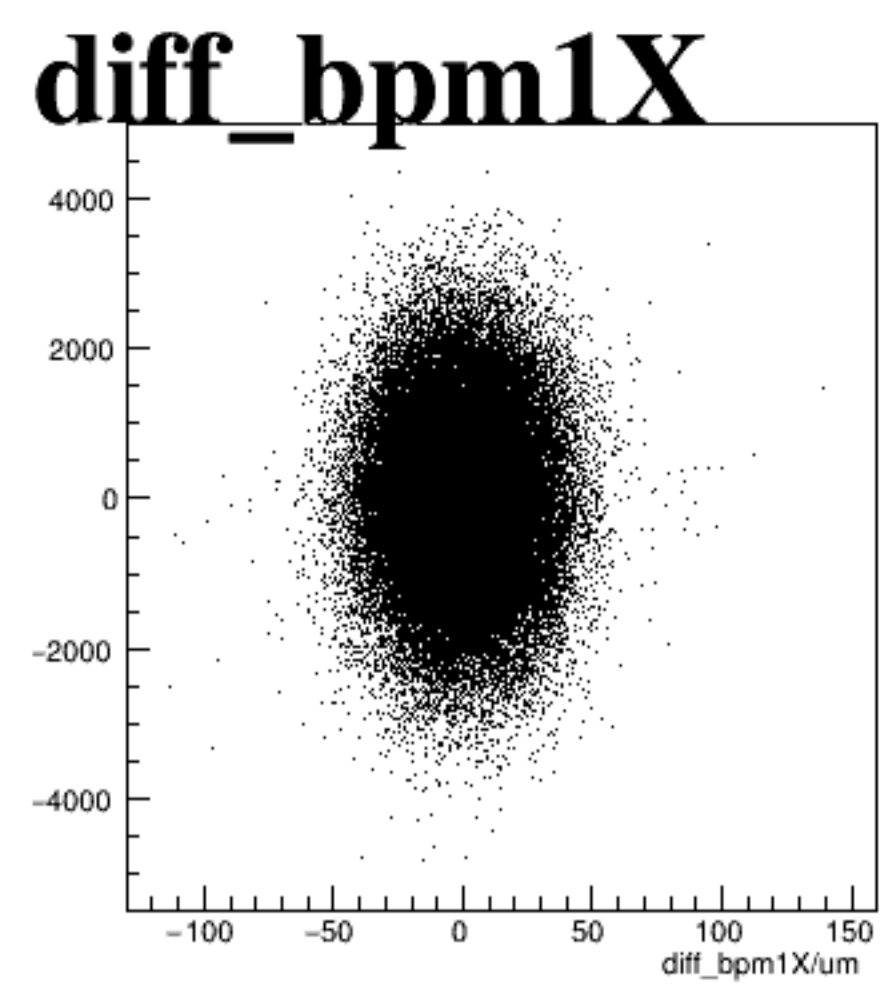
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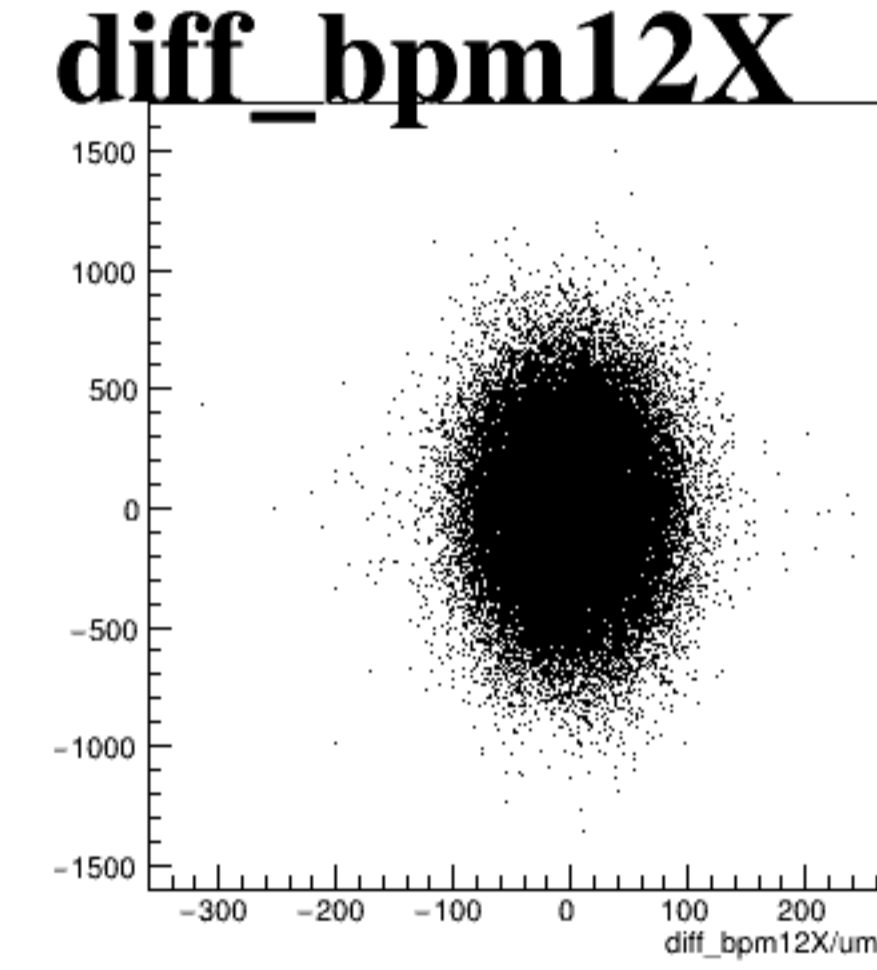
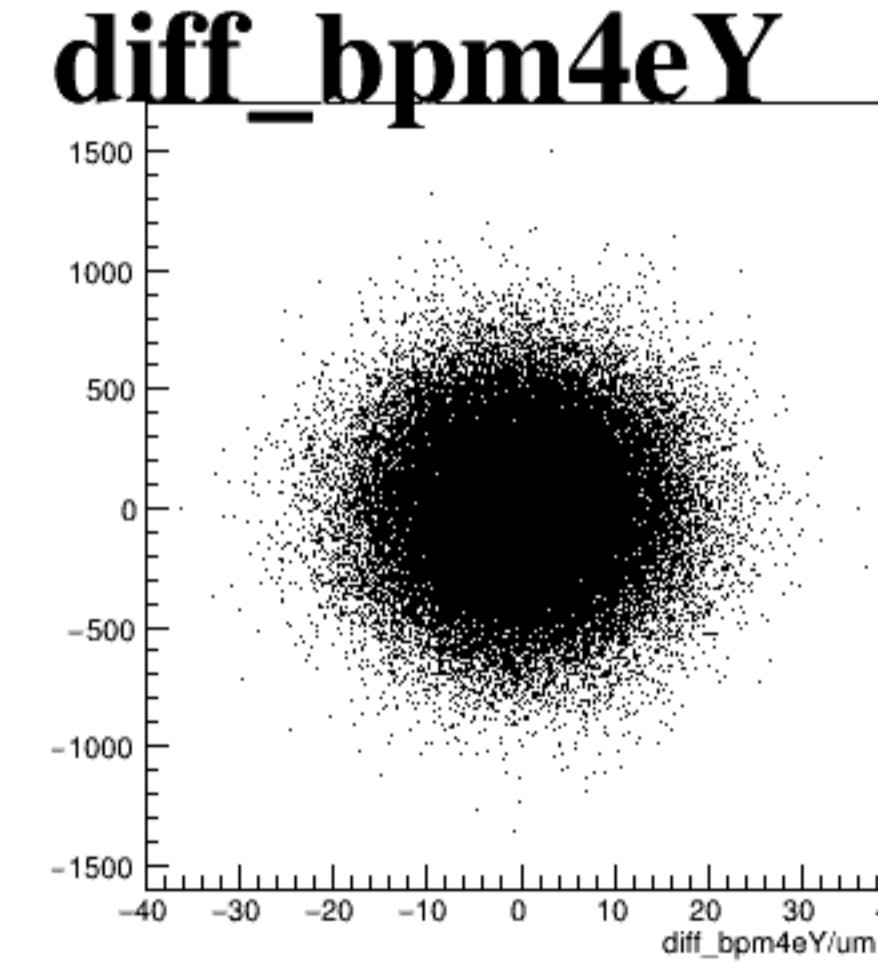
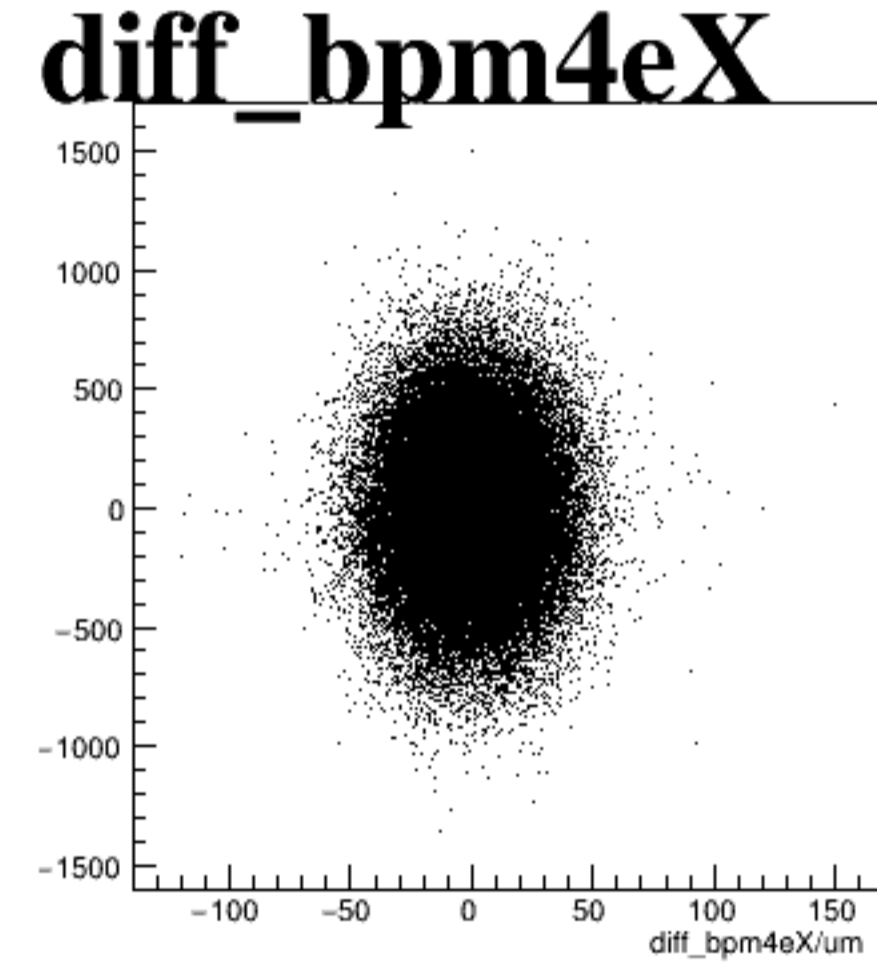
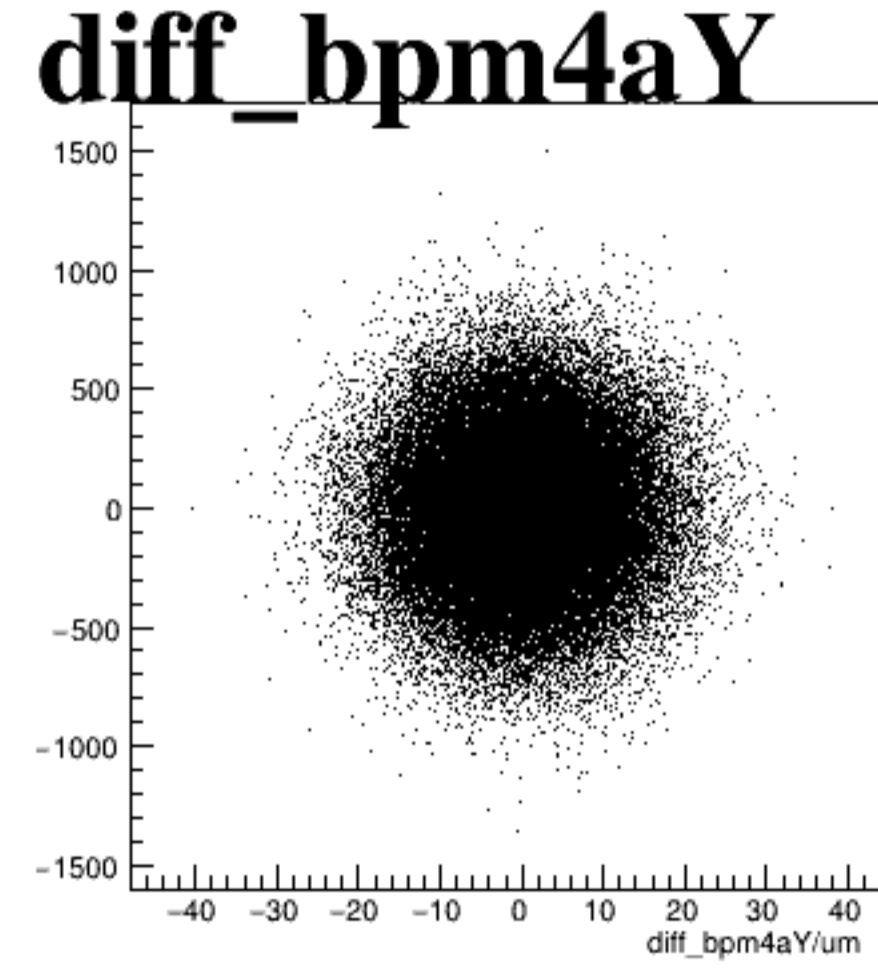
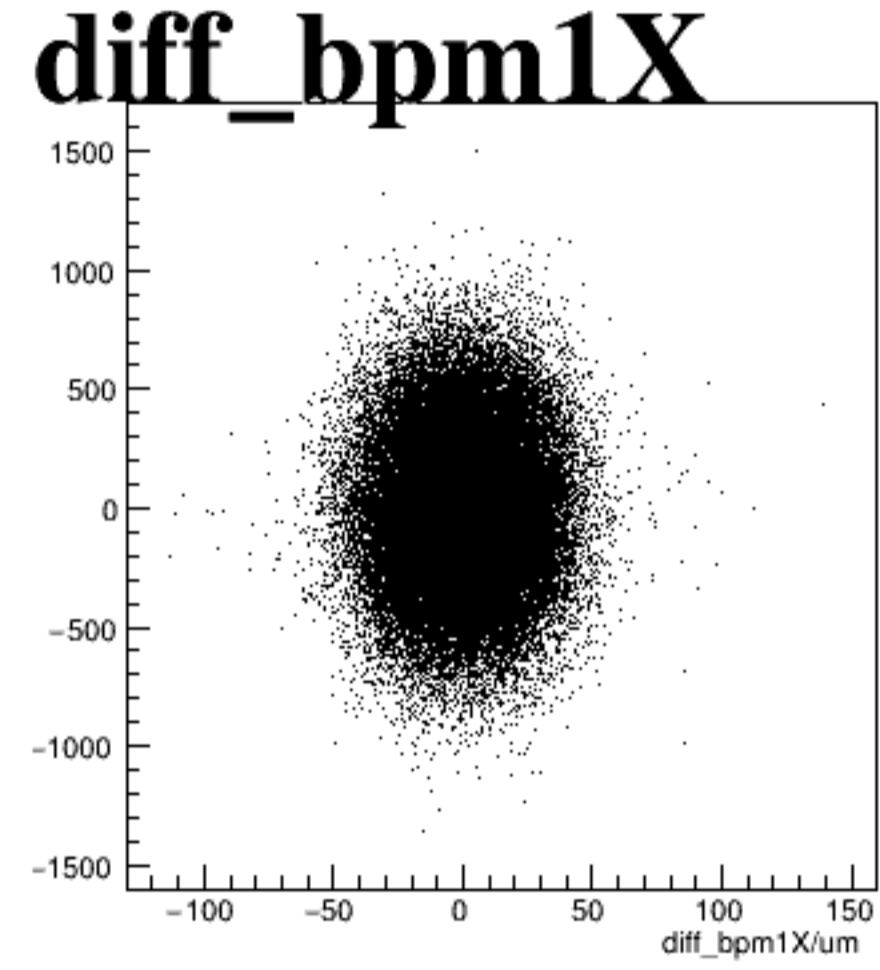
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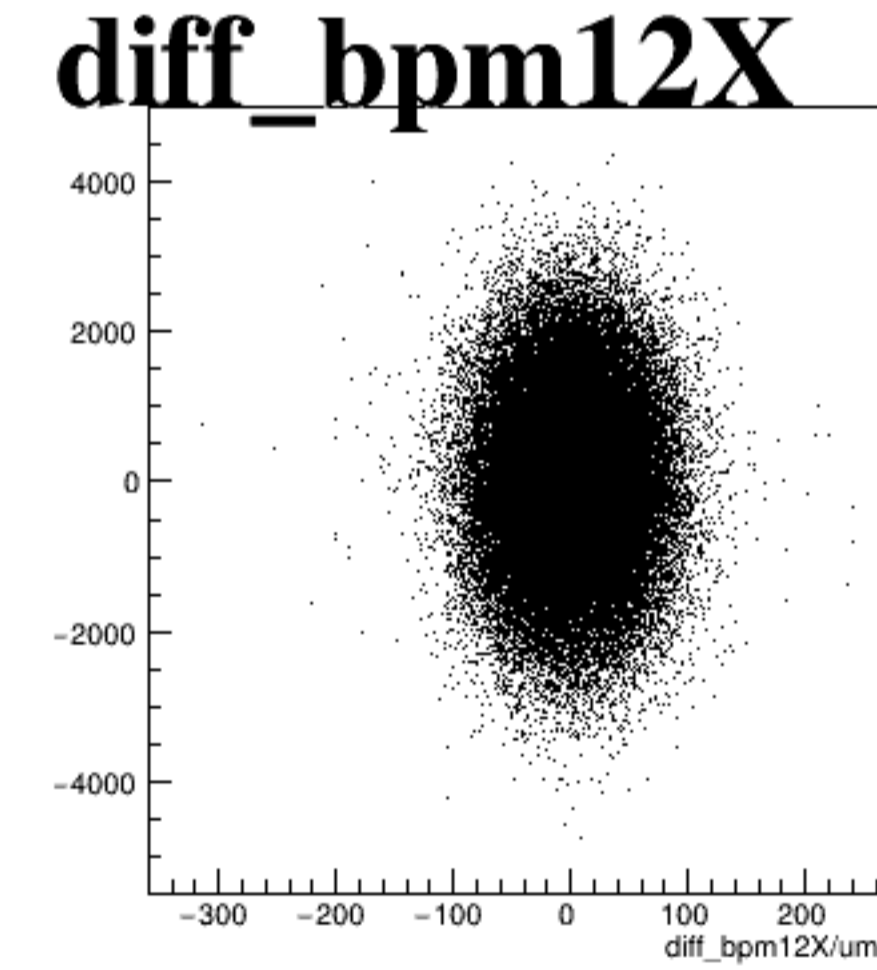
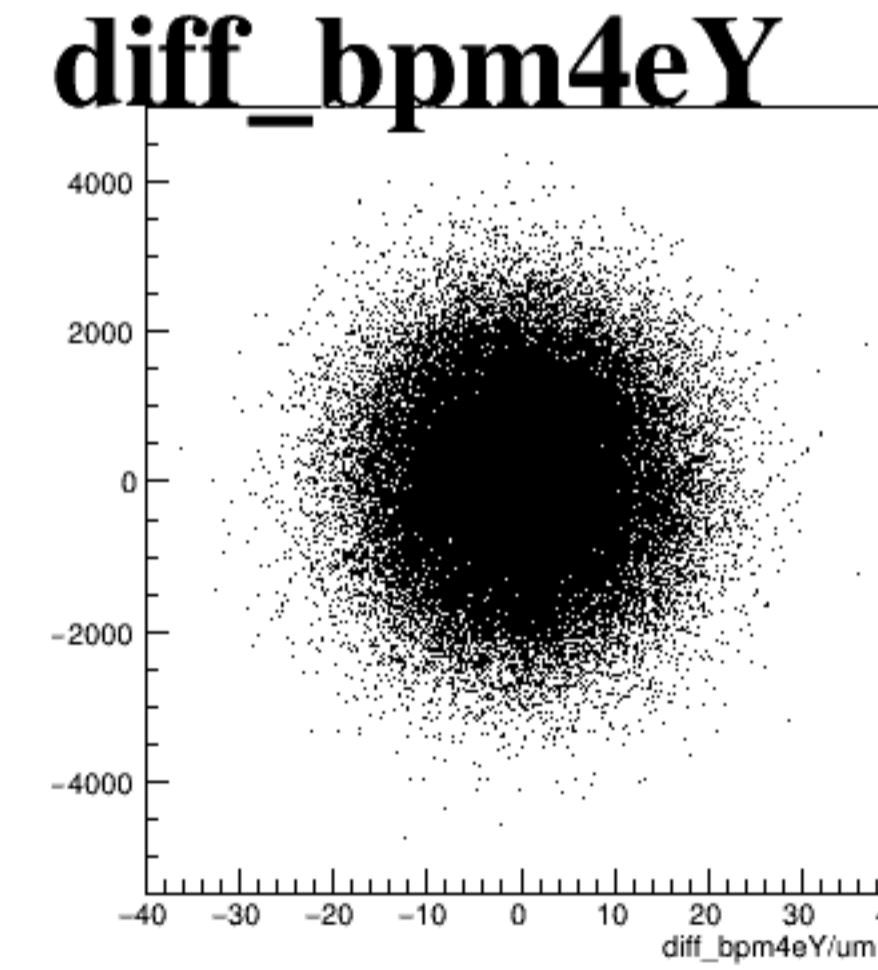
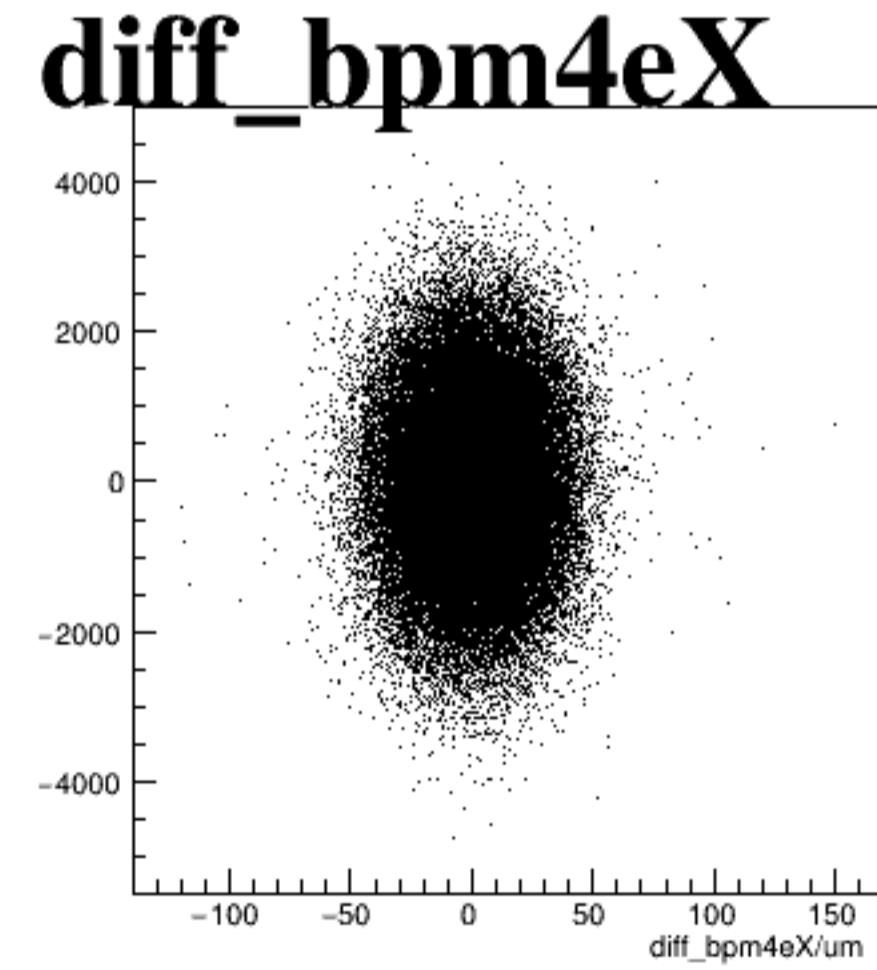
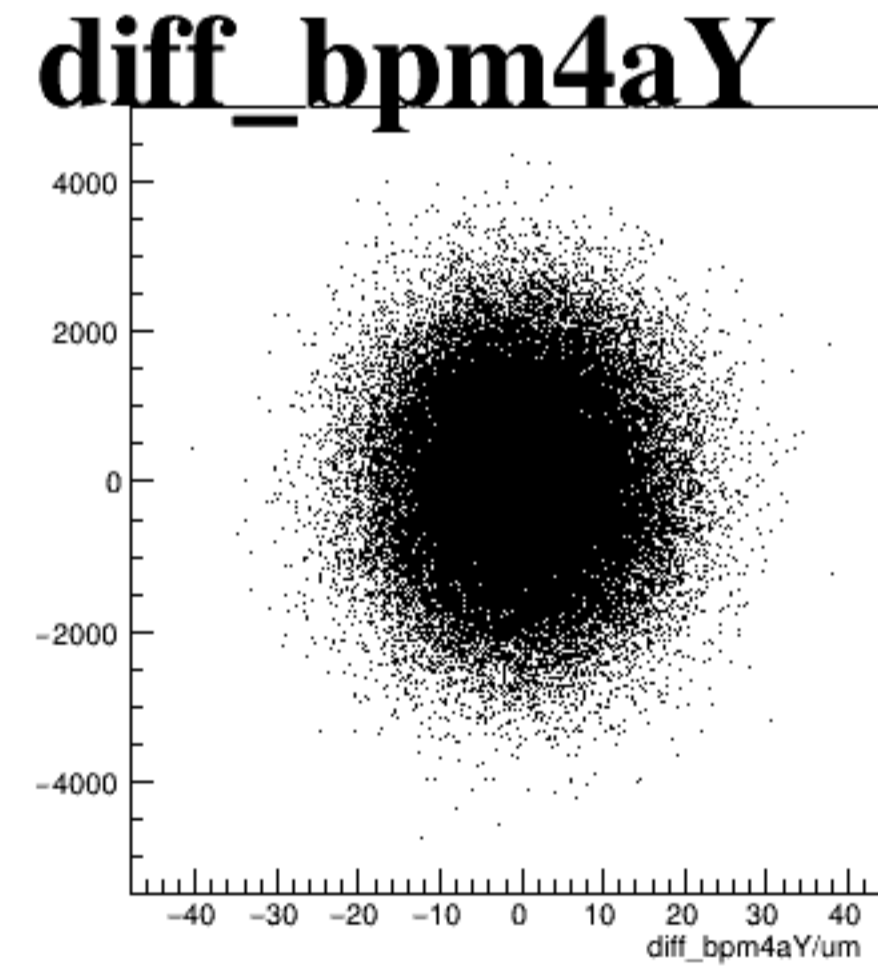
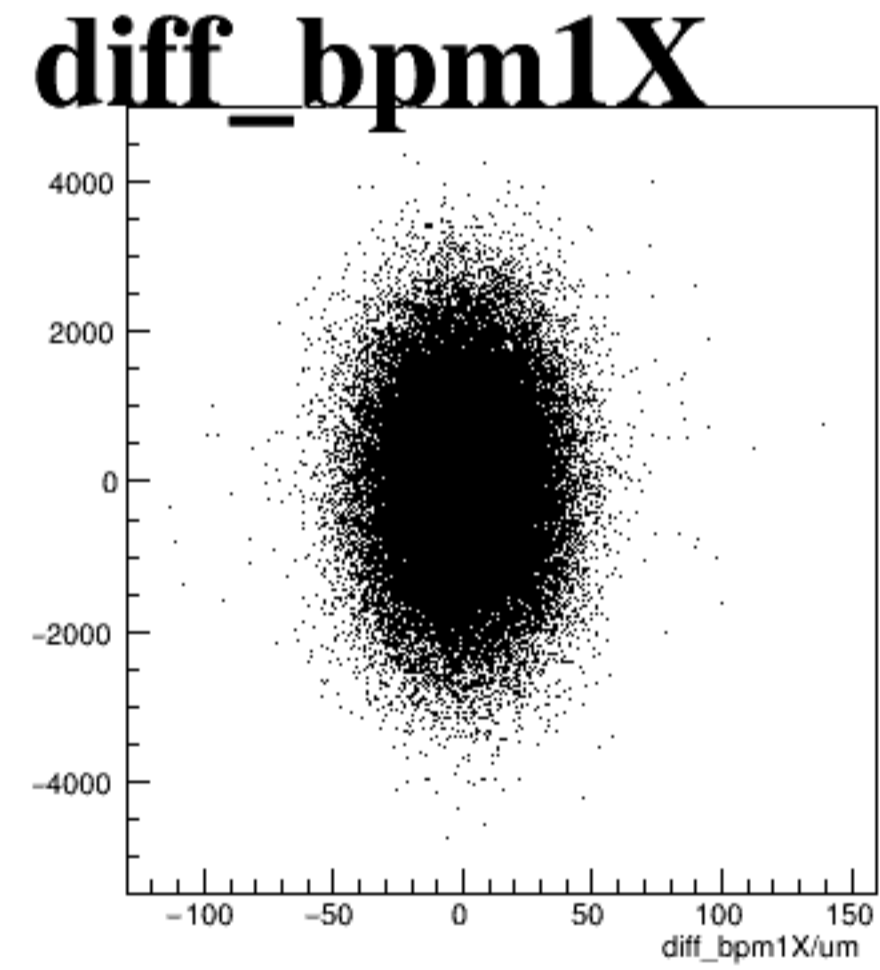
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reg_asym_dsl



reg_asym_usr



reg_asym_dsr

