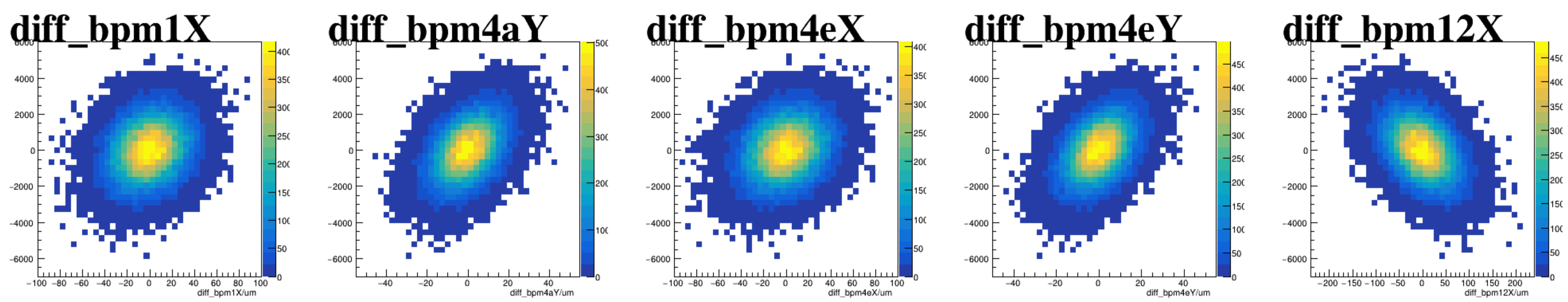
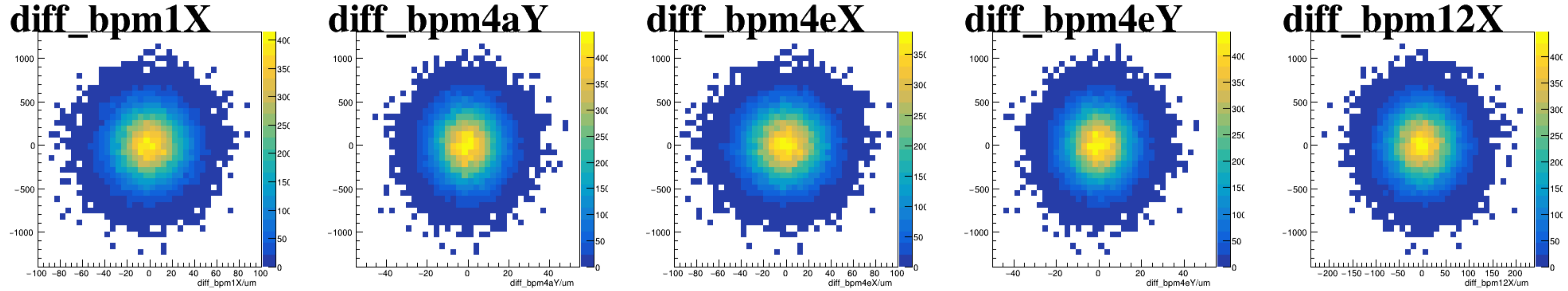


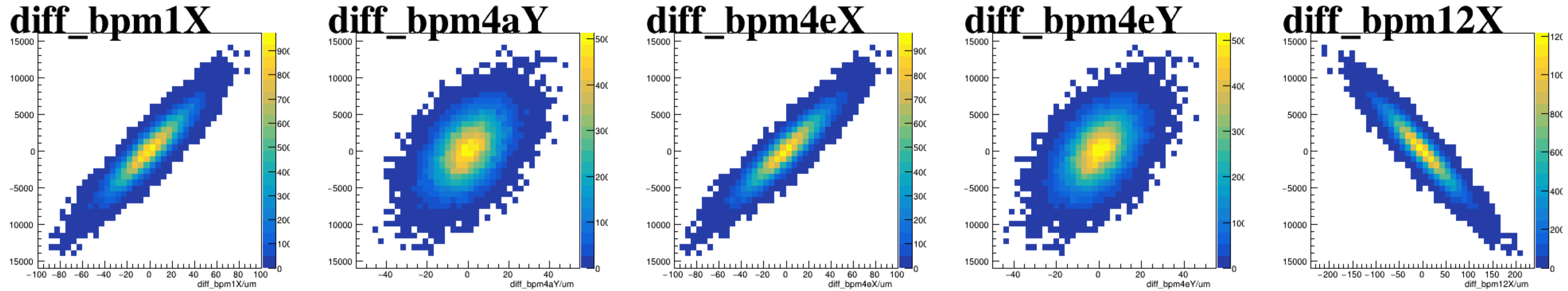
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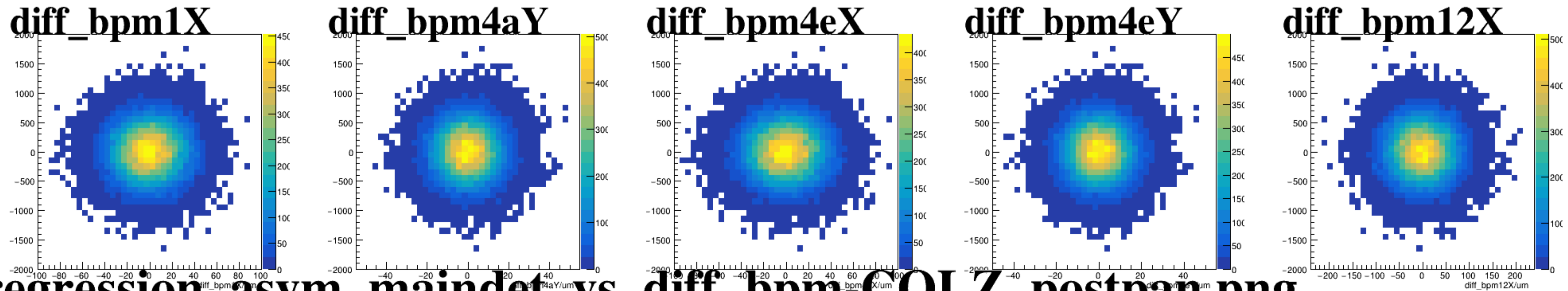
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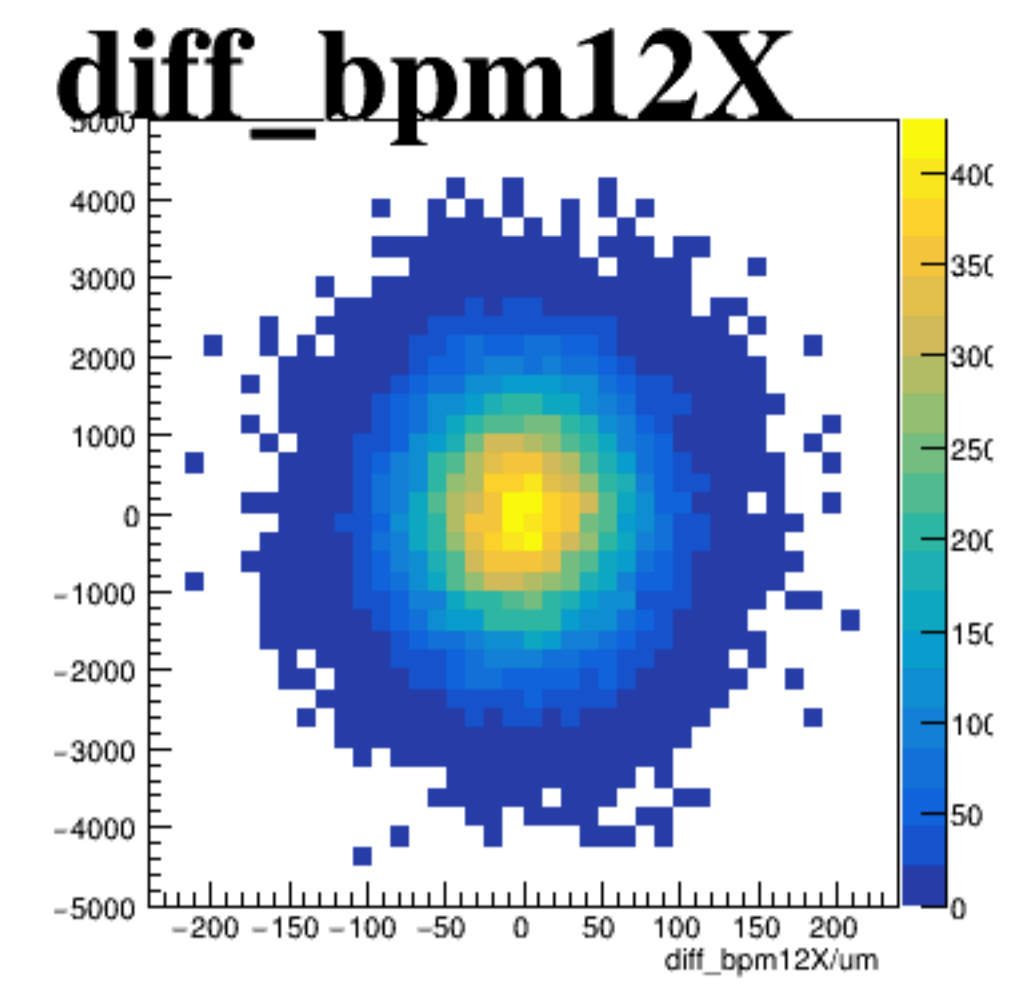
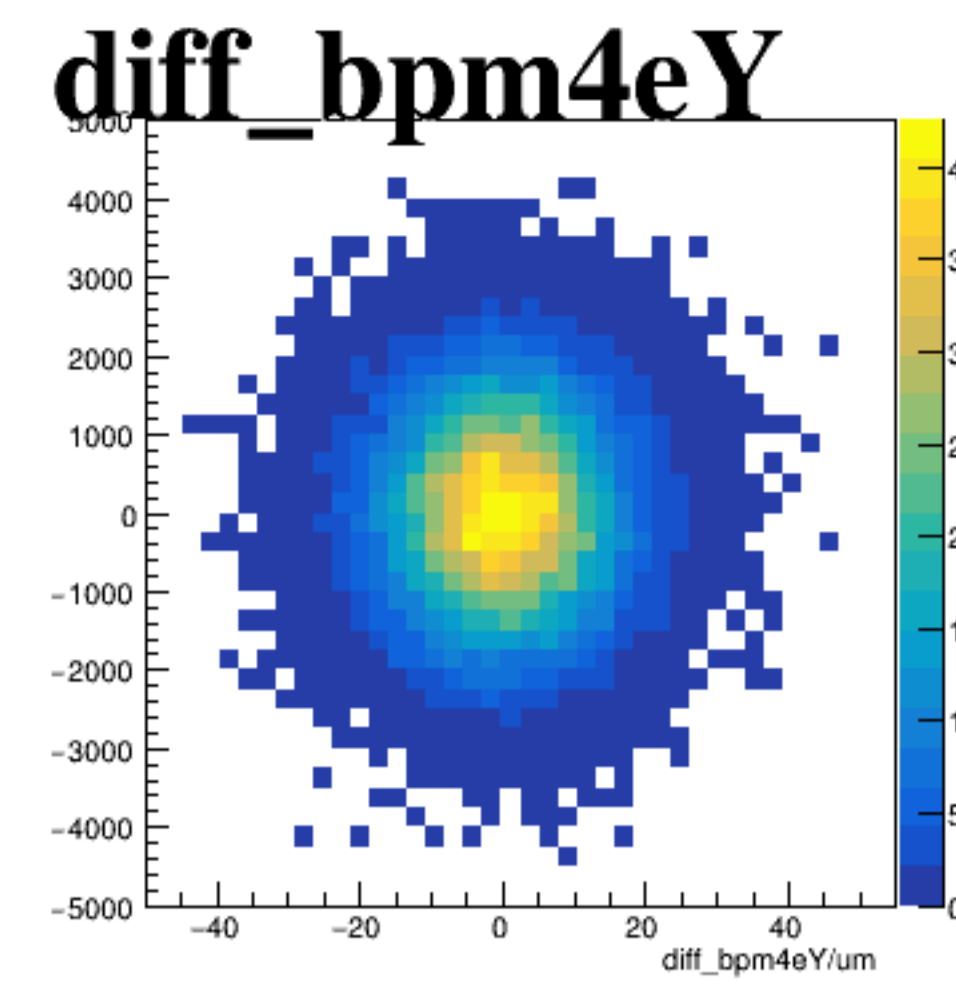
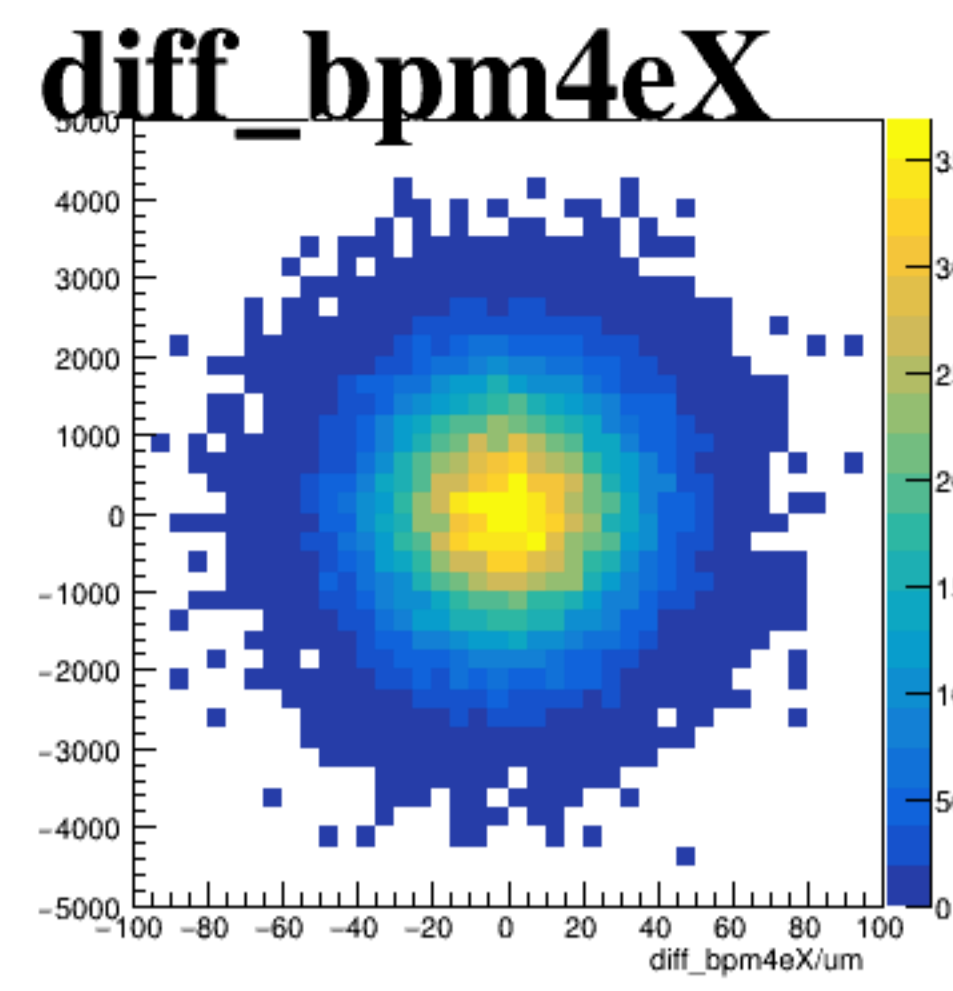
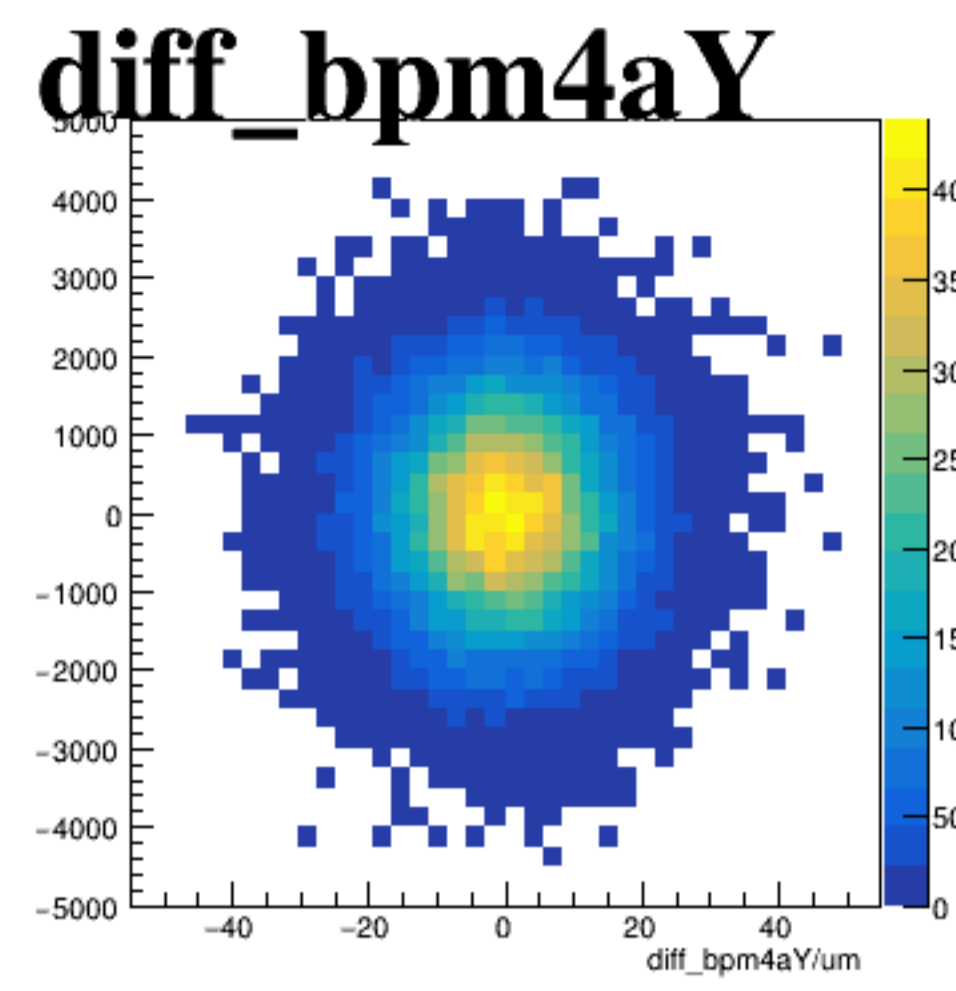
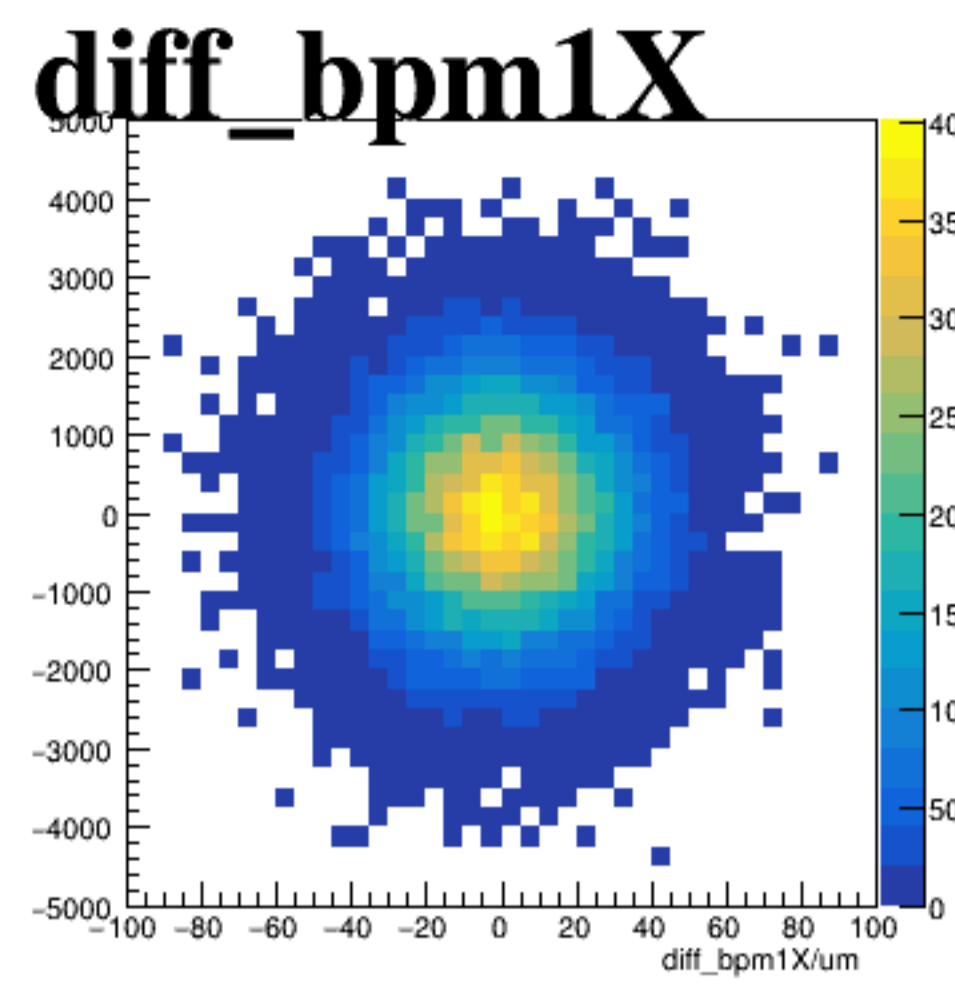
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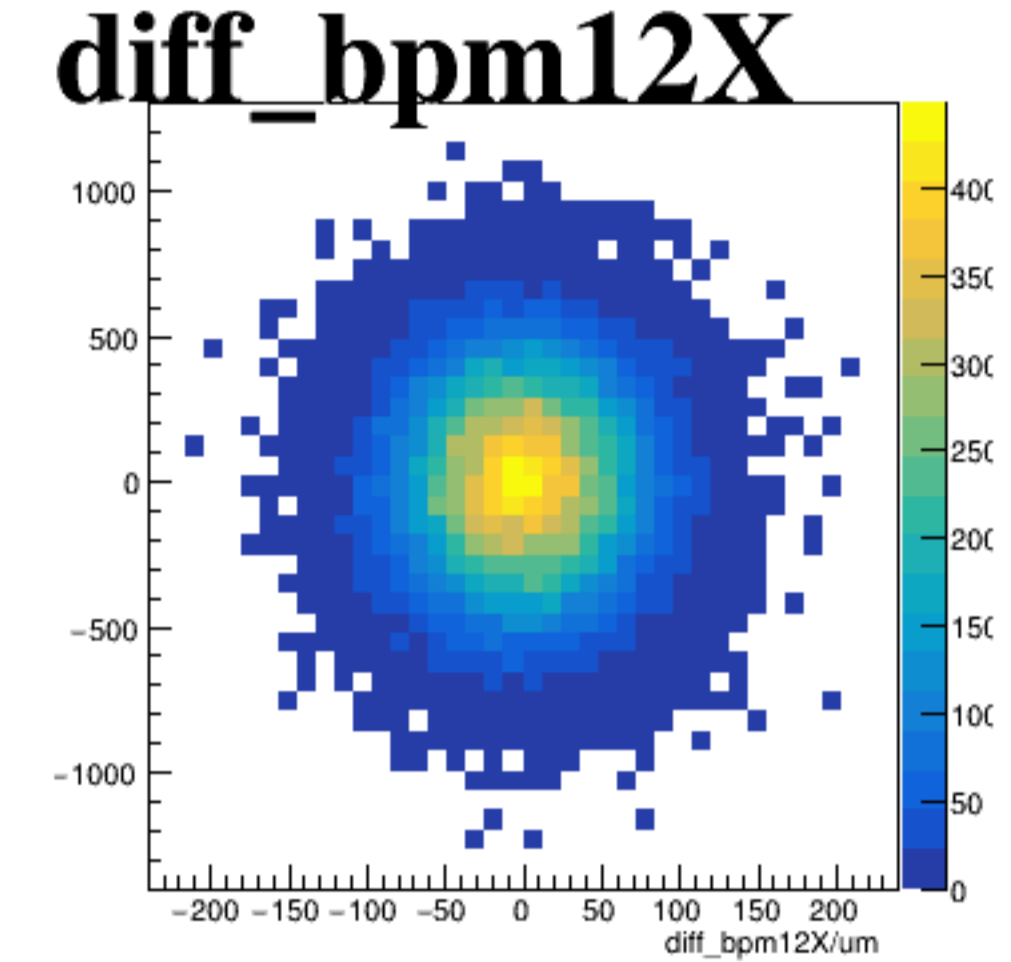
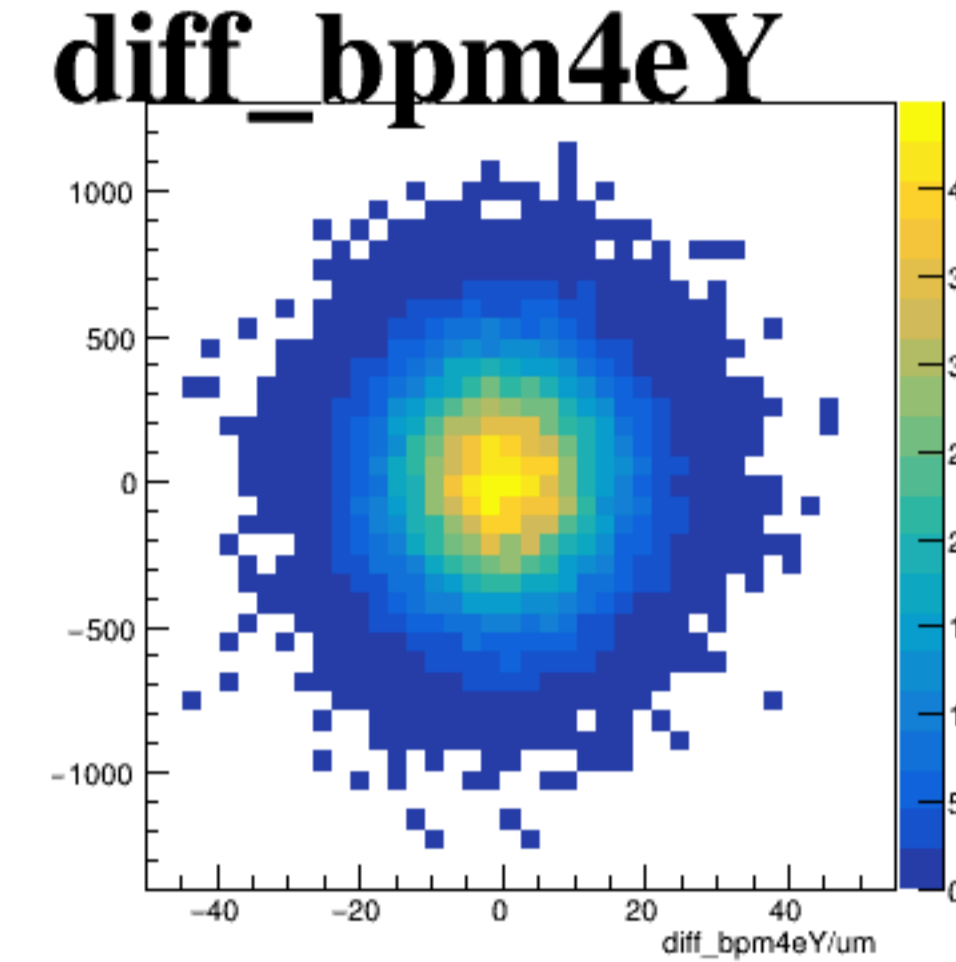
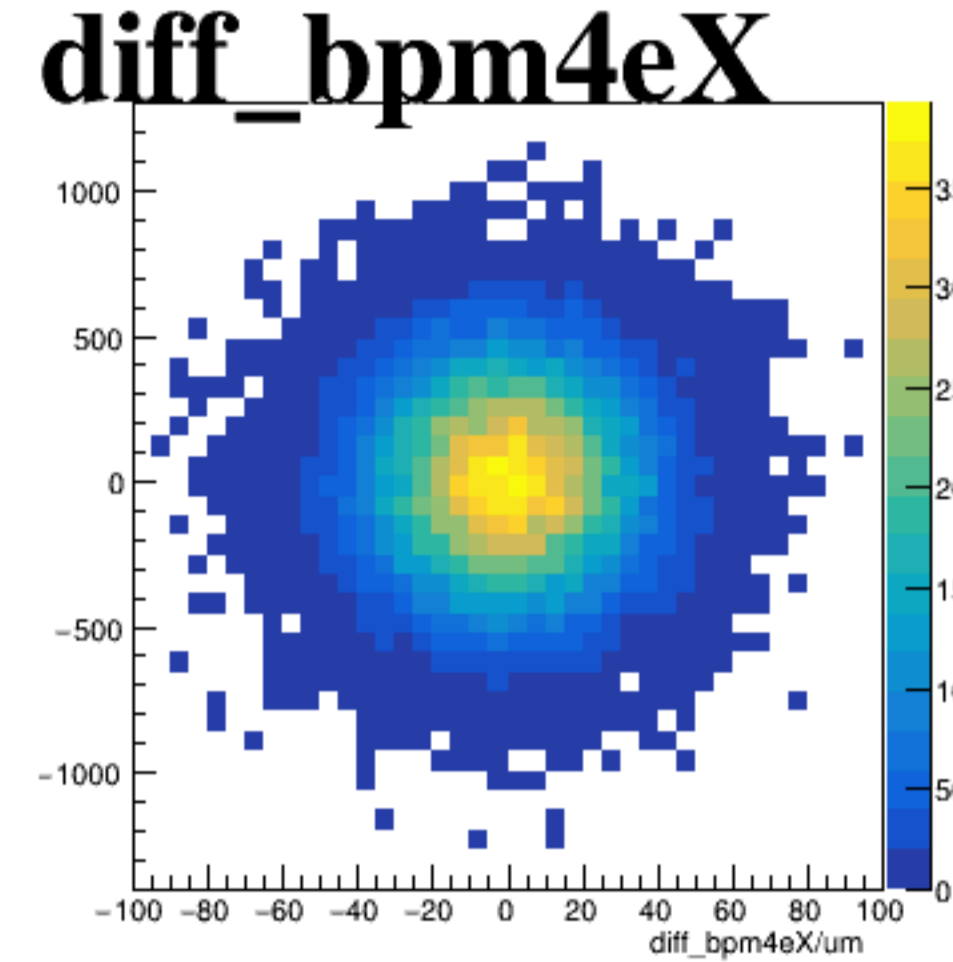
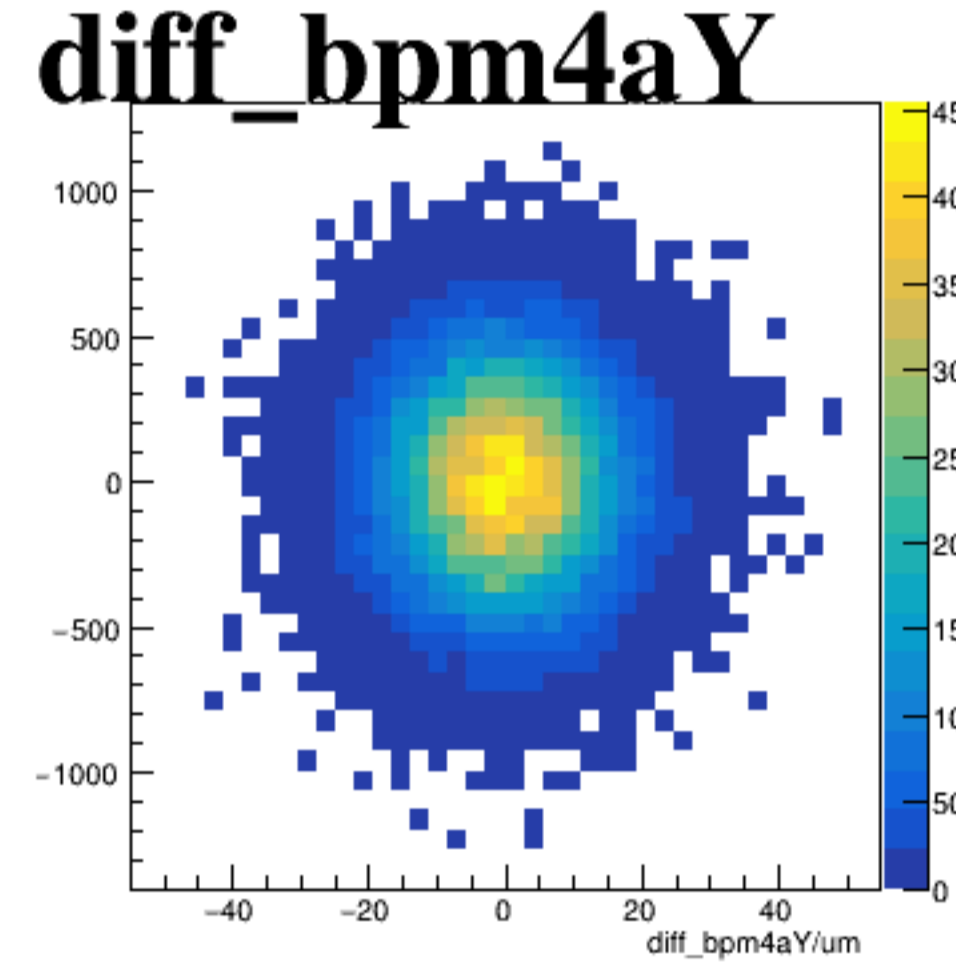
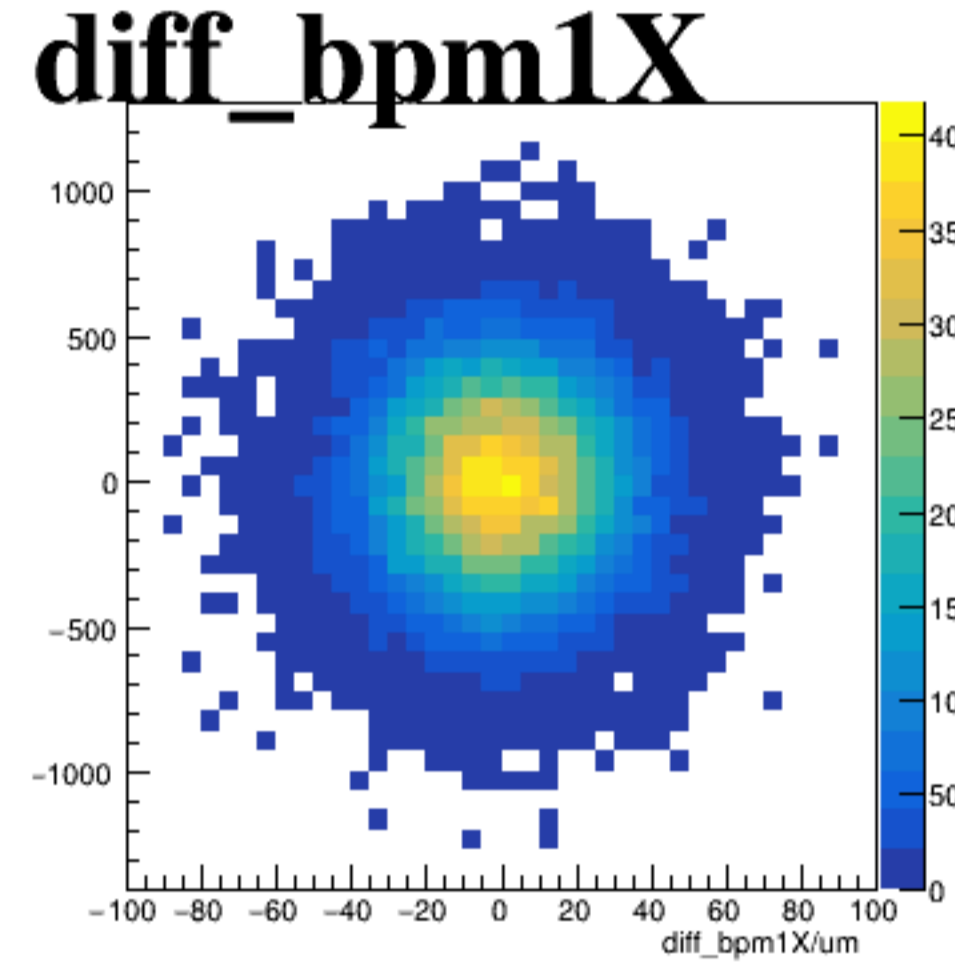
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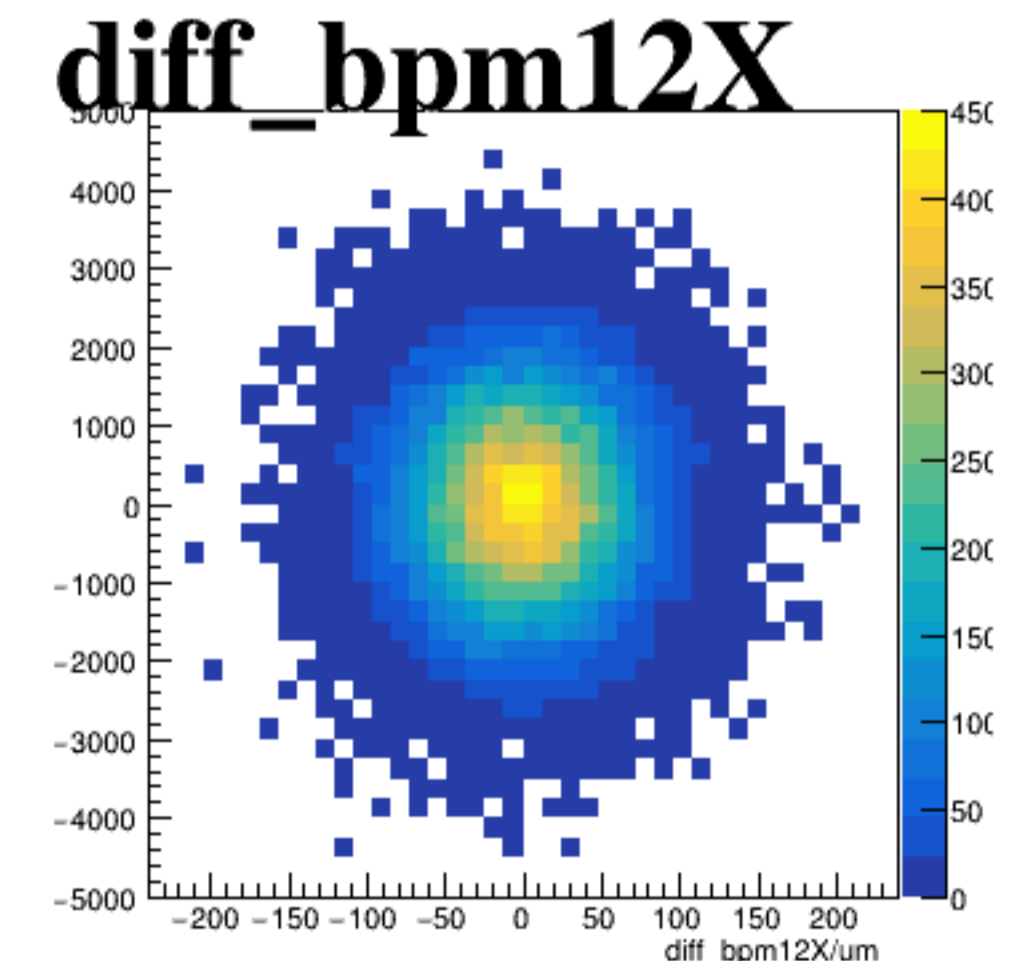
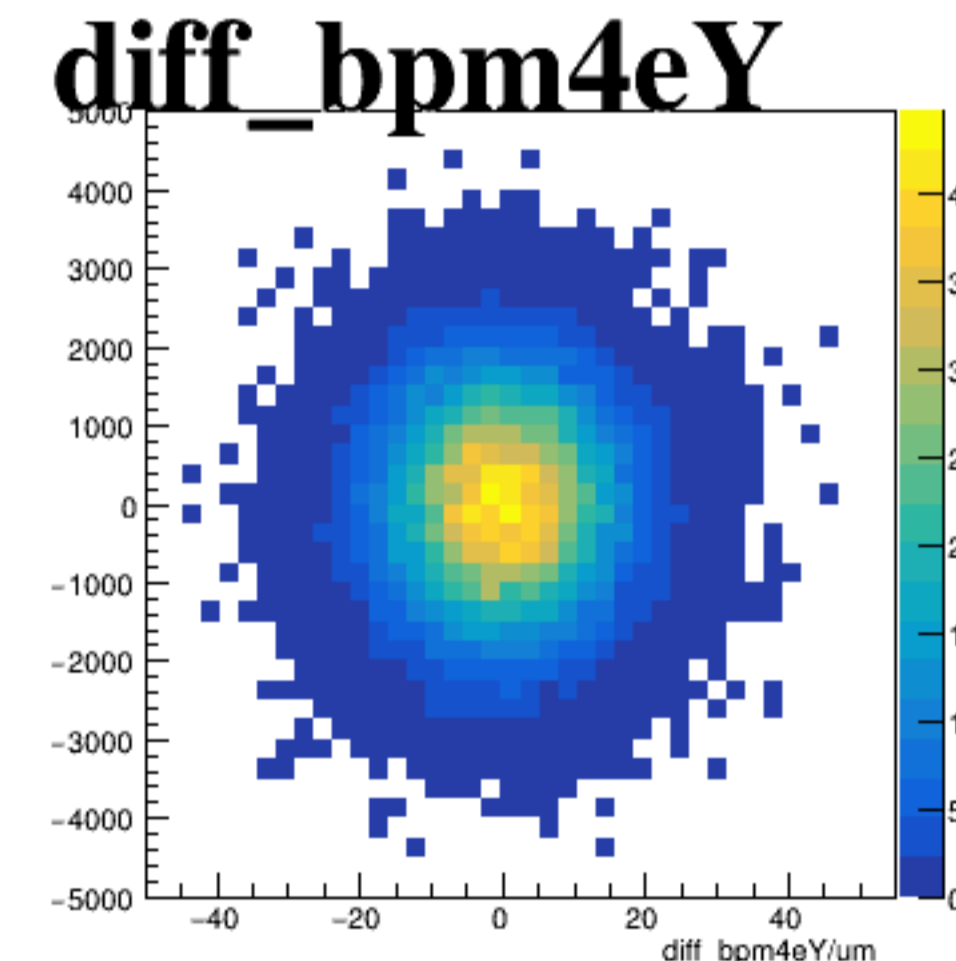
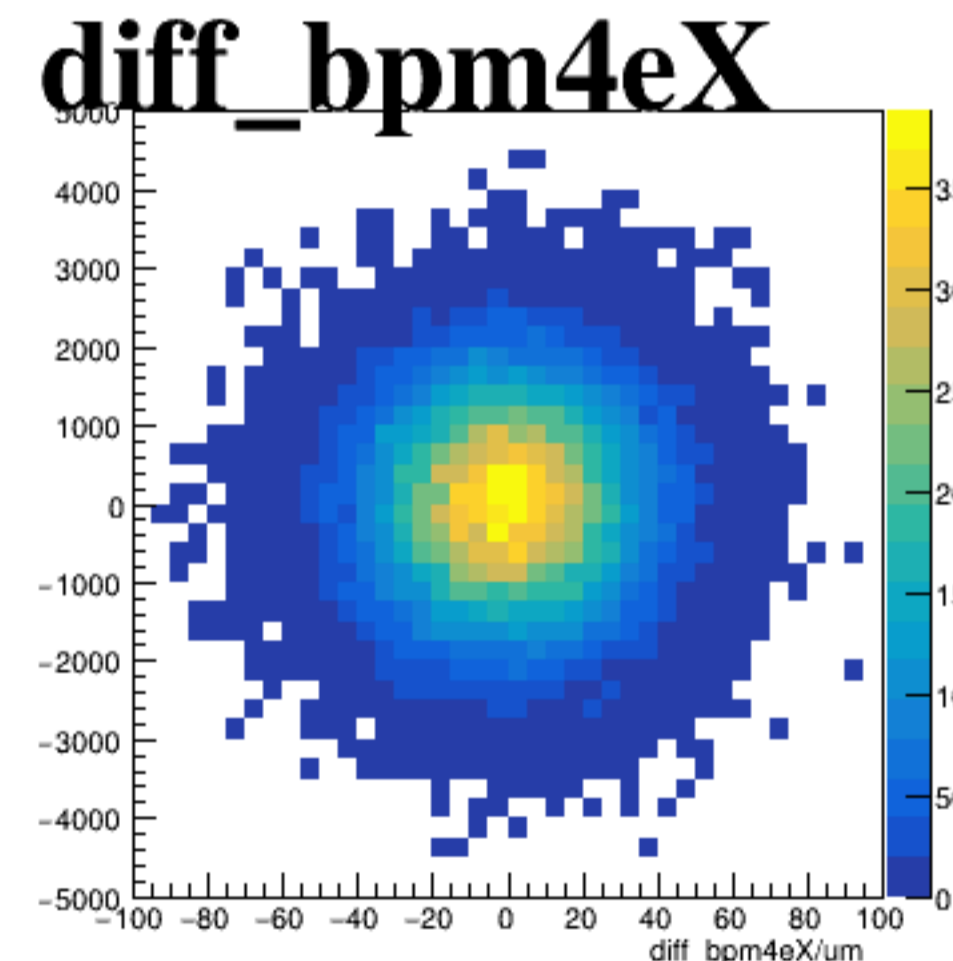
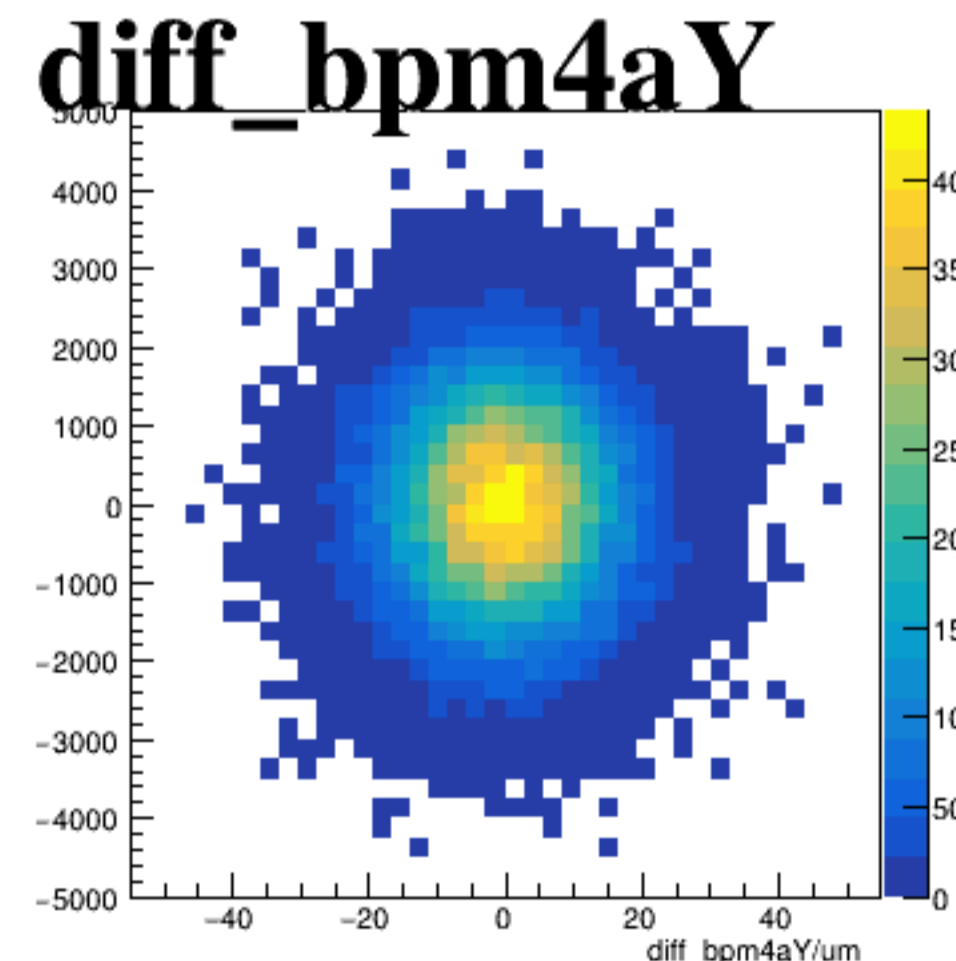
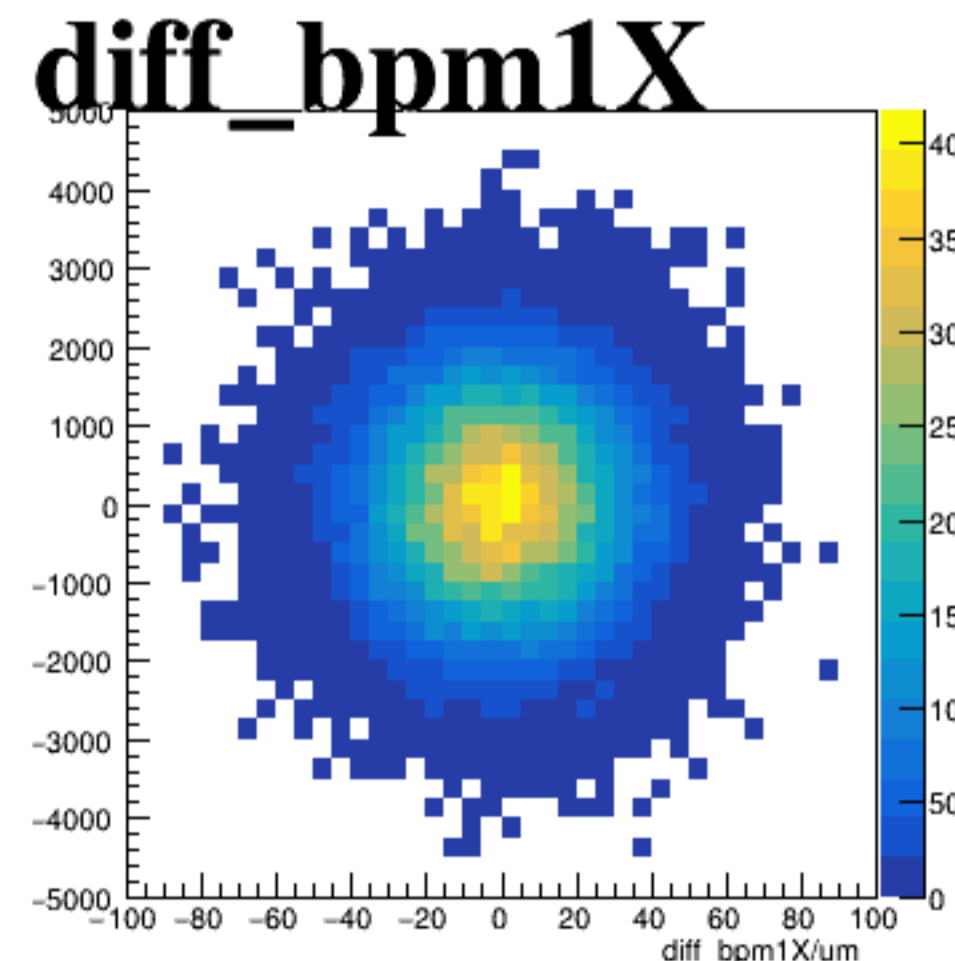
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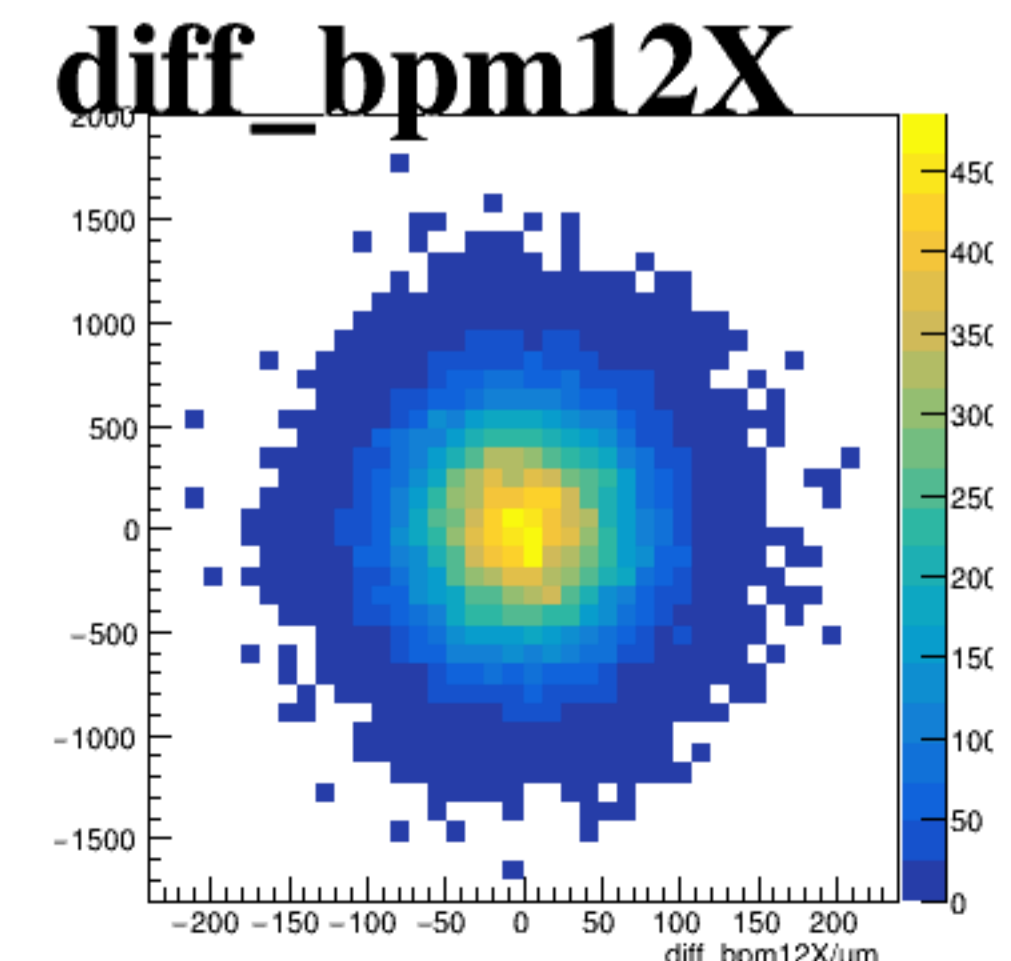
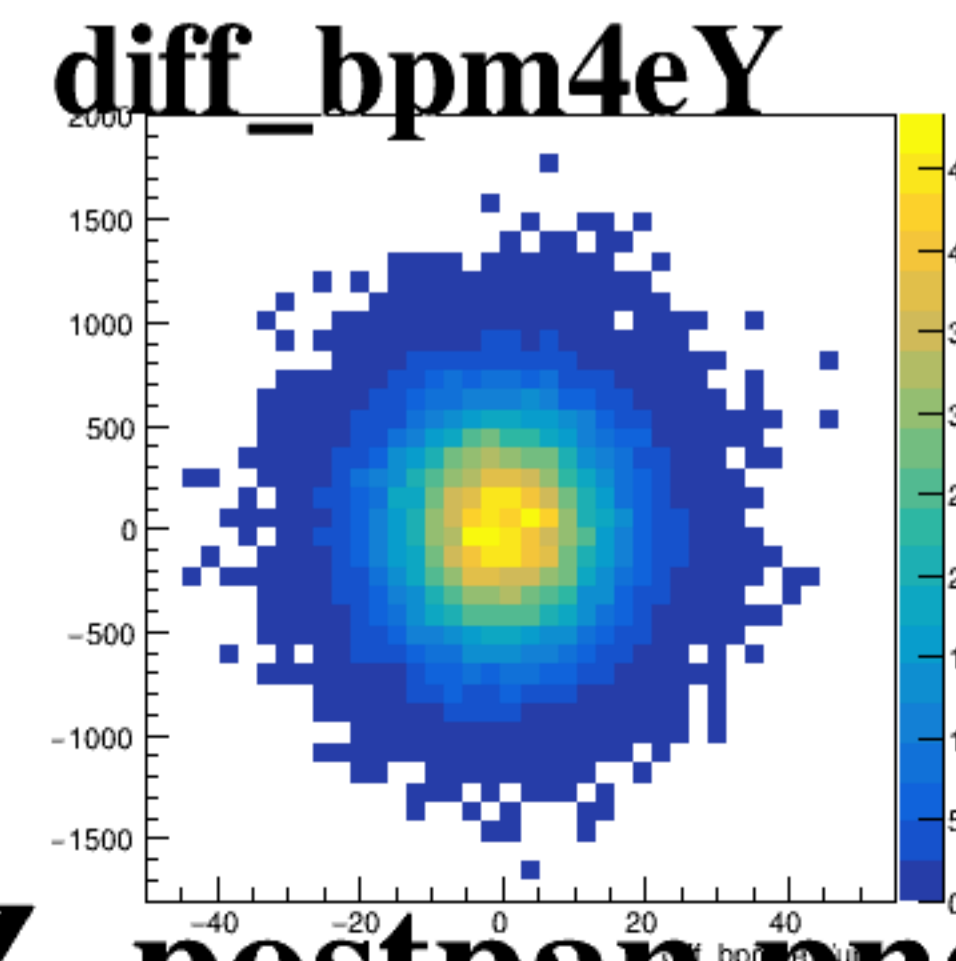
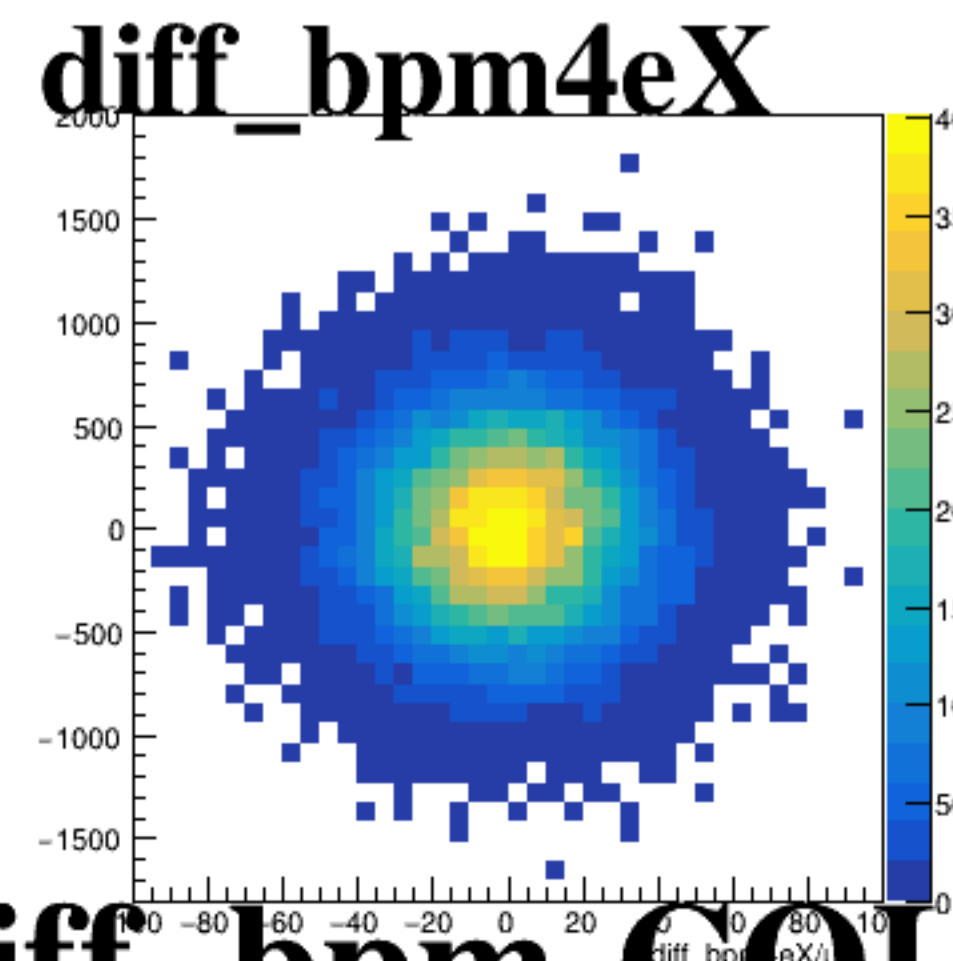
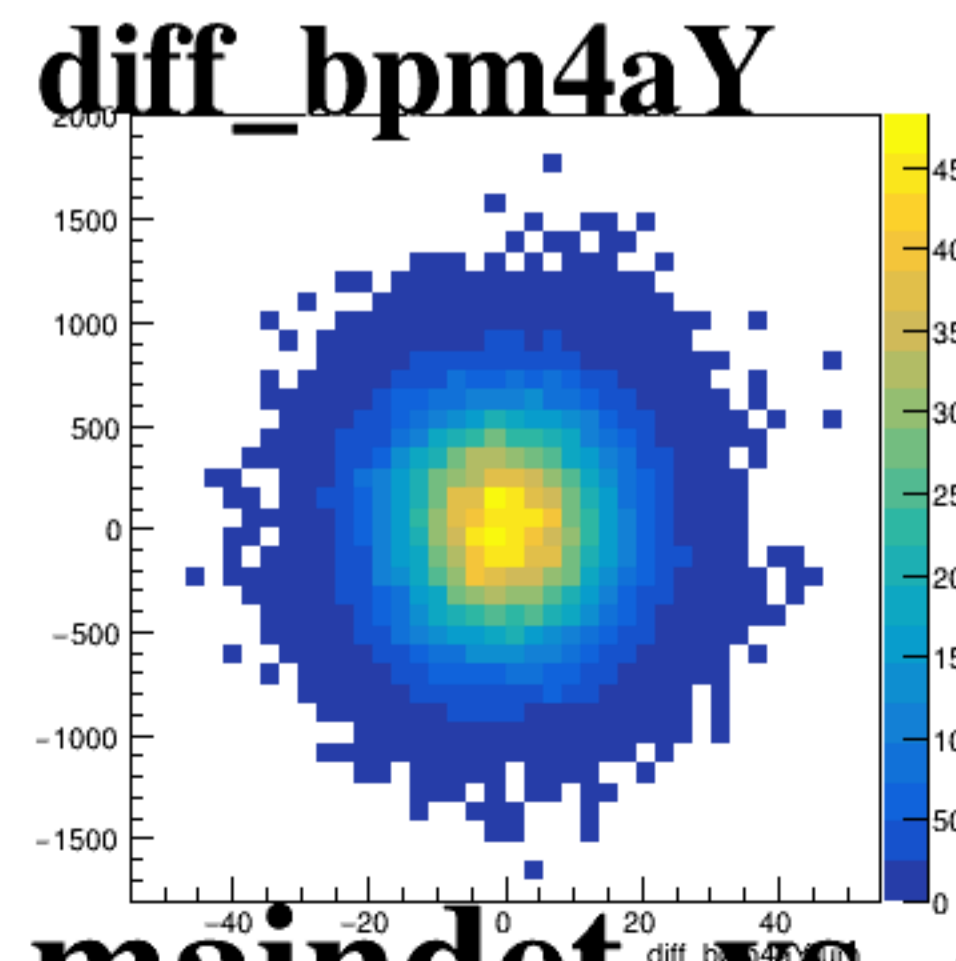
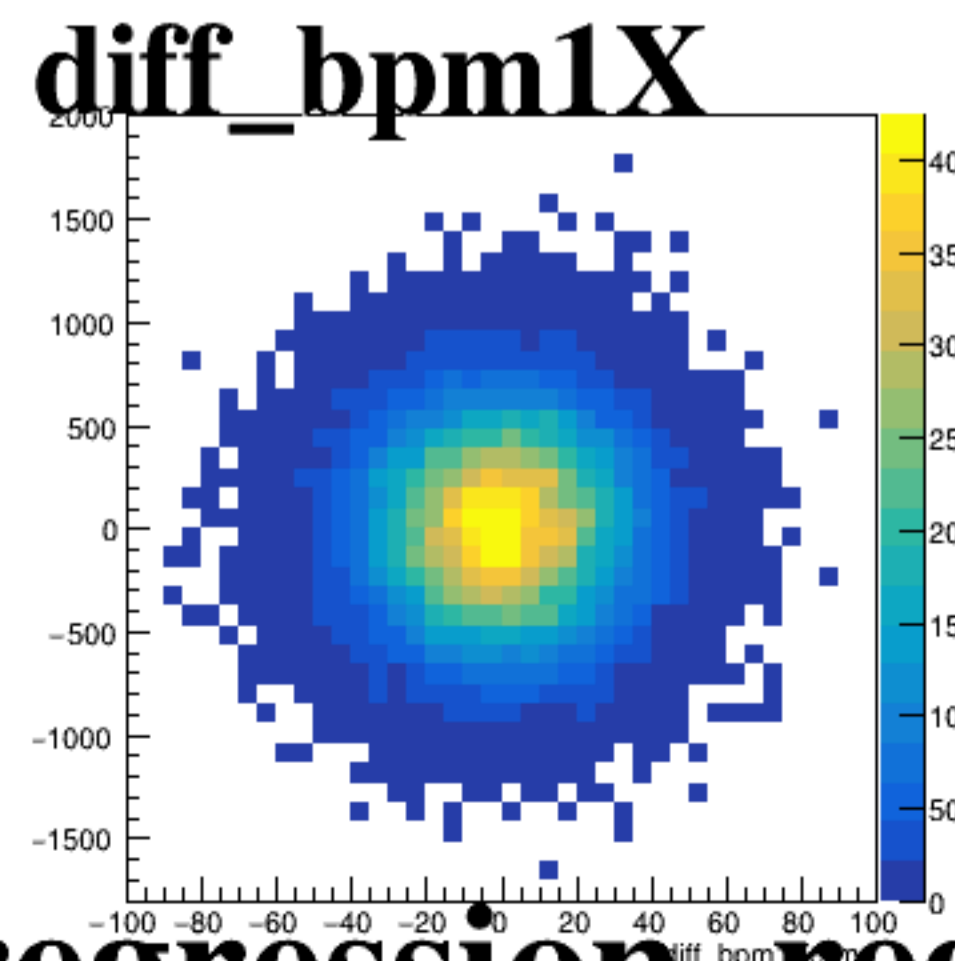
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reg\_asym\_usr

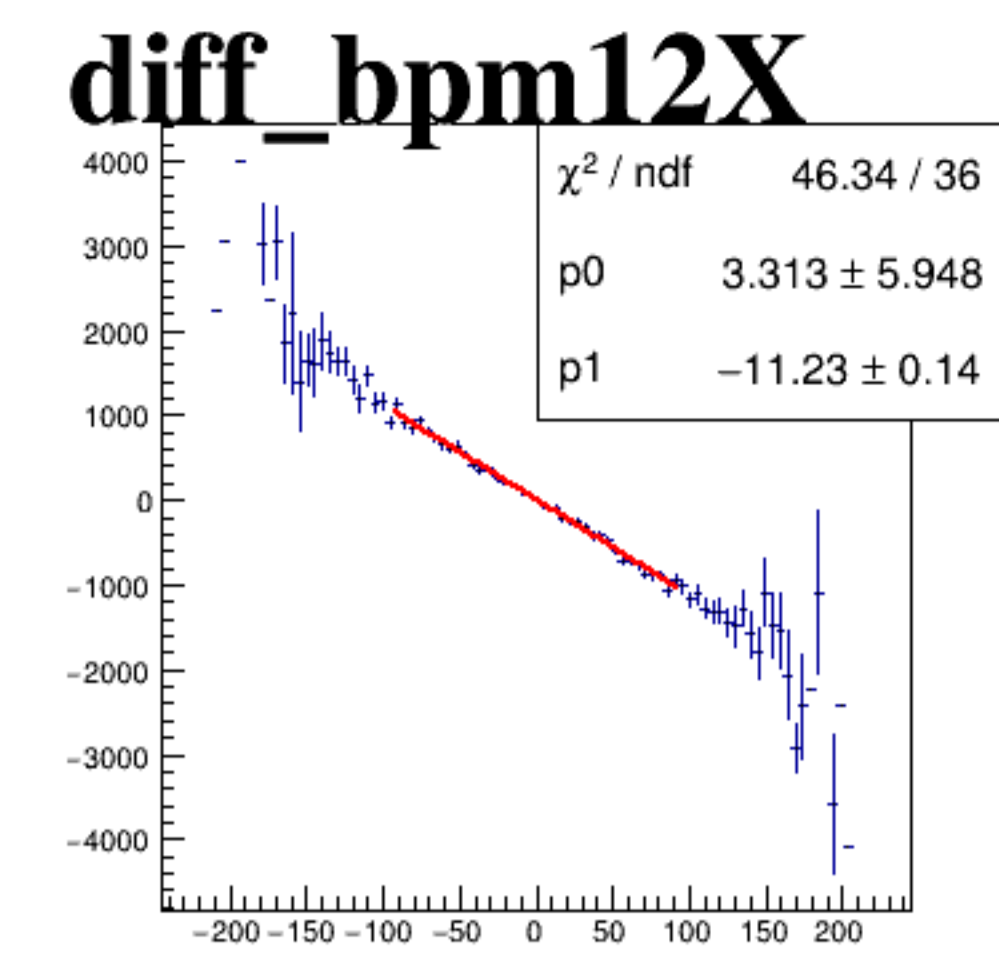
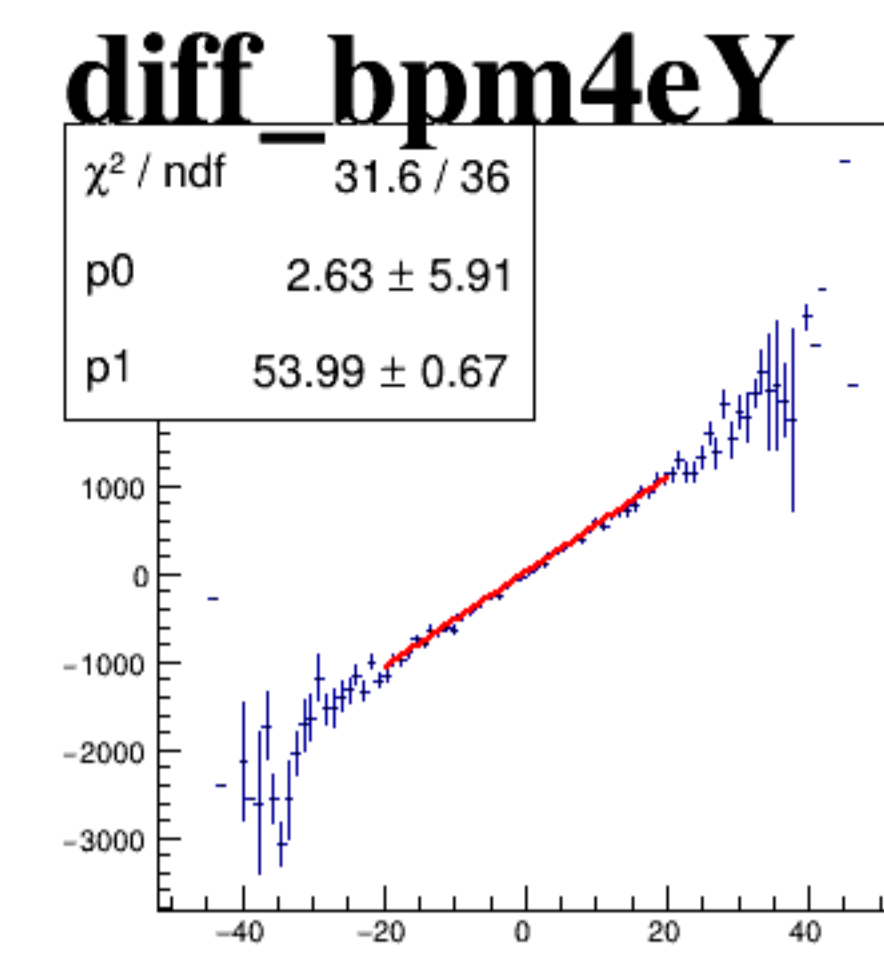
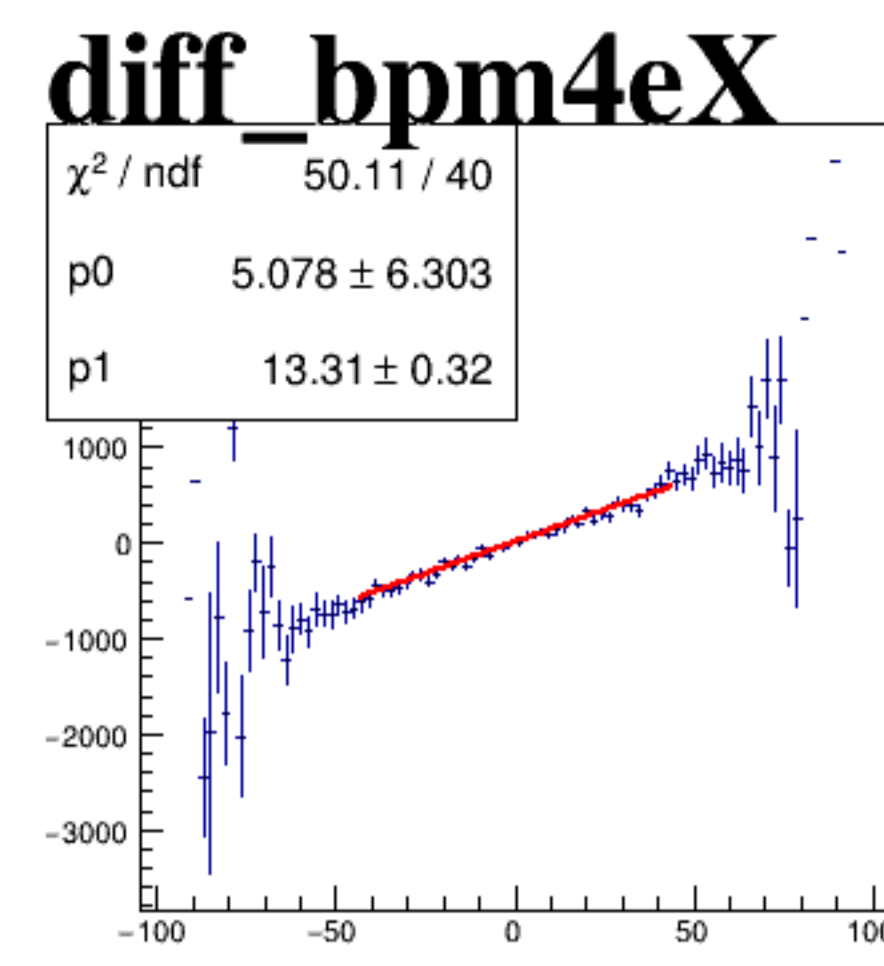
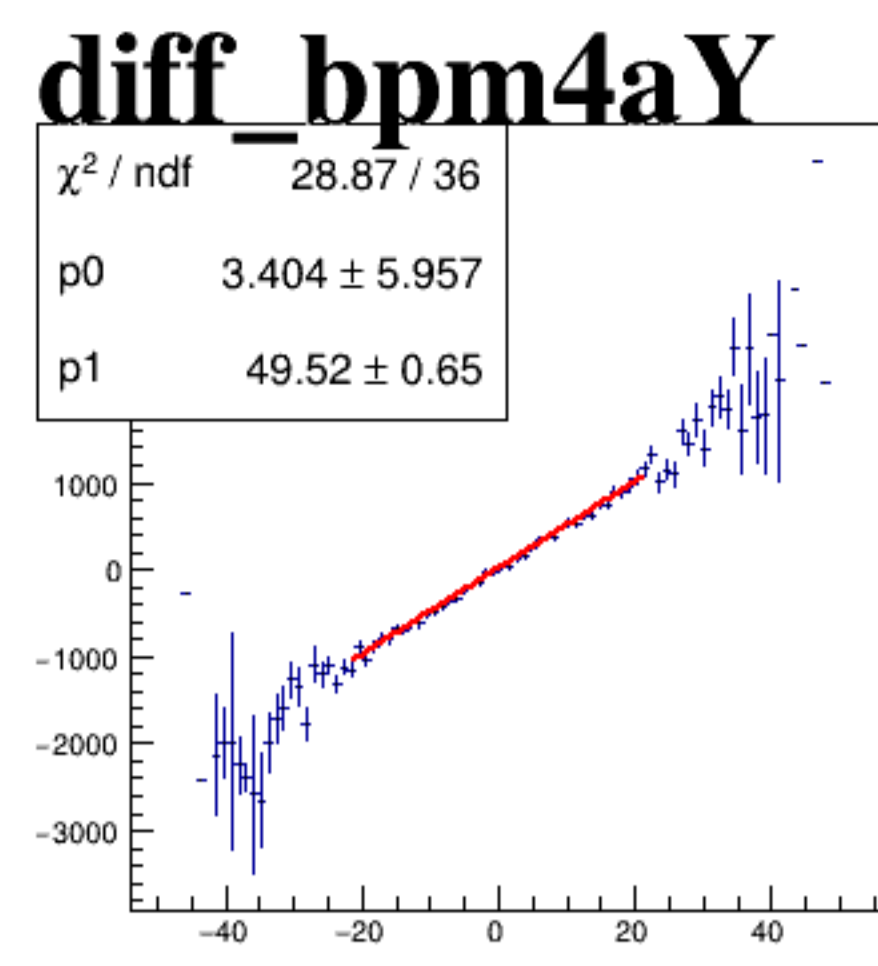
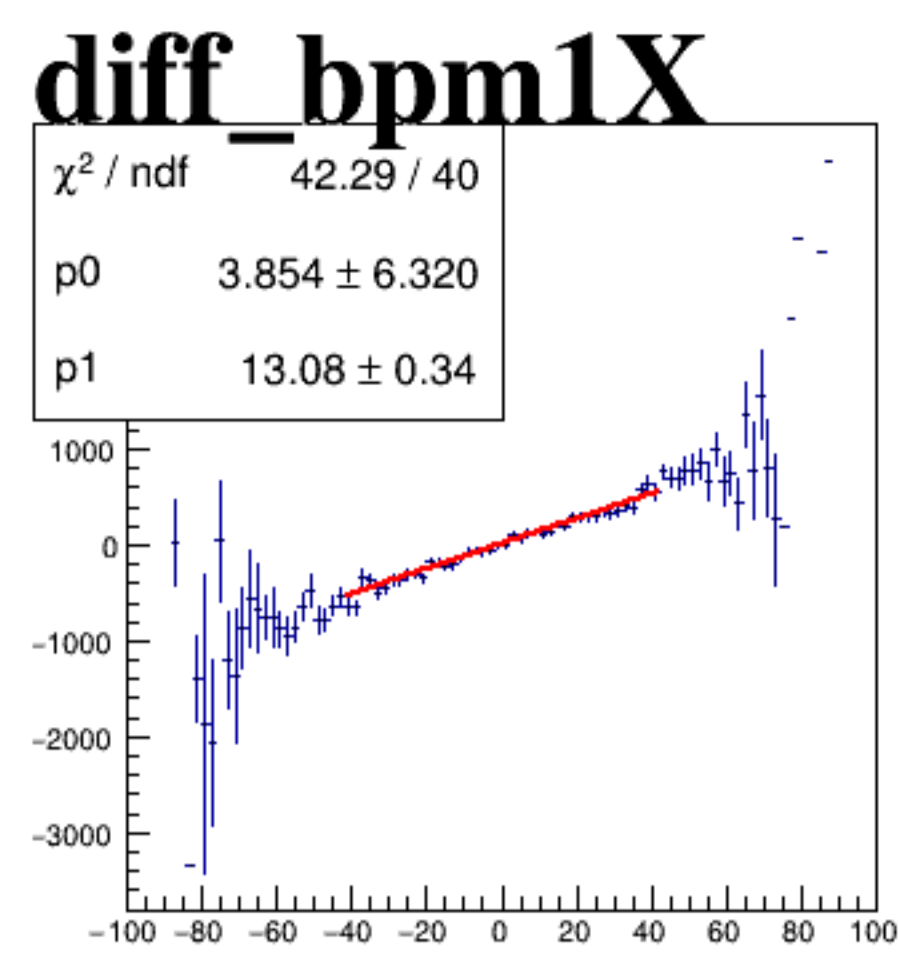


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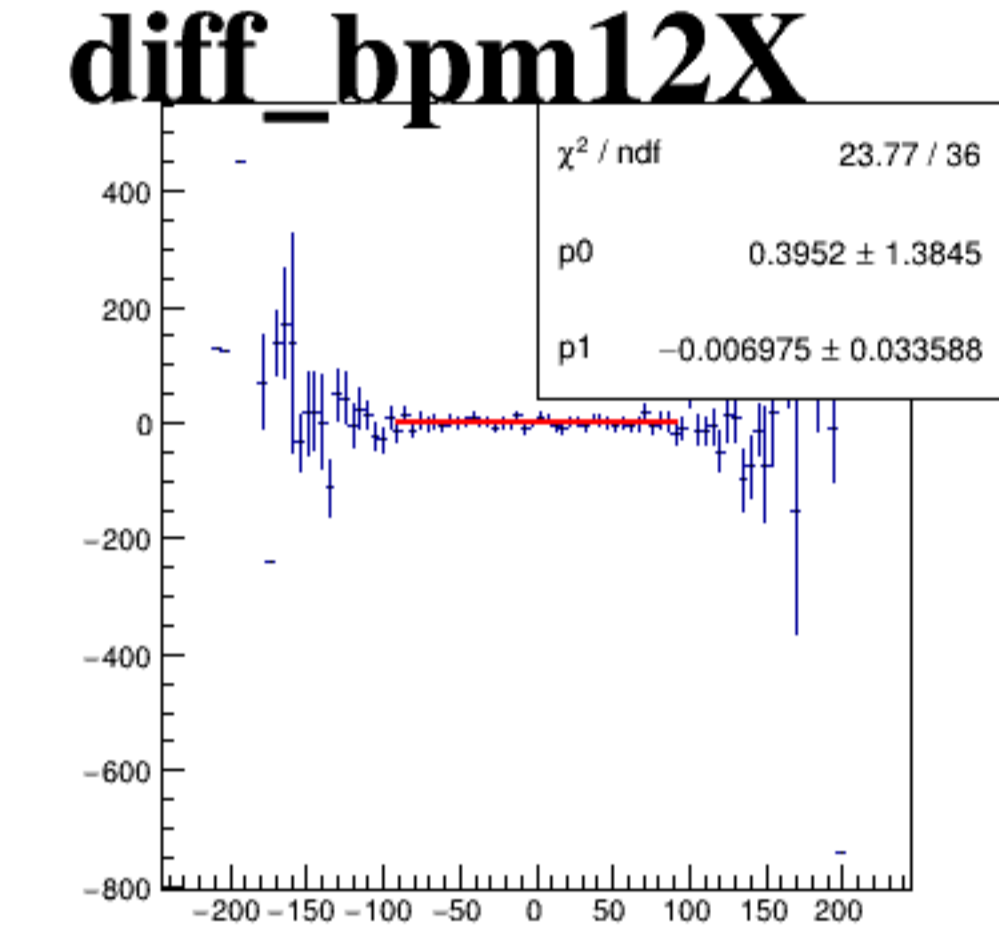
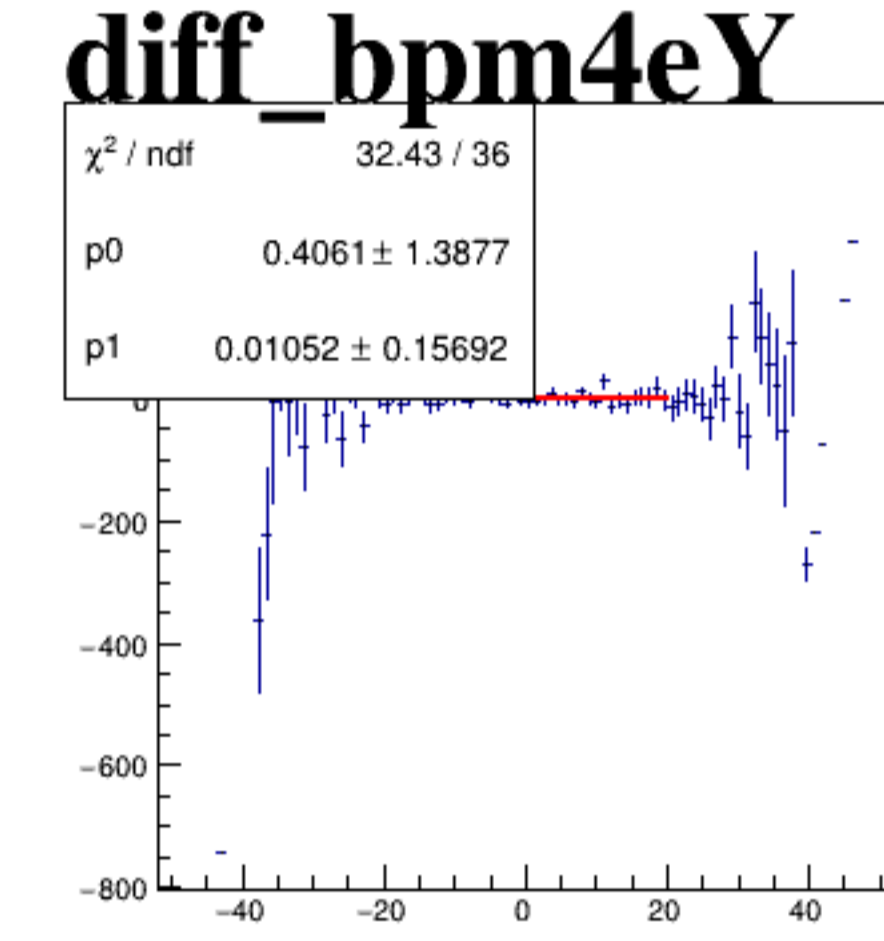
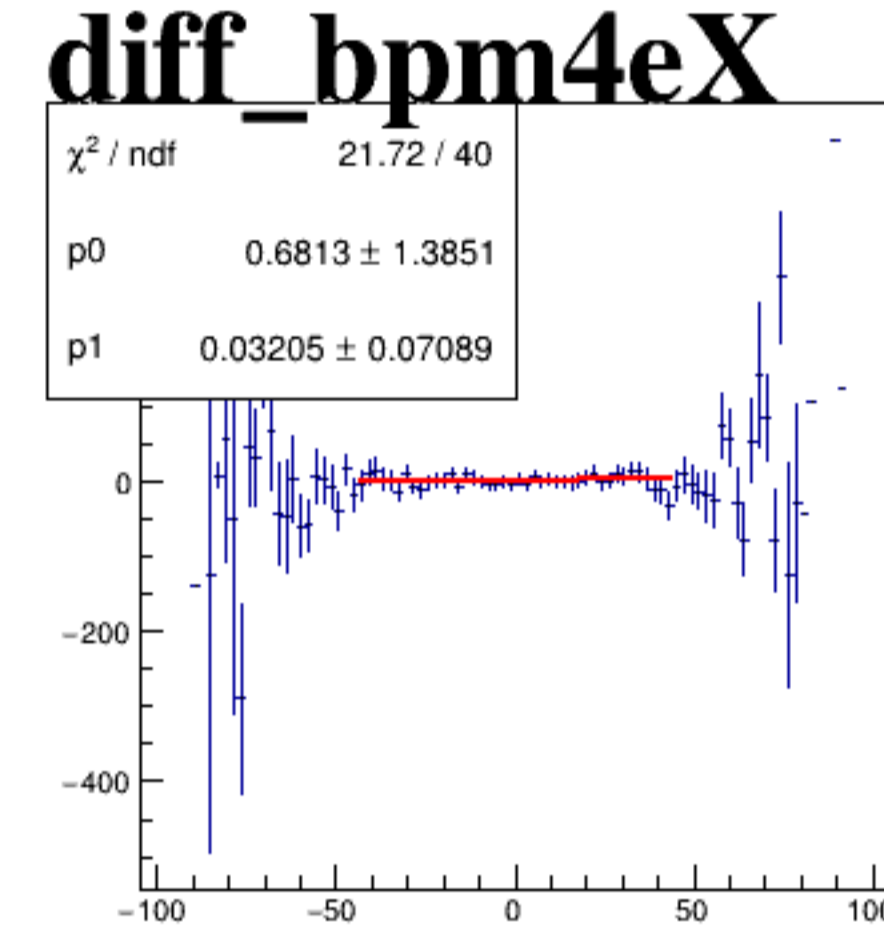
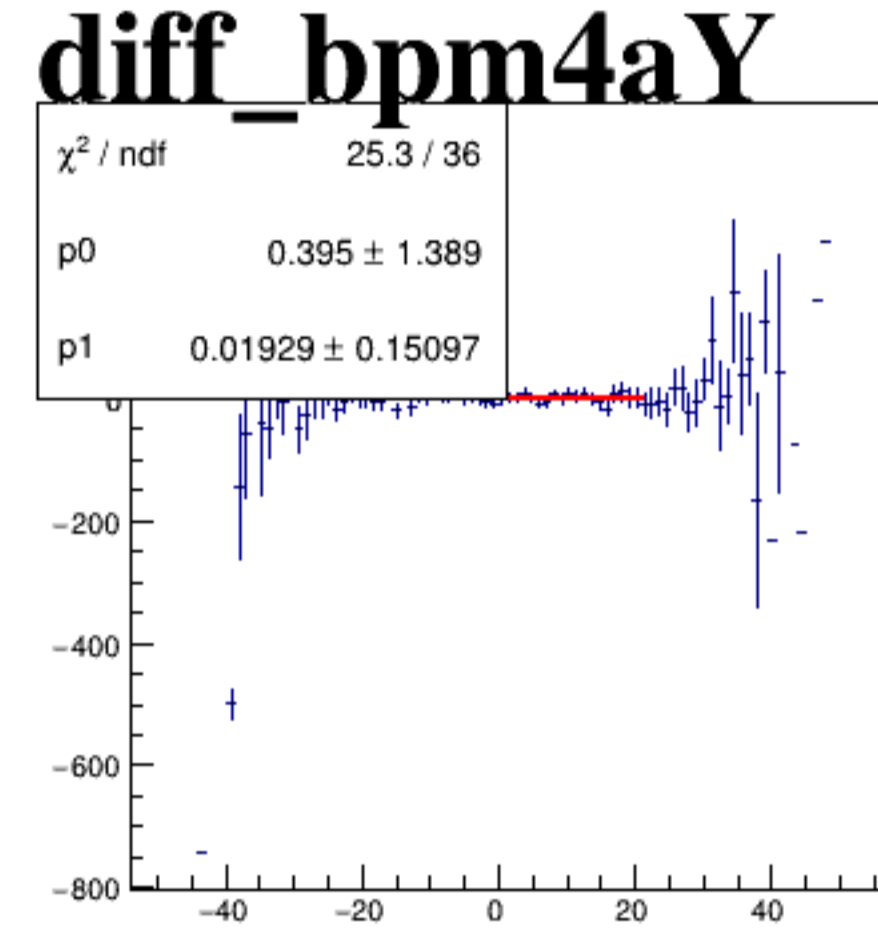
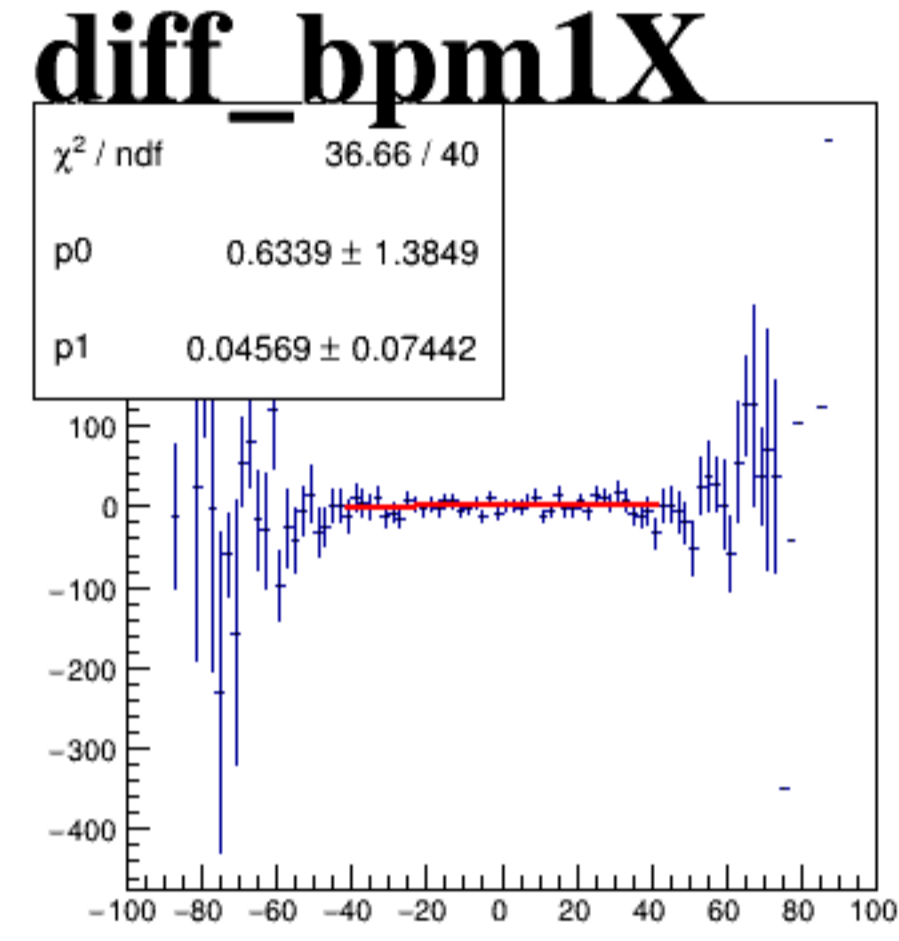


run8205\_000\_regression\_reg\_maintet\_vs\_diff\_bpm-COLZ\_postpan.png

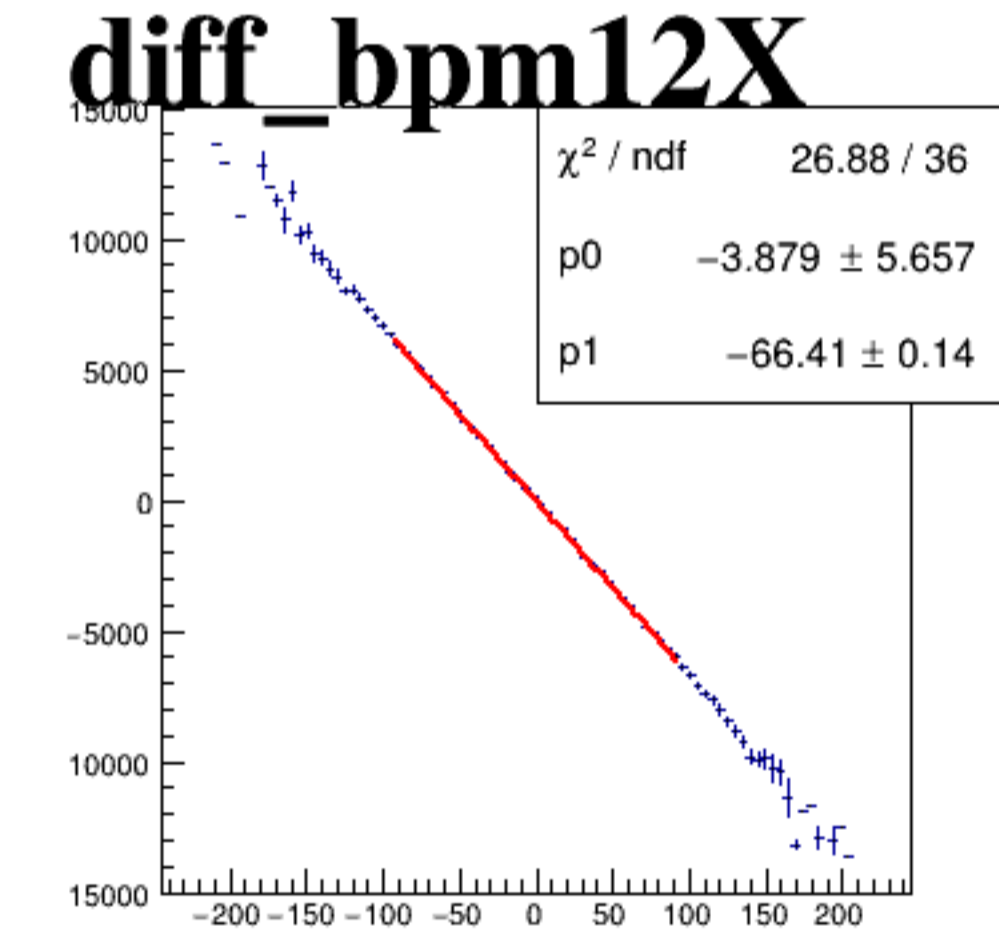
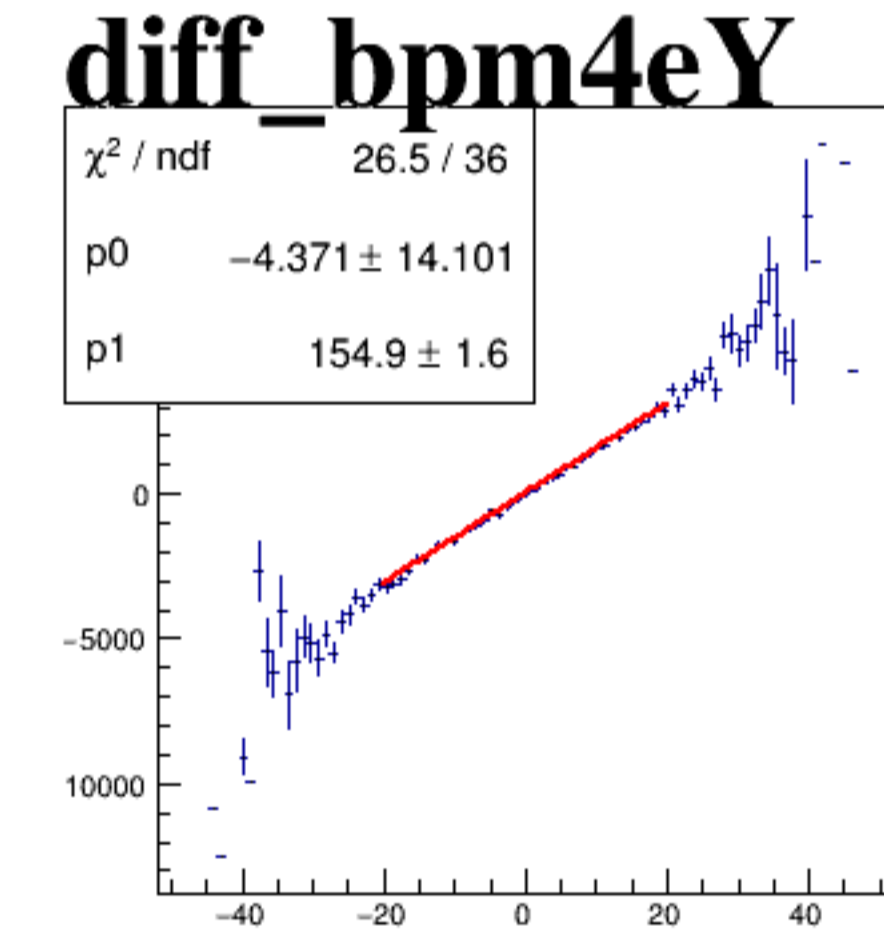
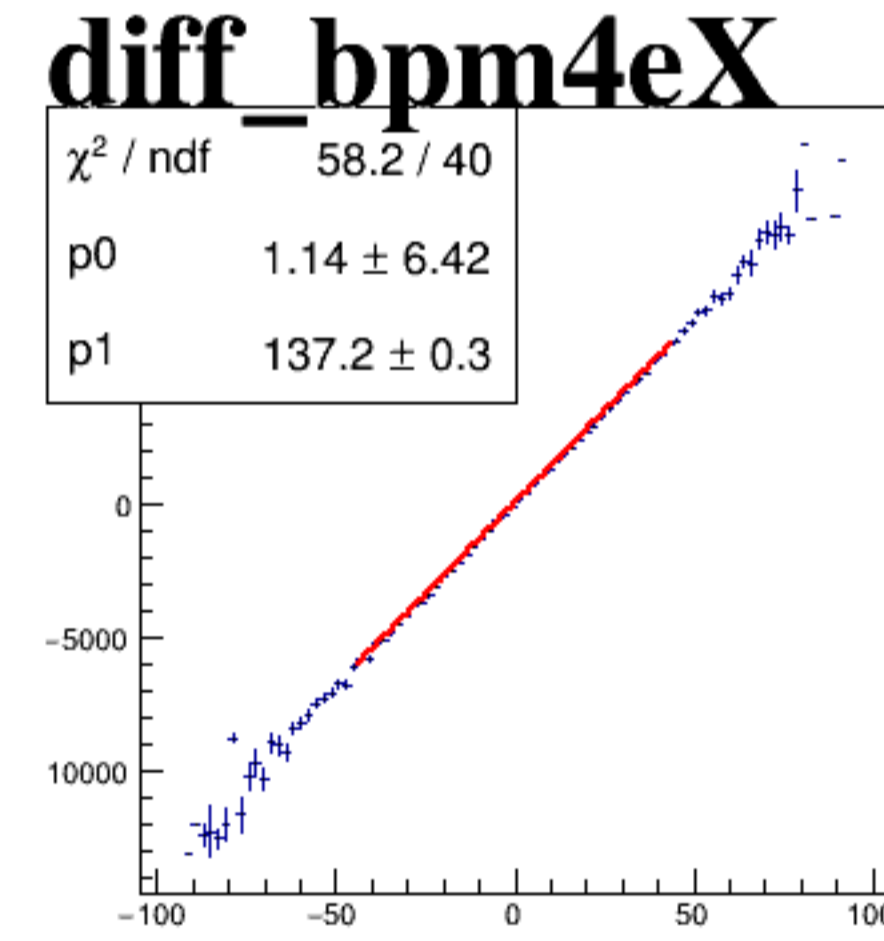
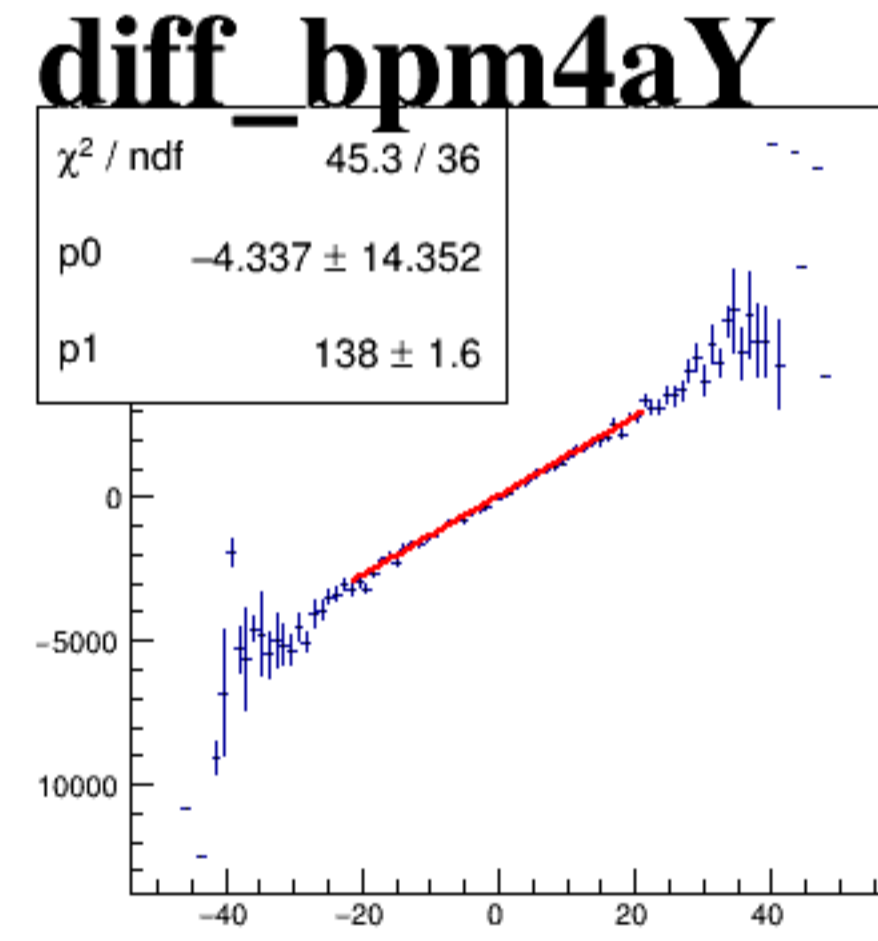
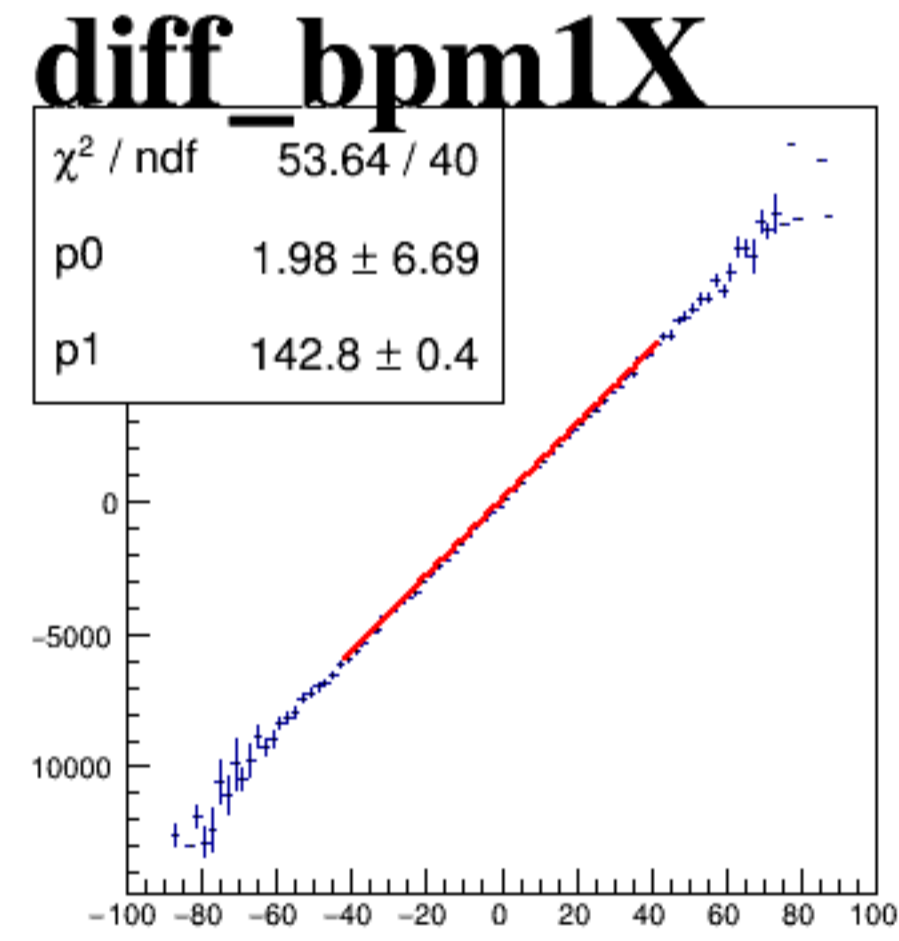
asym\_usl



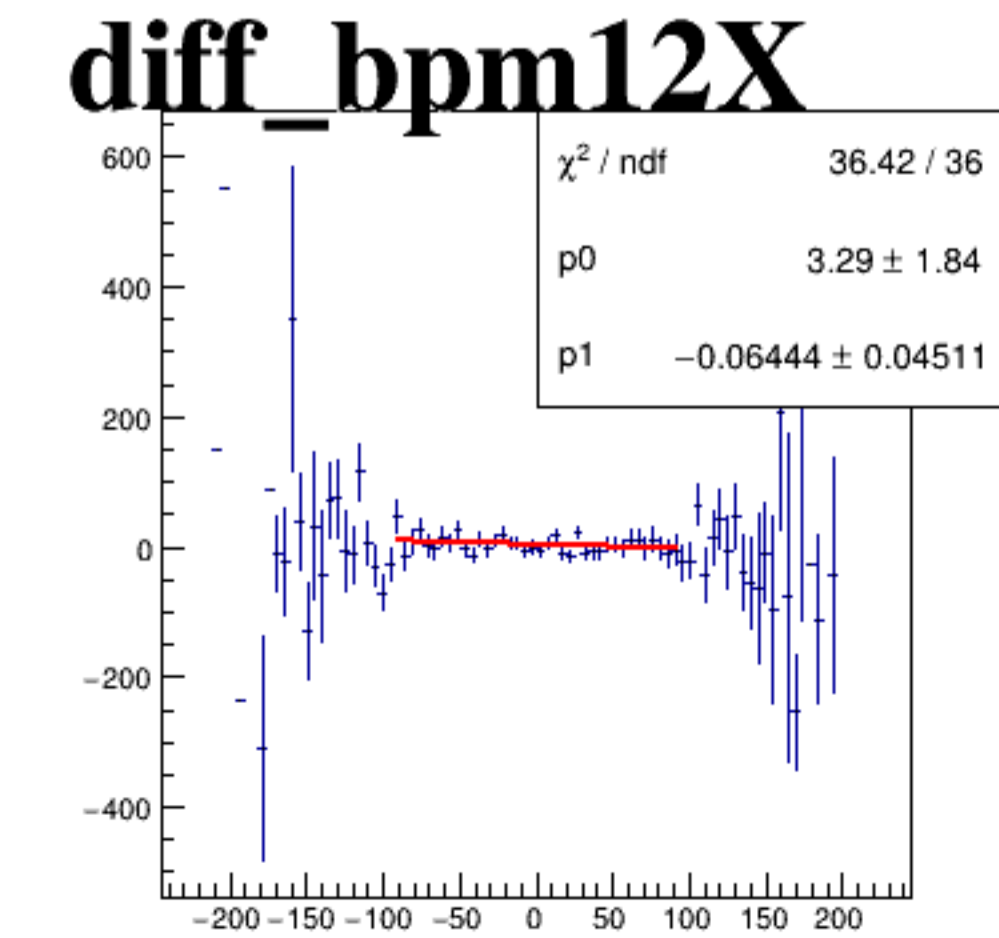
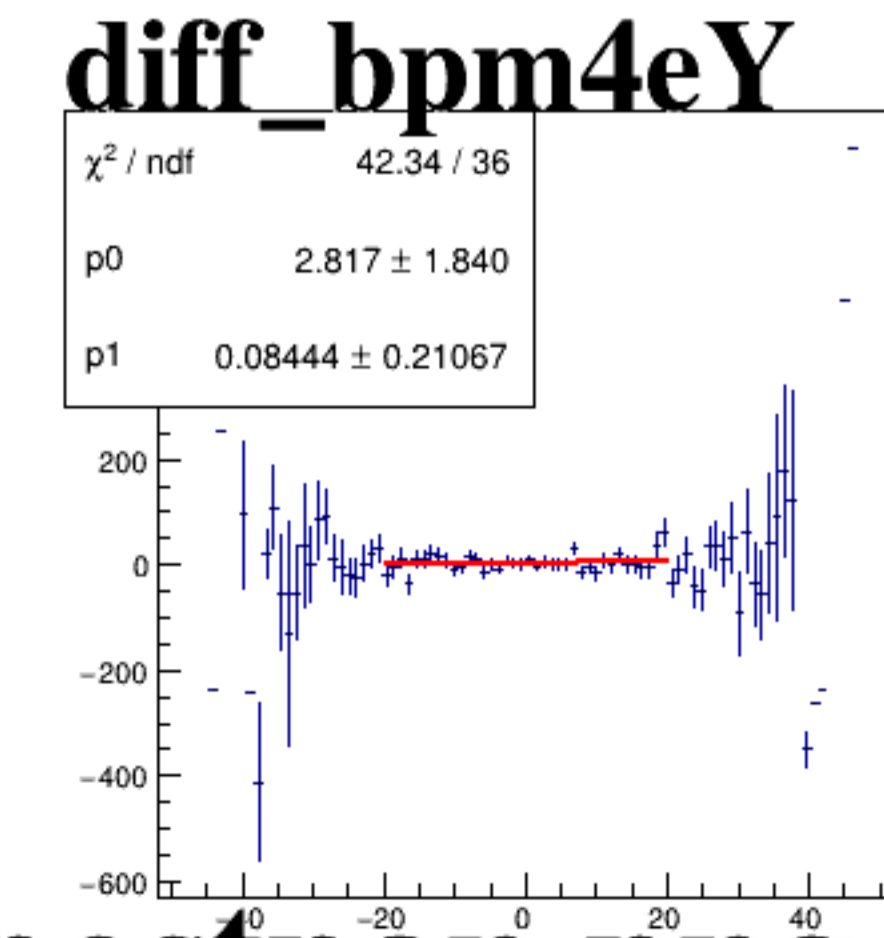
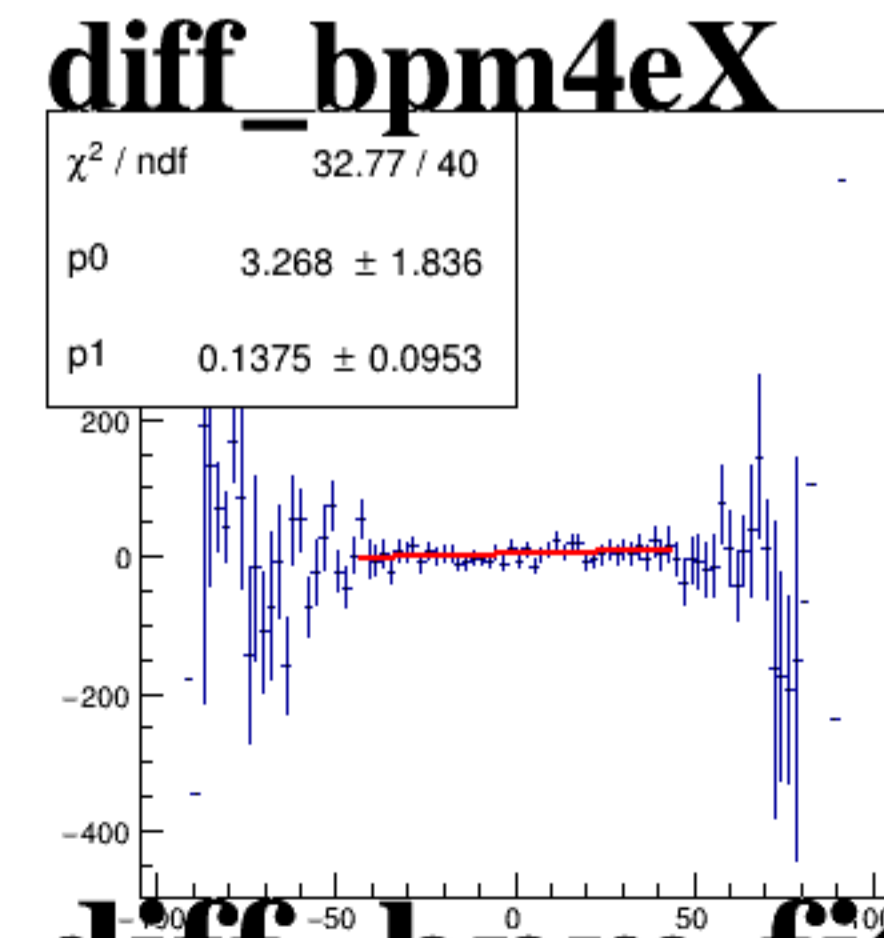
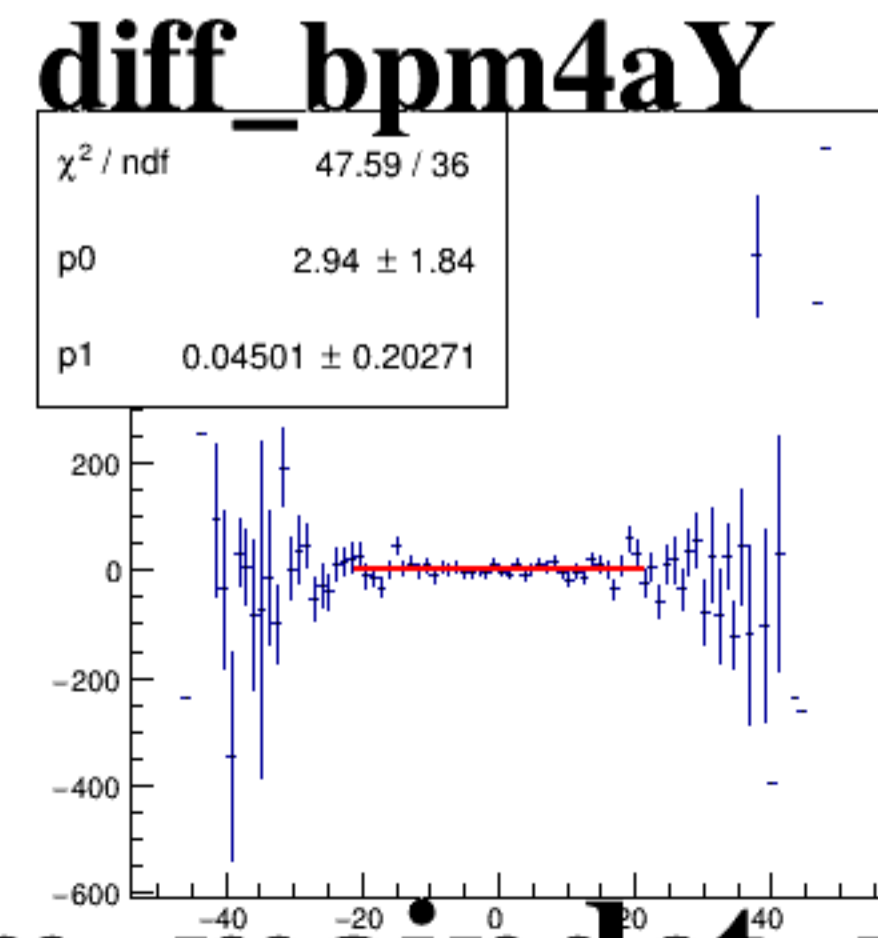
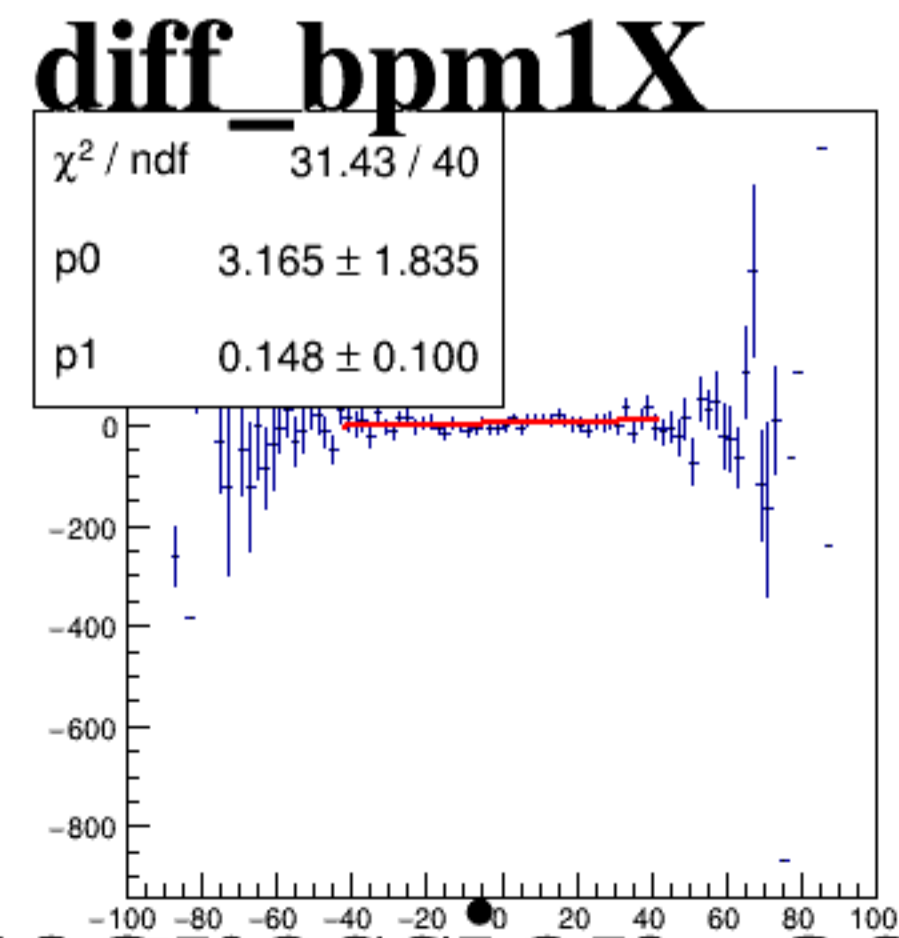
asym\_dsl



asym\_usr

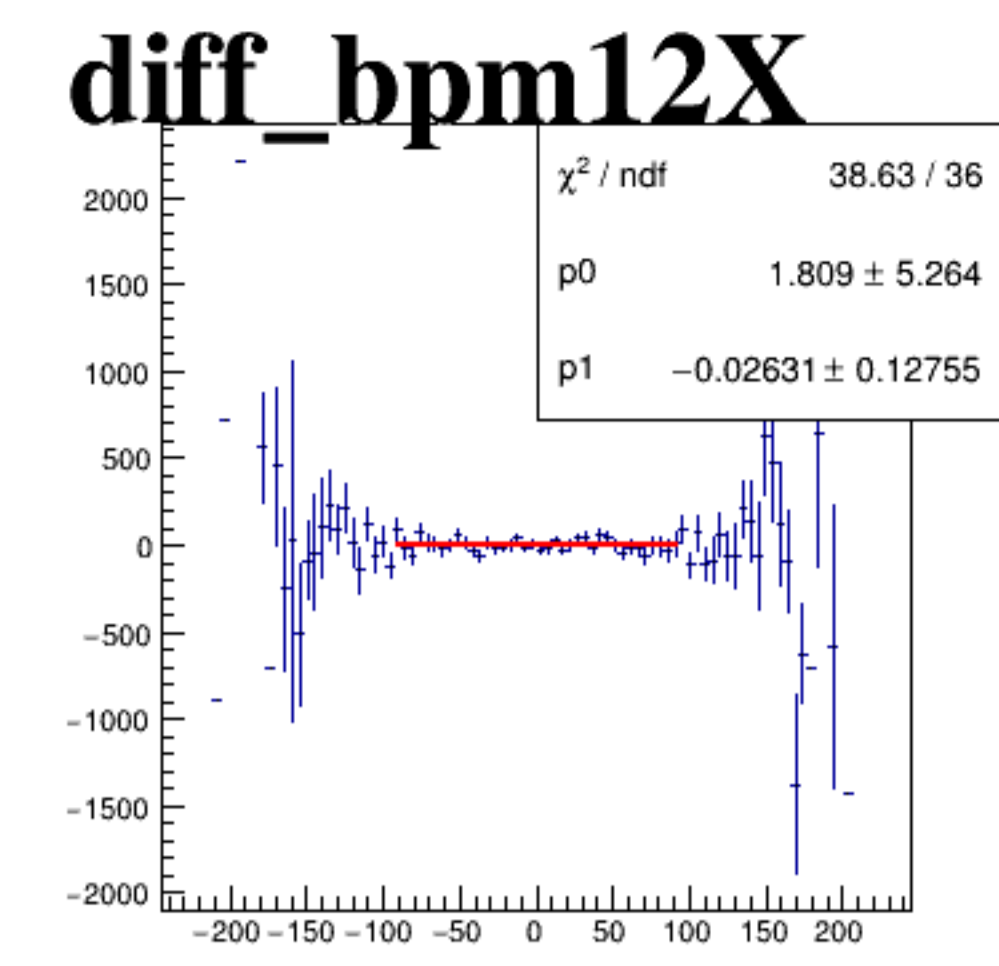
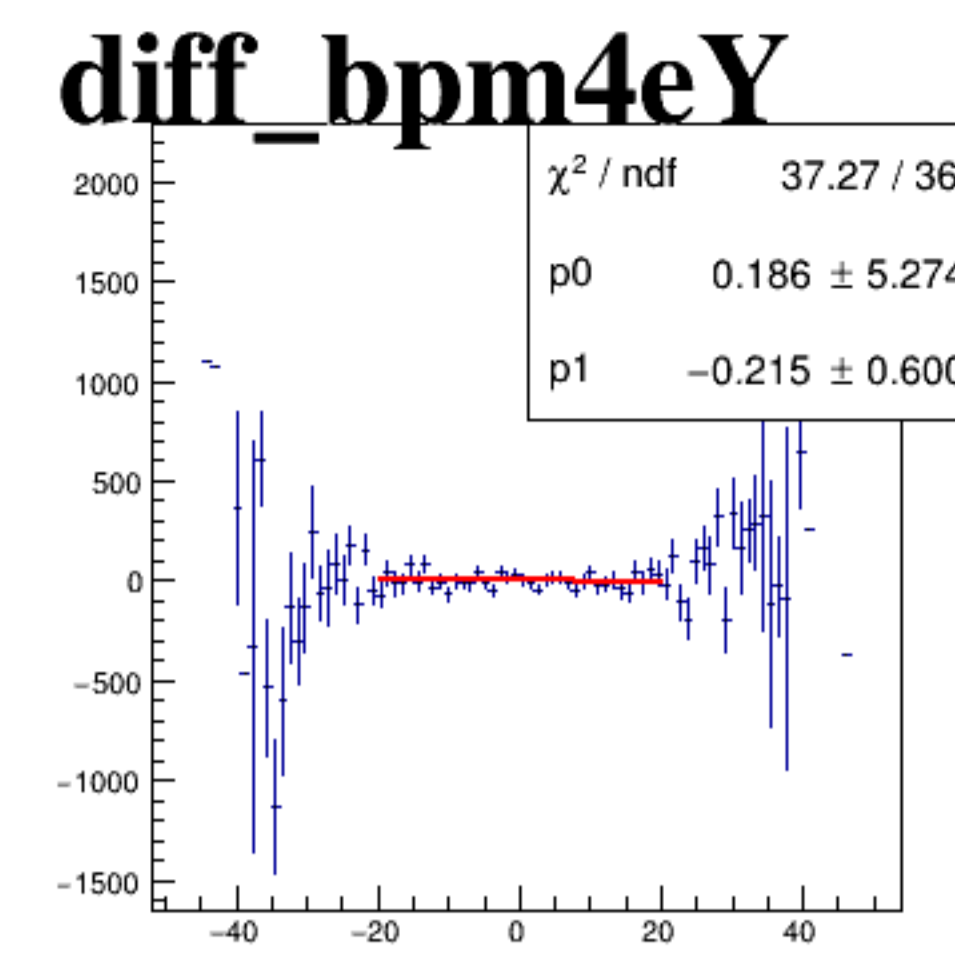
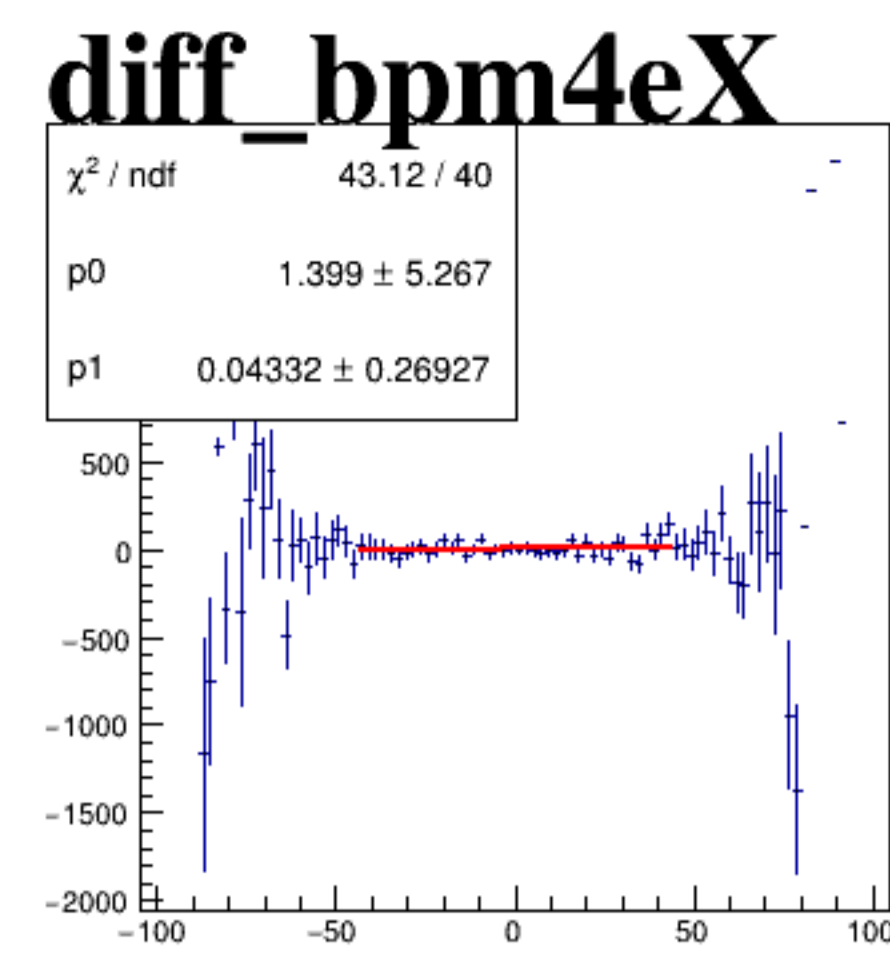
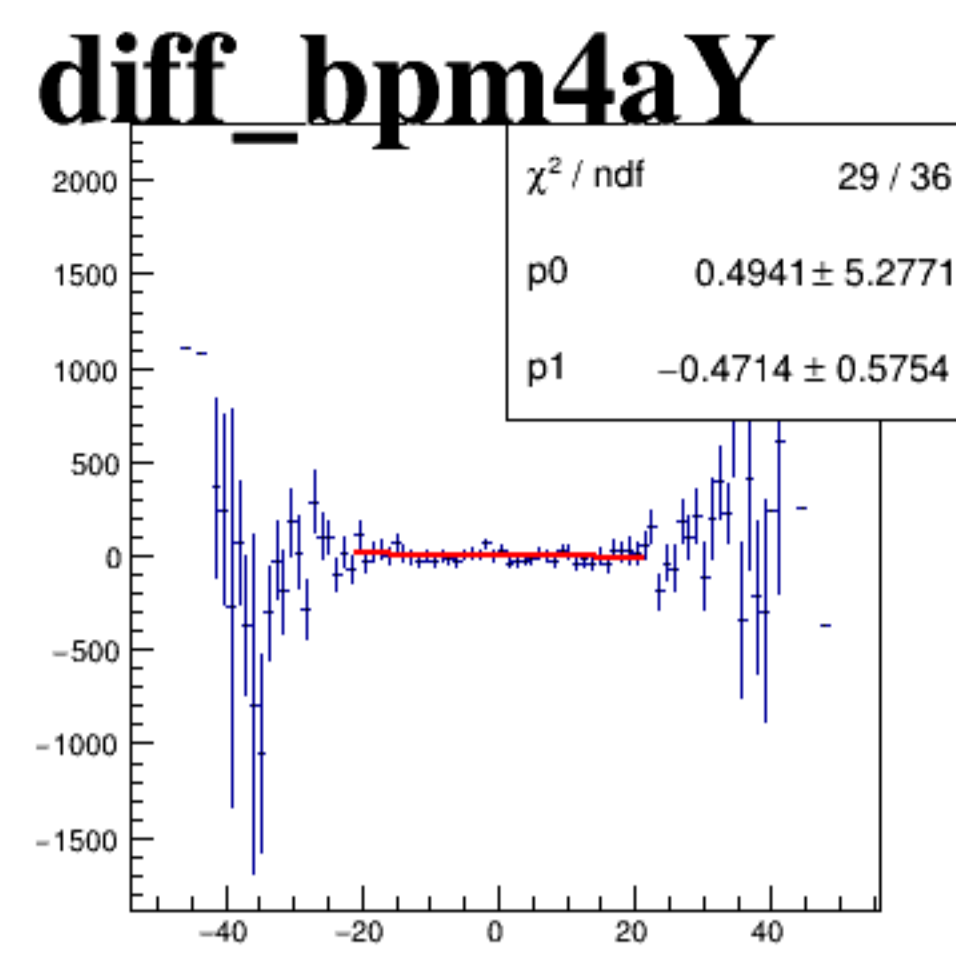
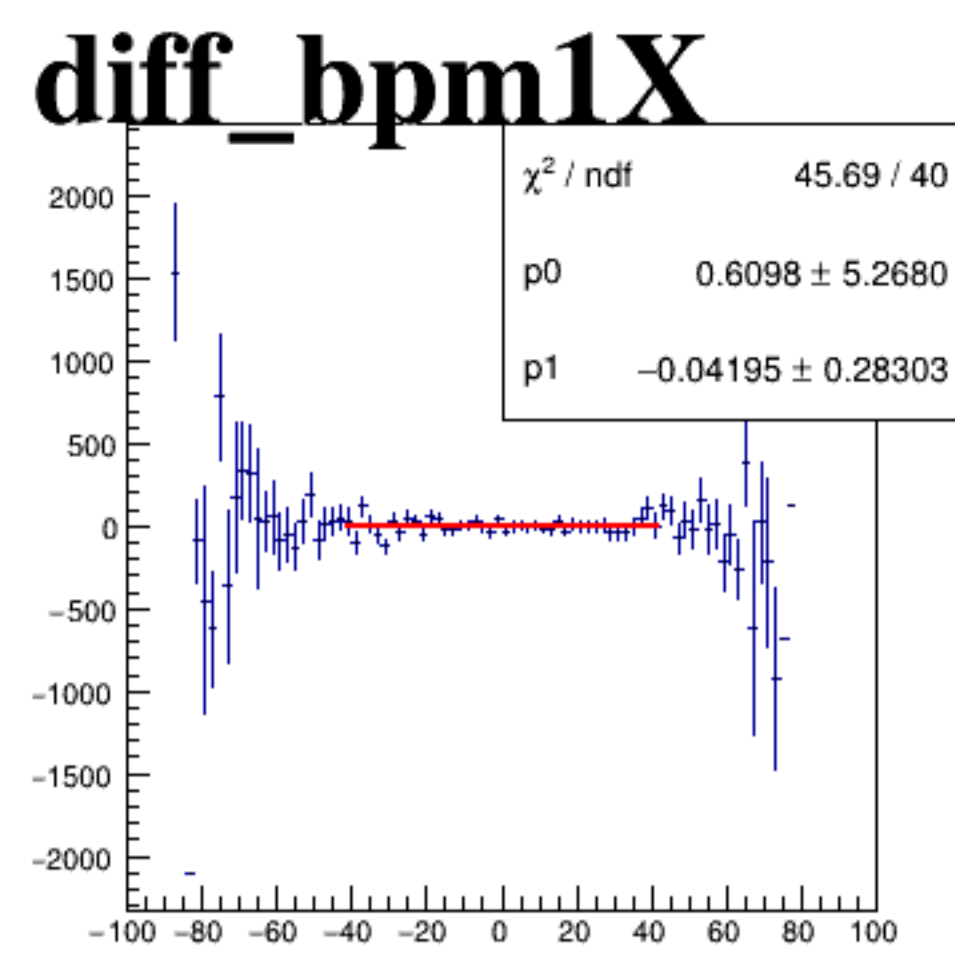


asym\_dsr

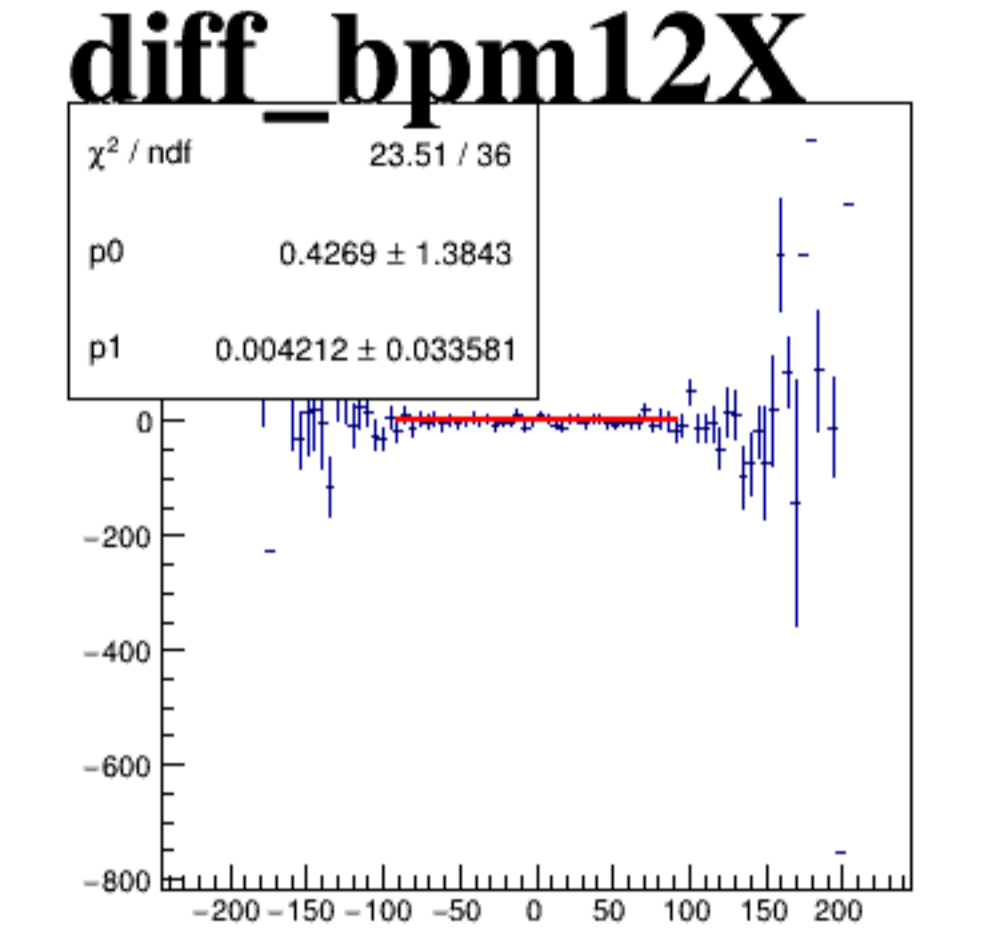
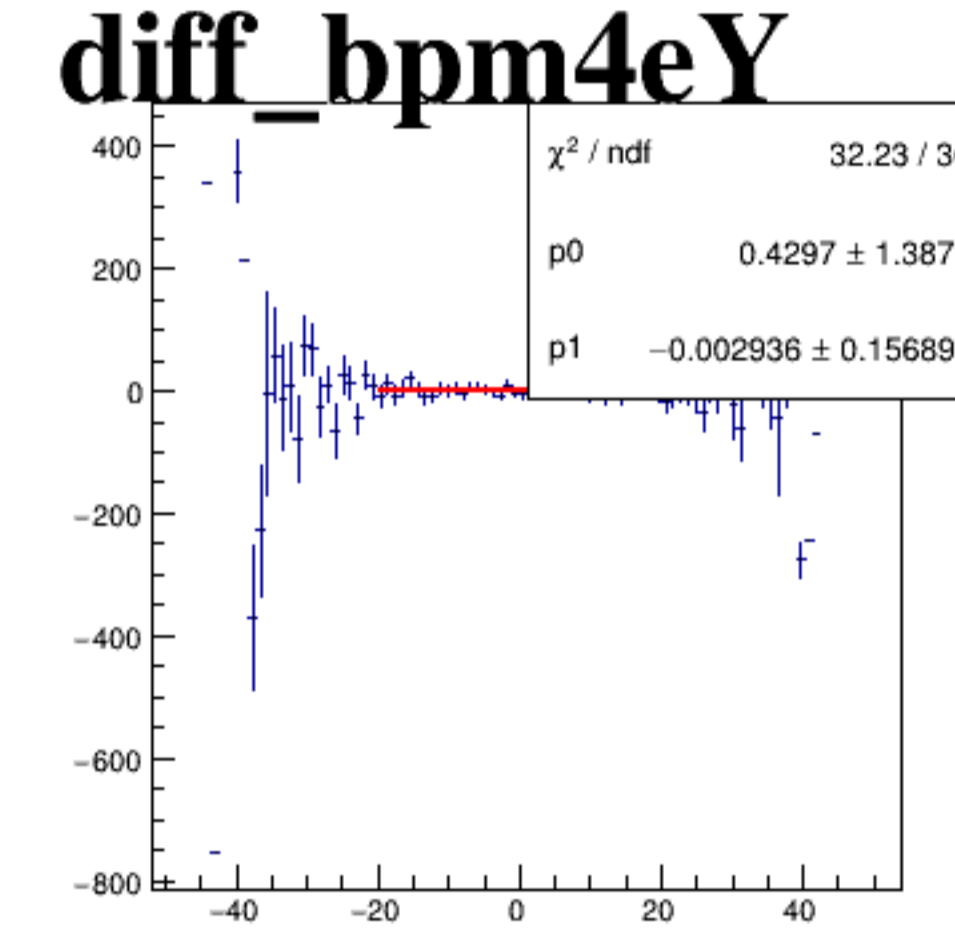
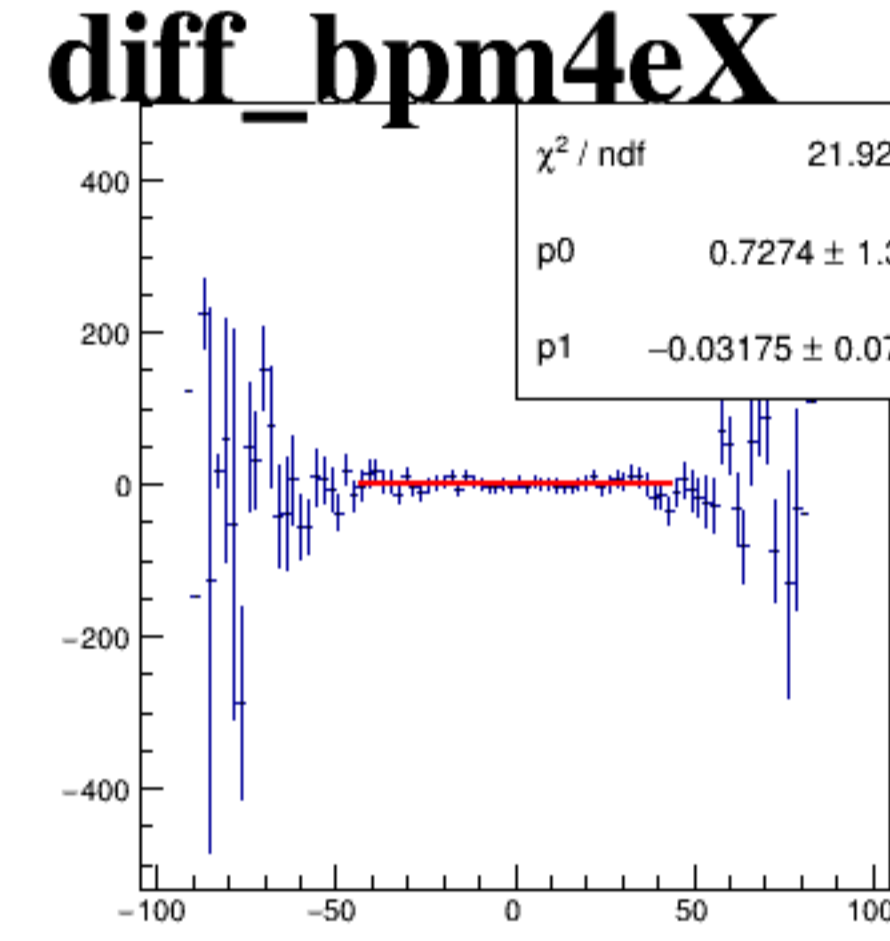
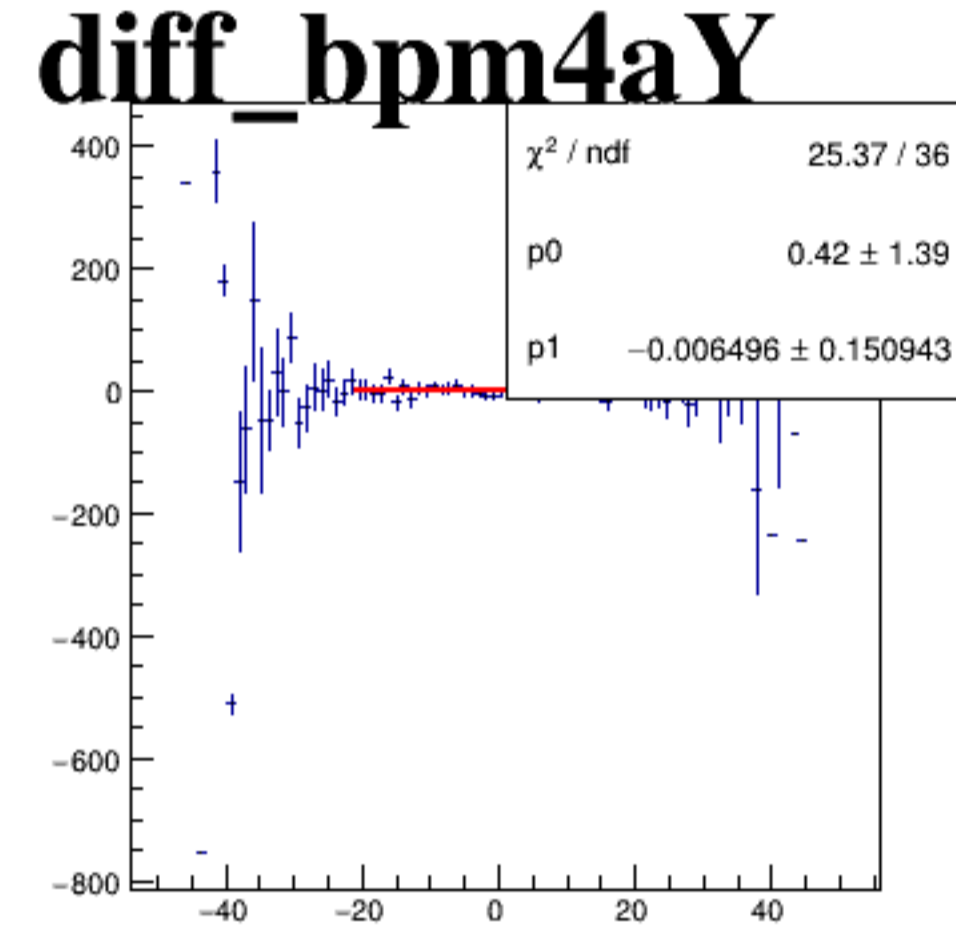
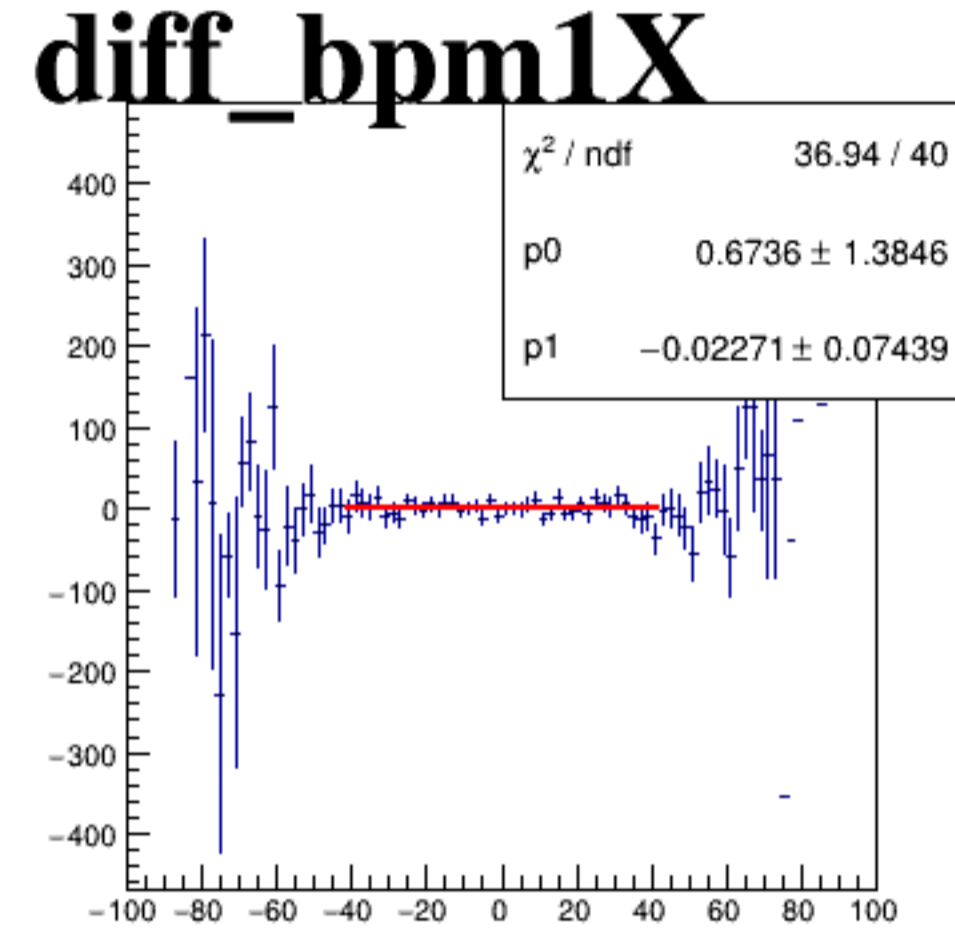


run8205\_000\_regression\_asym\_maintet\_vs\_diff\_bpm-fit\_postpan.png

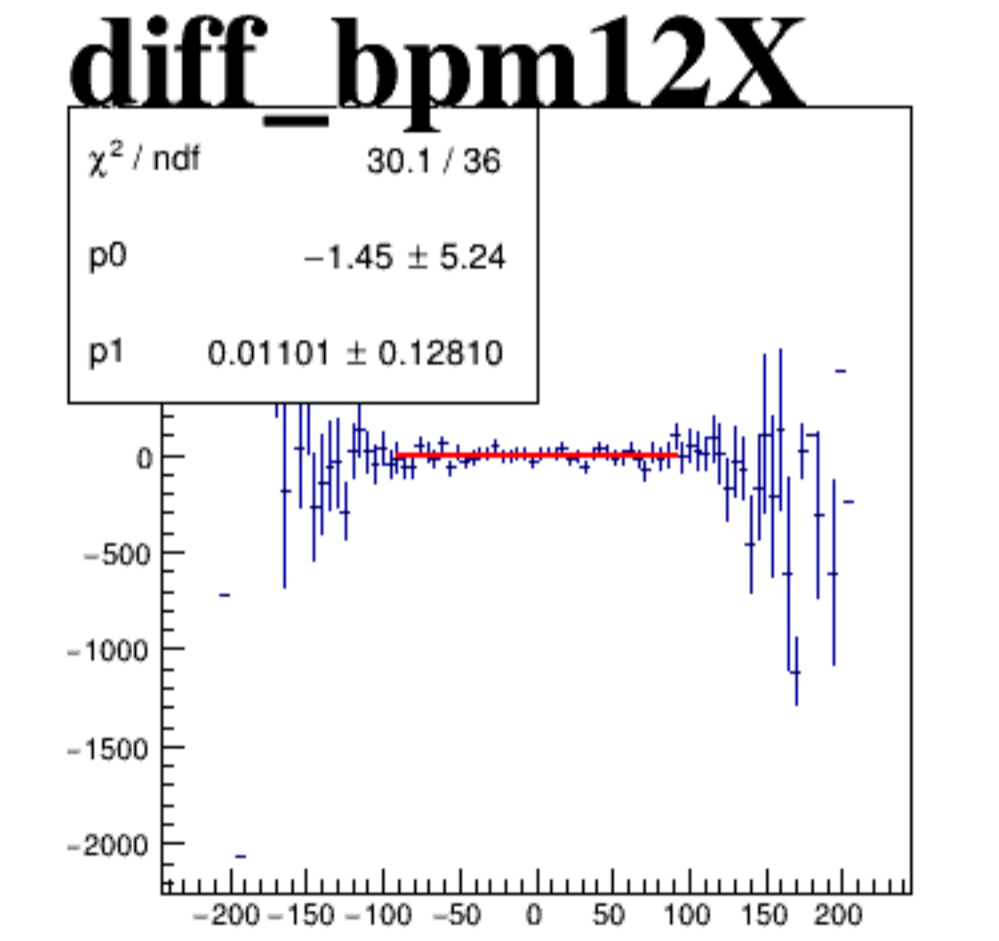
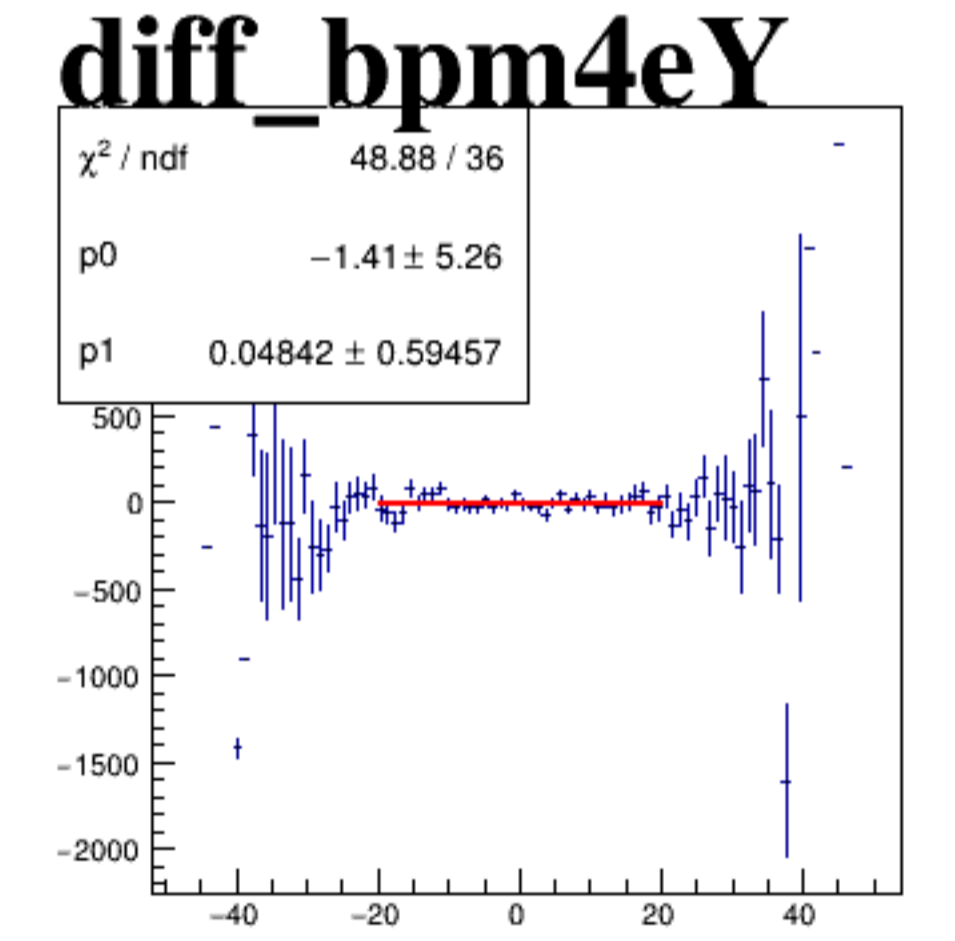
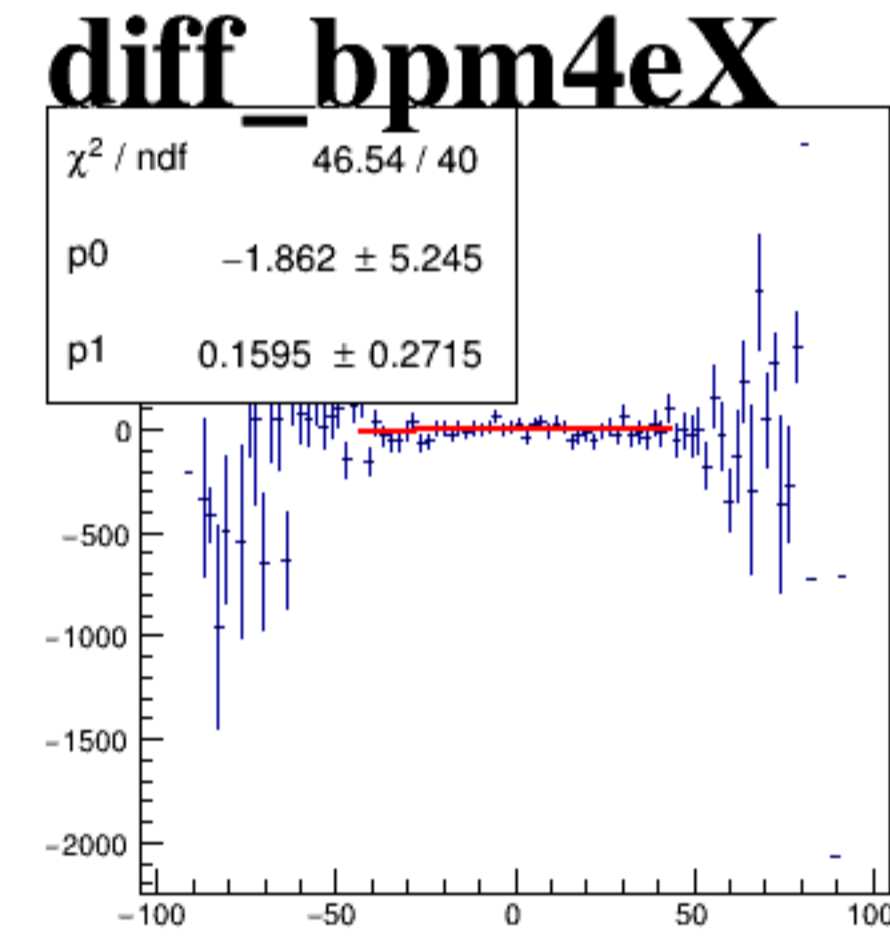
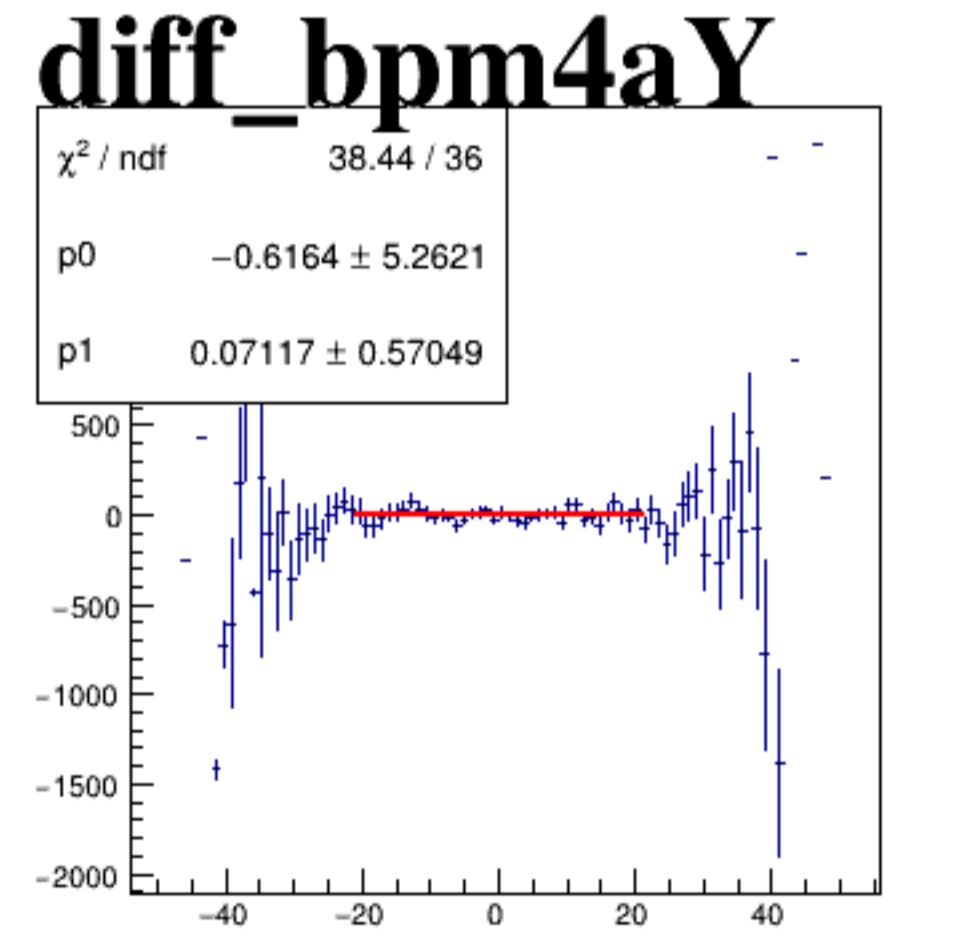
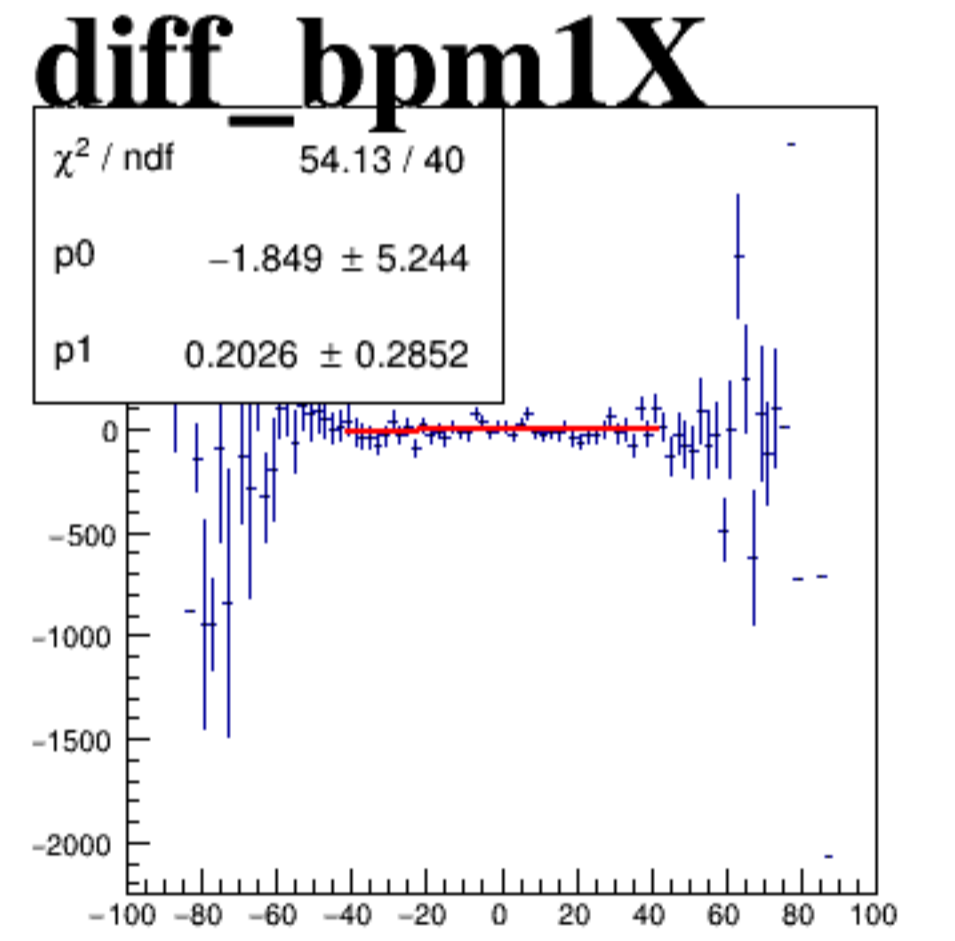
reg\_asym\_usl



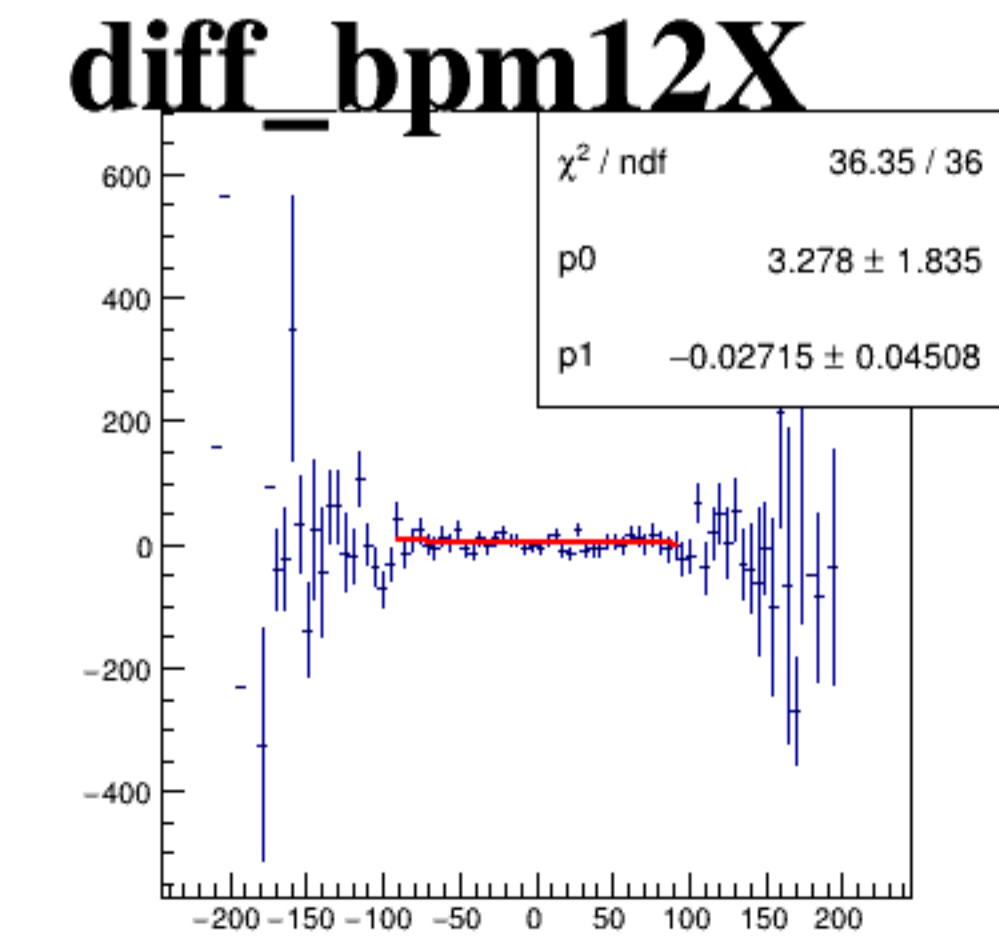
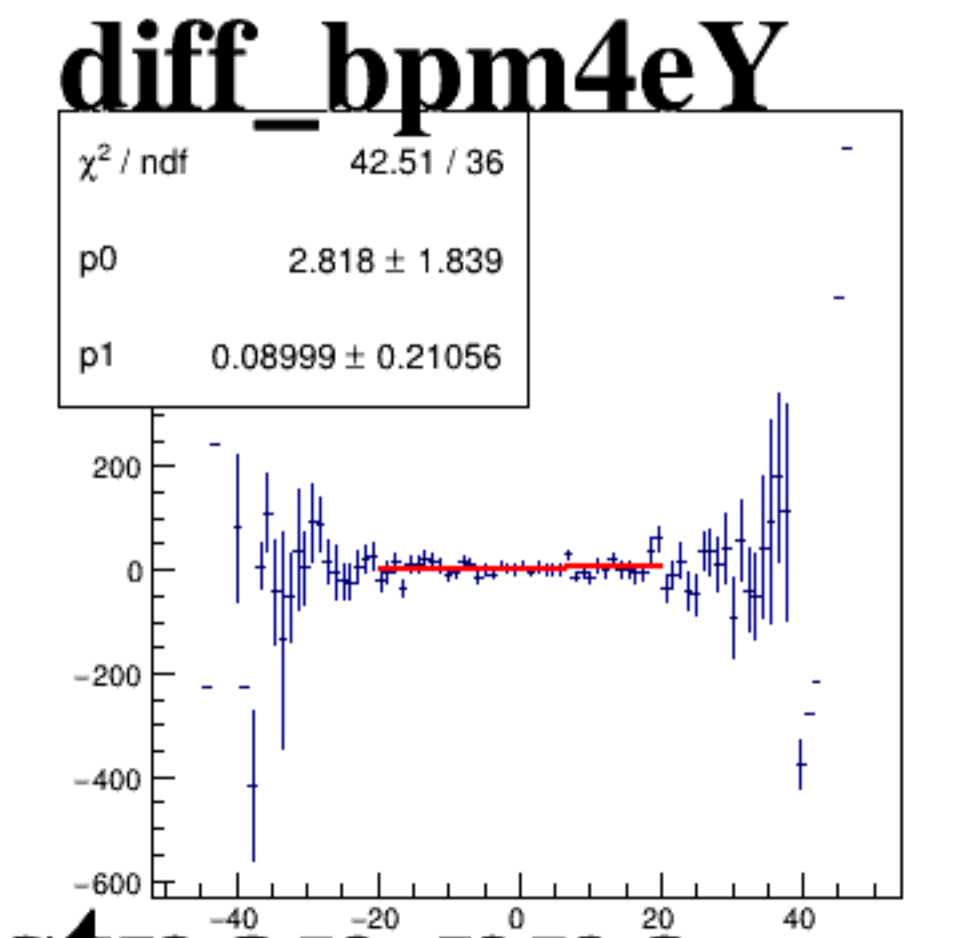
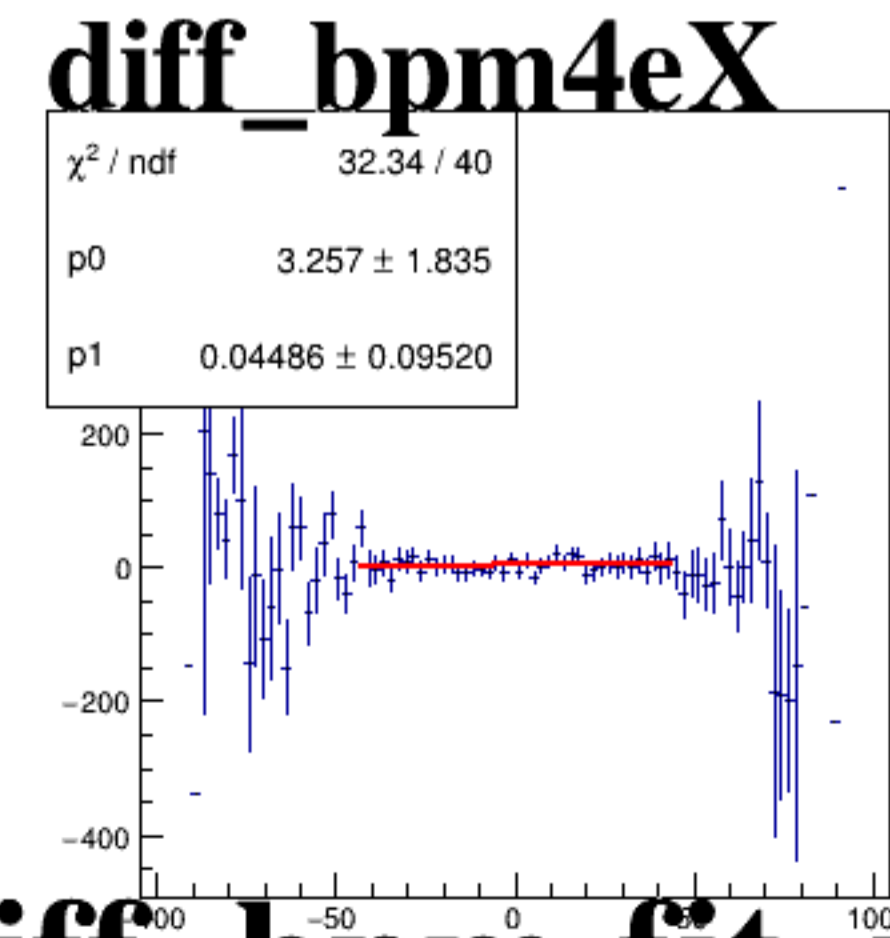
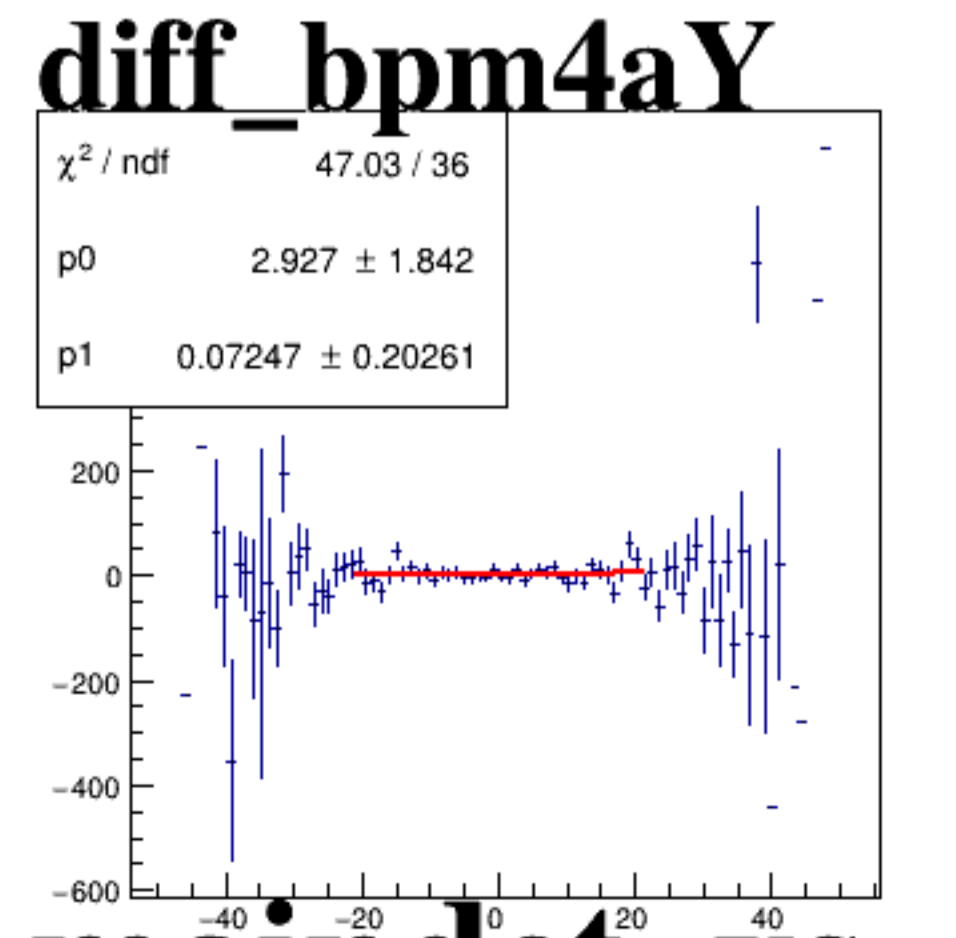
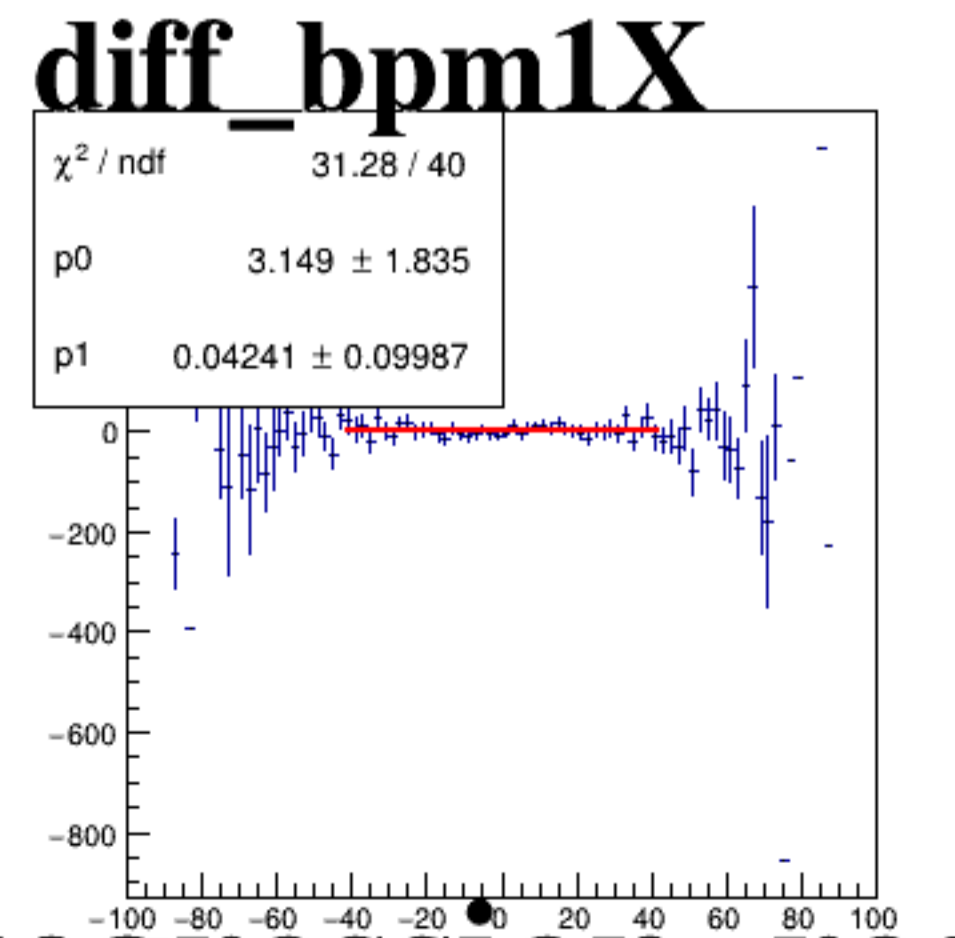
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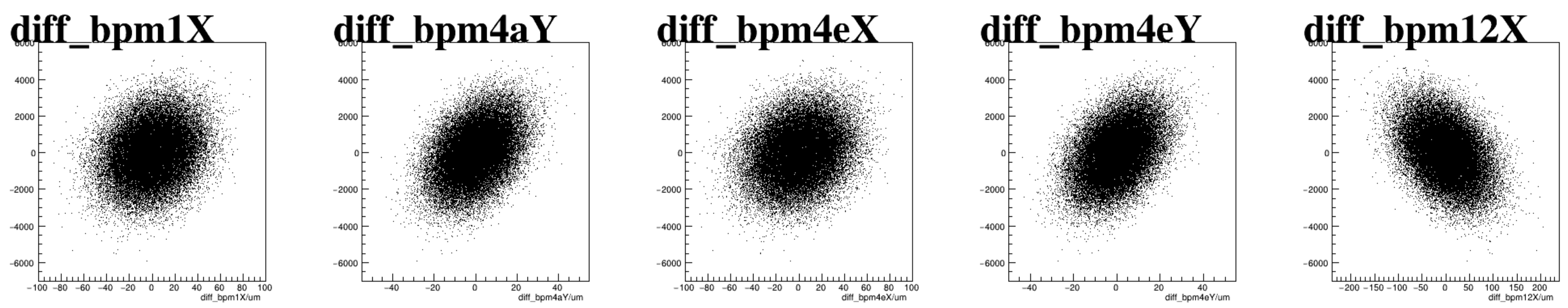
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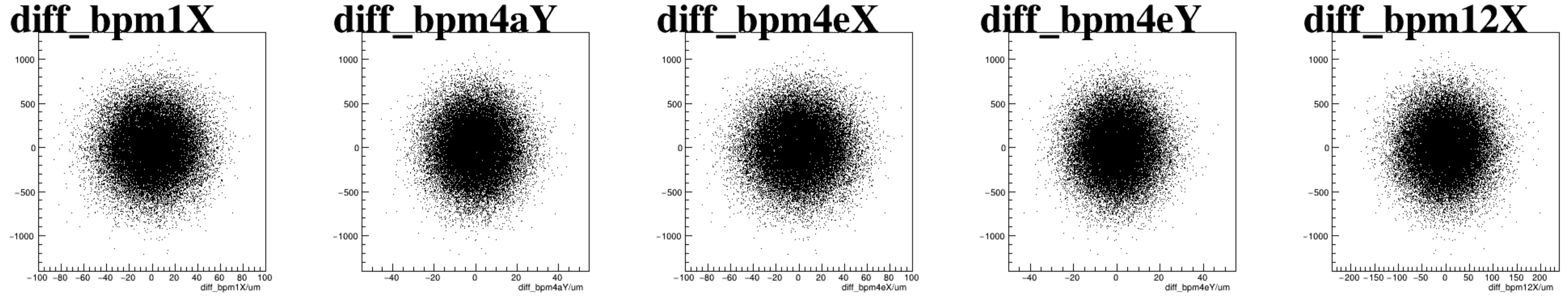
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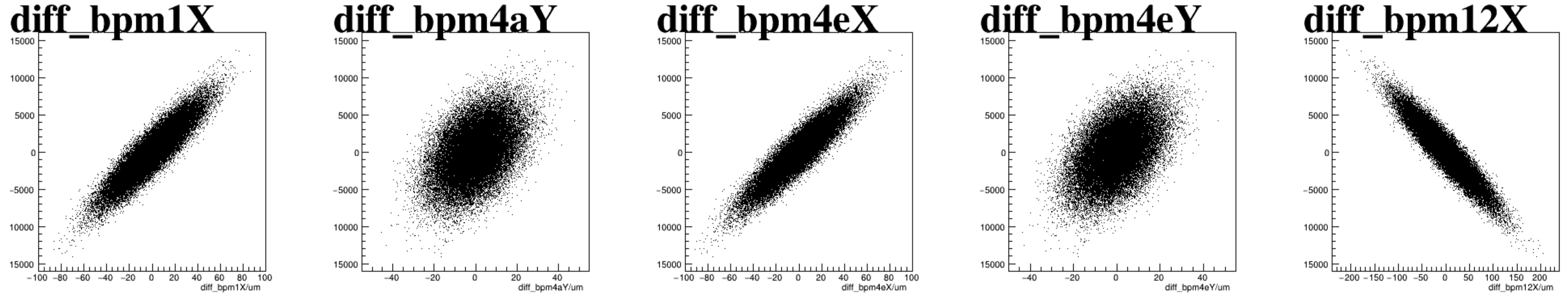
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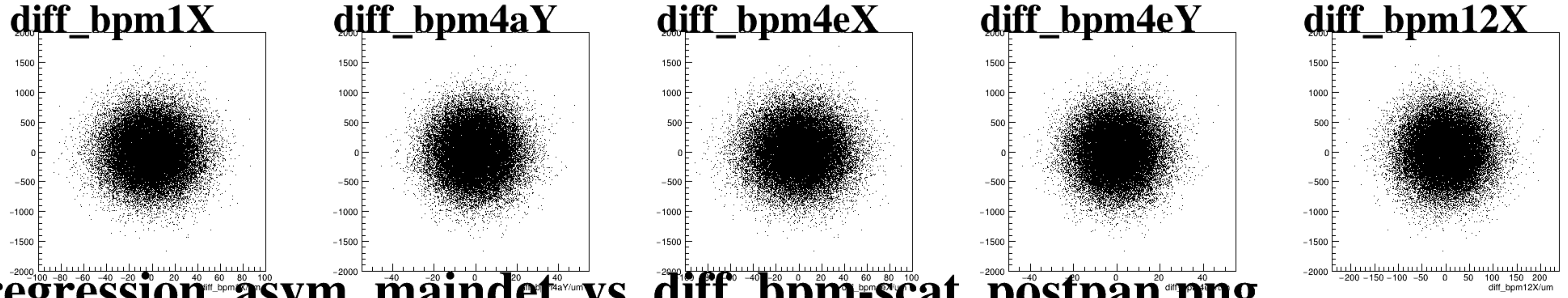
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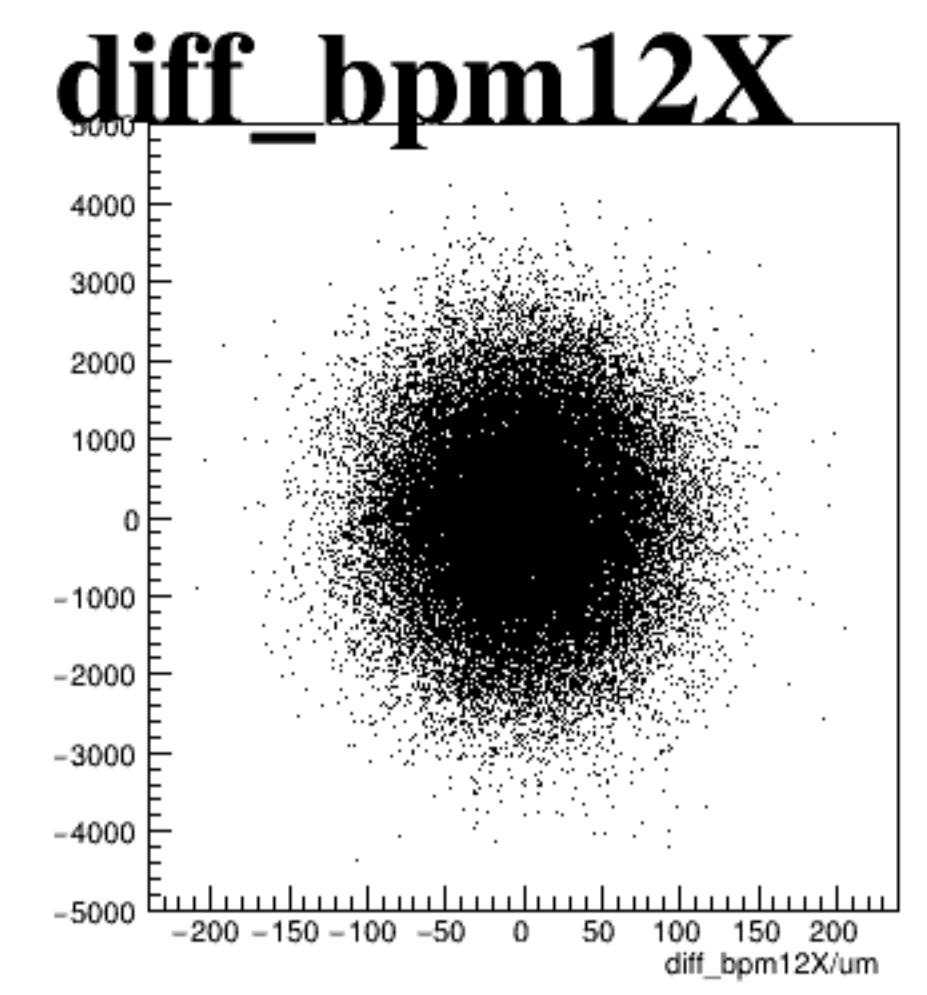
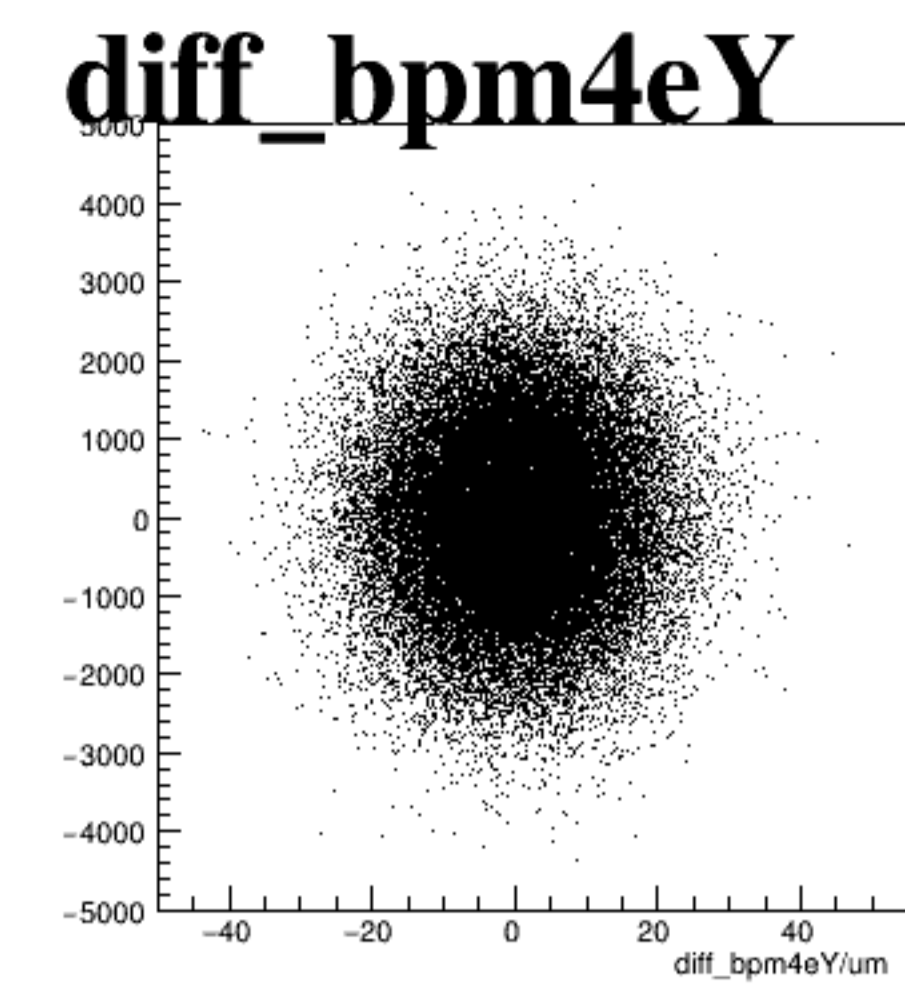
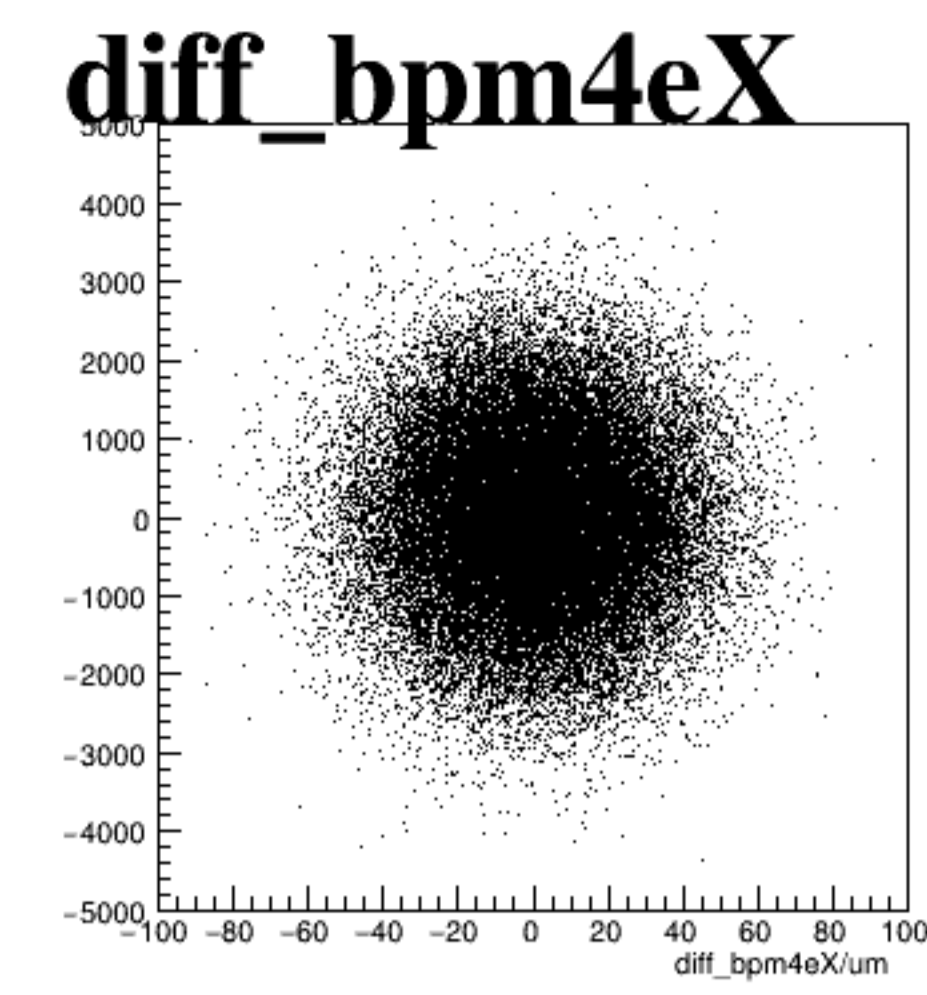
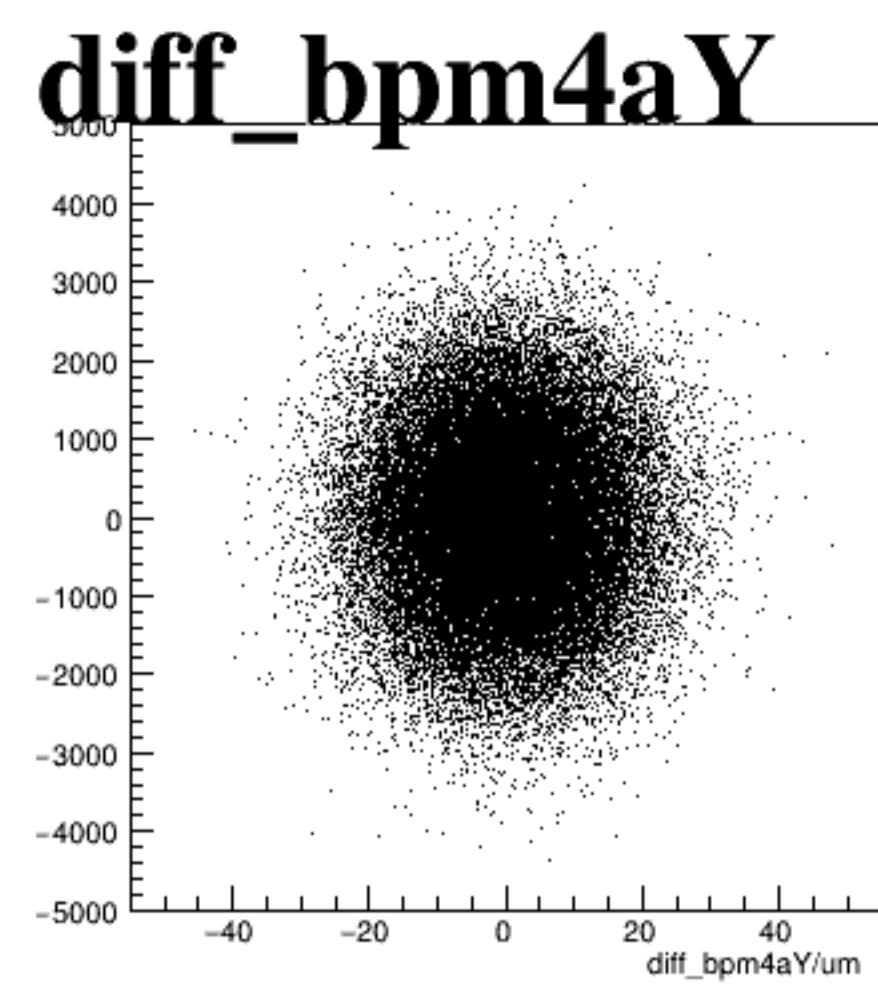
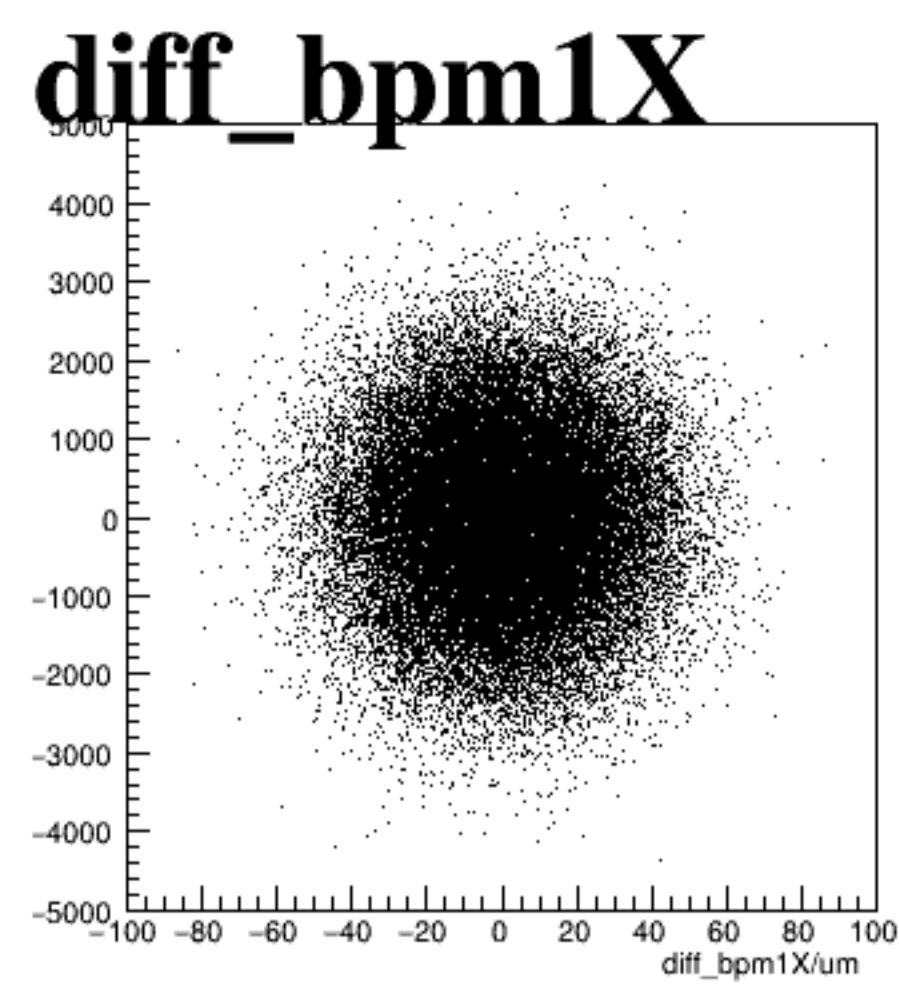
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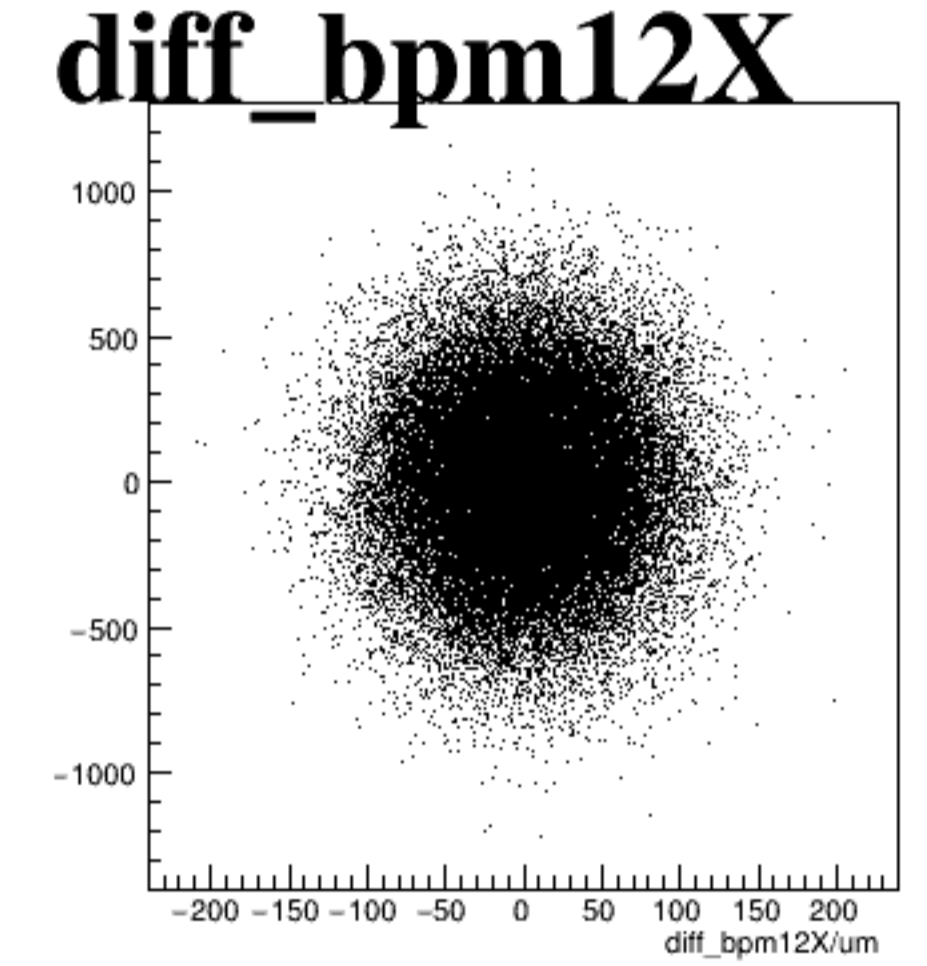
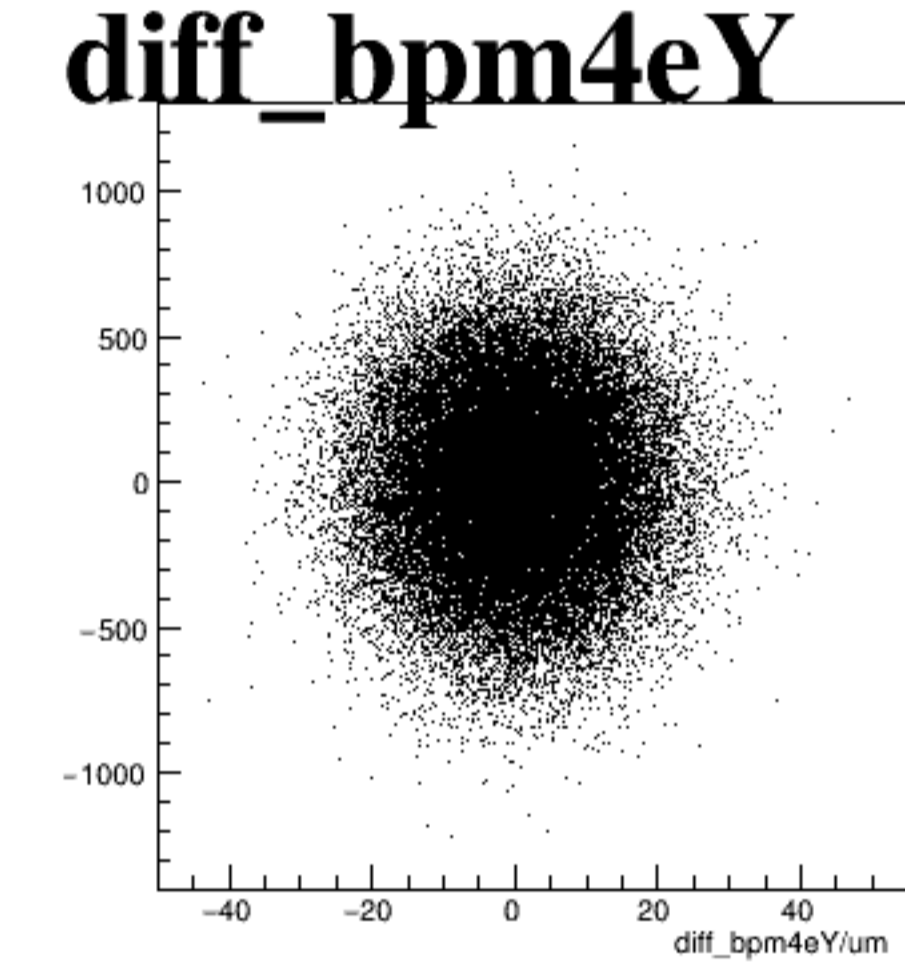
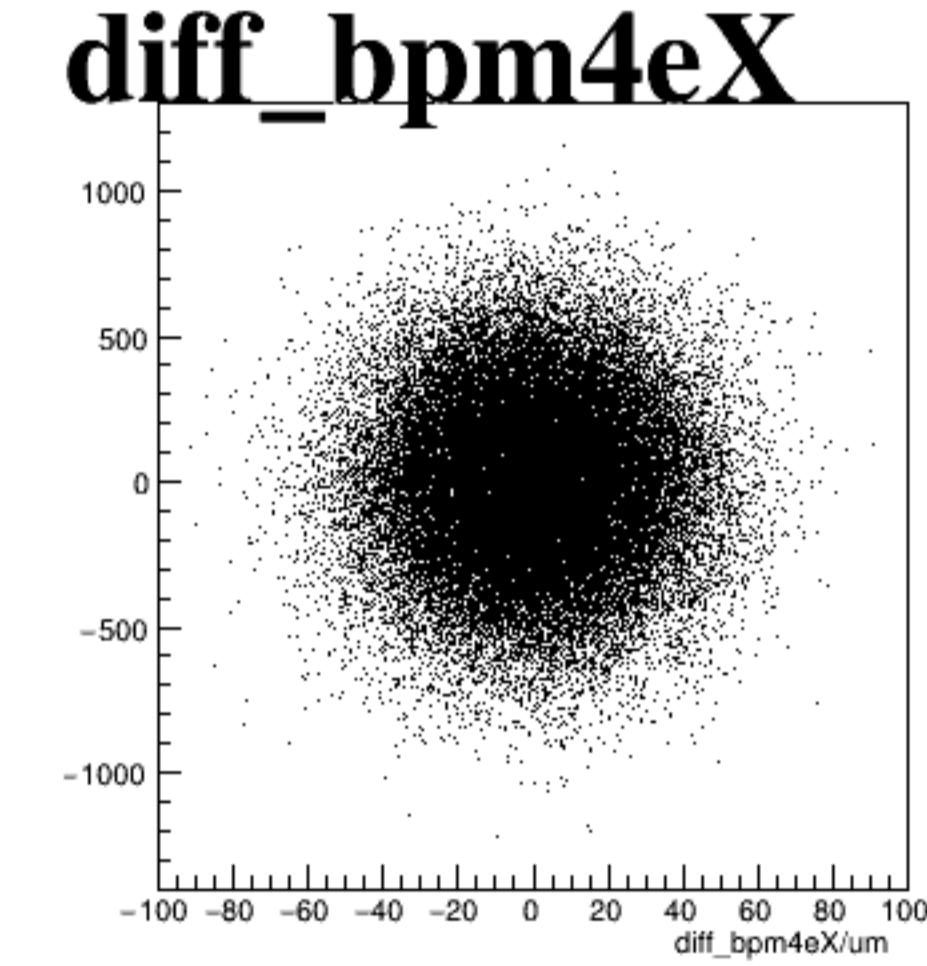
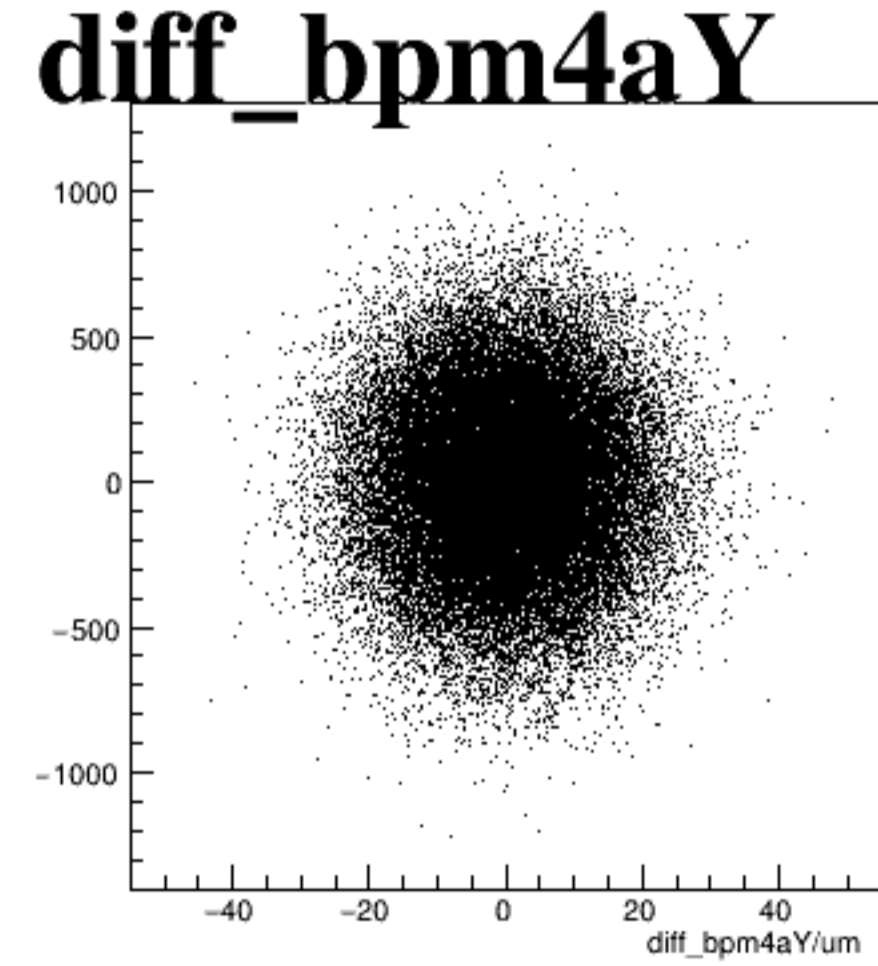
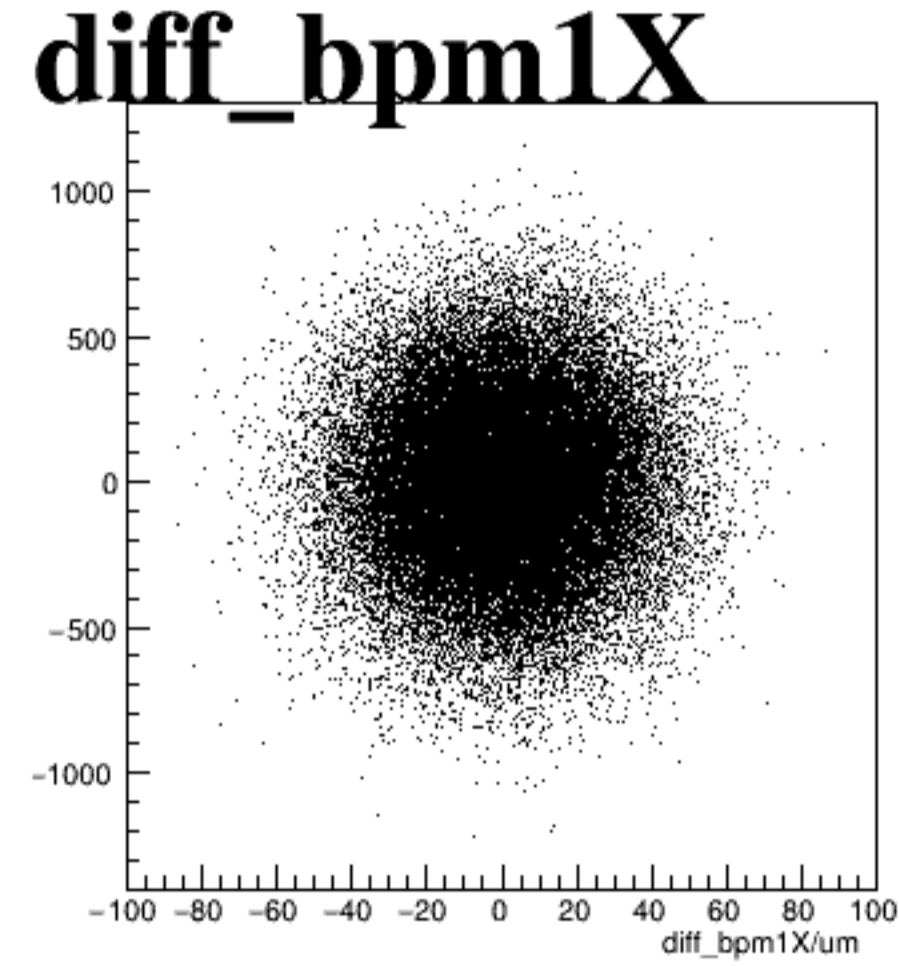
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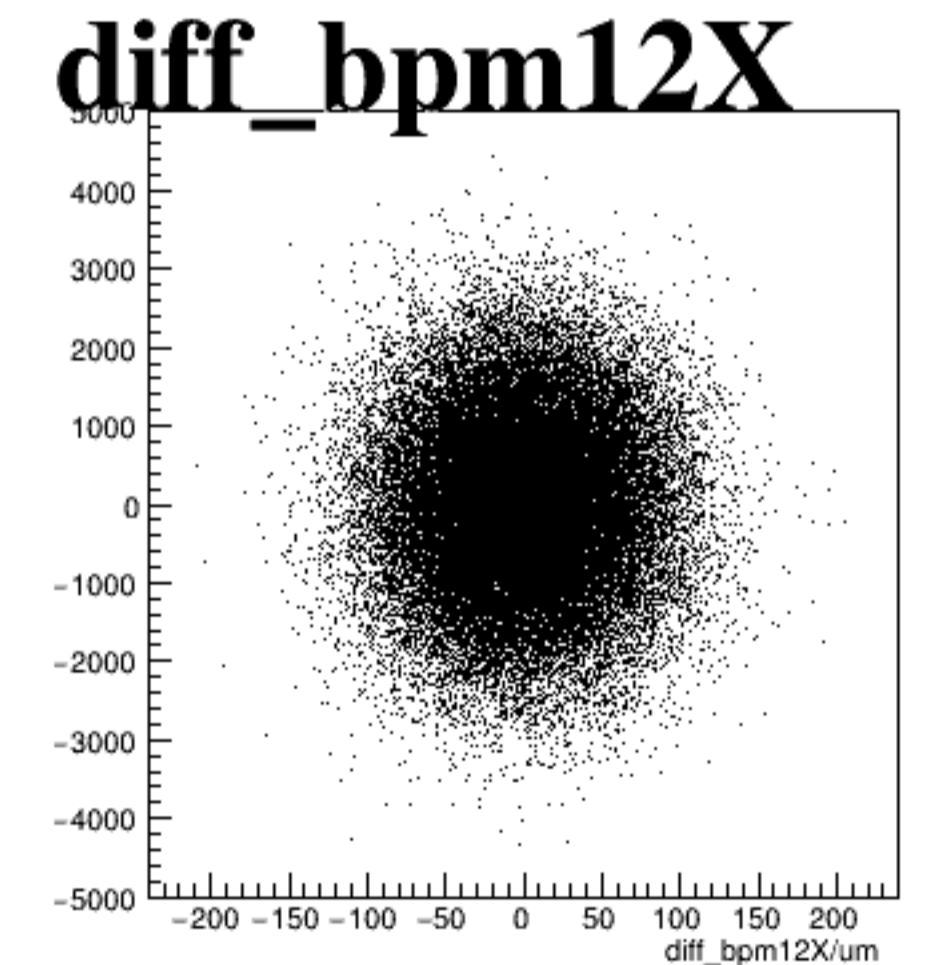
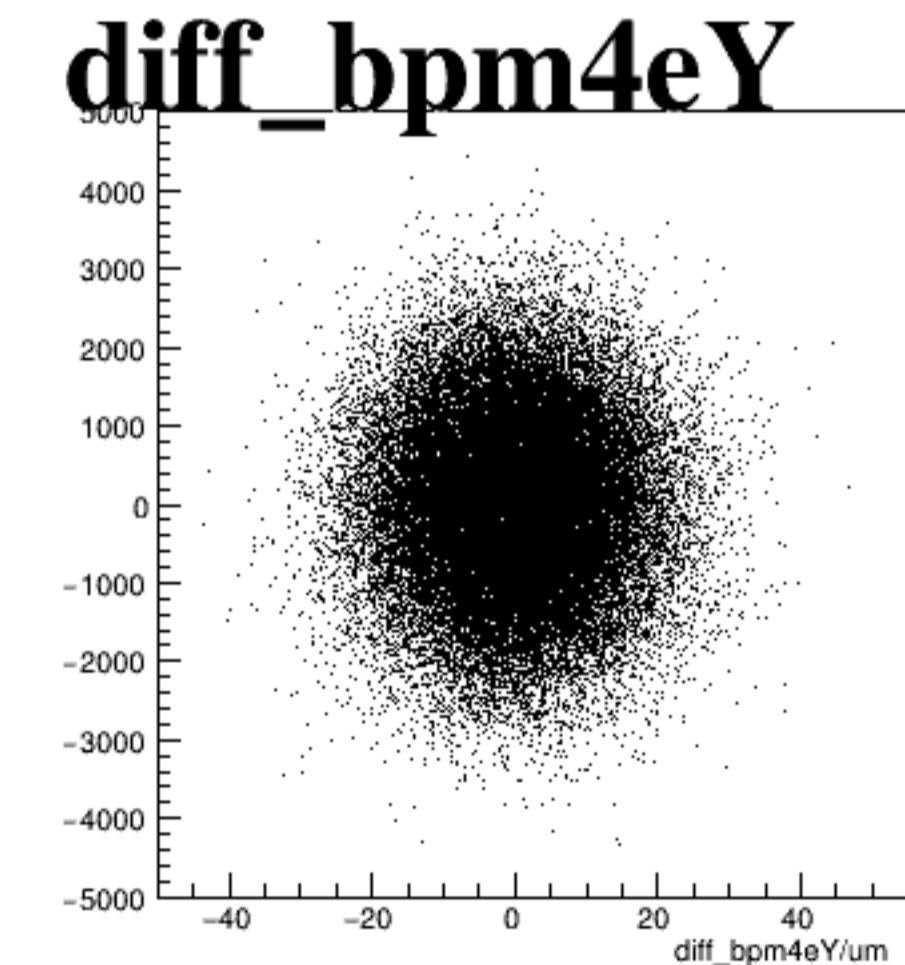
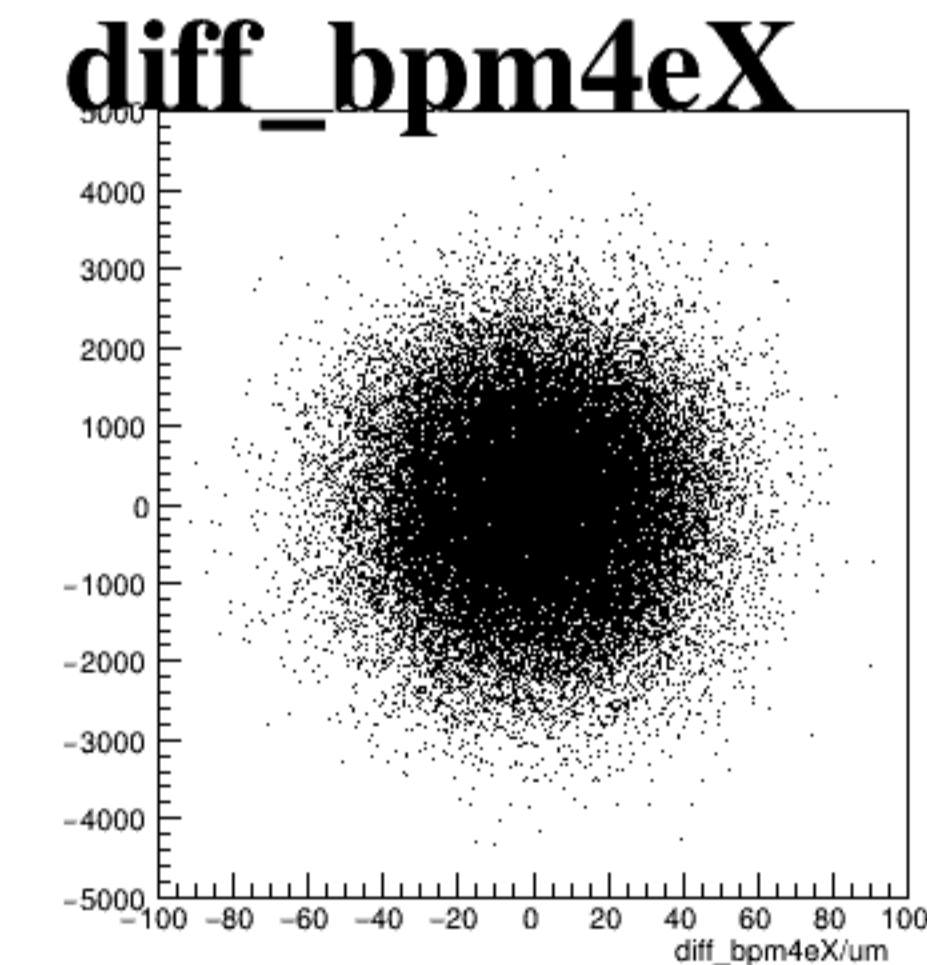
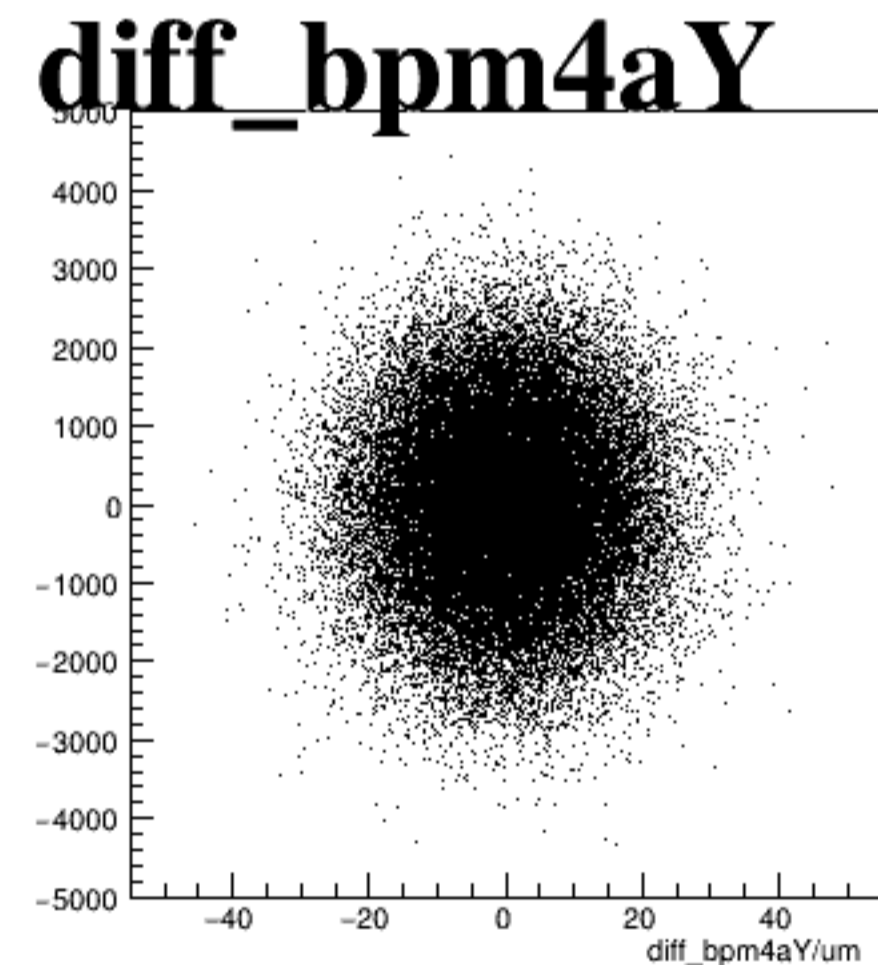
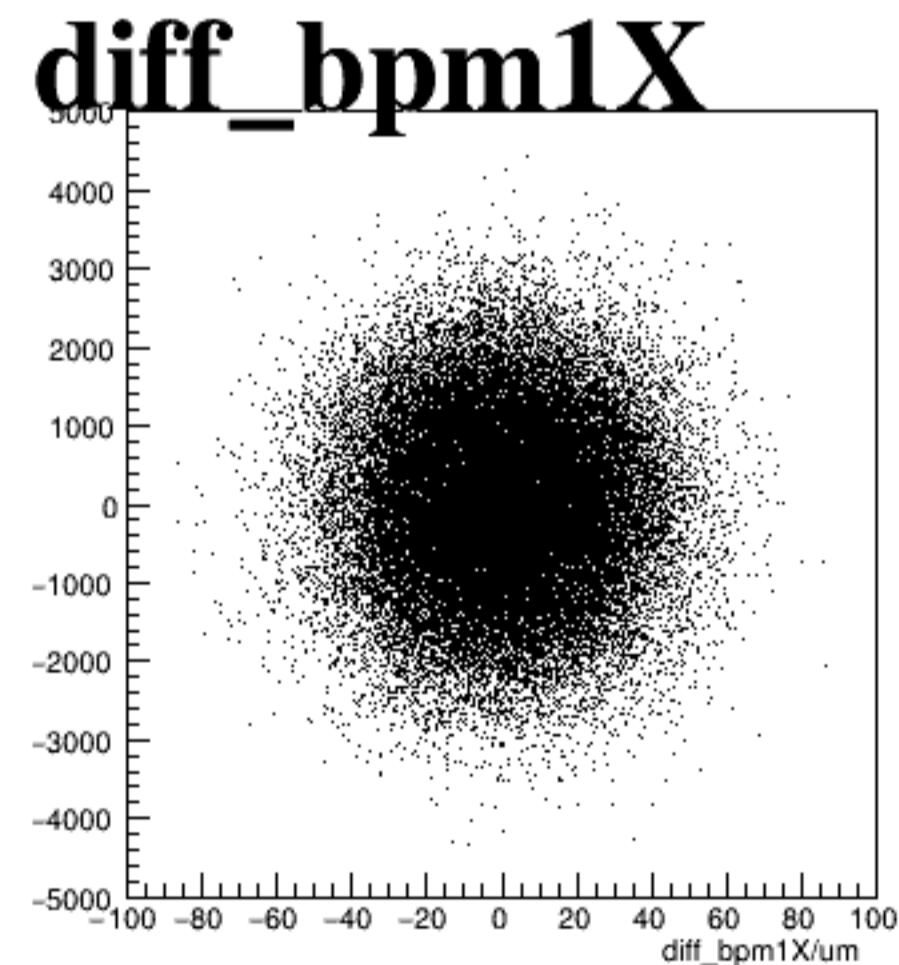
reg\_asym\_usl



reg\_asym\_dsl



reg\_asym\_usr



reg\_asym\_dsr

