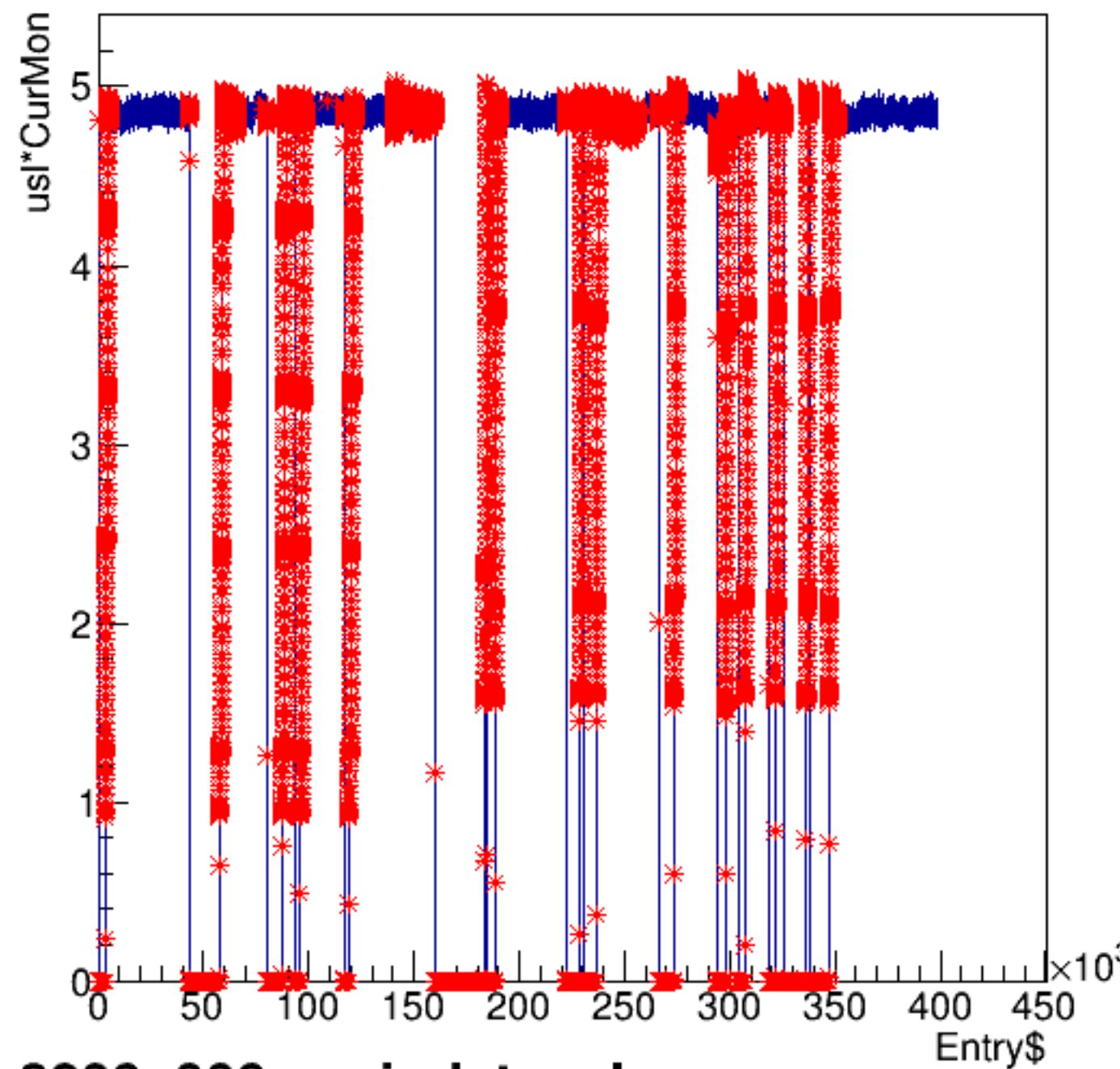
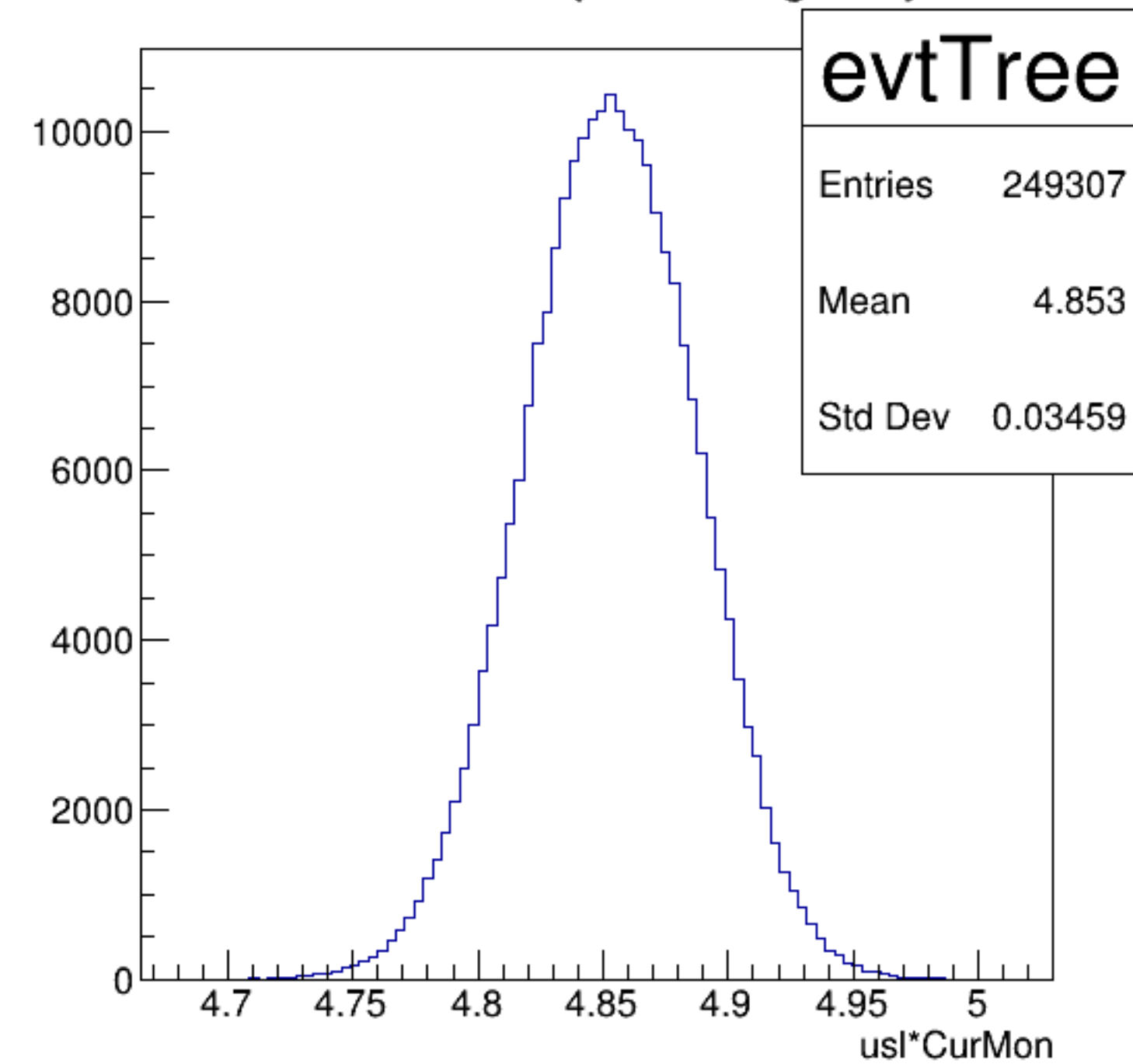


usl*CurMon:Entry\$

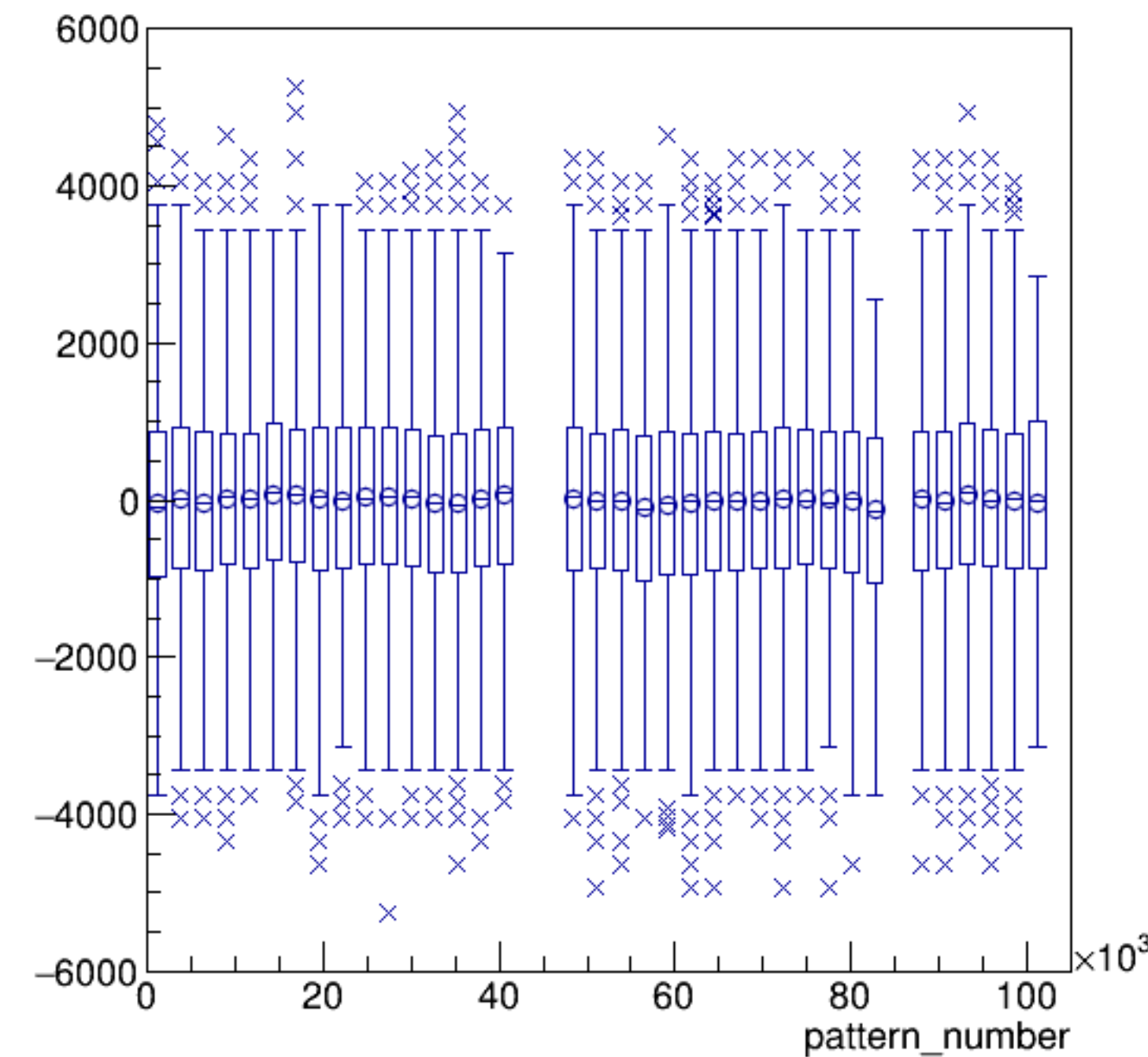


run8299_000_maindet_usl.png

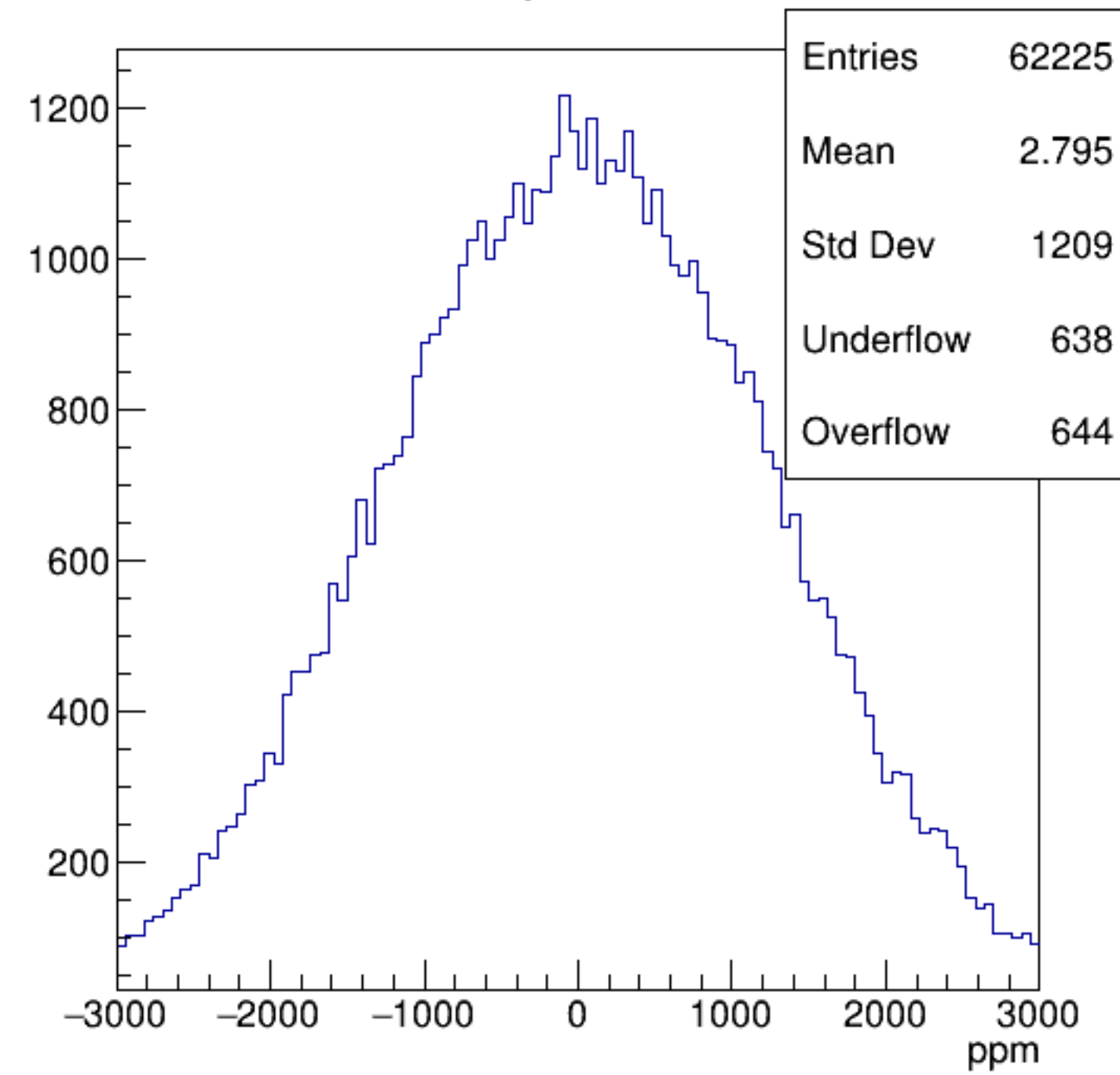
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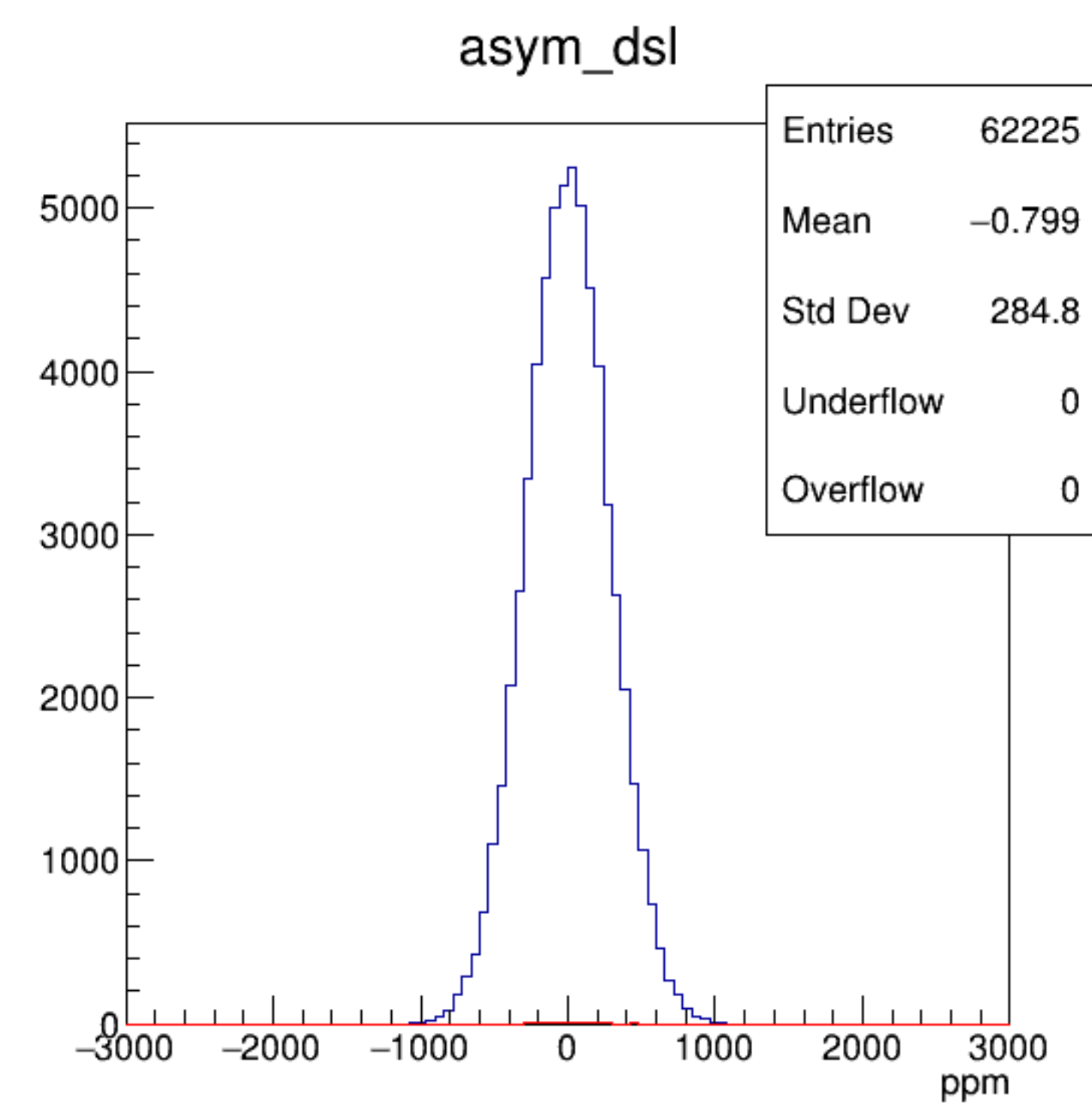
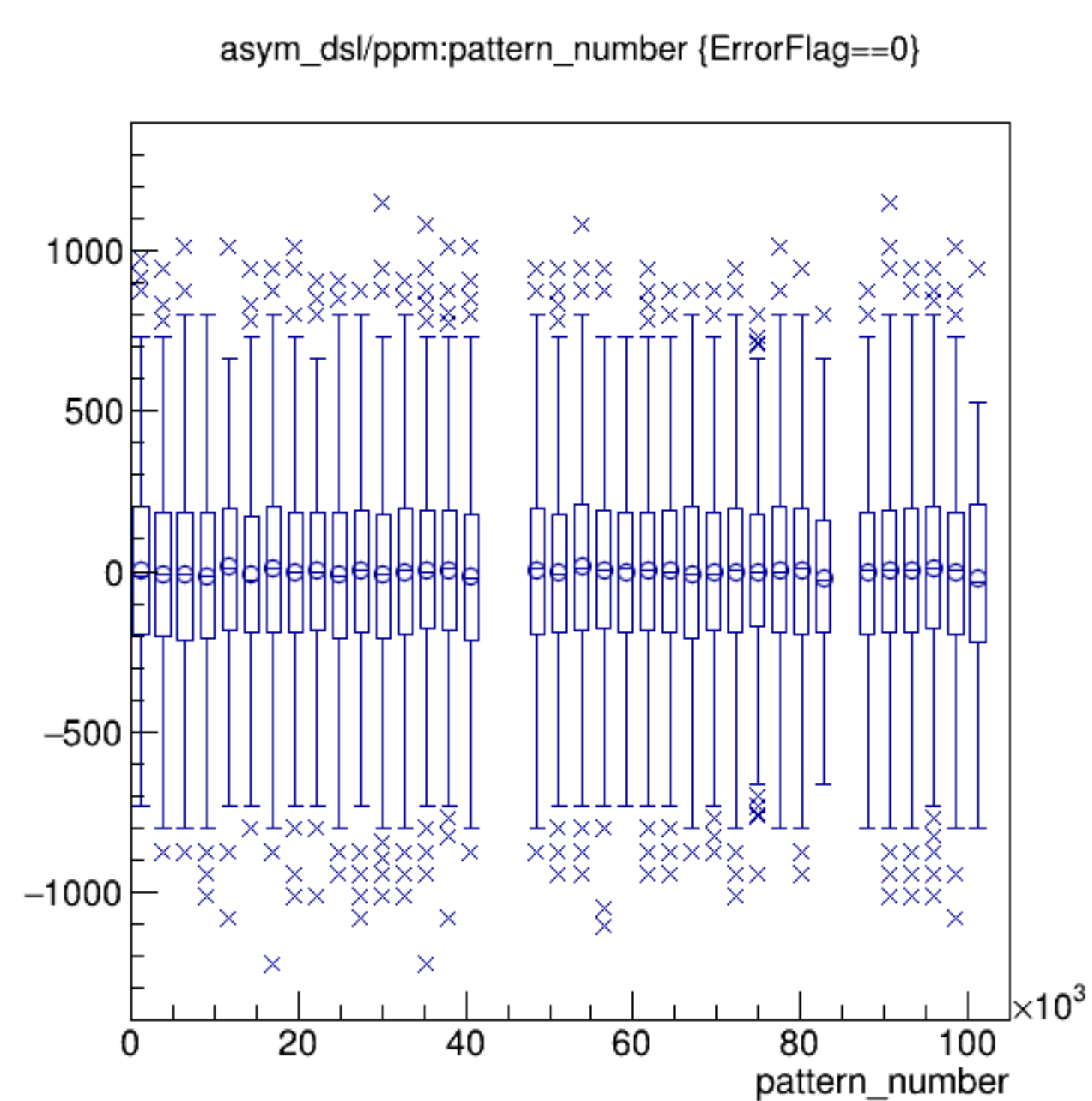
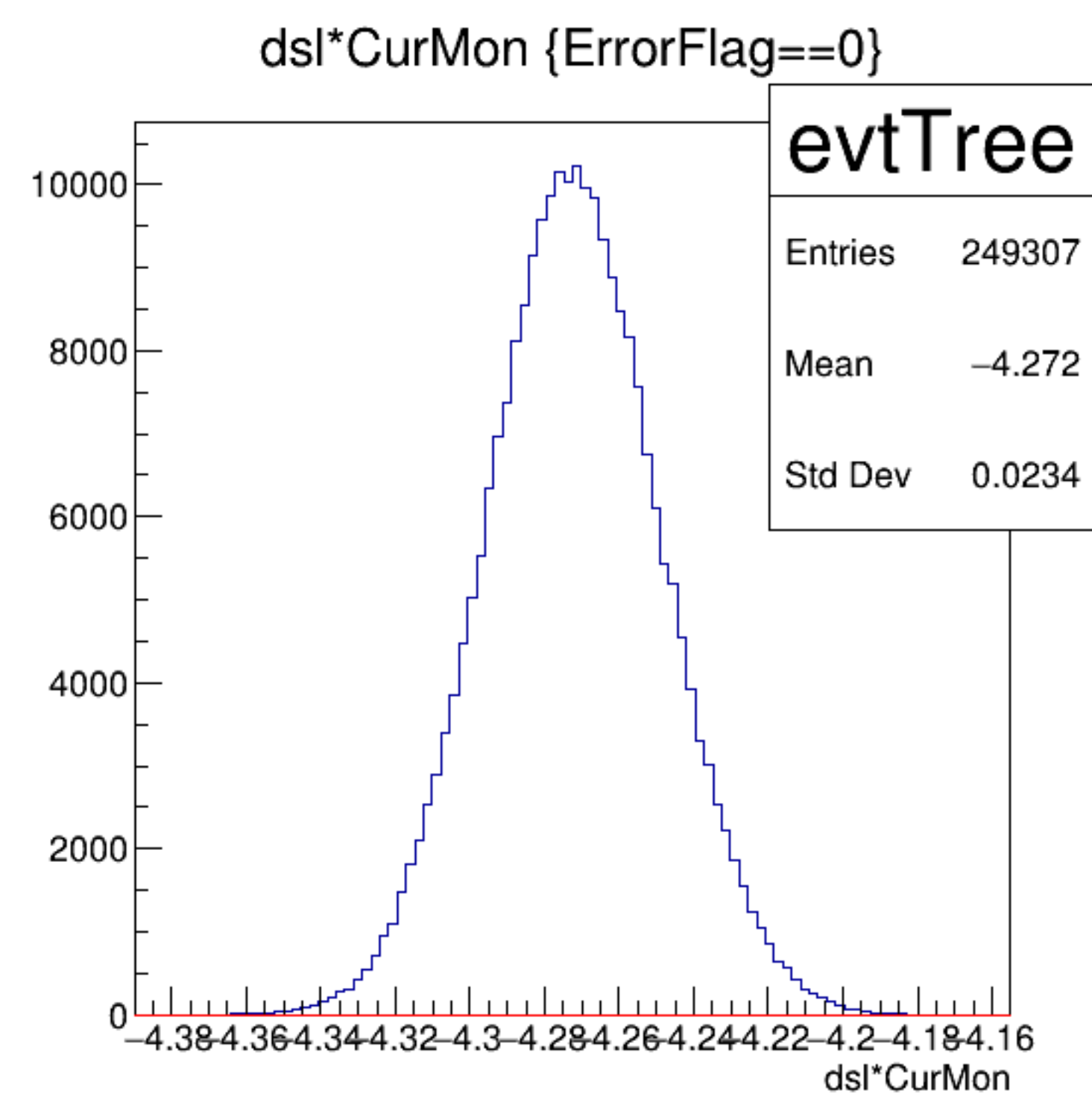
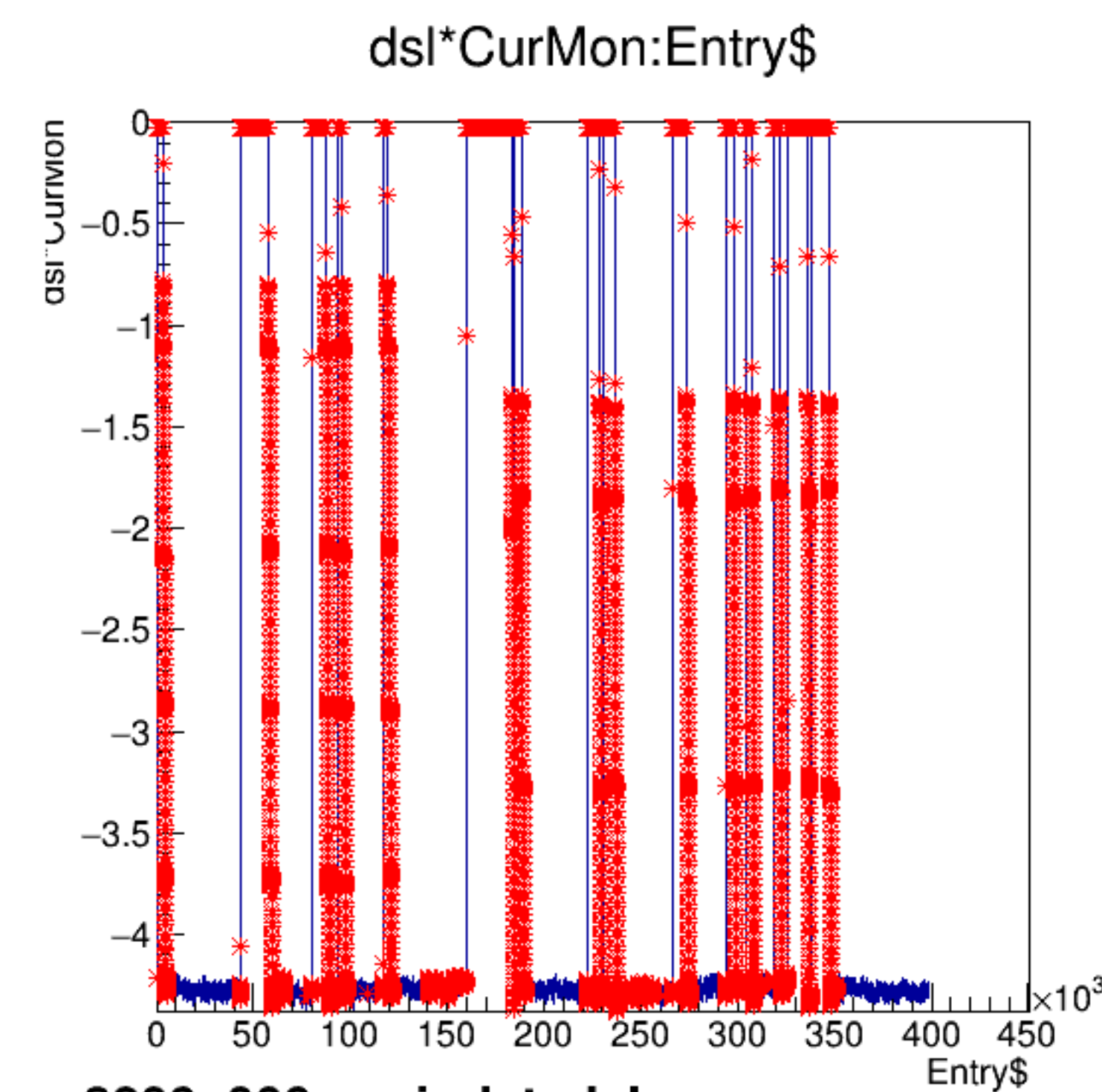


asym_usl/ppm:pattern_number {ErrorFlag==0}

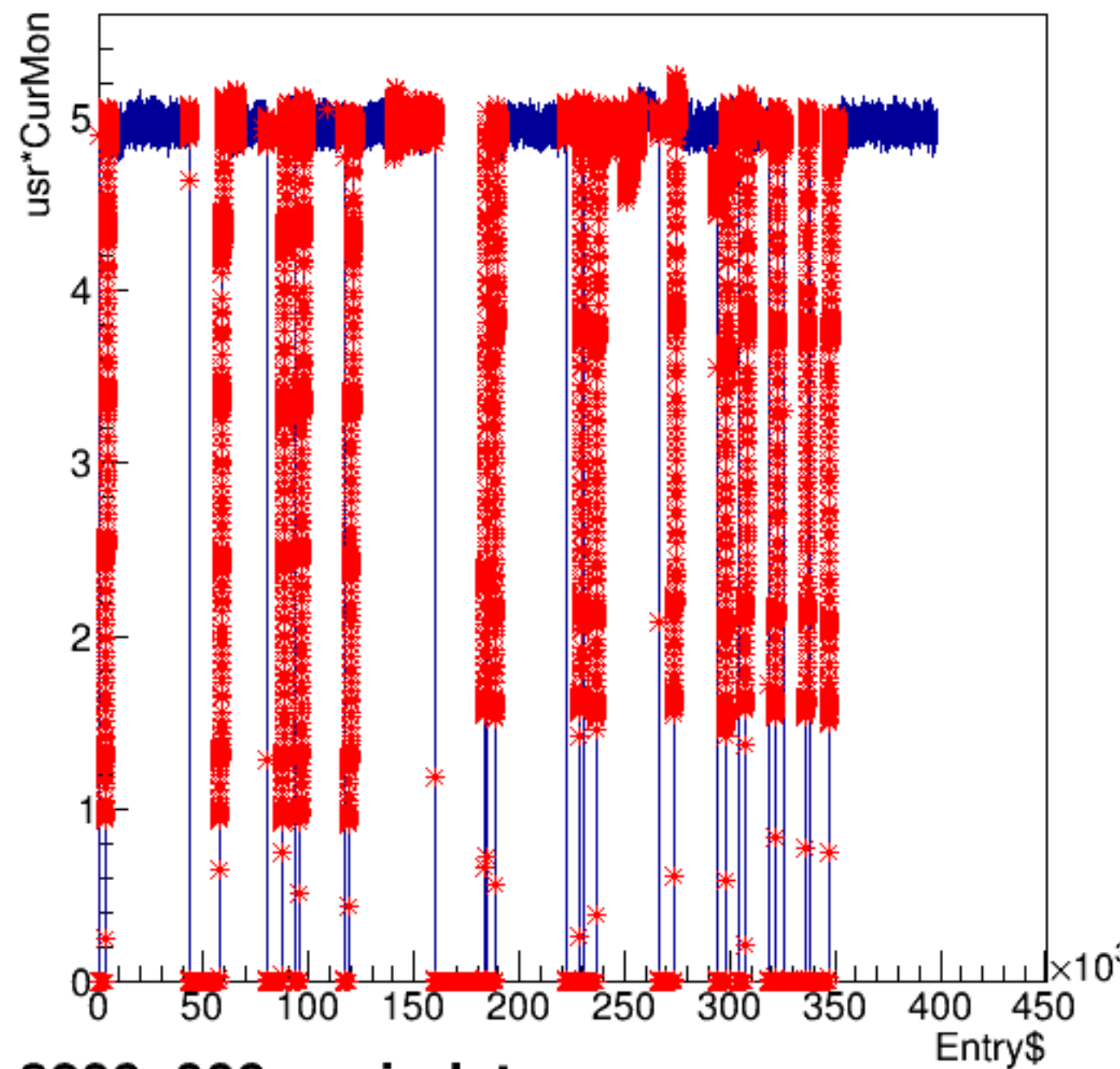


asym_usl



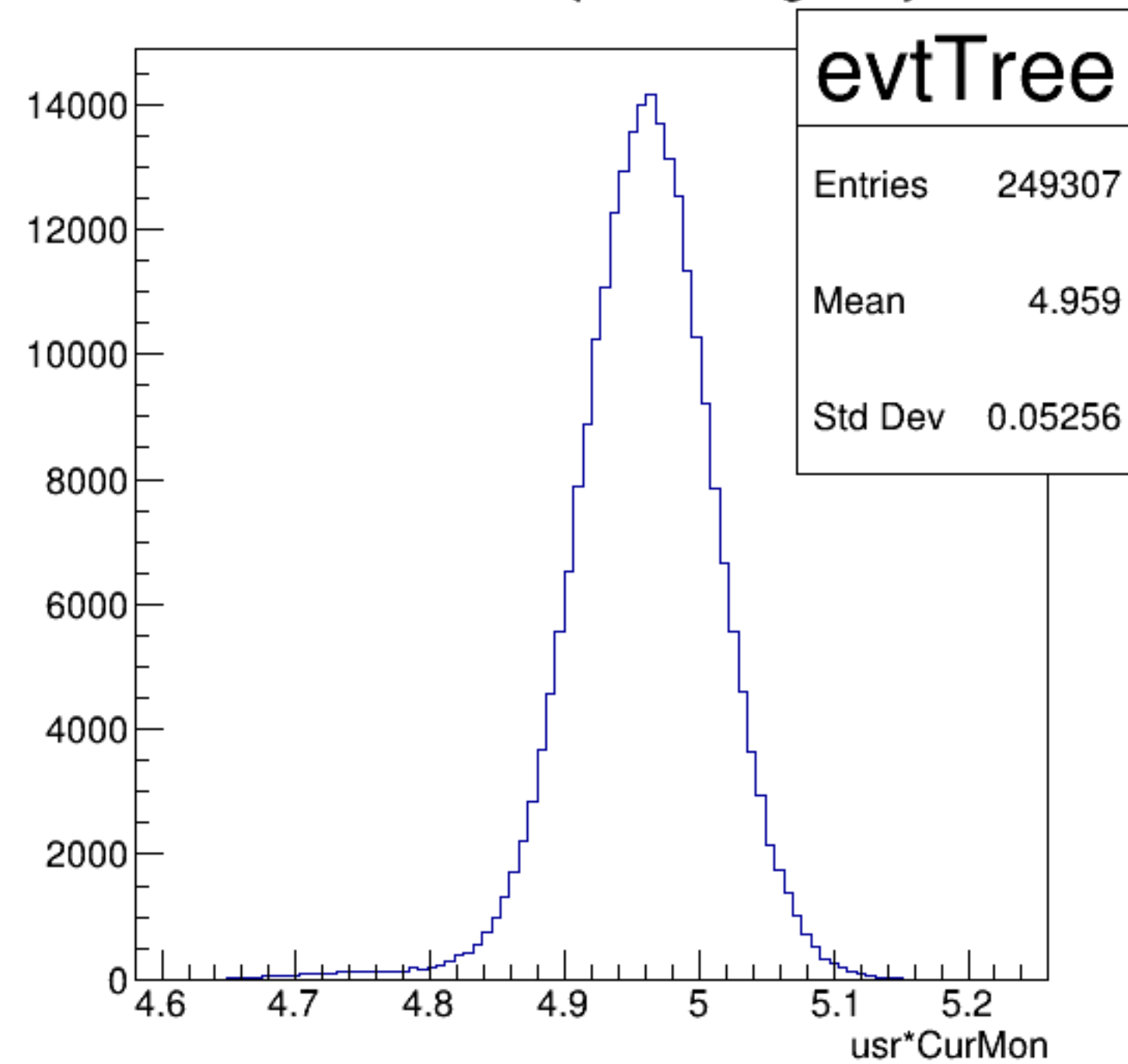


usr*CurMon:Entry\$

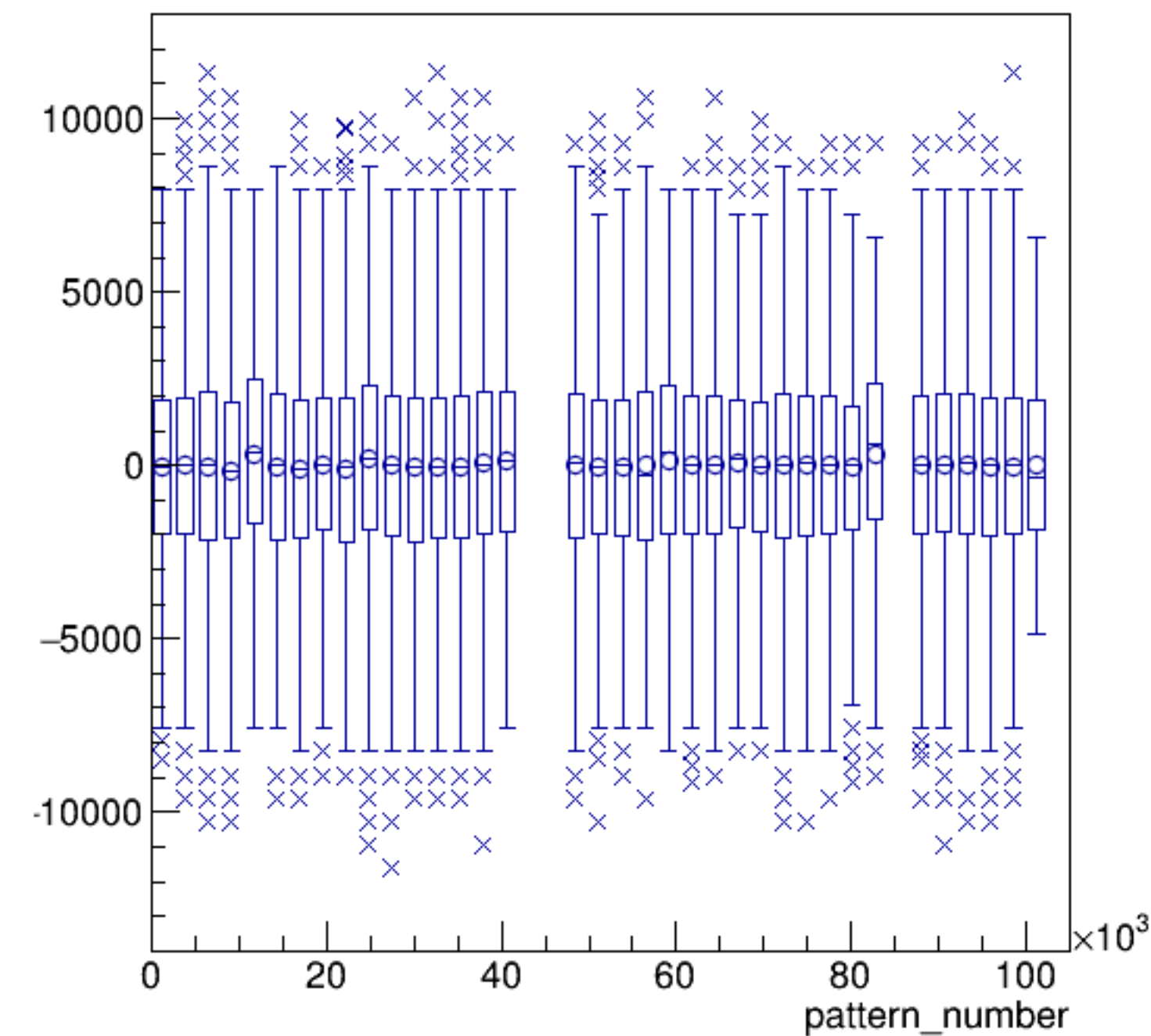


run8299_000_maindet_usr.png

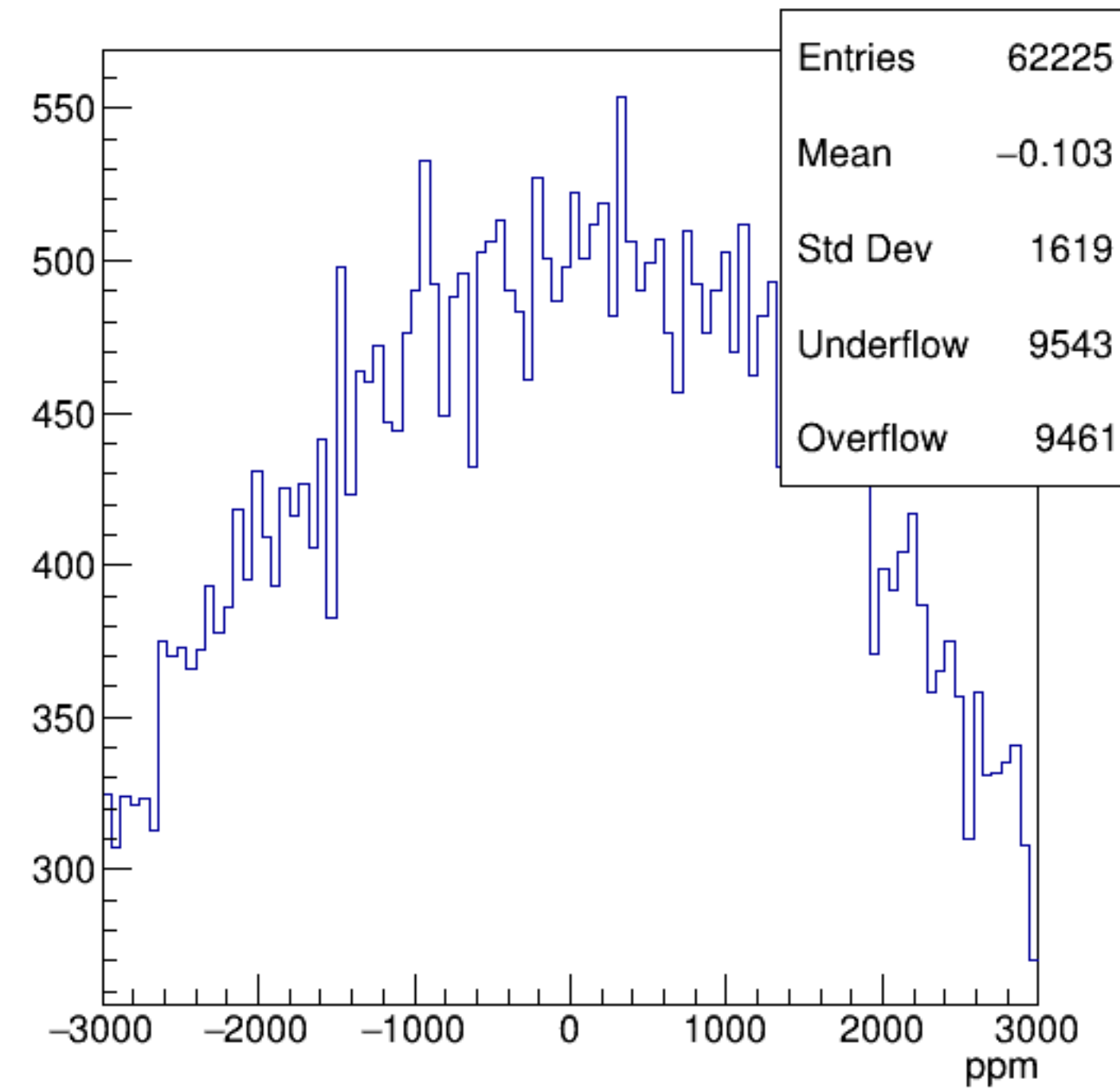
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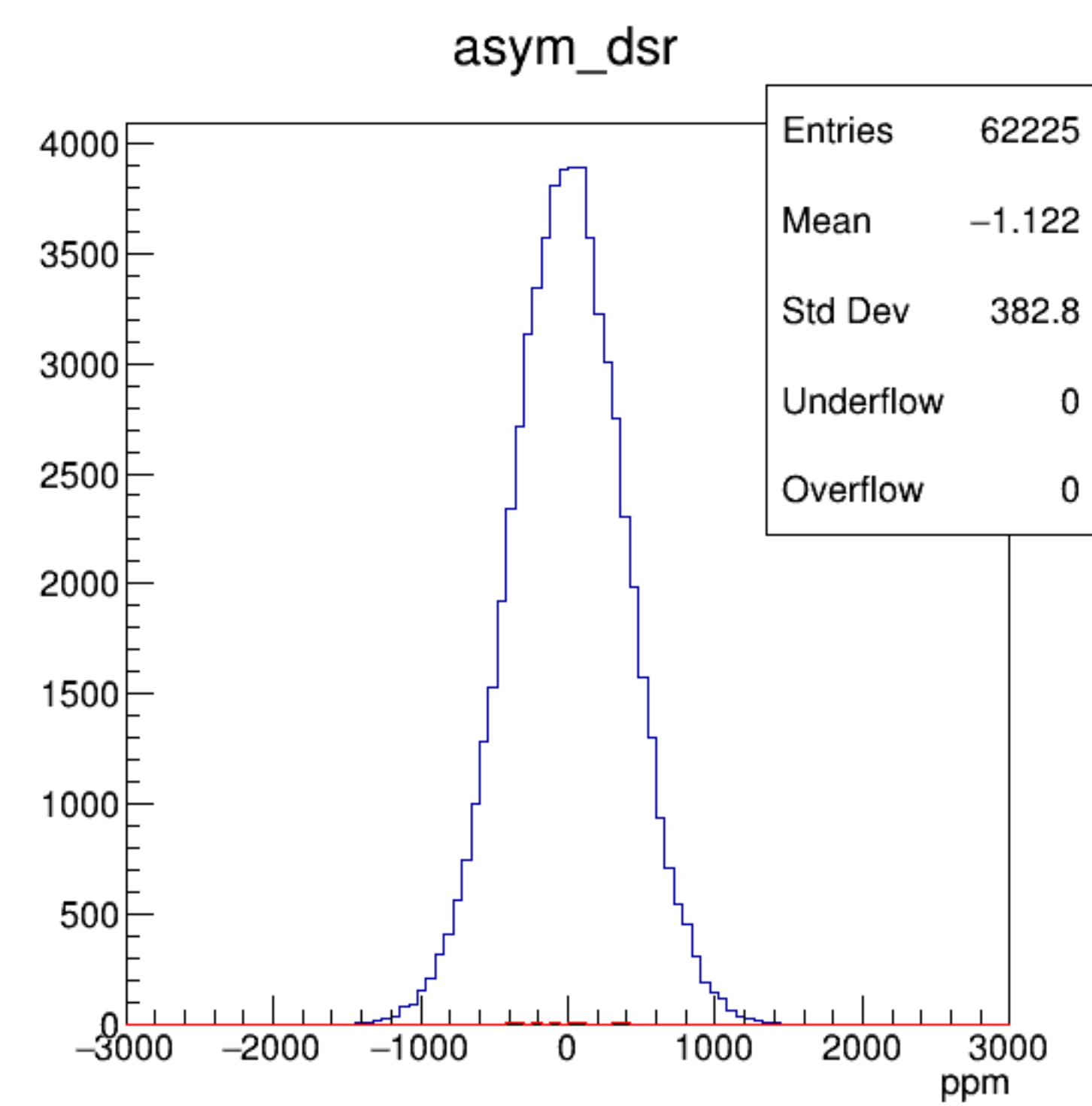
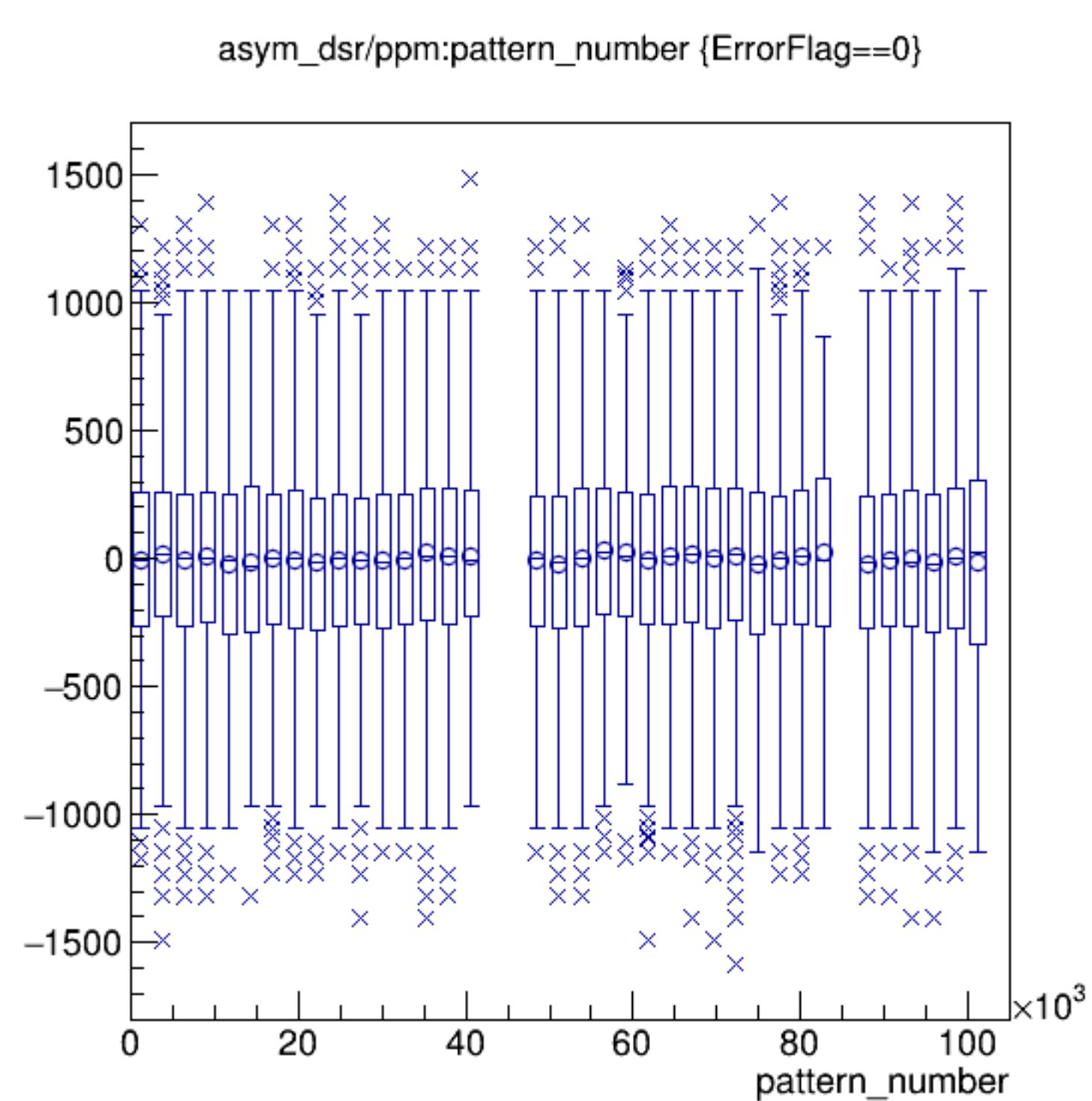
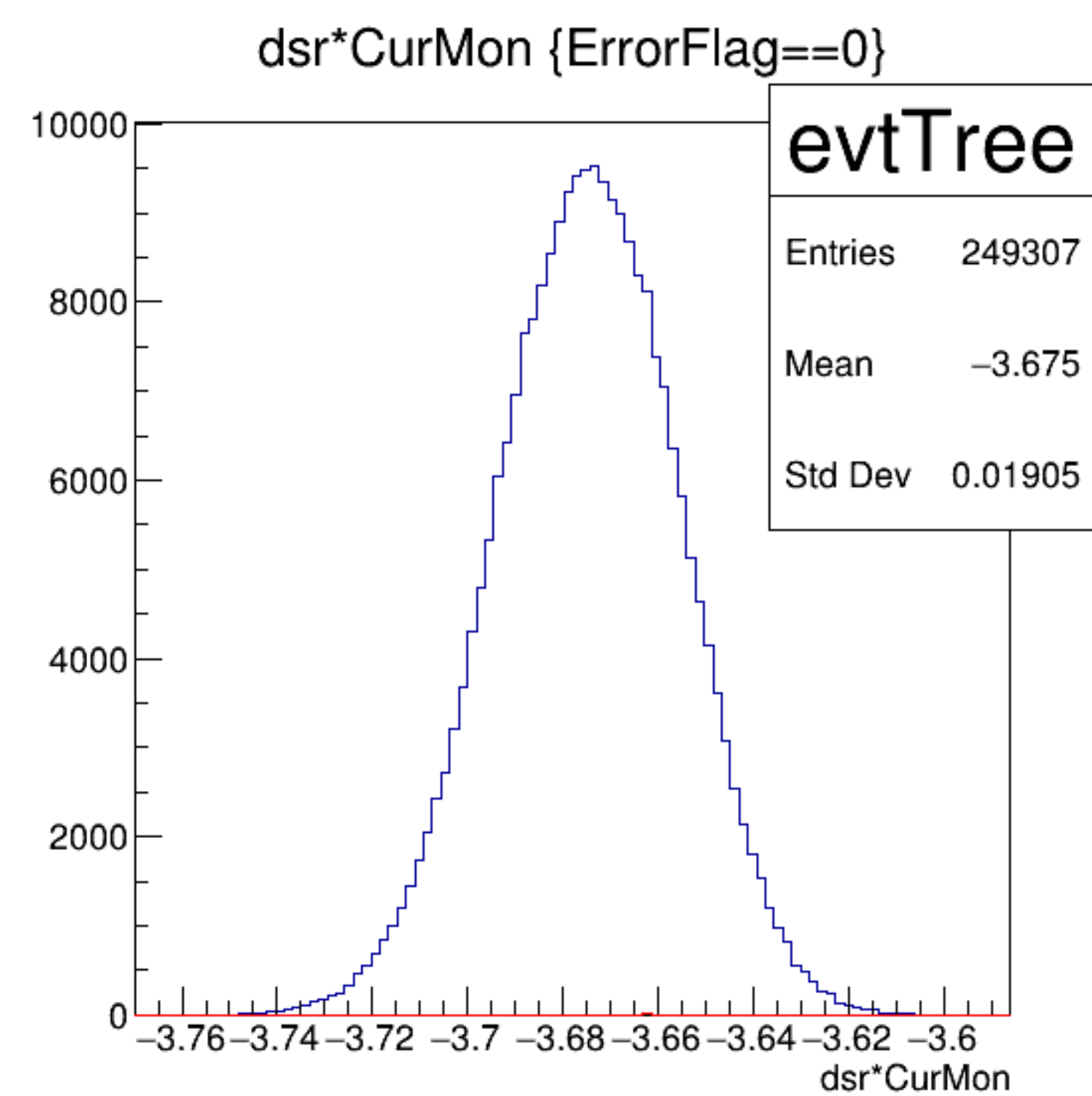
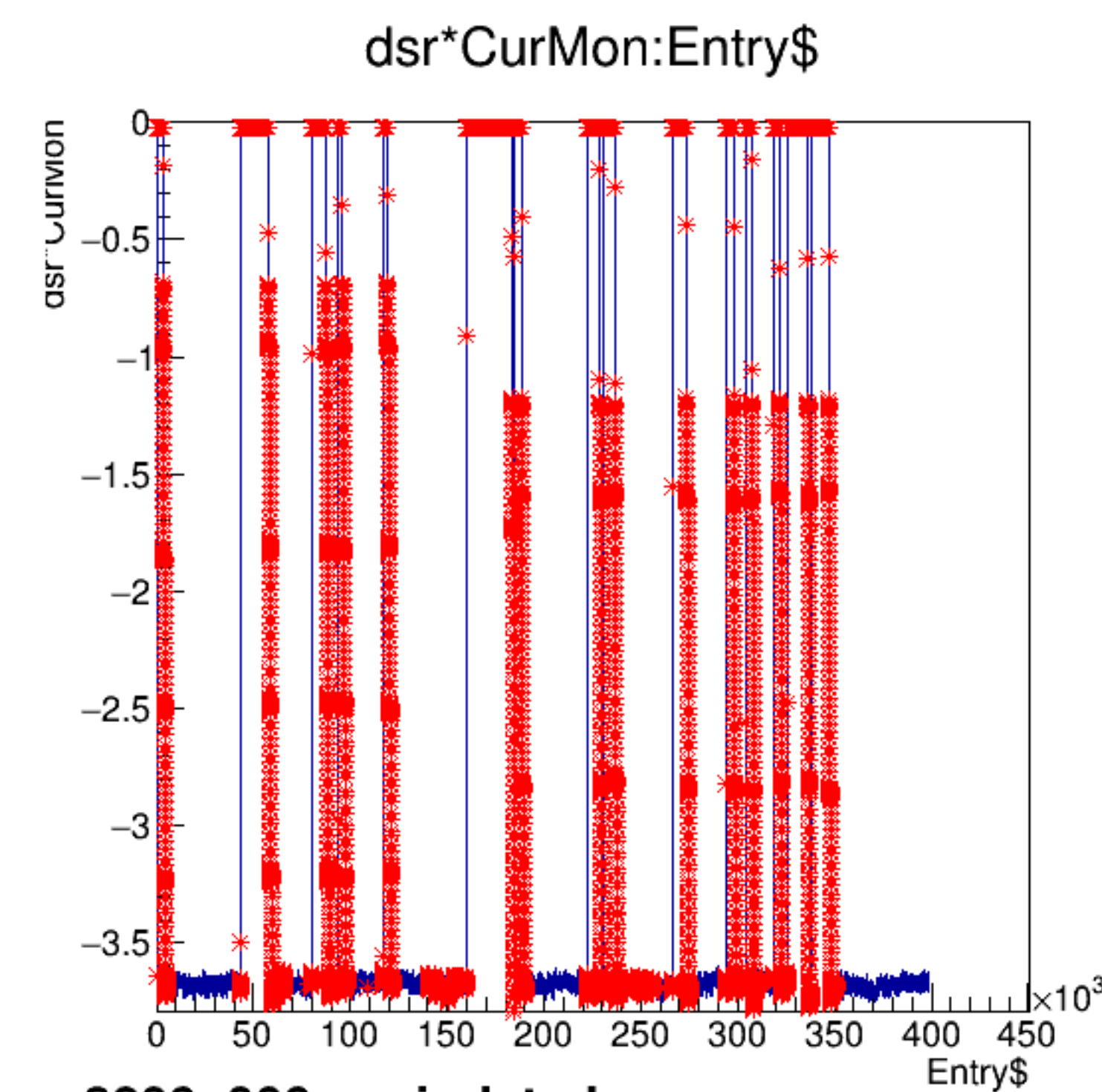


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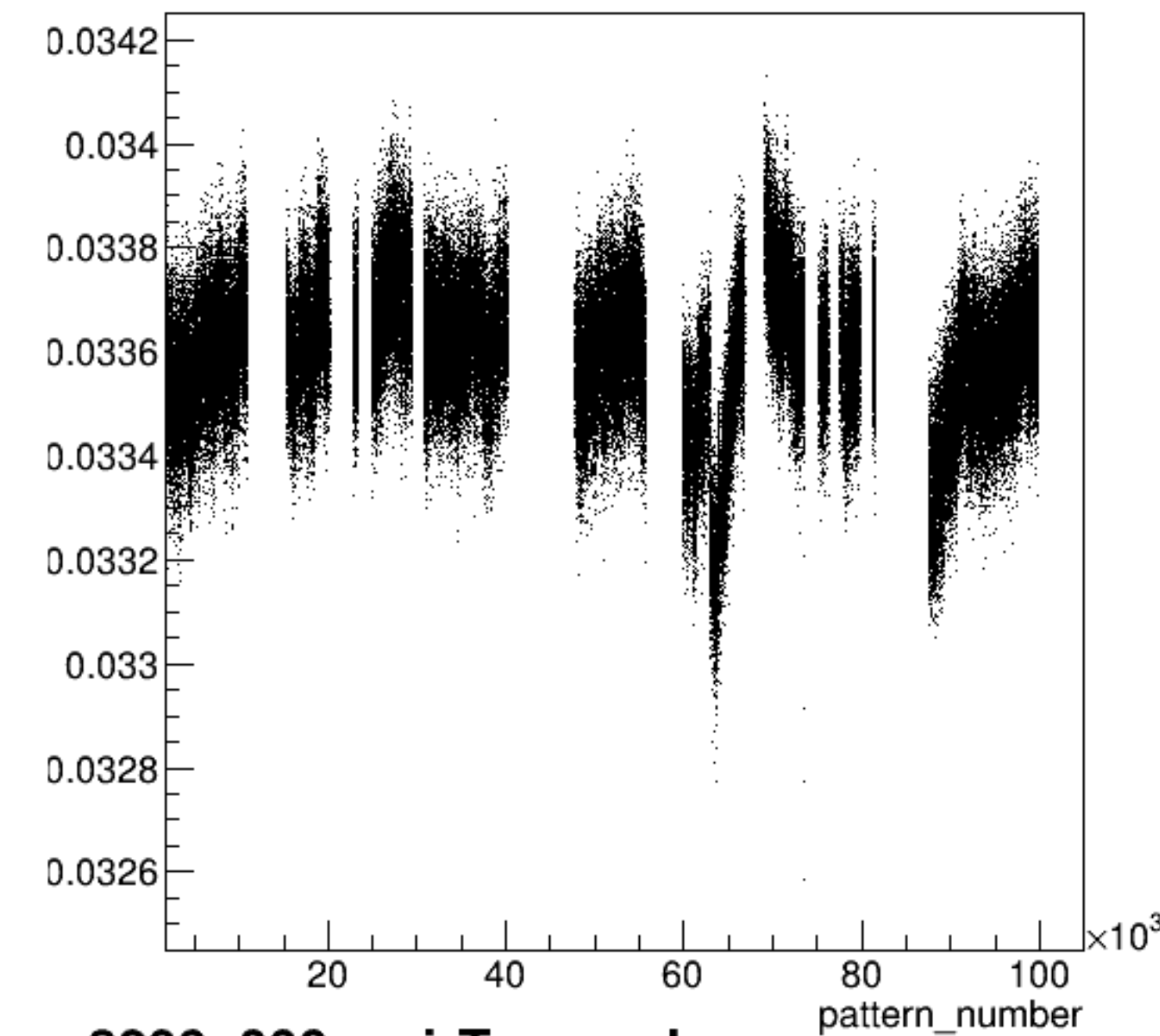


asym_usr

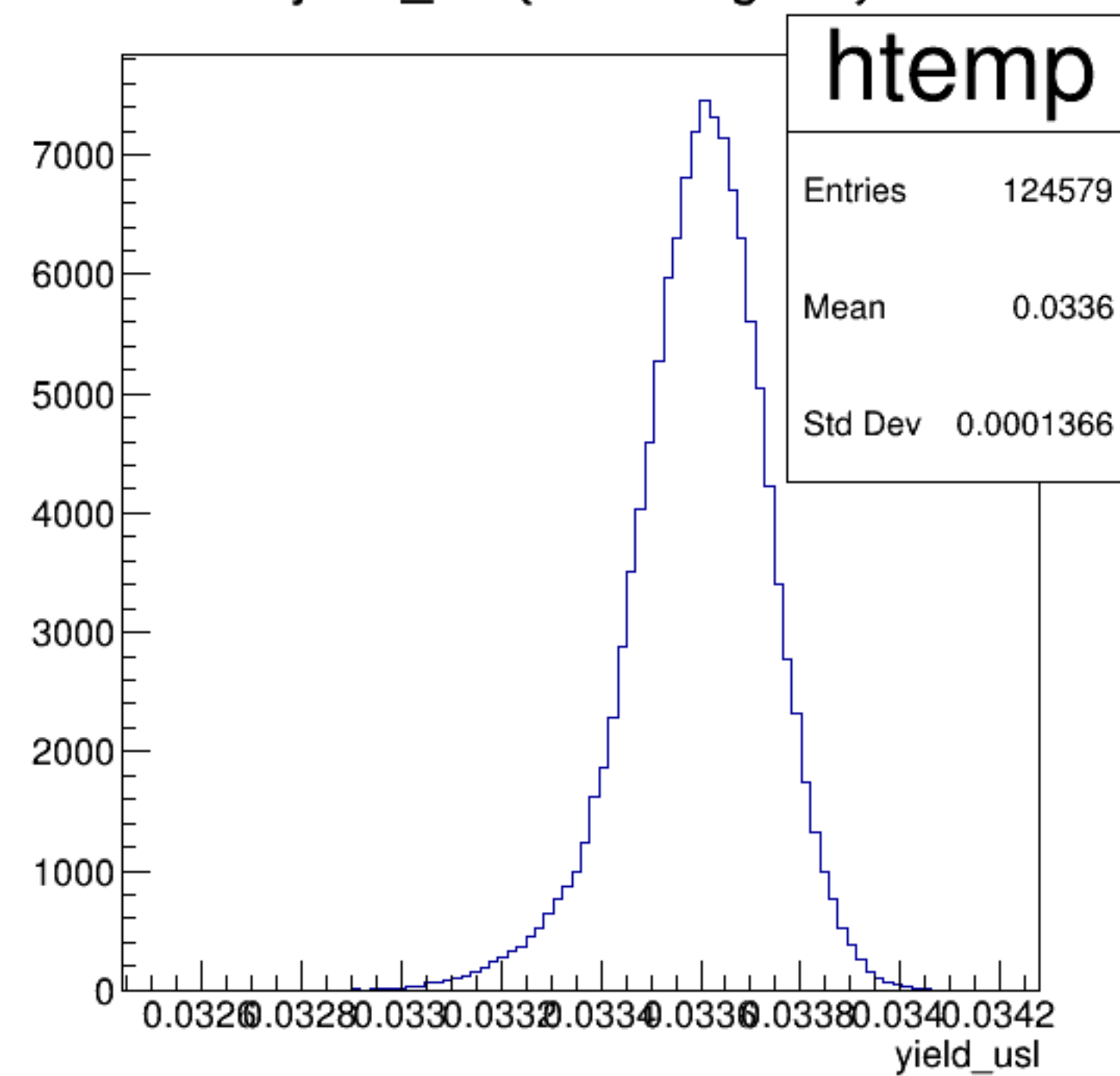




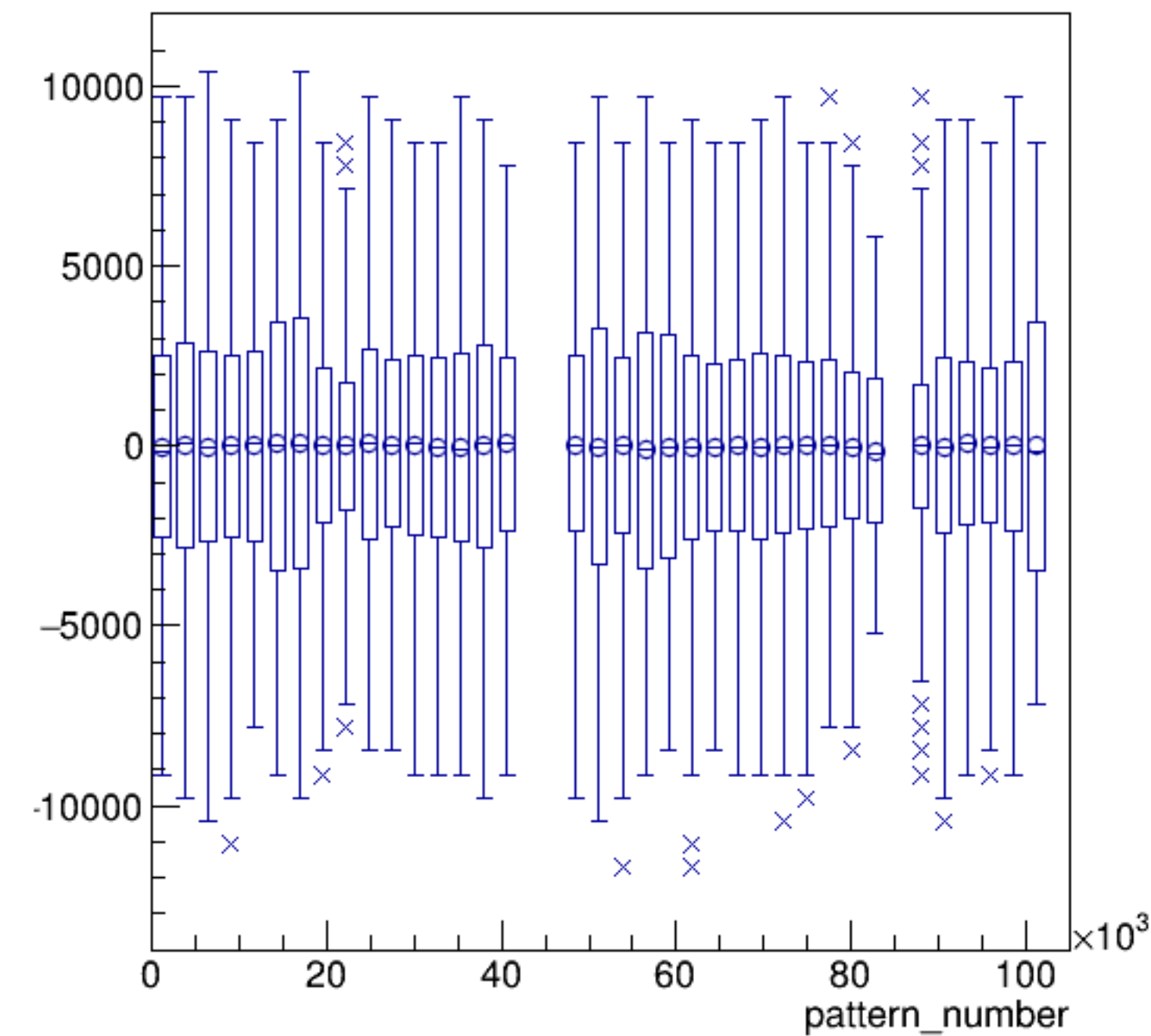
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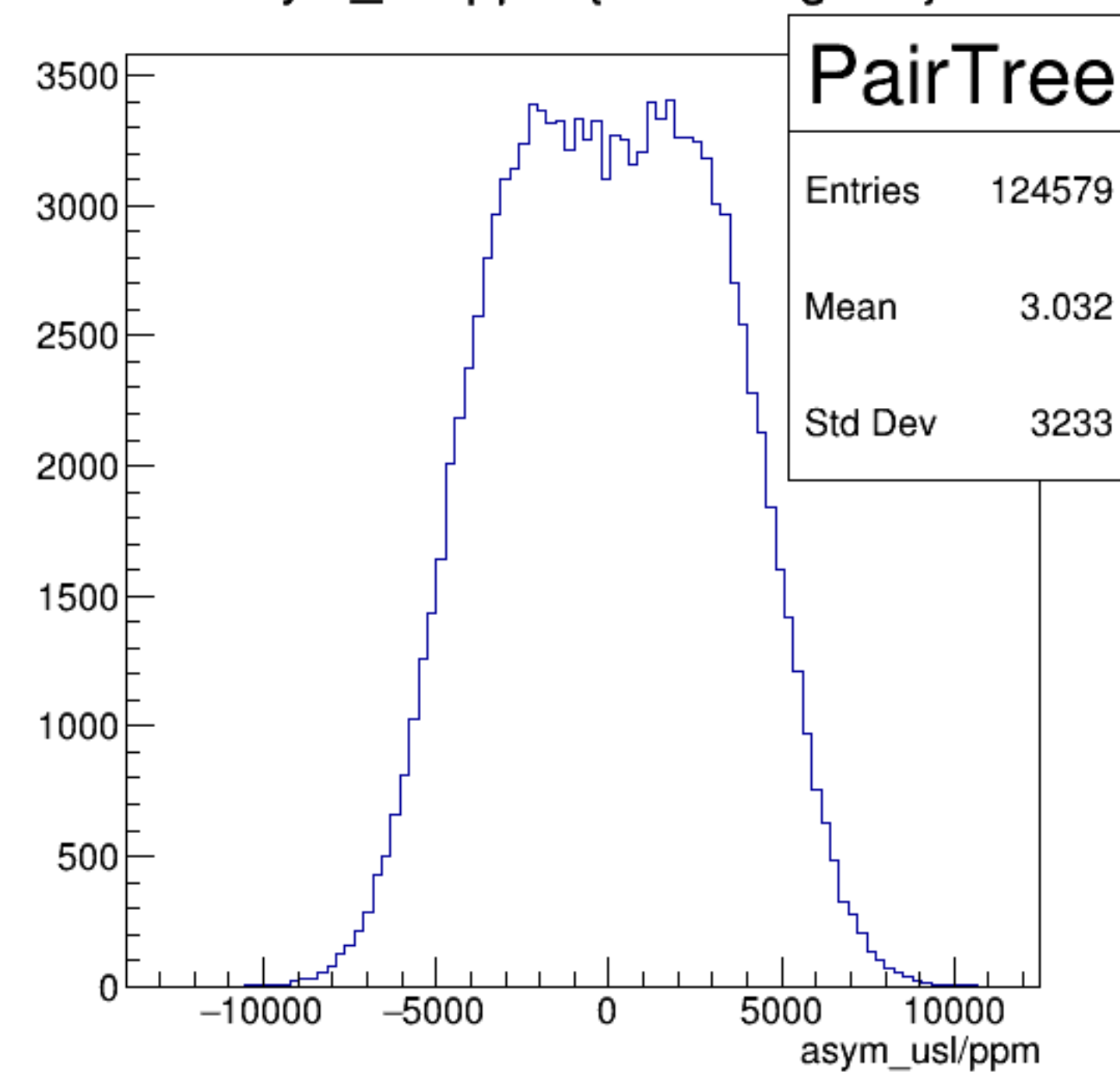
yield_usl {ErrorFlag==0}



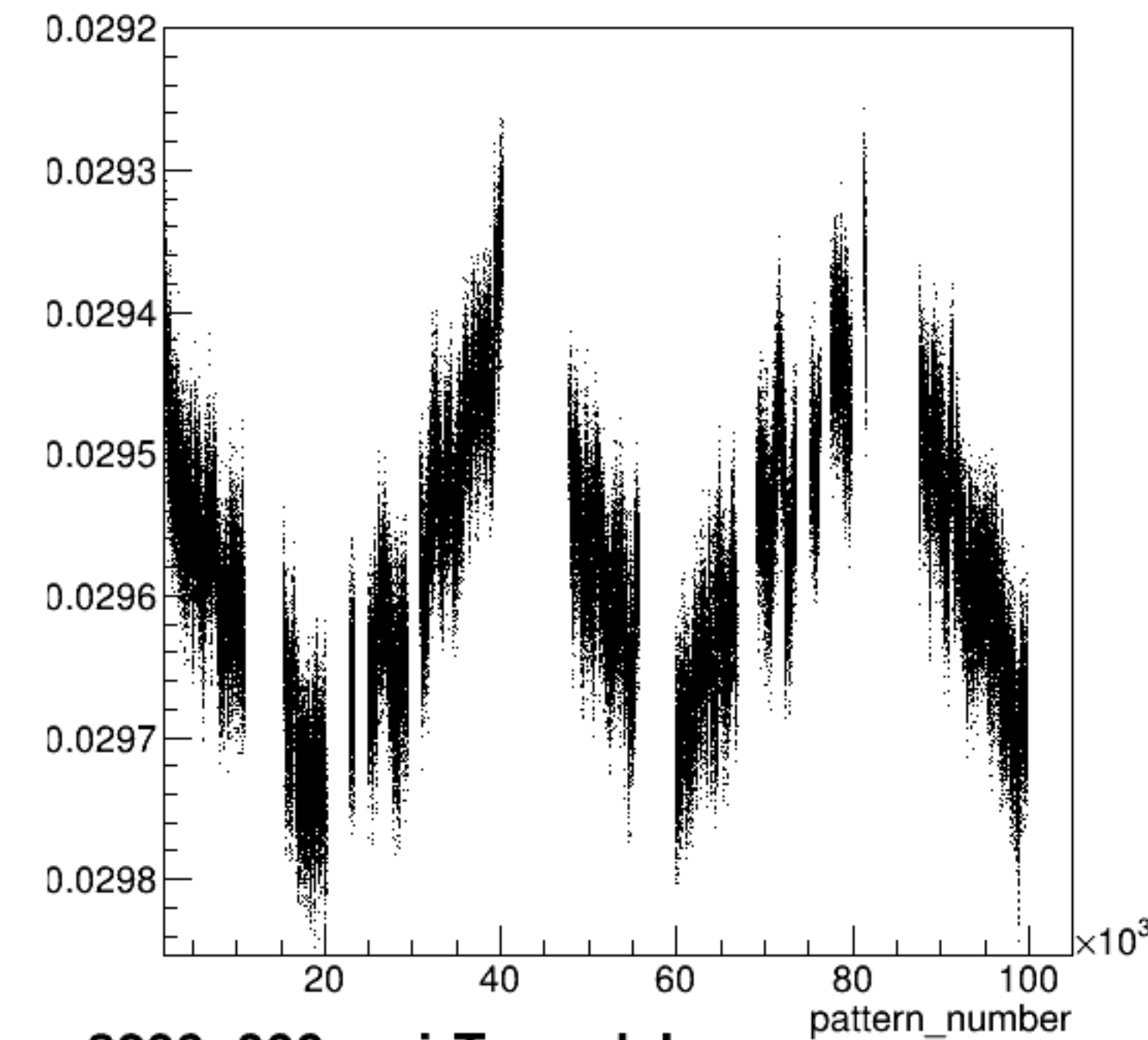
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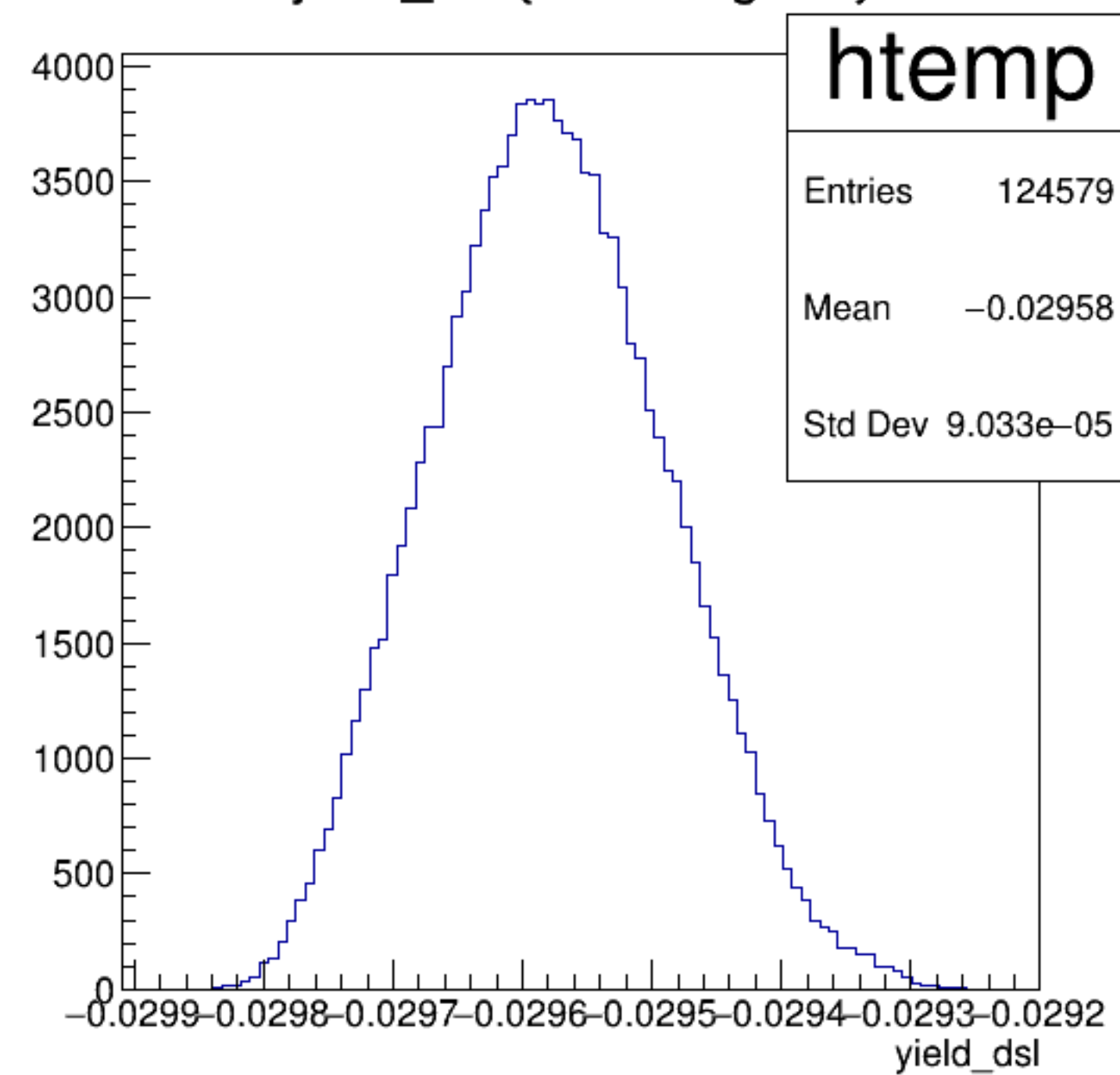


yield_dsl:pattern_number {ErrorFlag==0}

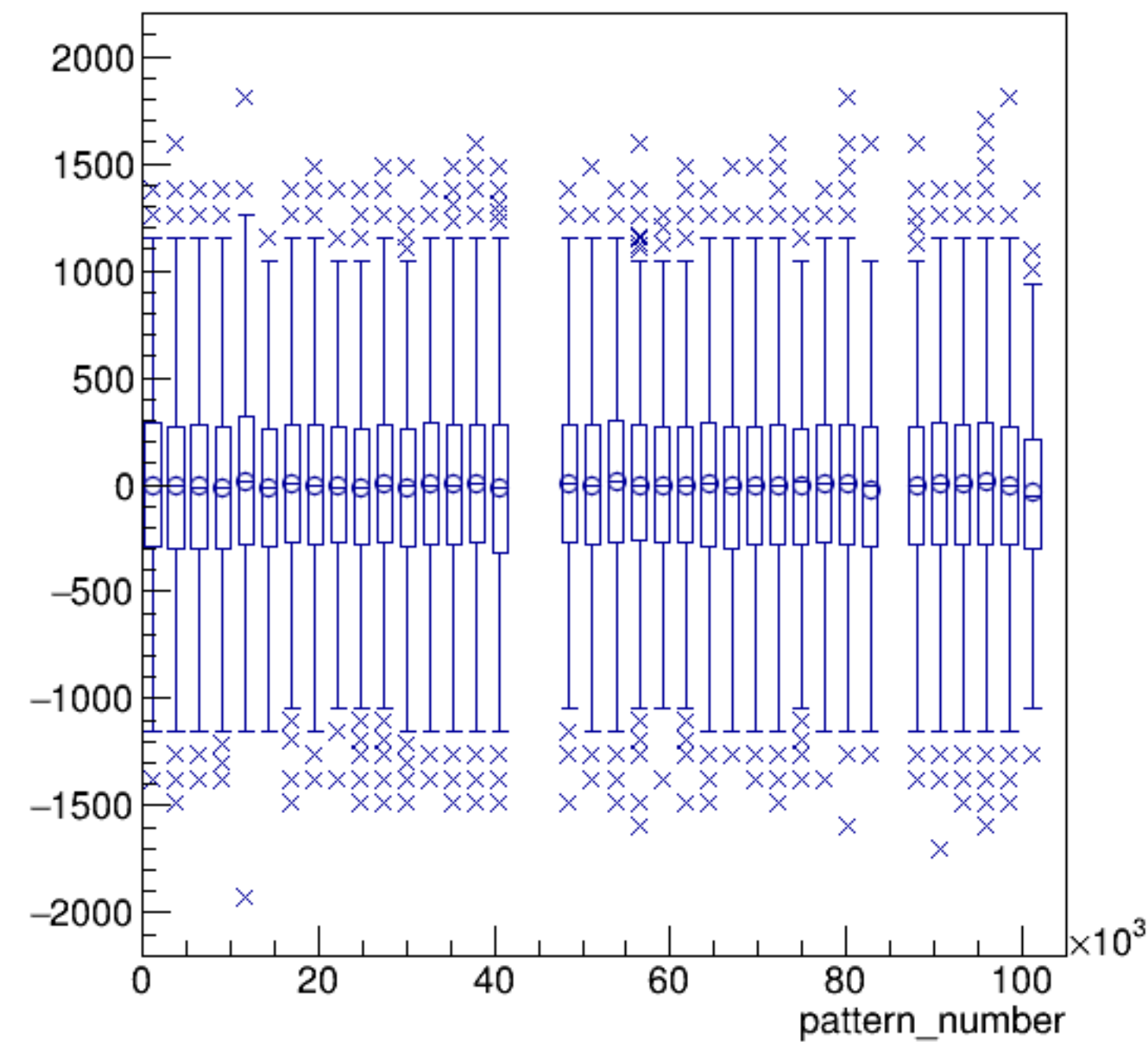


run8299_000_pairTree_dsl.png

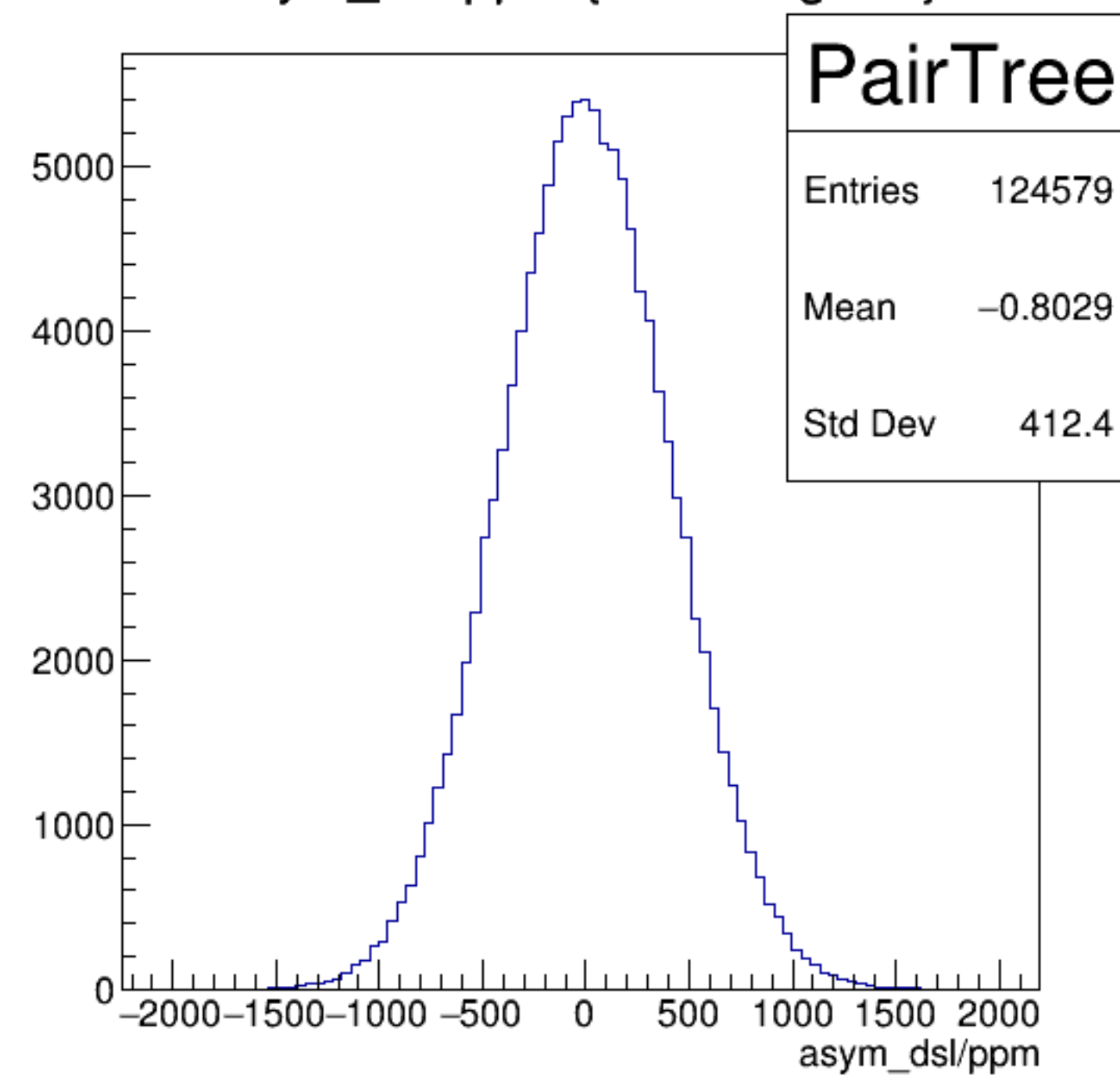
yield_dsl {ErrorFlag==0}



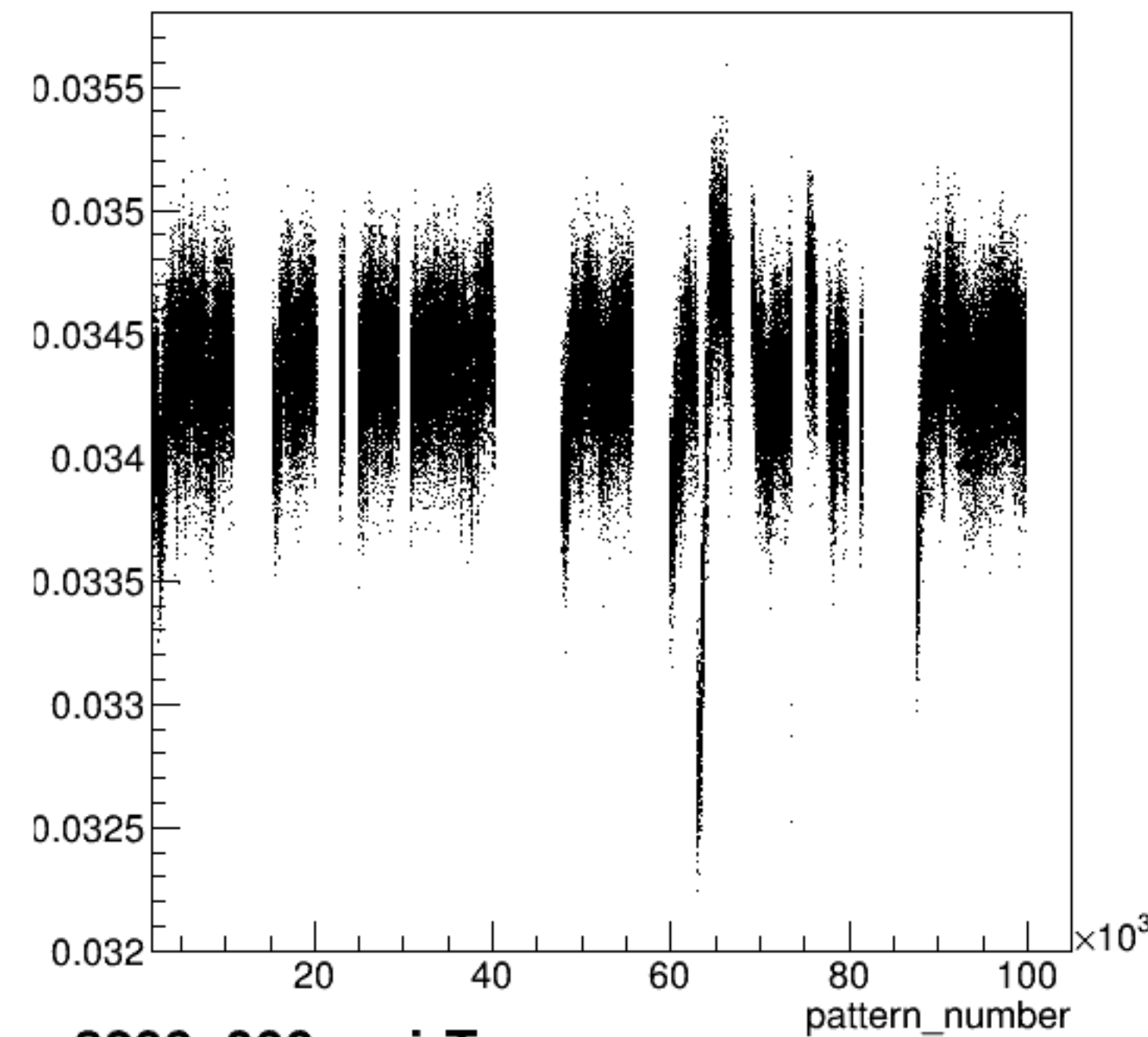
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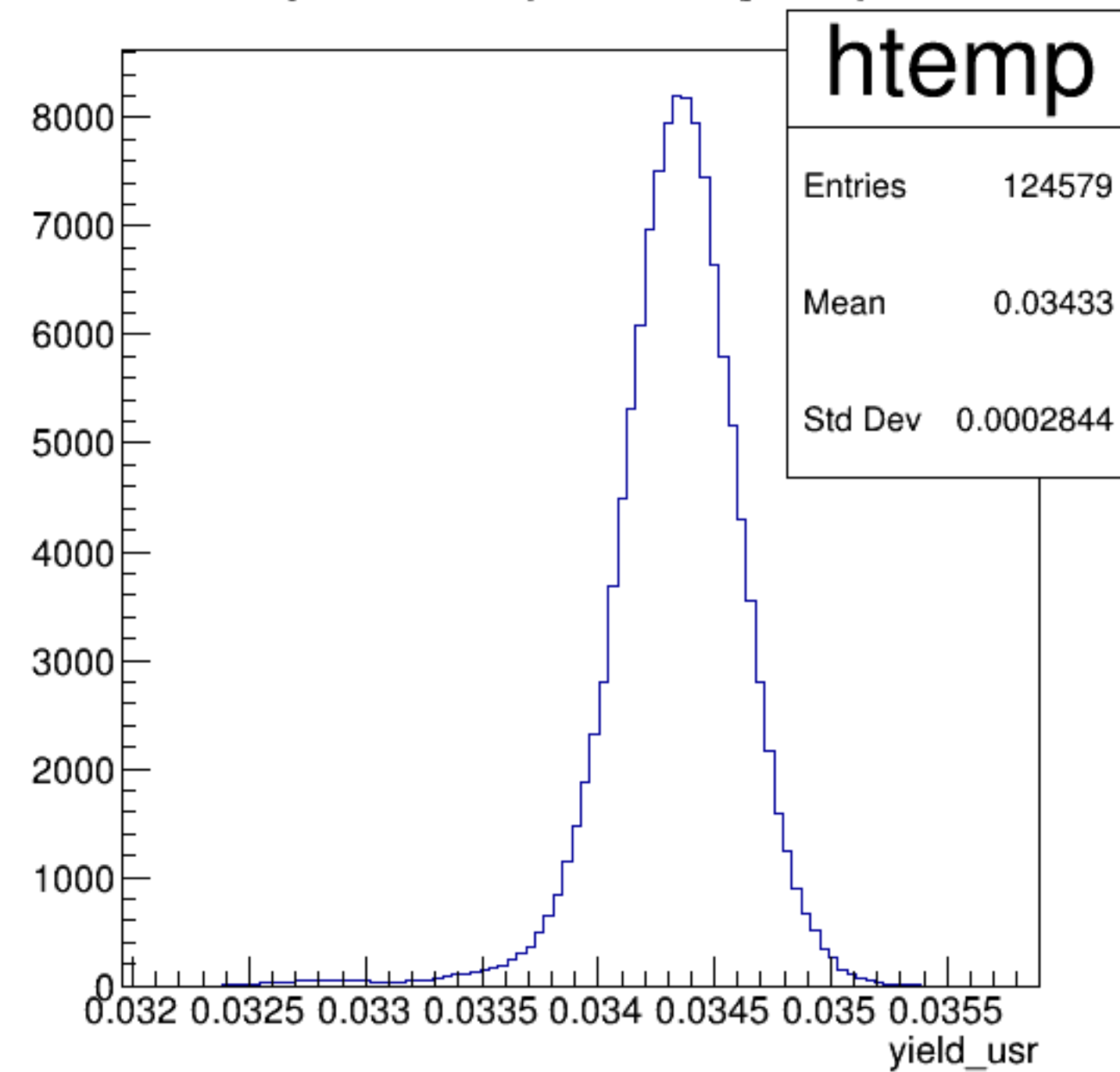


yield_usr:pattern_number {ErrorFlag==0}

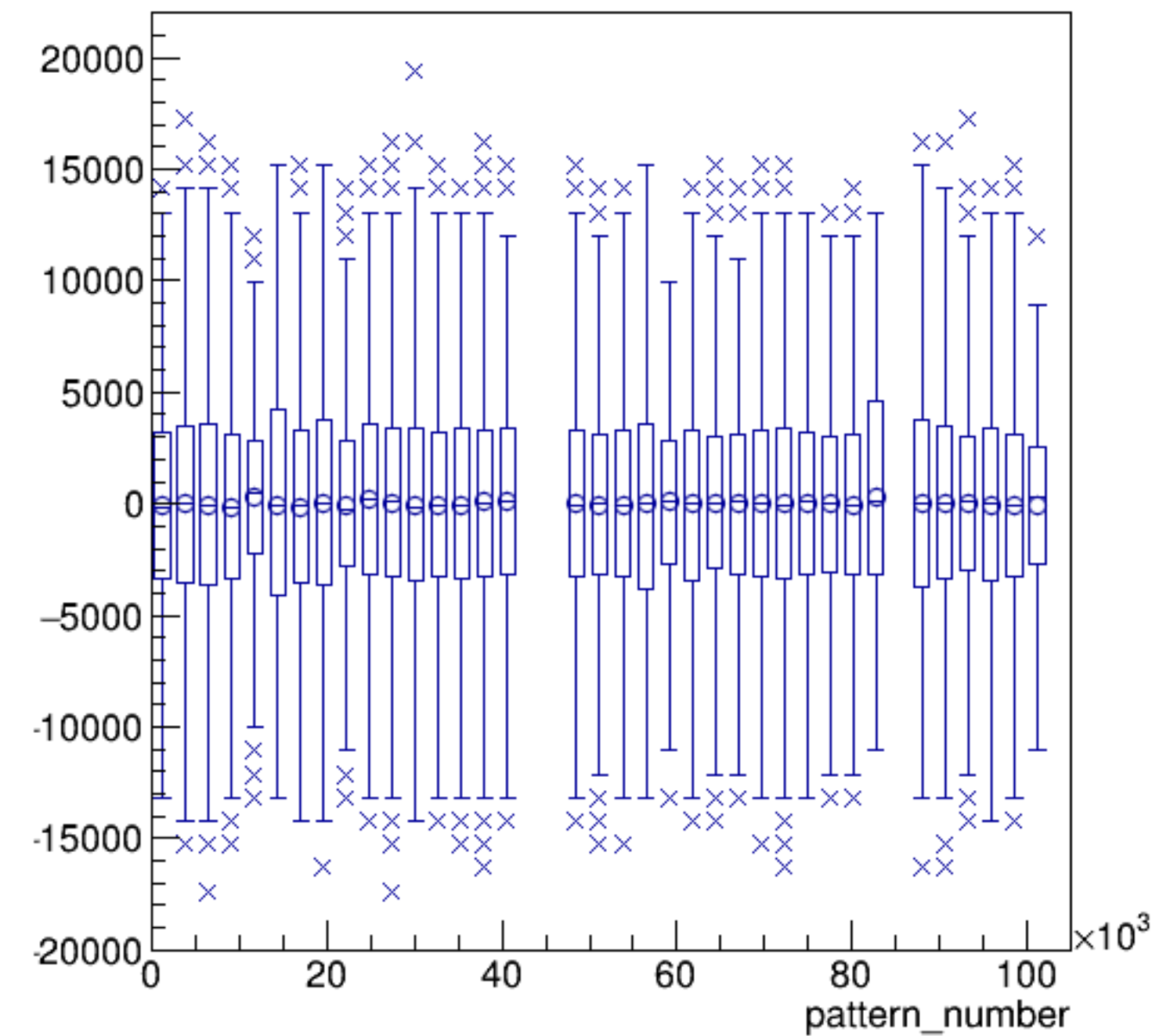


run8299_000_pairTree_usr.png

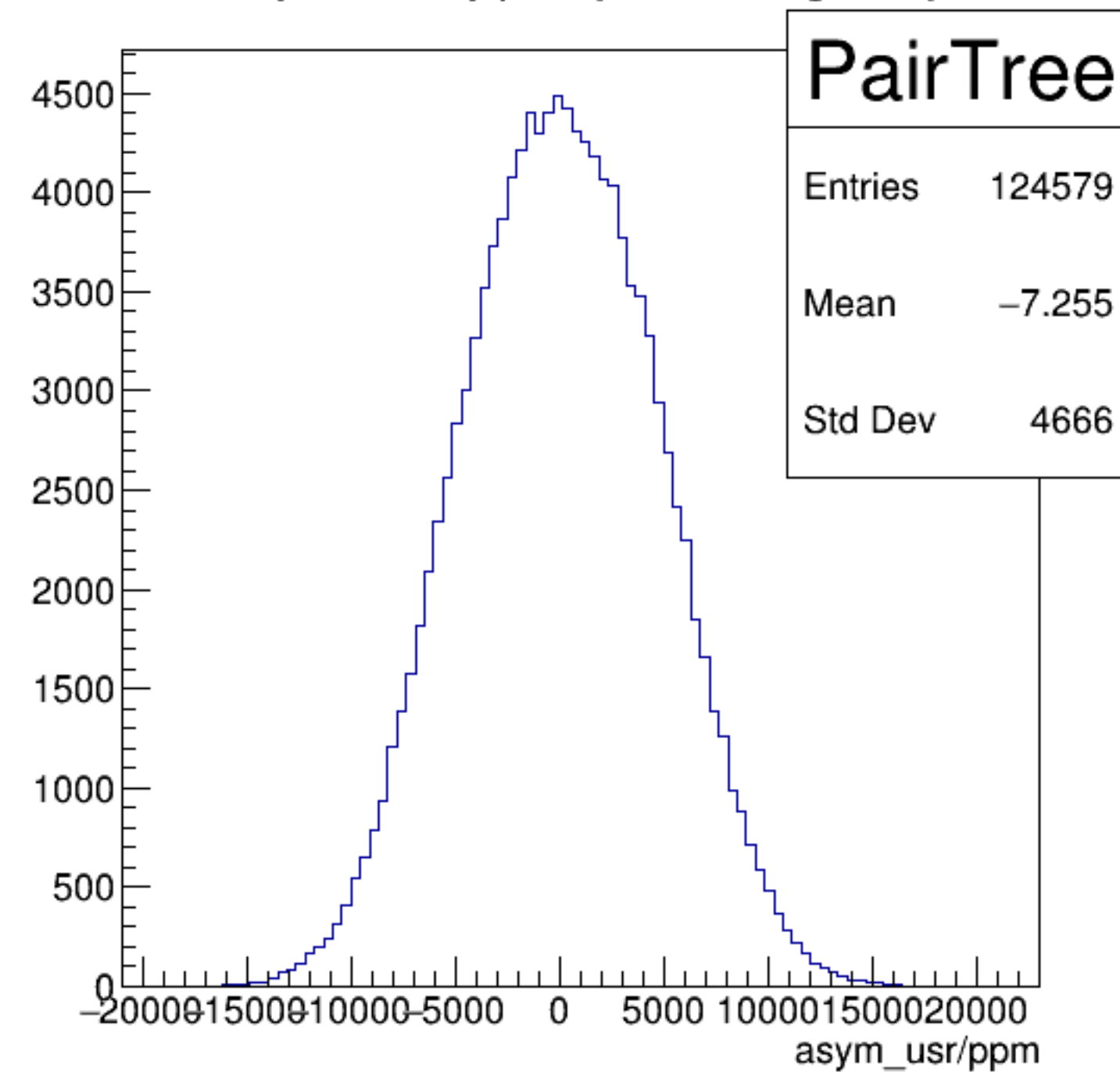
yield_usr {ErrorFlag==0}



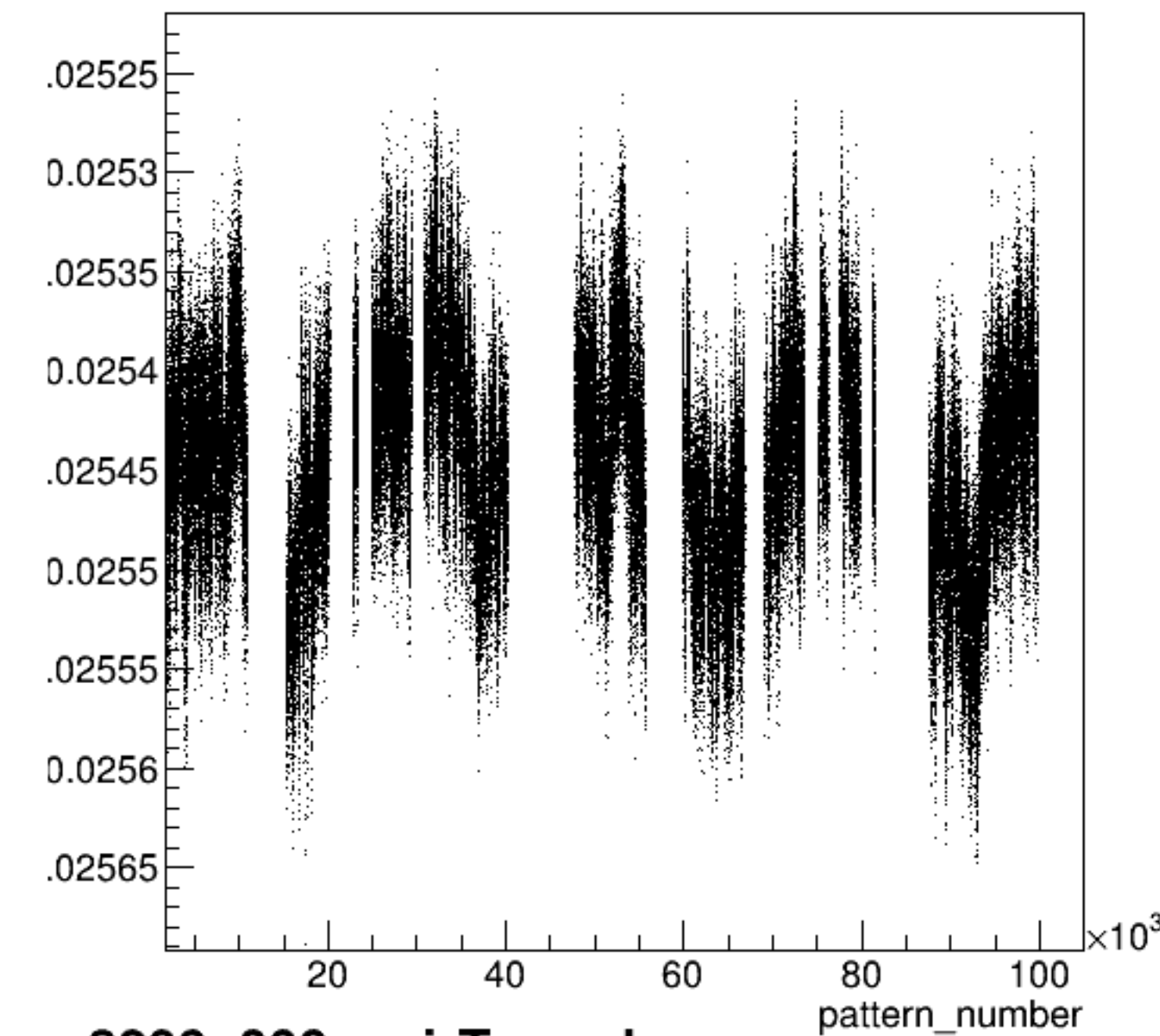
asym_usr/ppm:pattern_number {ErrorFlag==0}



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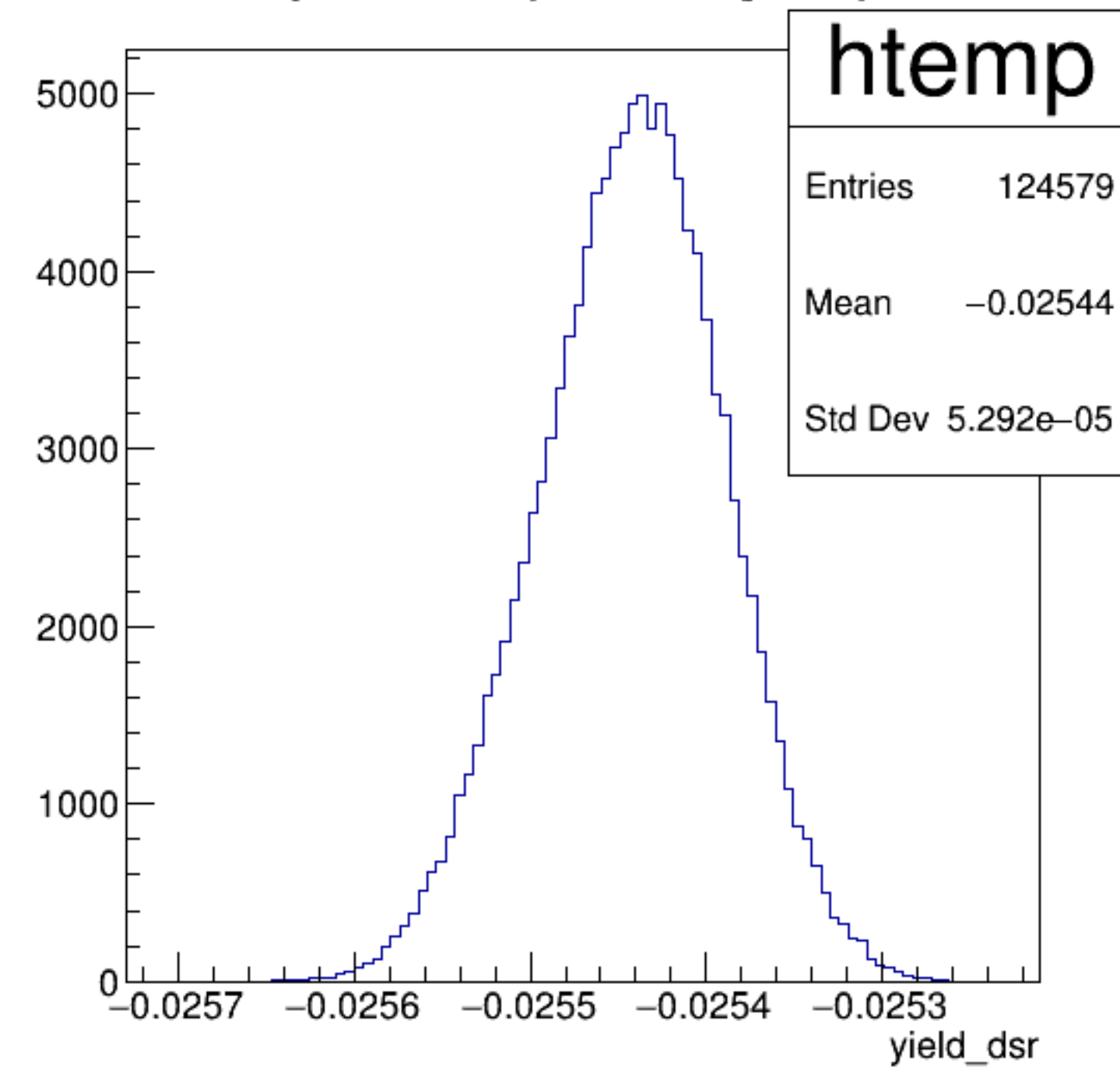


yield_dsr:pattern_number {ErrorFlag==0}

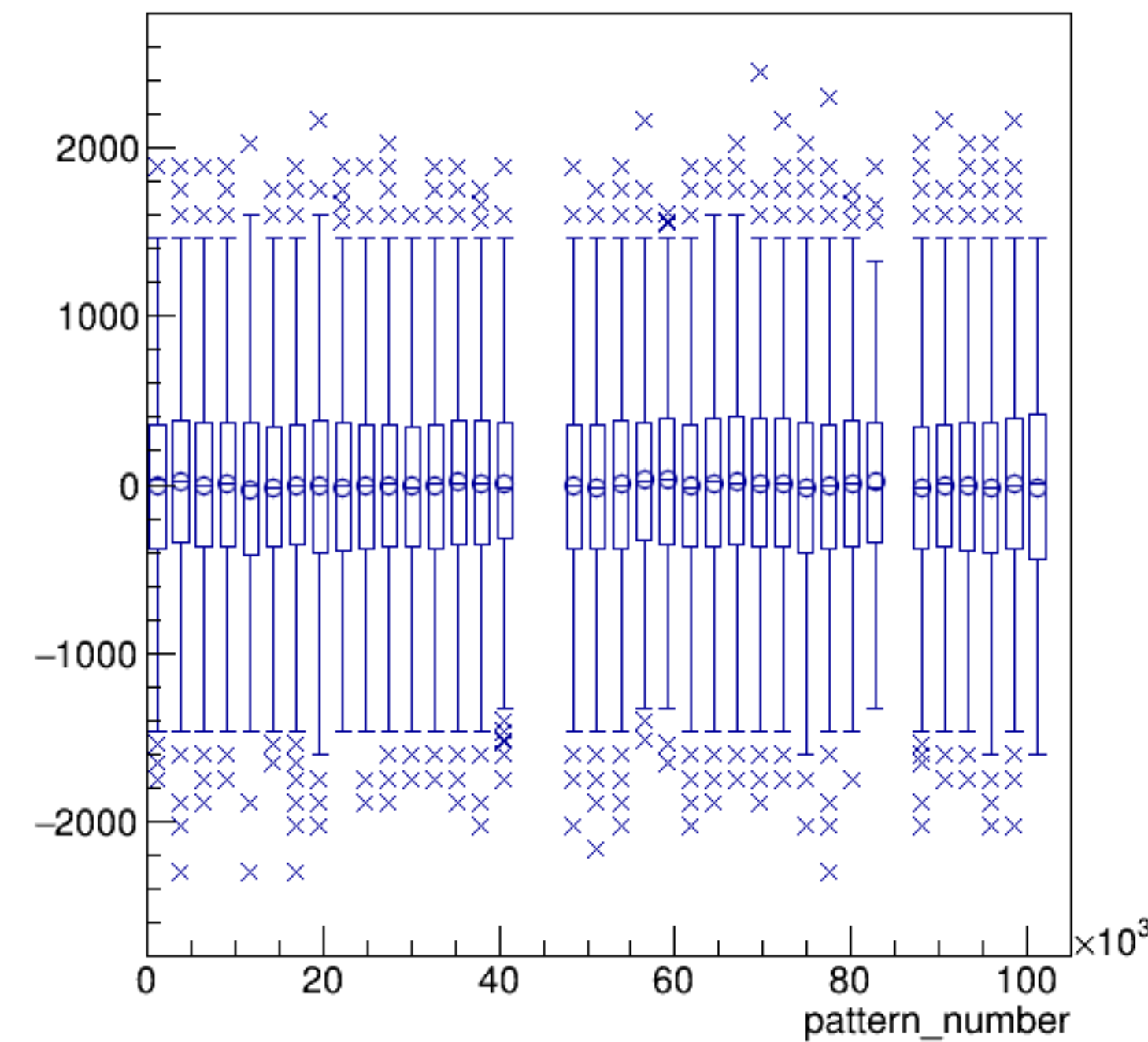


run8299_000_pairTree_dsr.png

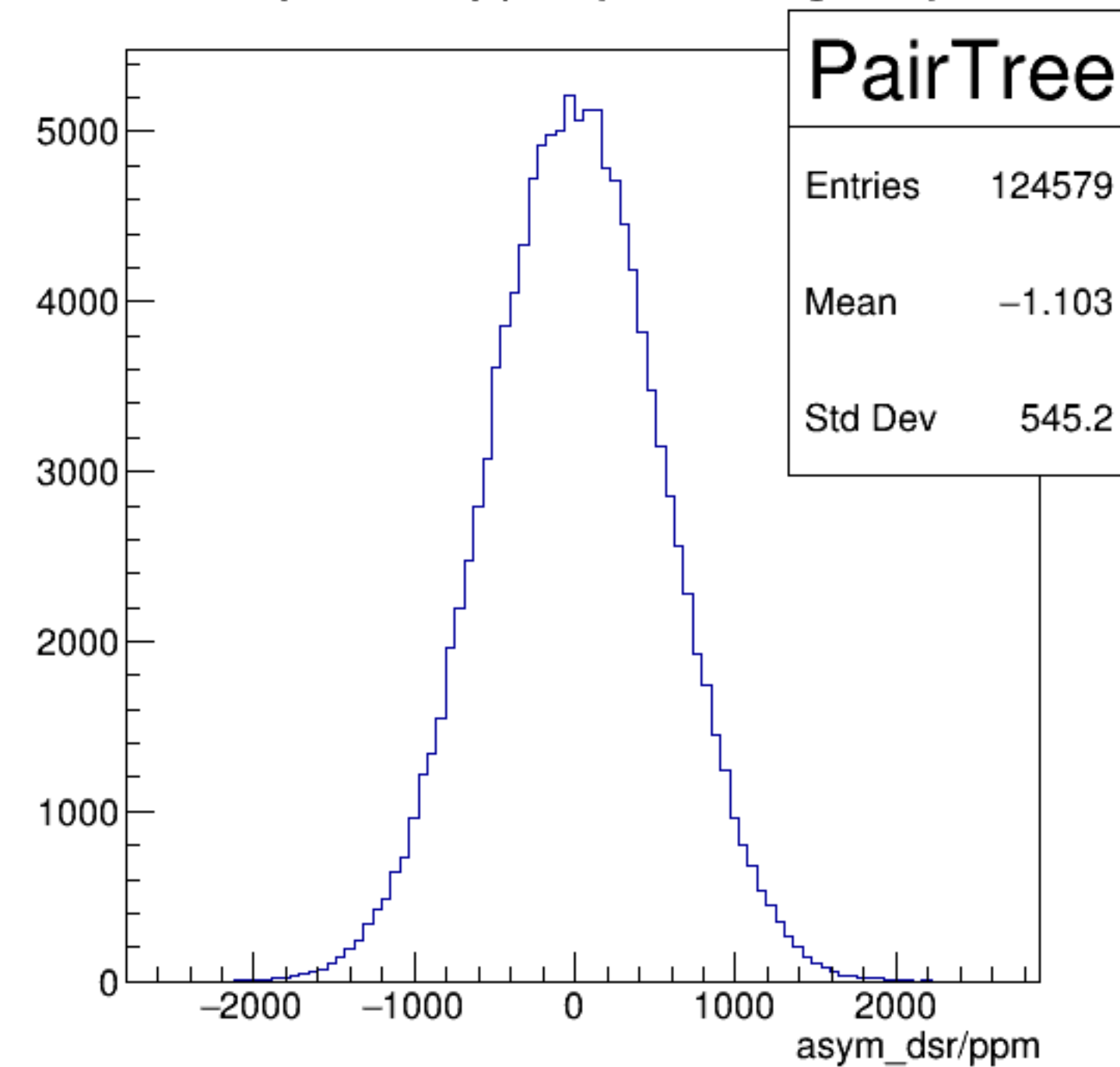
yield_dsr {ErrorFlag==0}



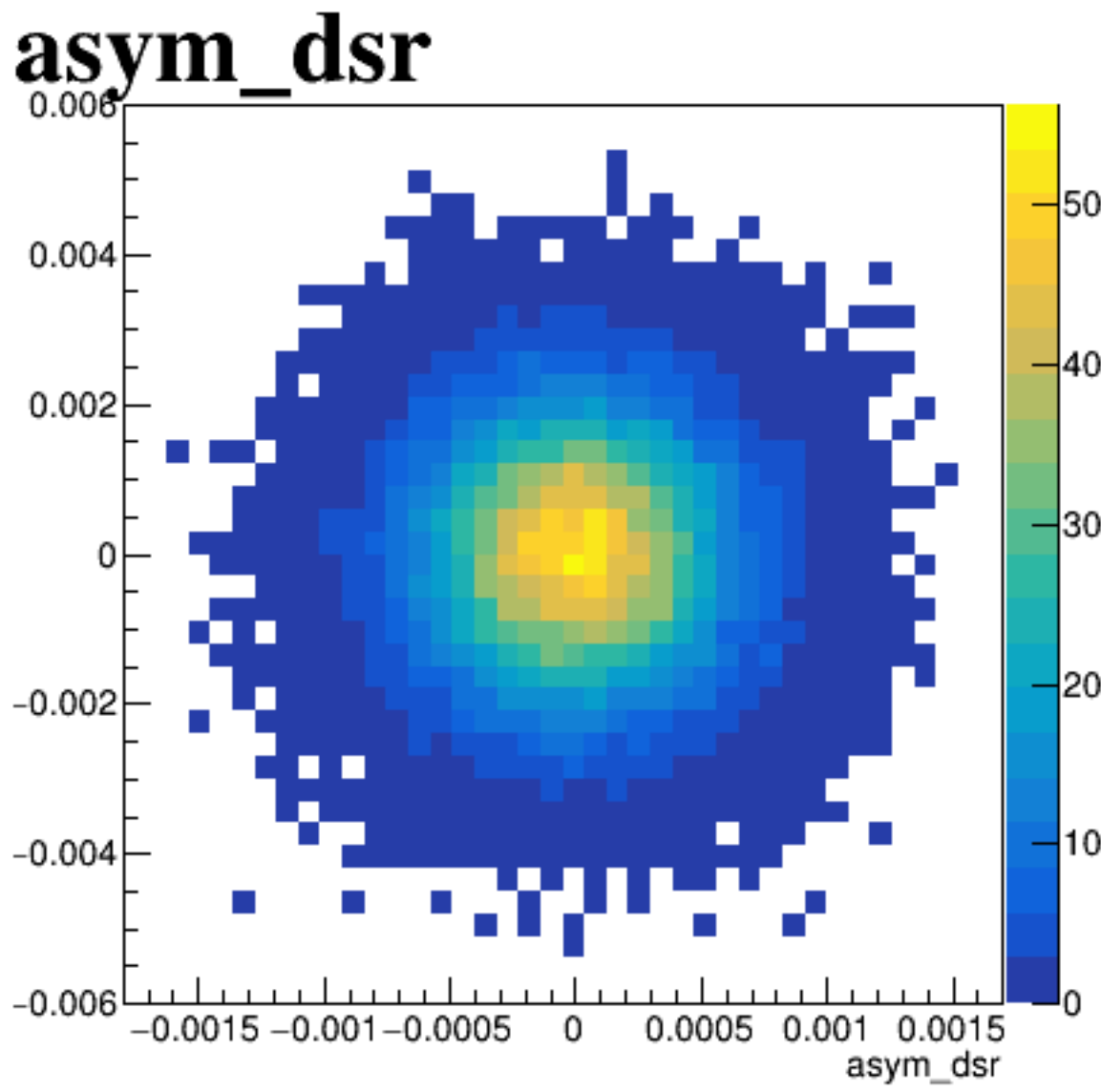
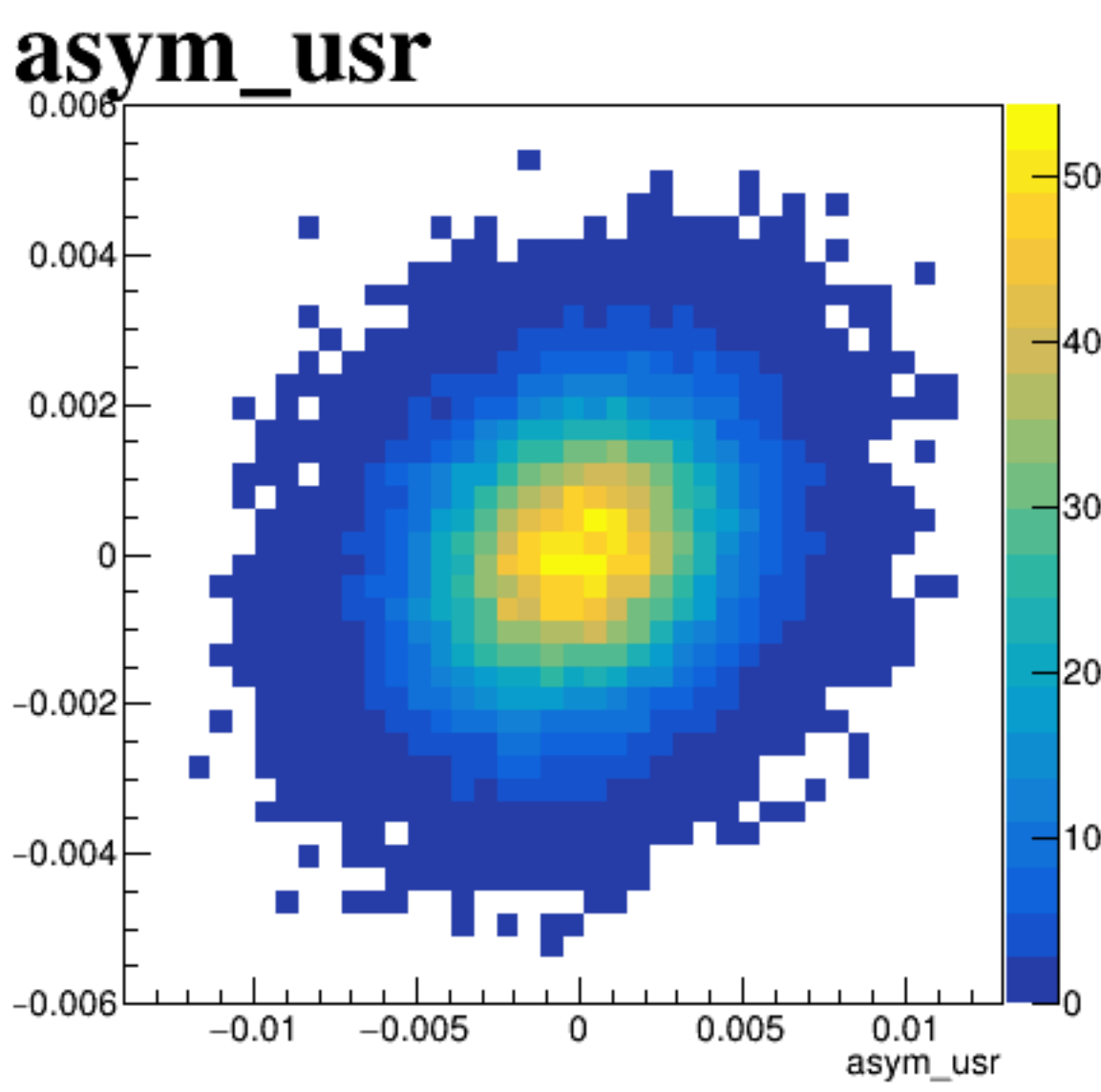
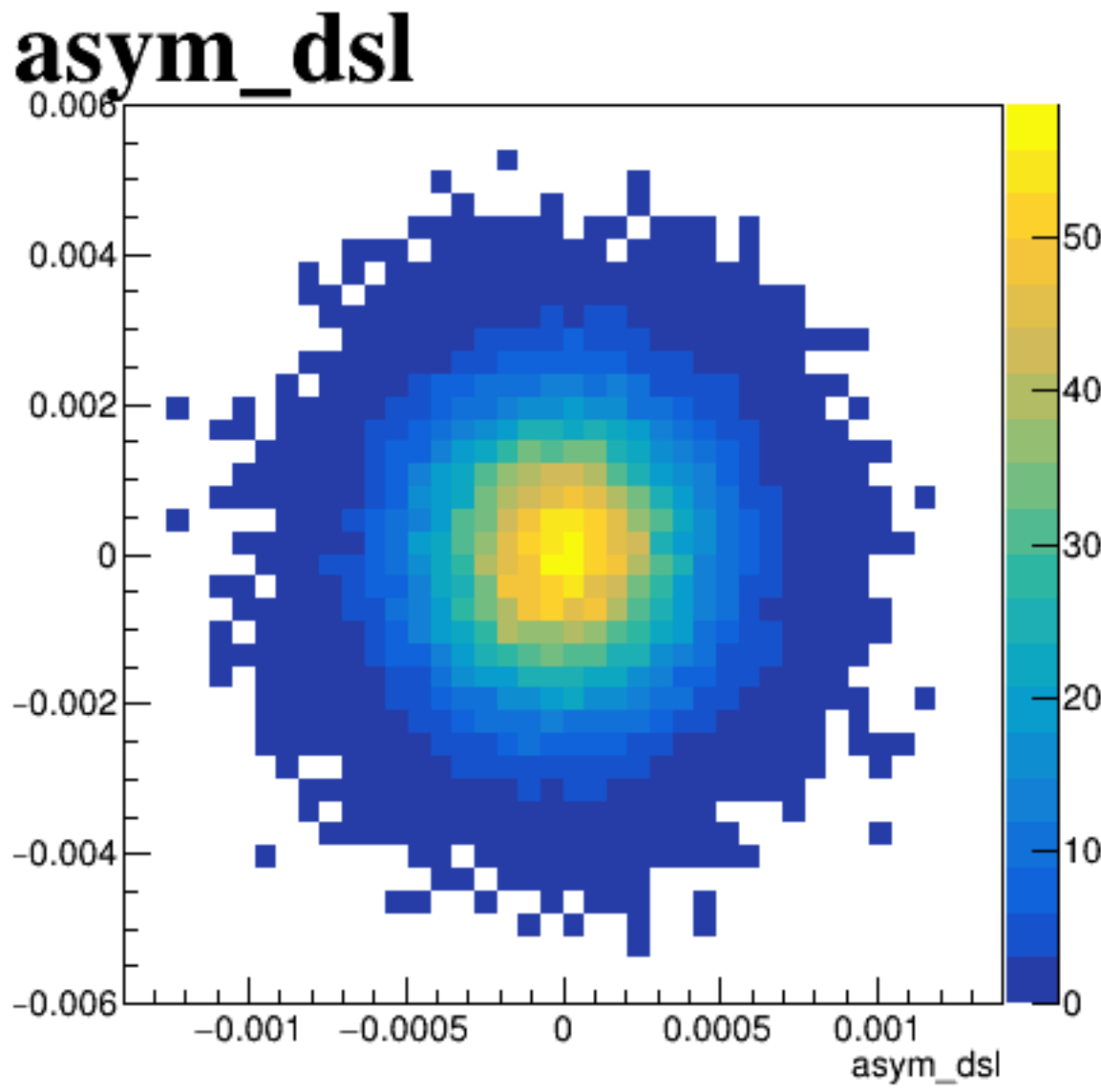
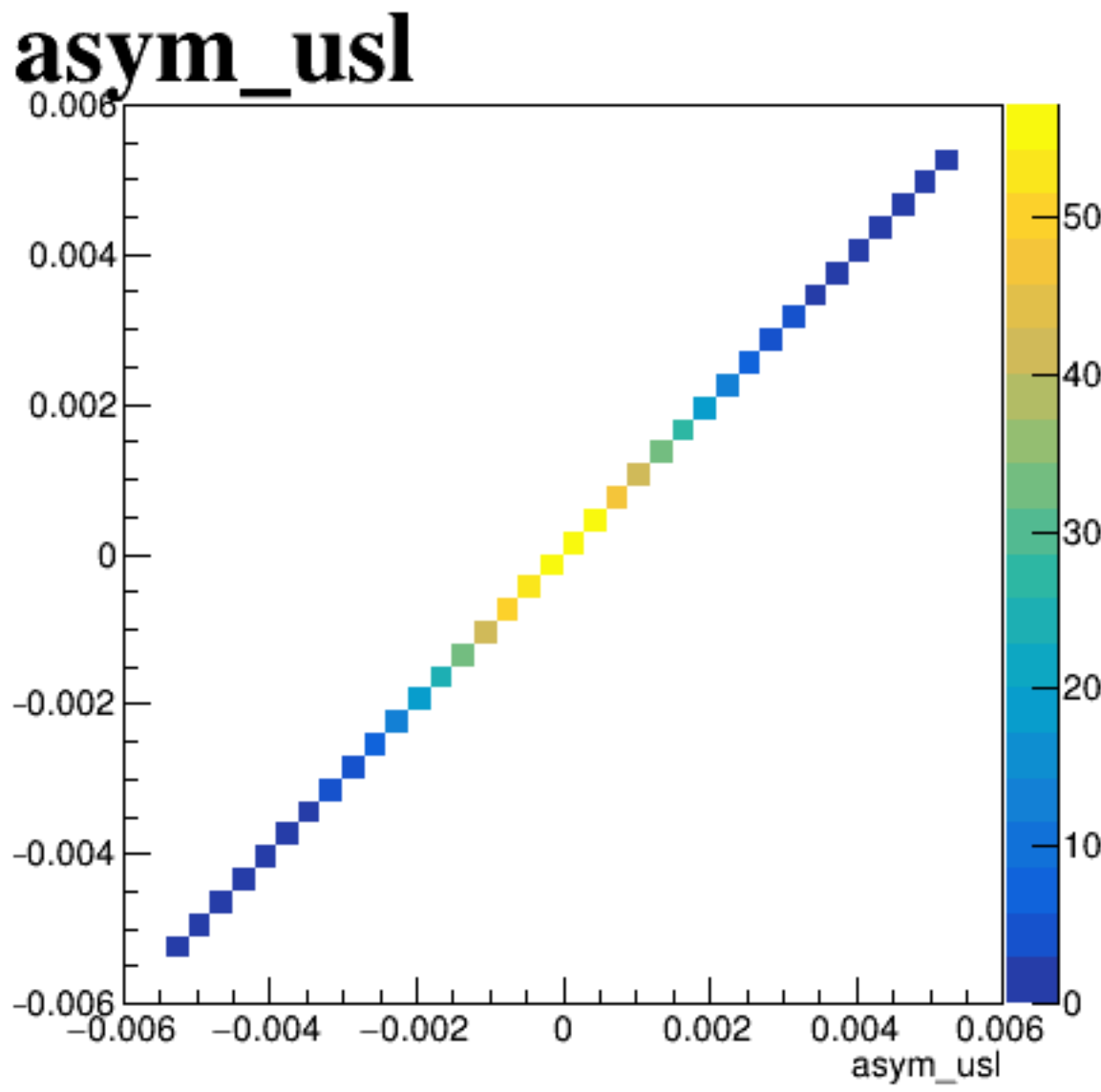
asym_dsr/ppm:pattern_number {ErrorFlag==0}



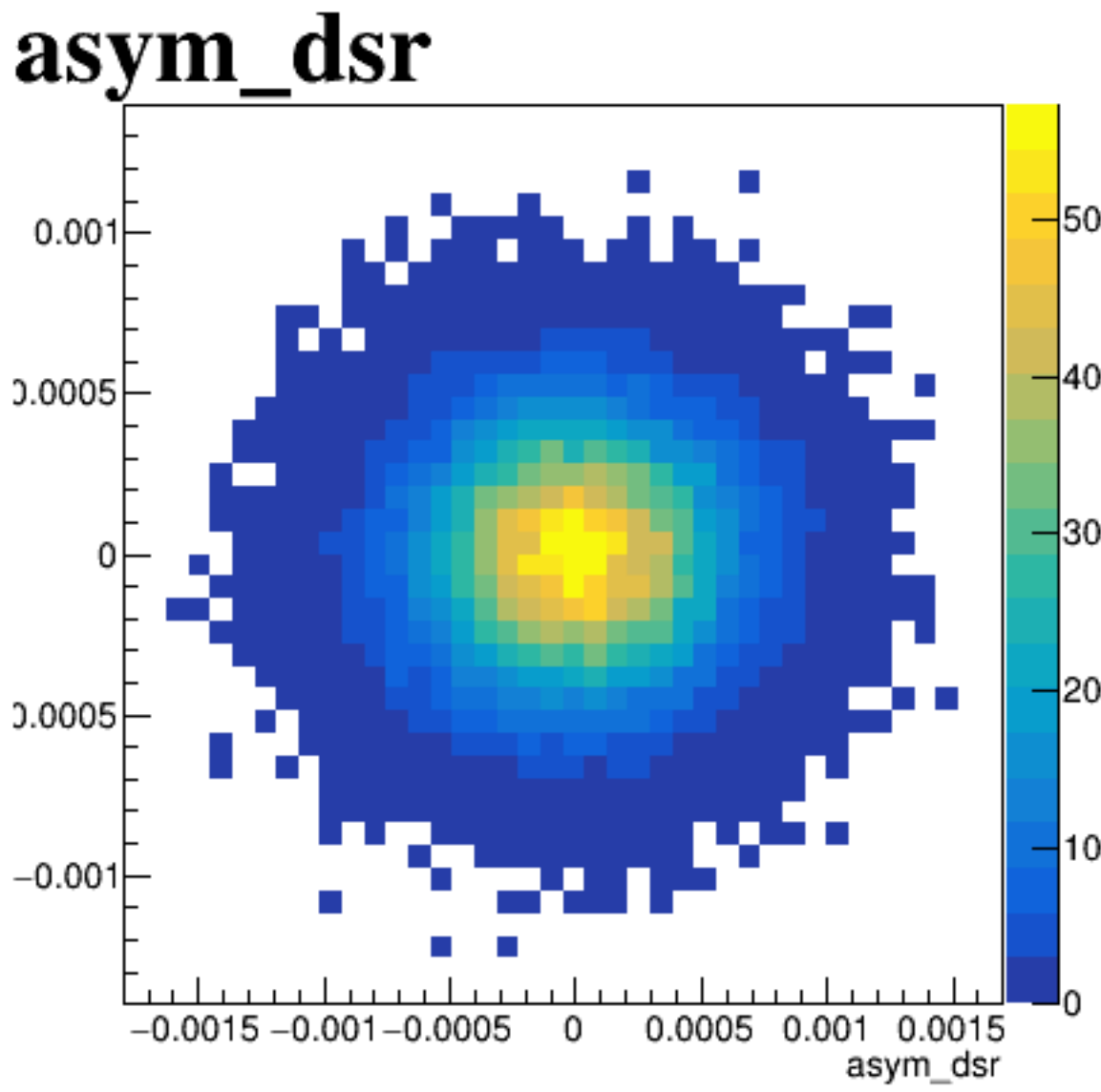
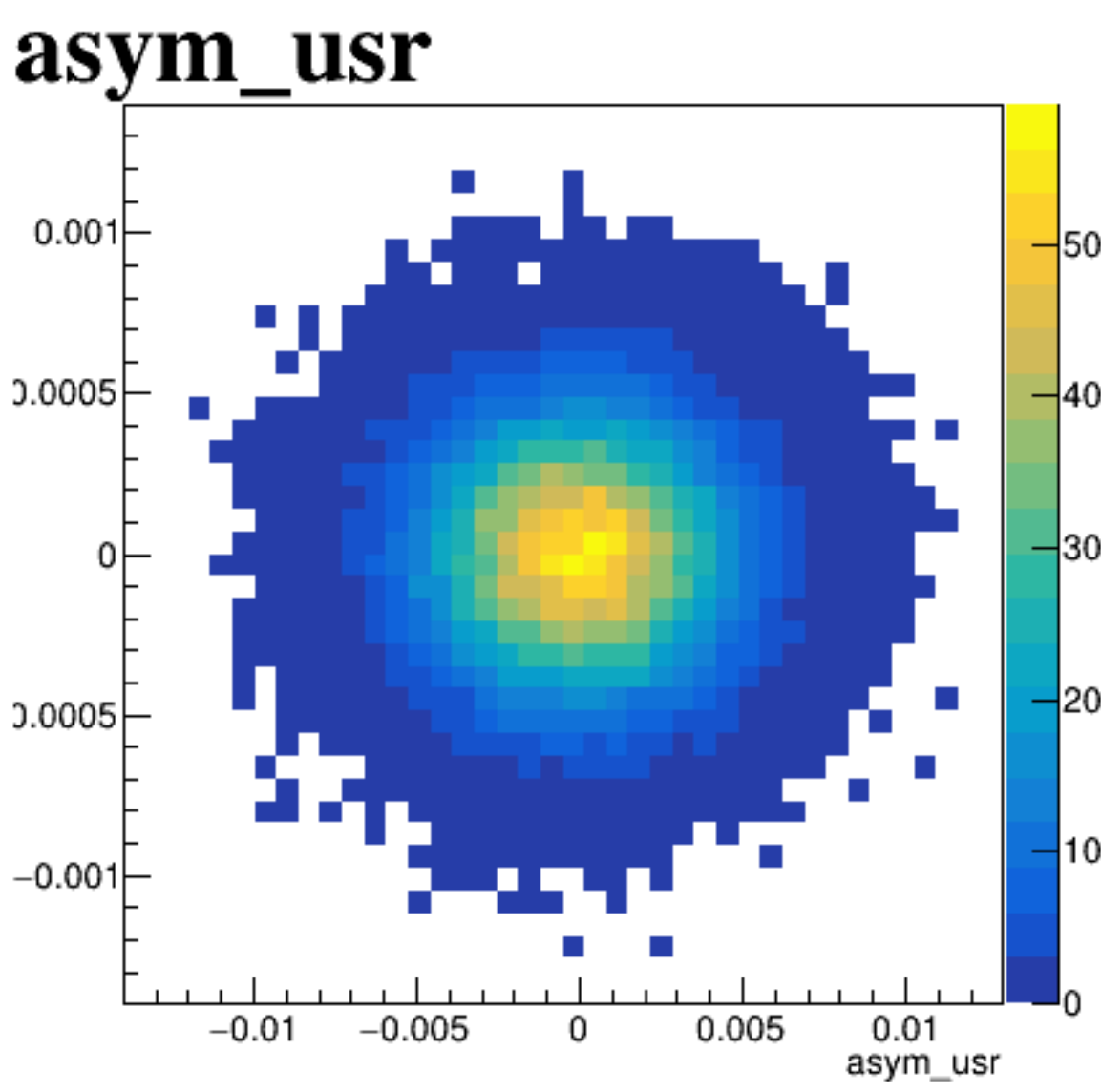
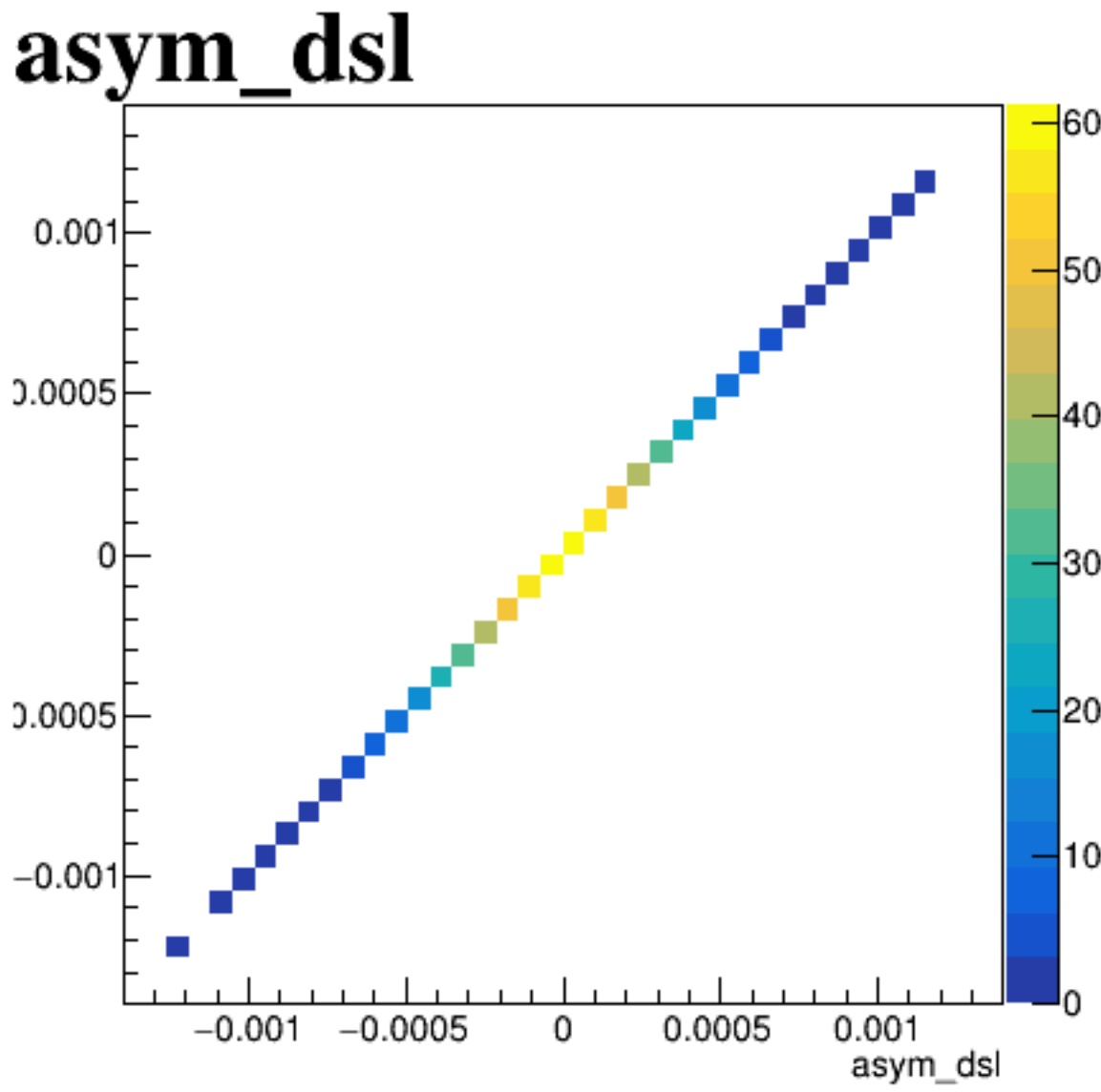
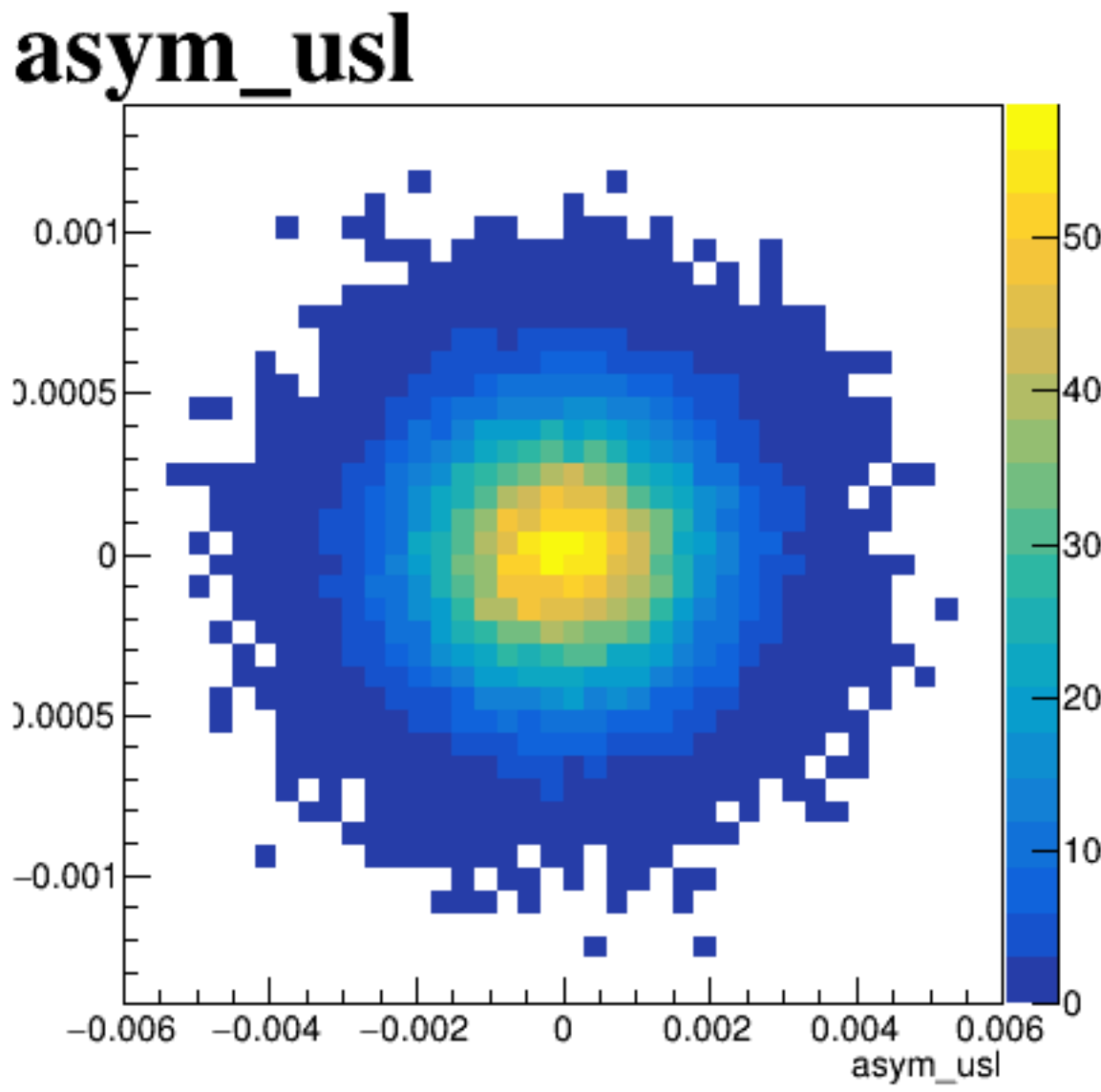
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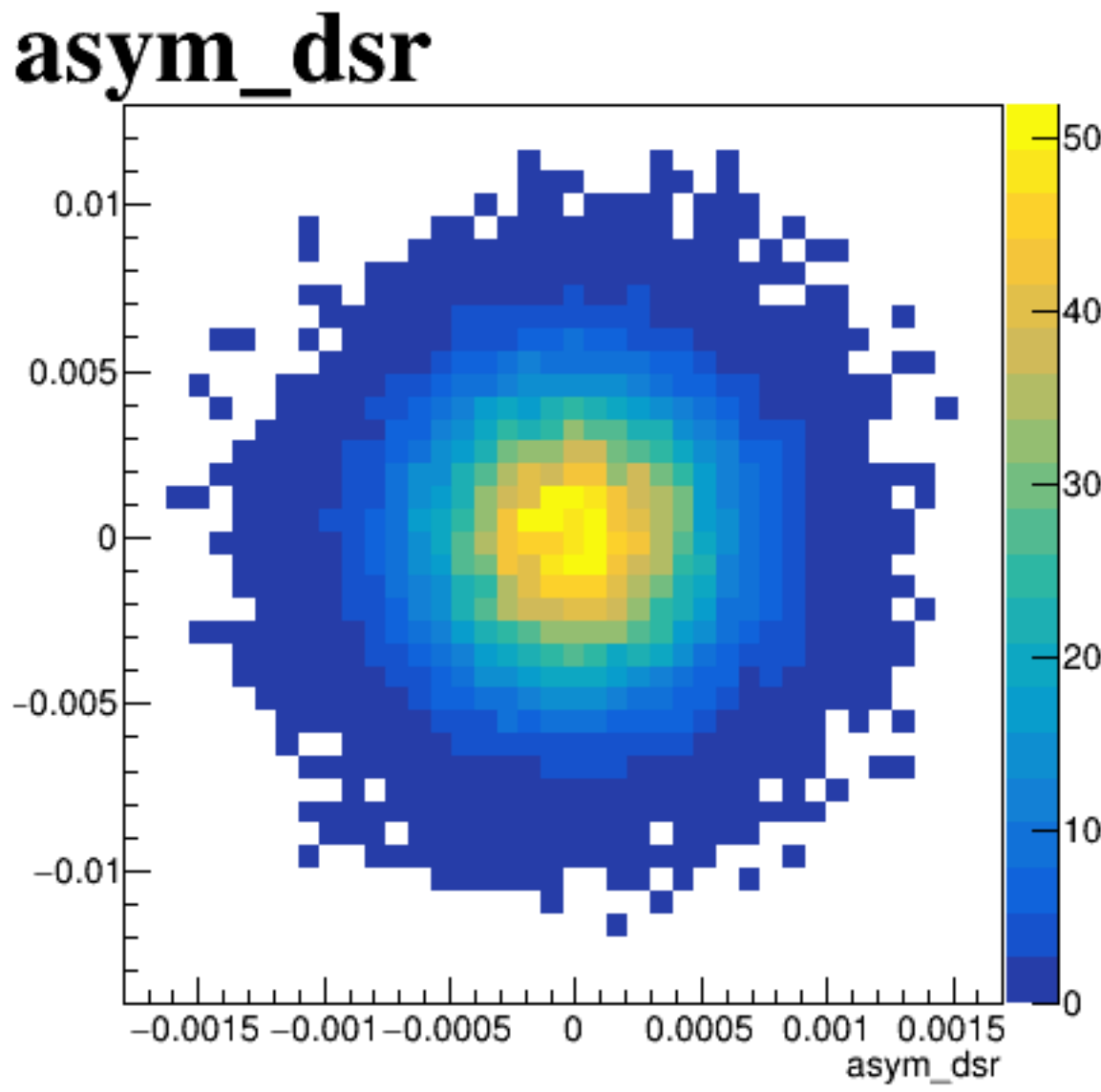
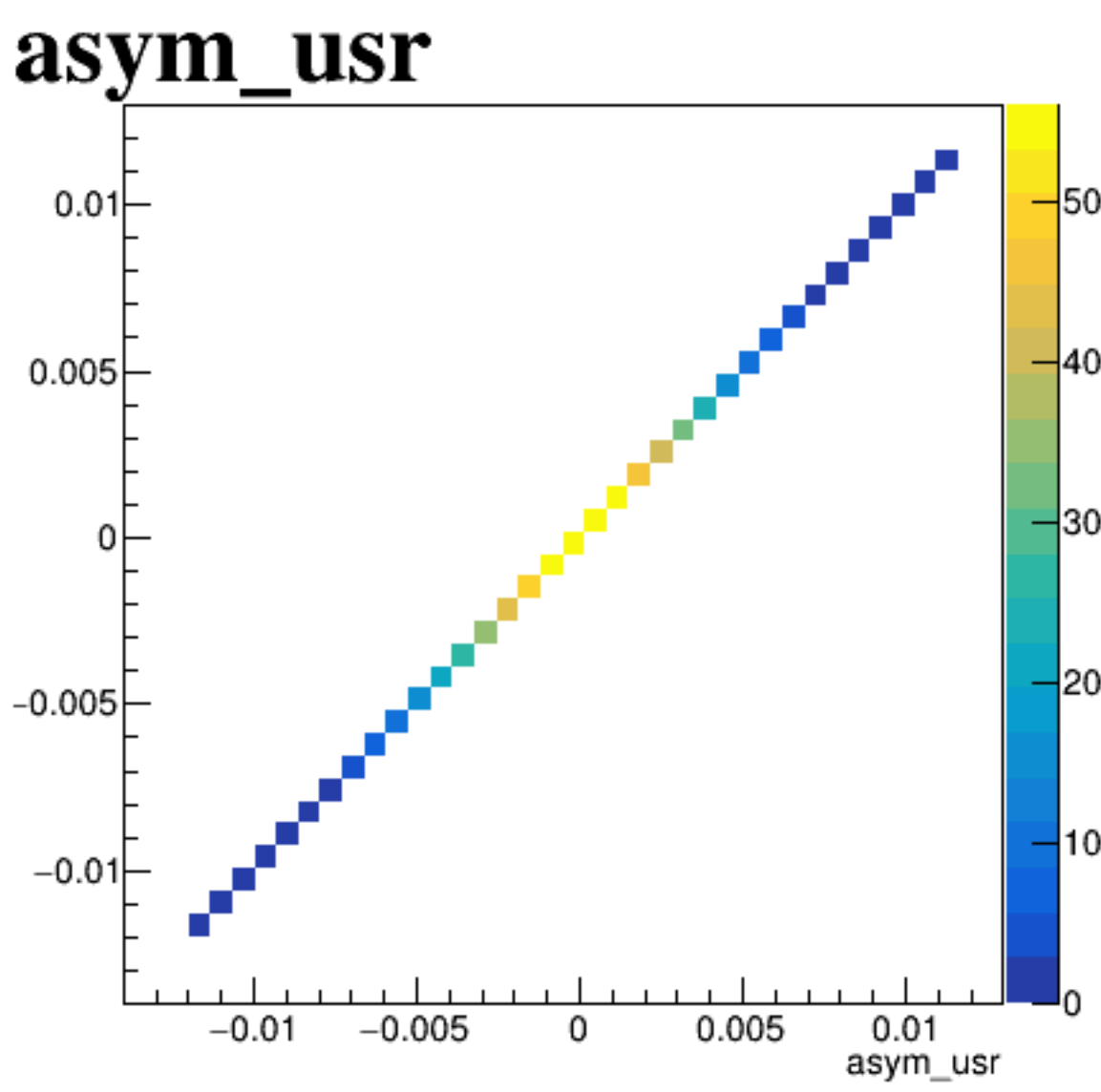
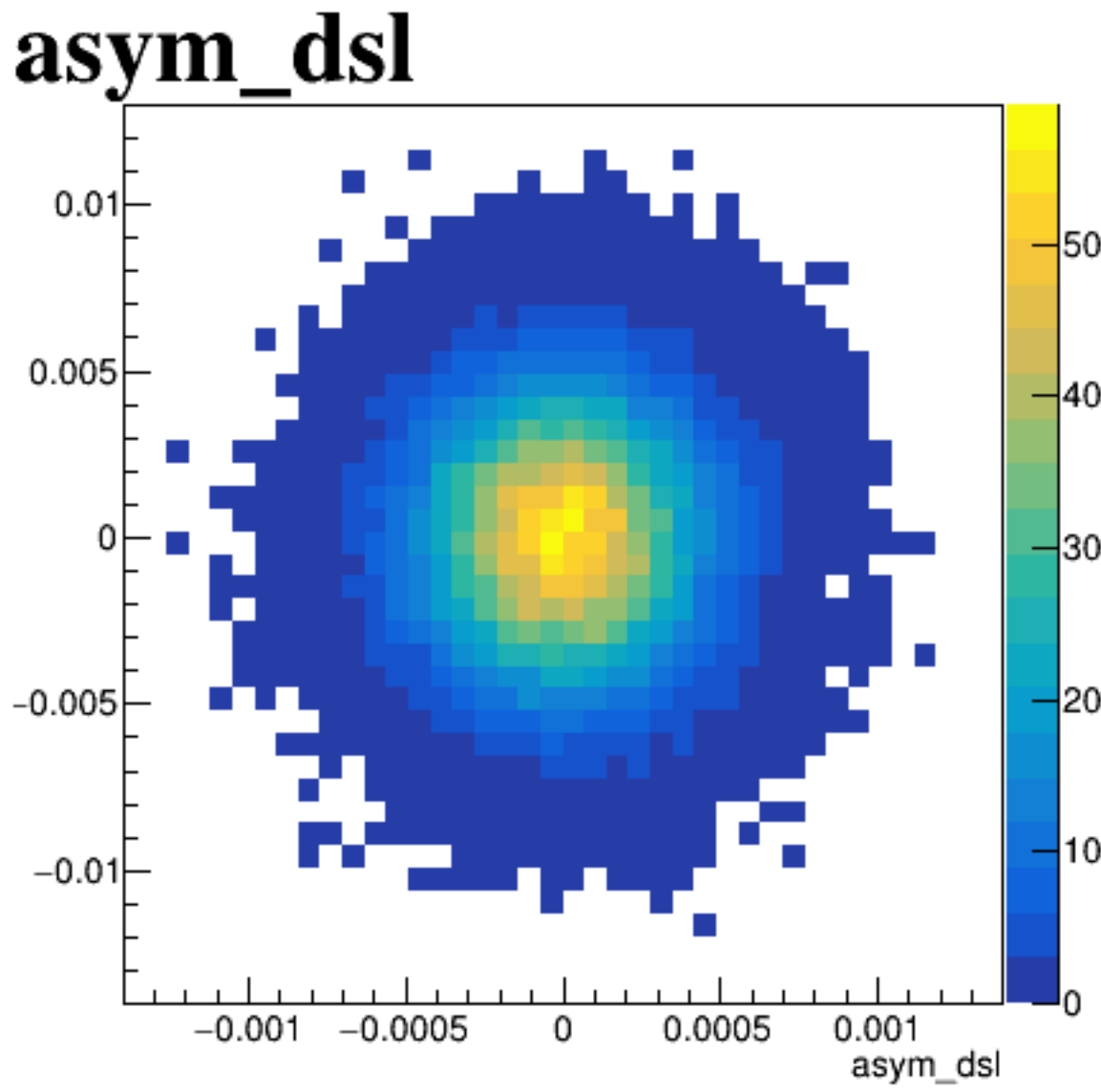
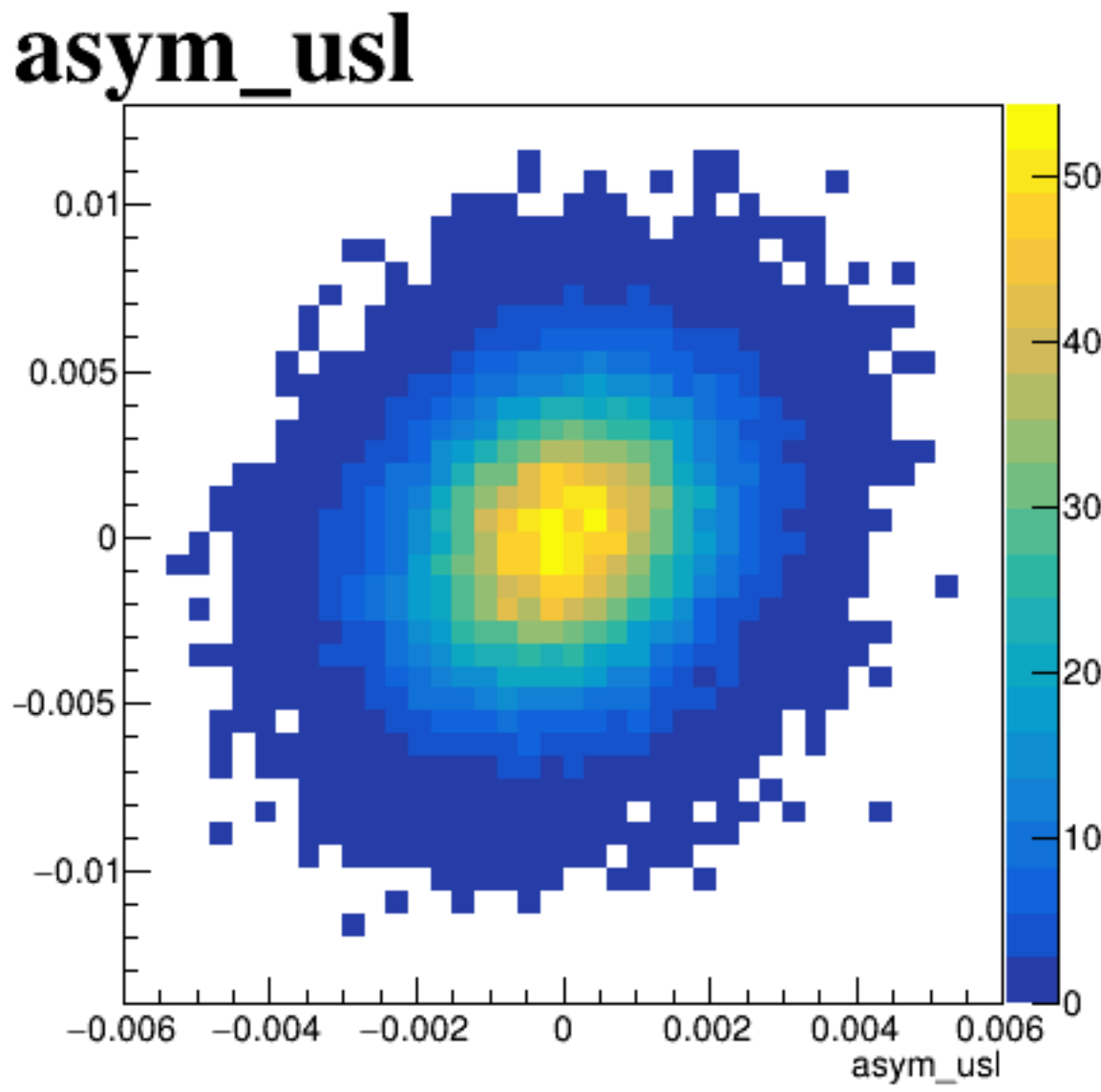
asym_usl



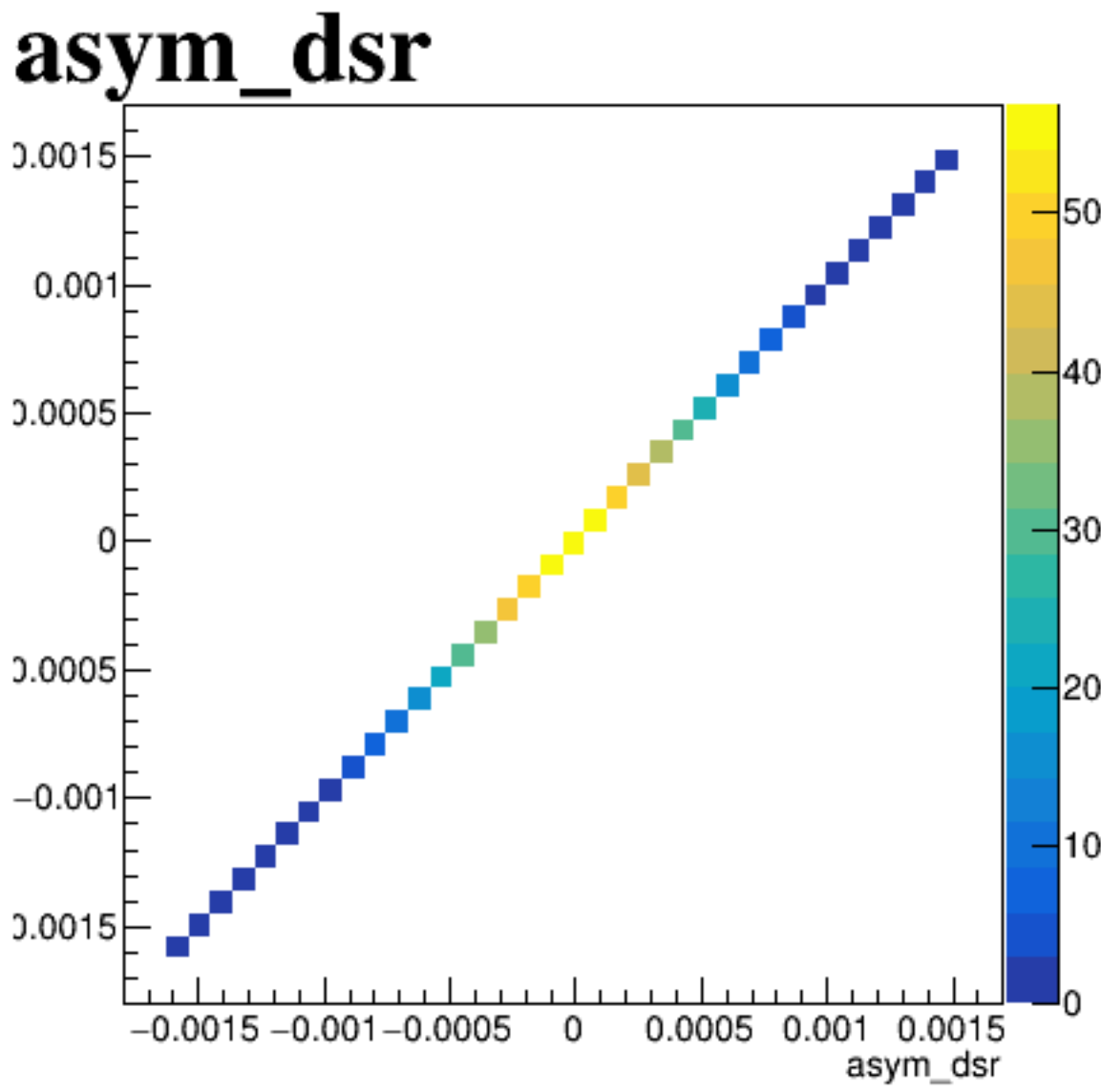
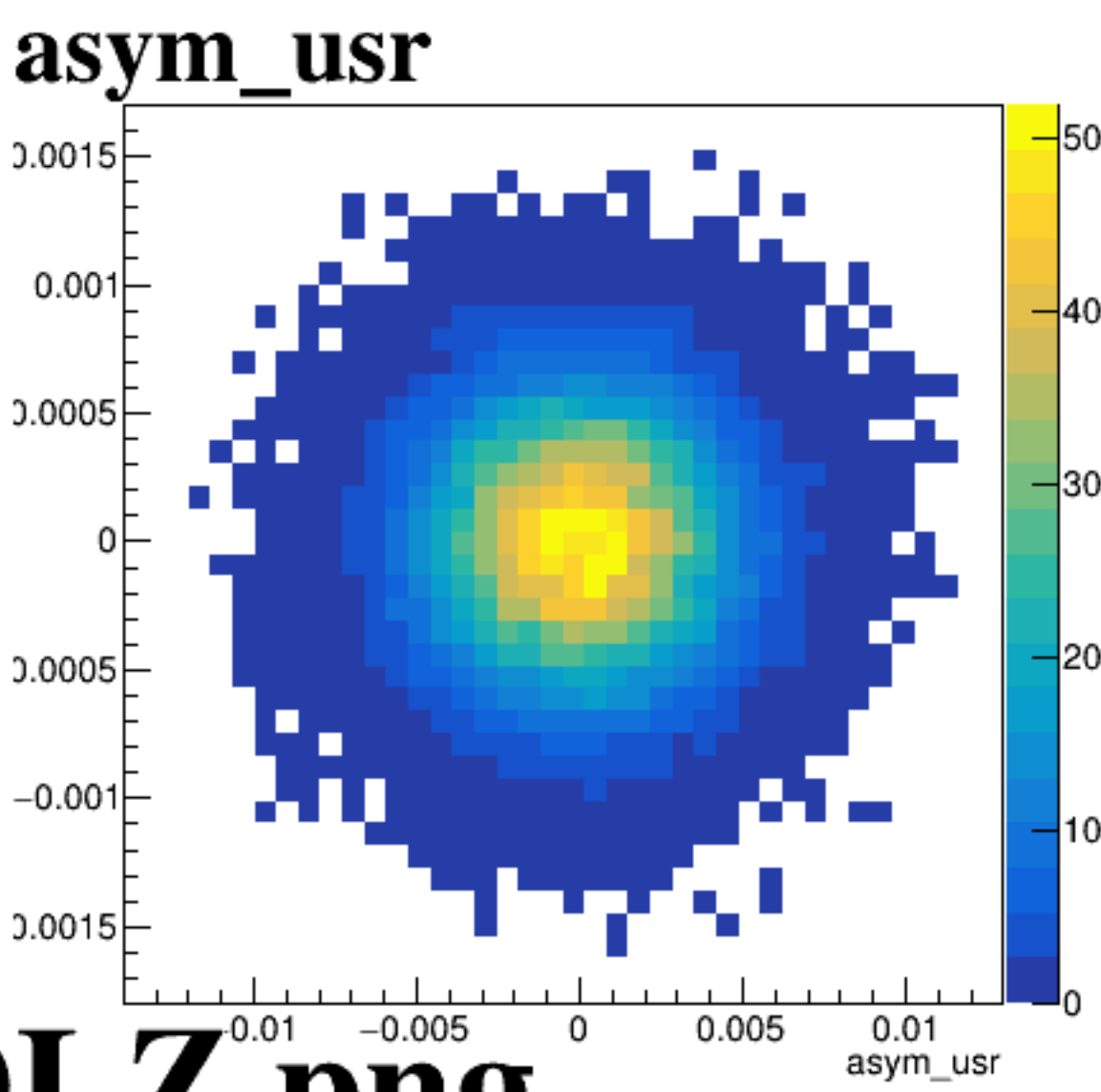
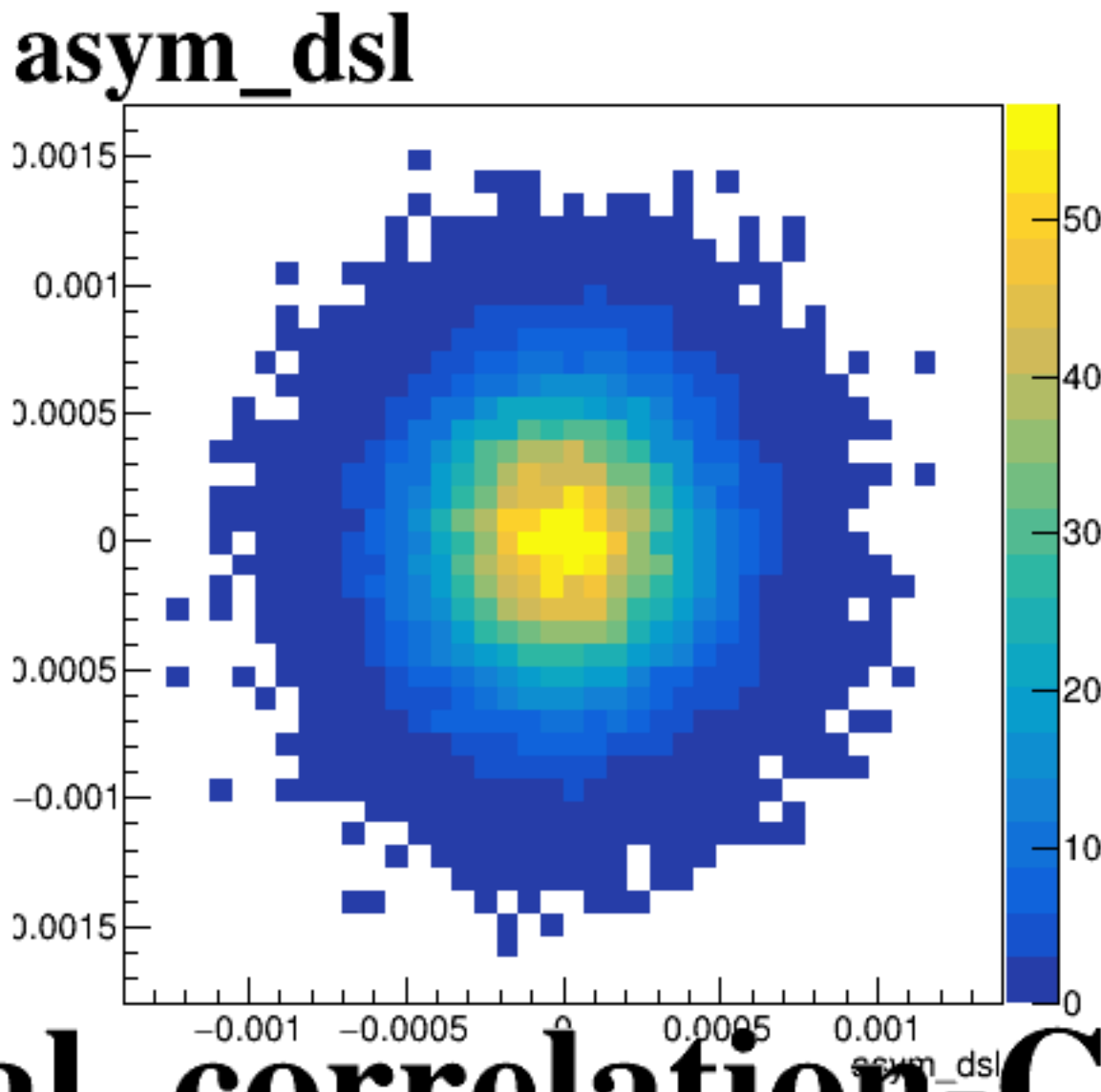
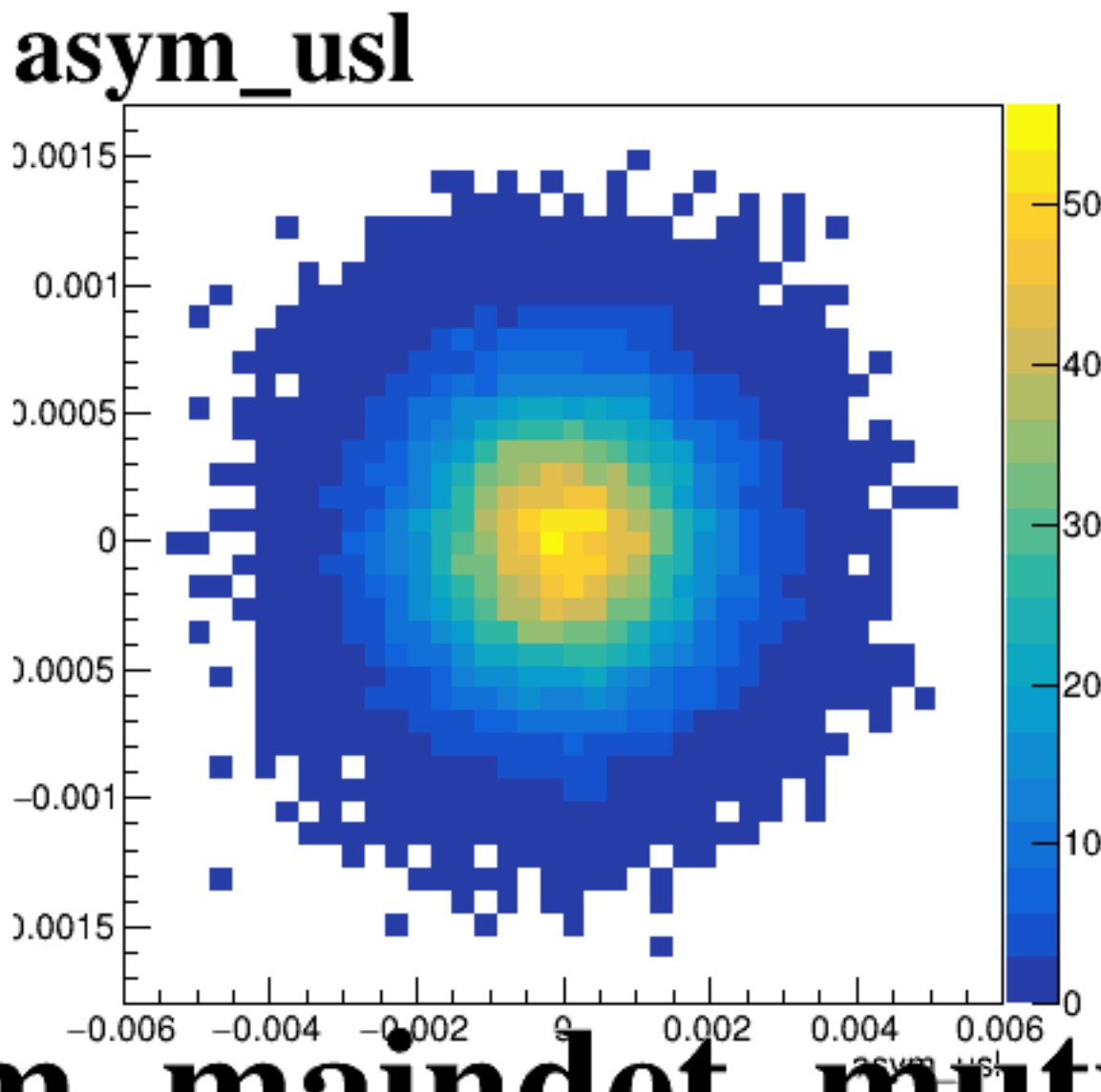
asym_dsl



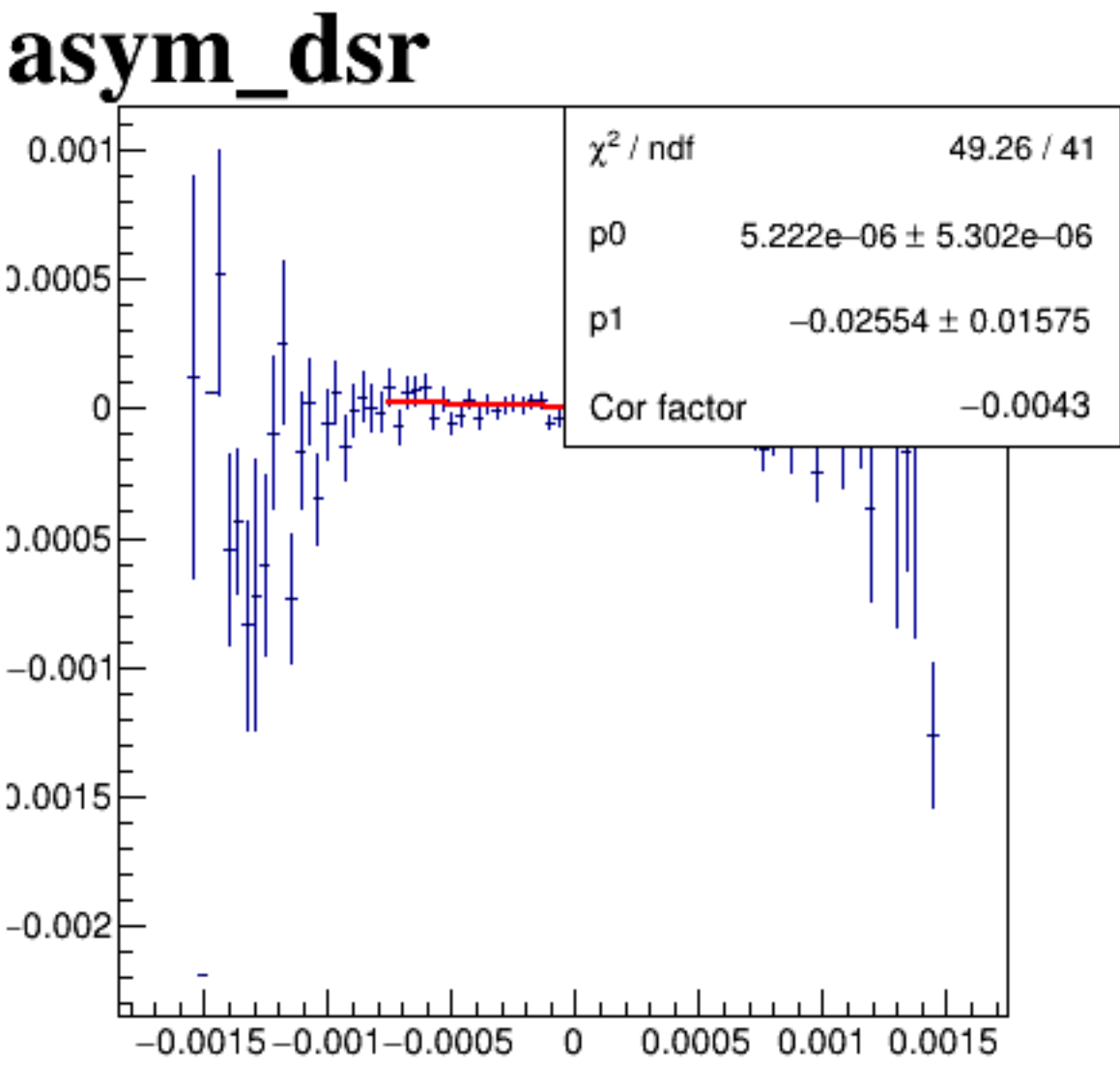
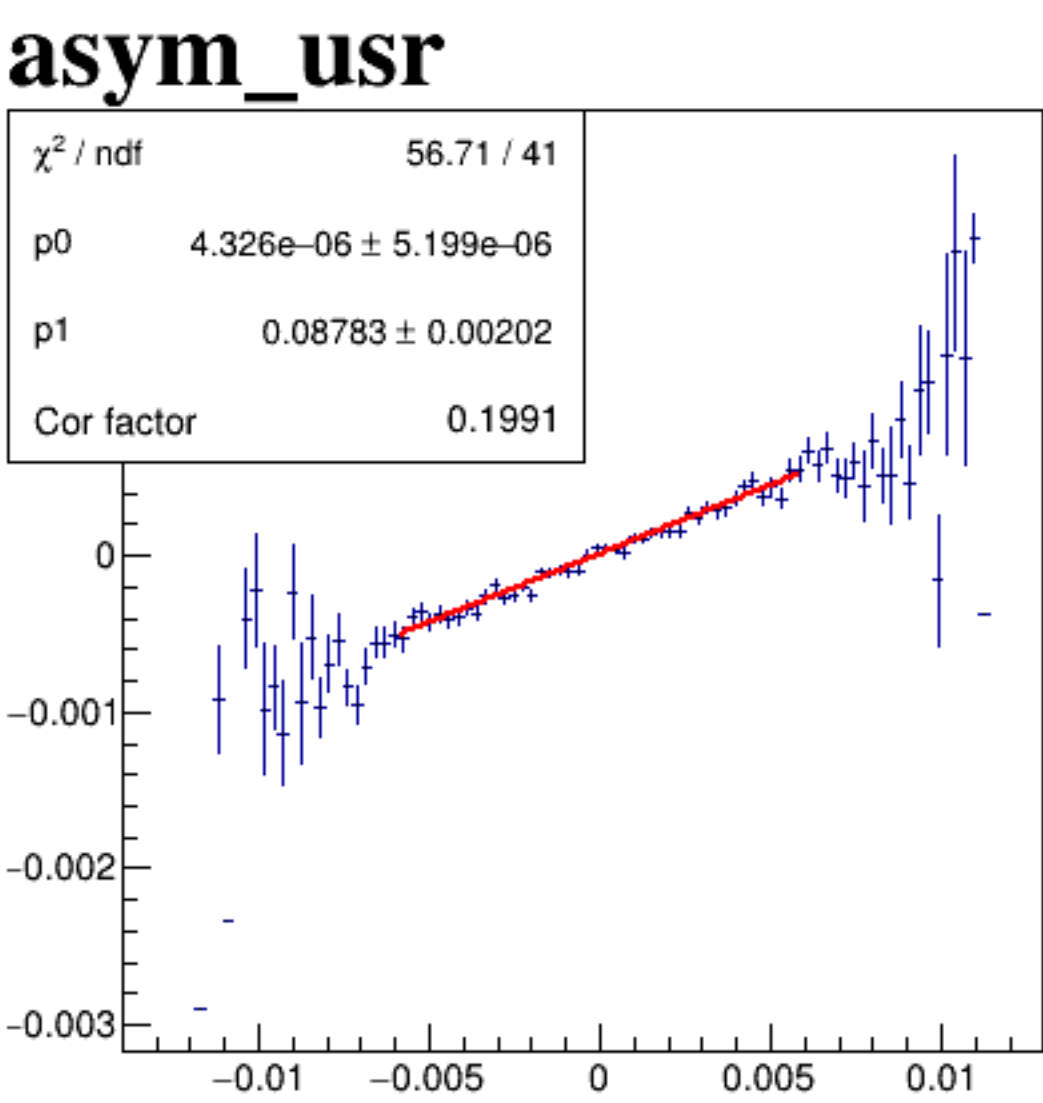
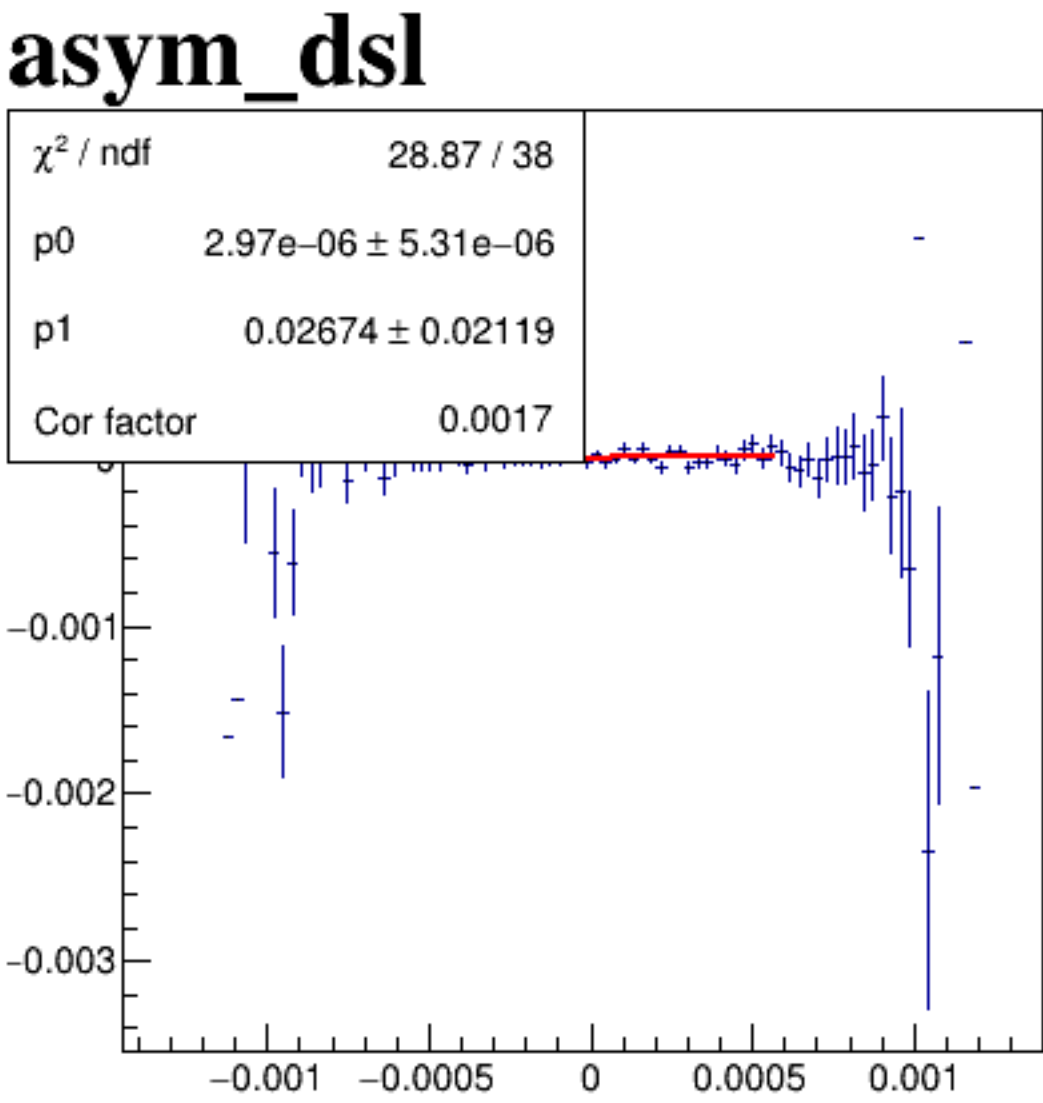
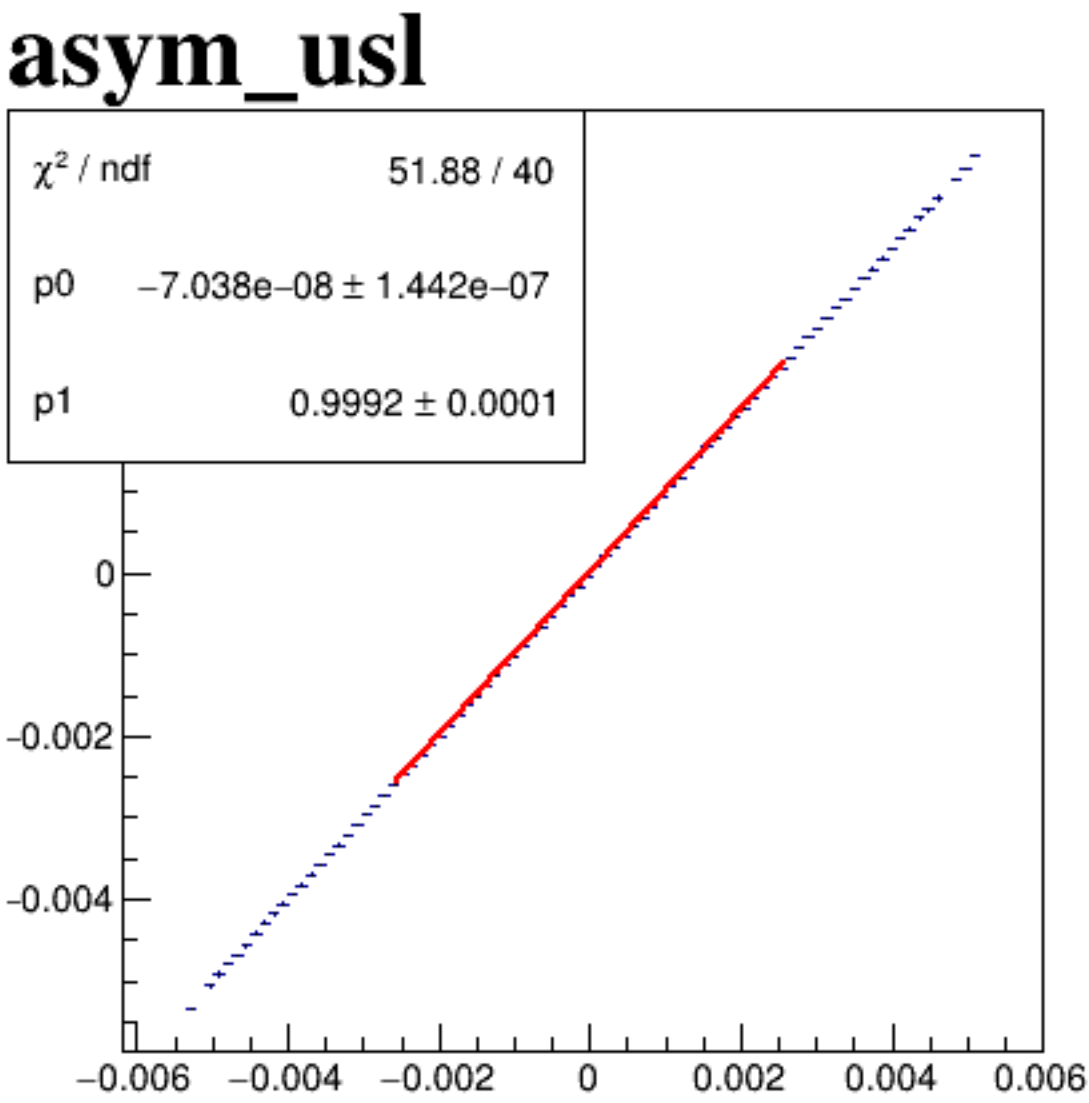
asym_usr



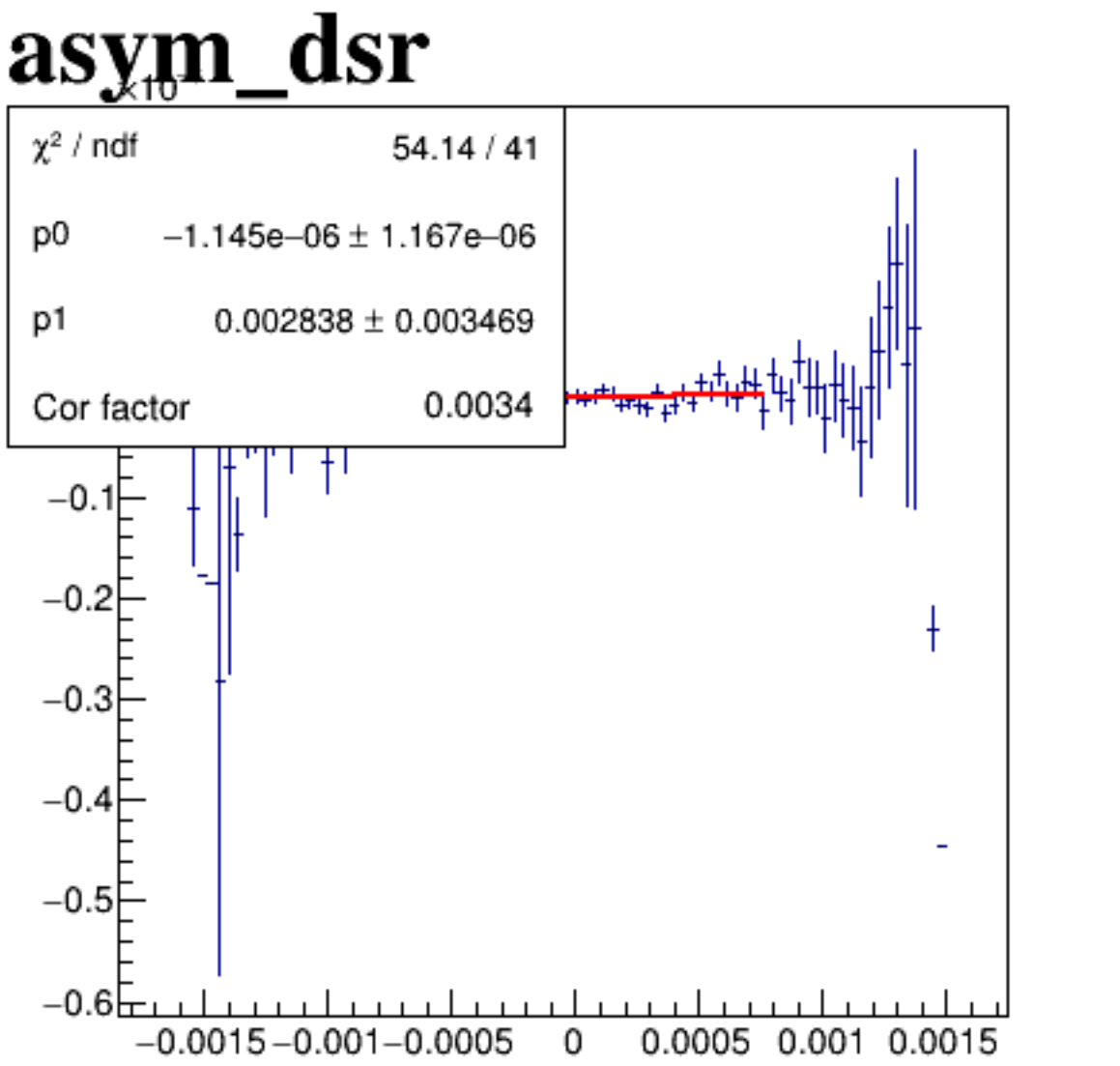
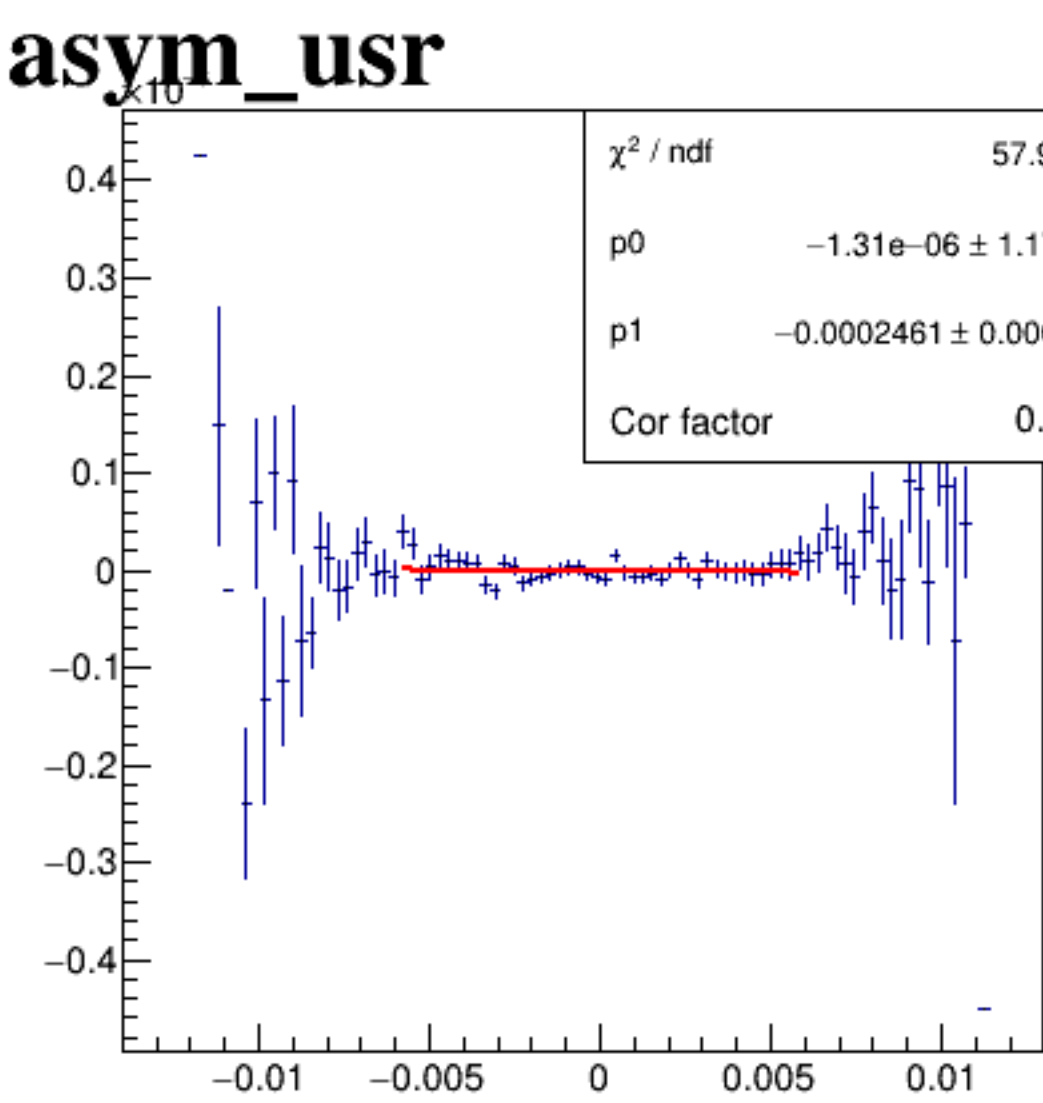
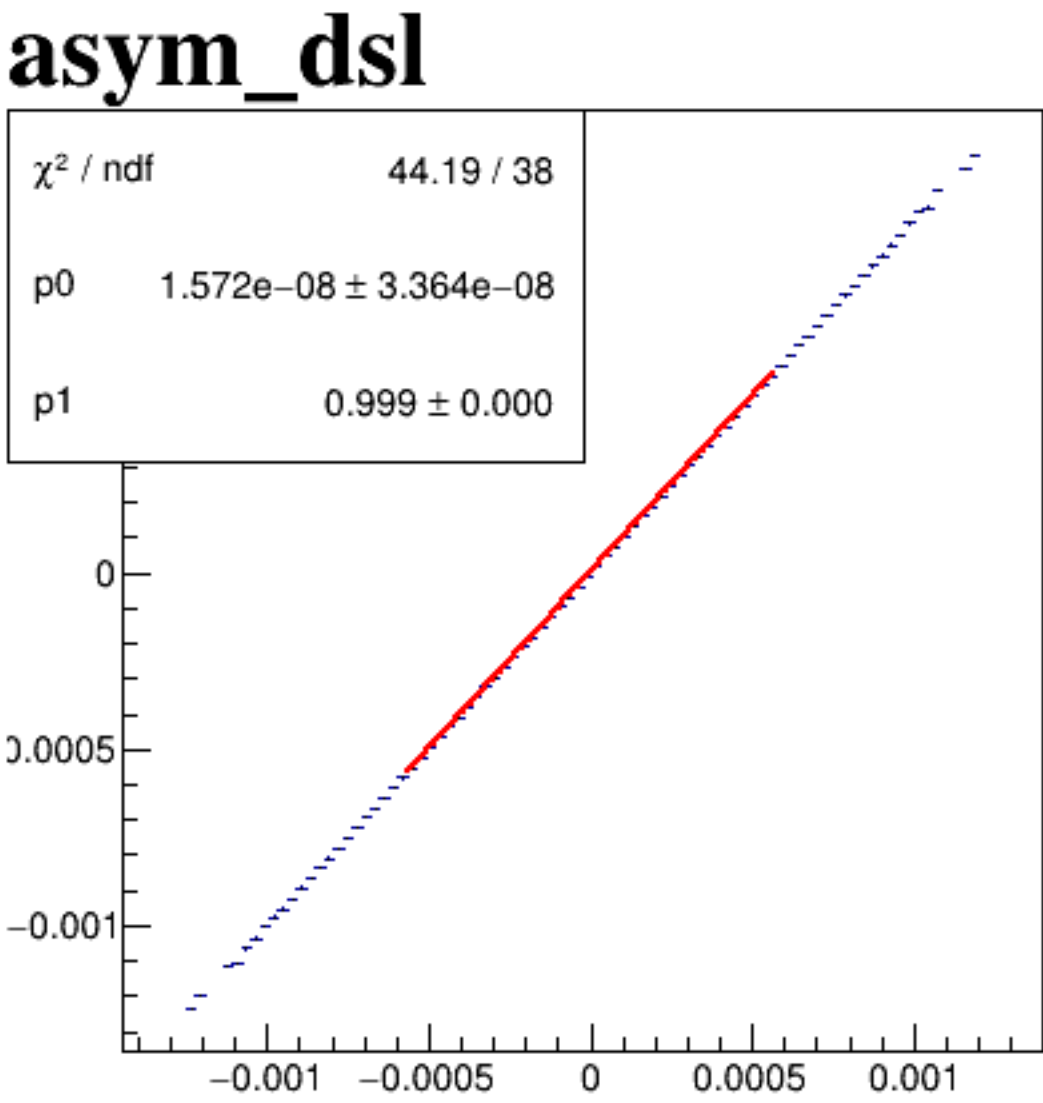
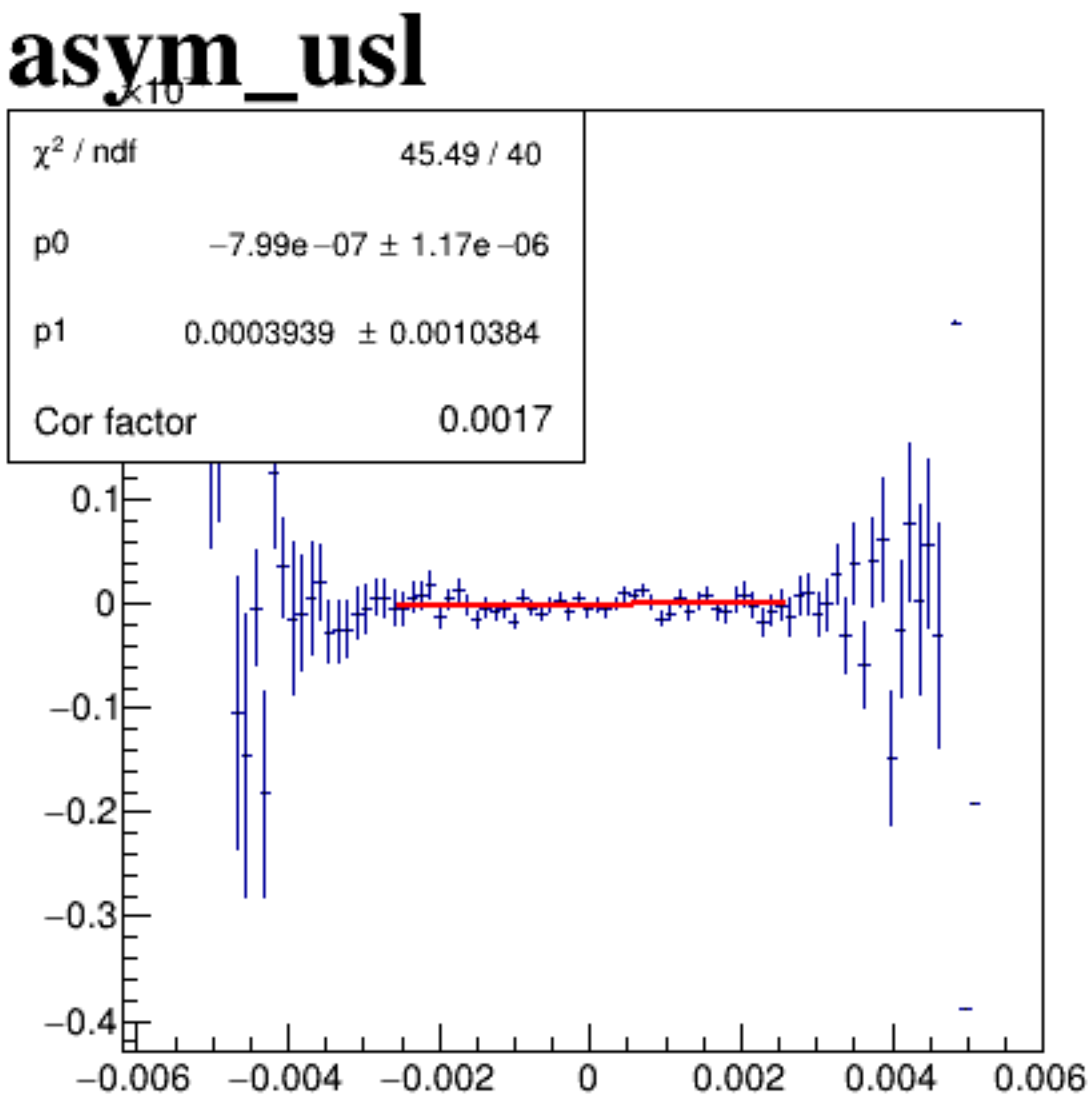
asym_dsr



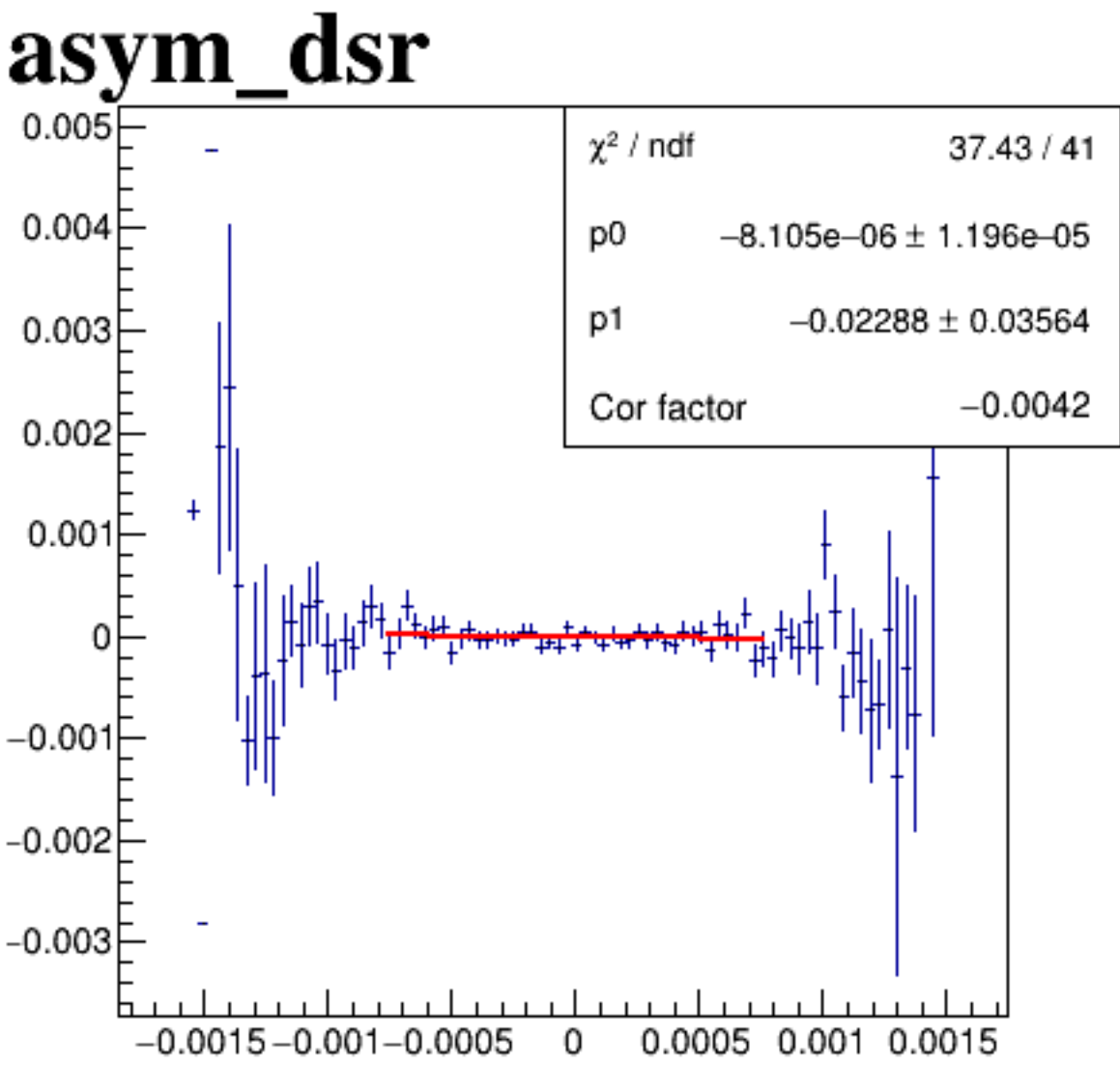
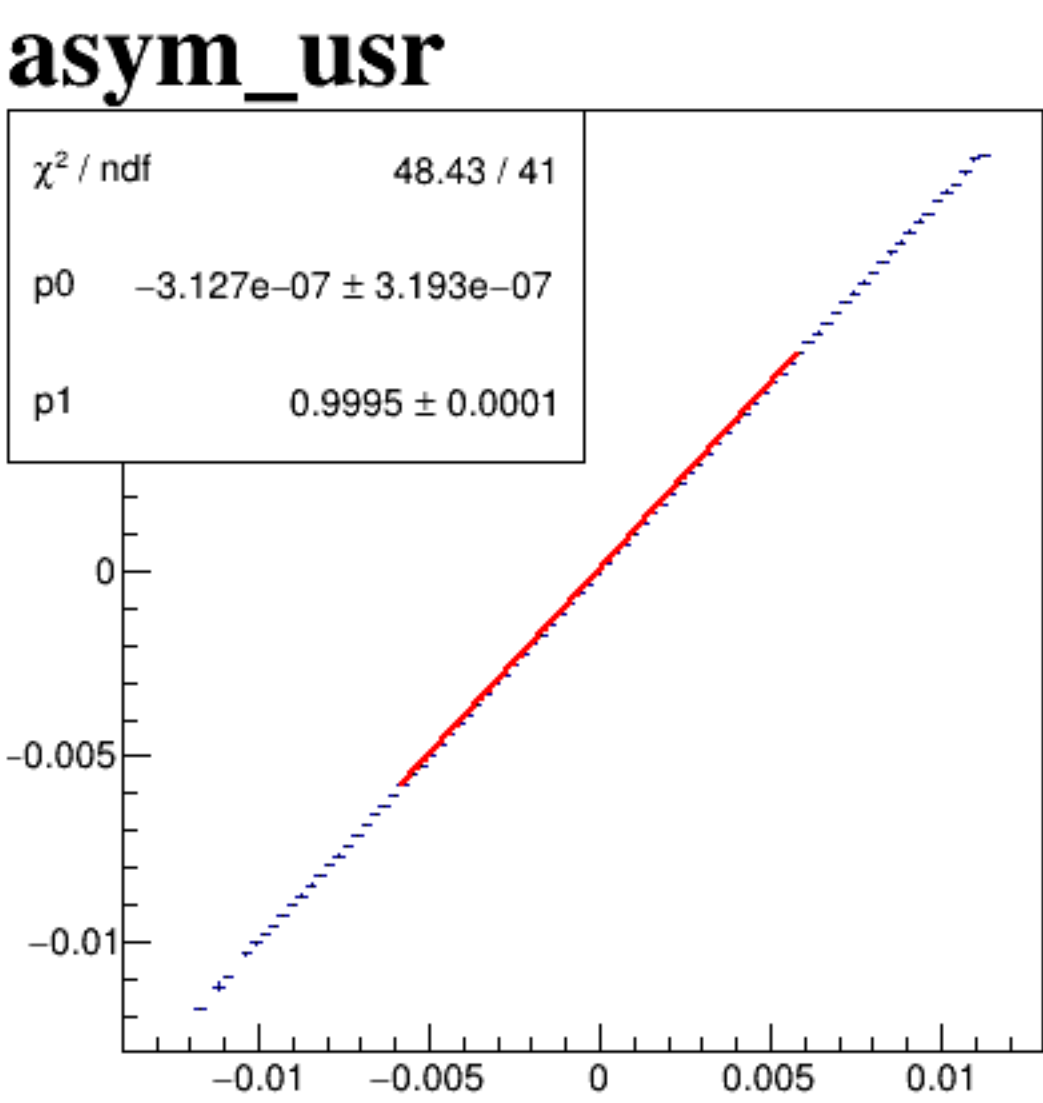
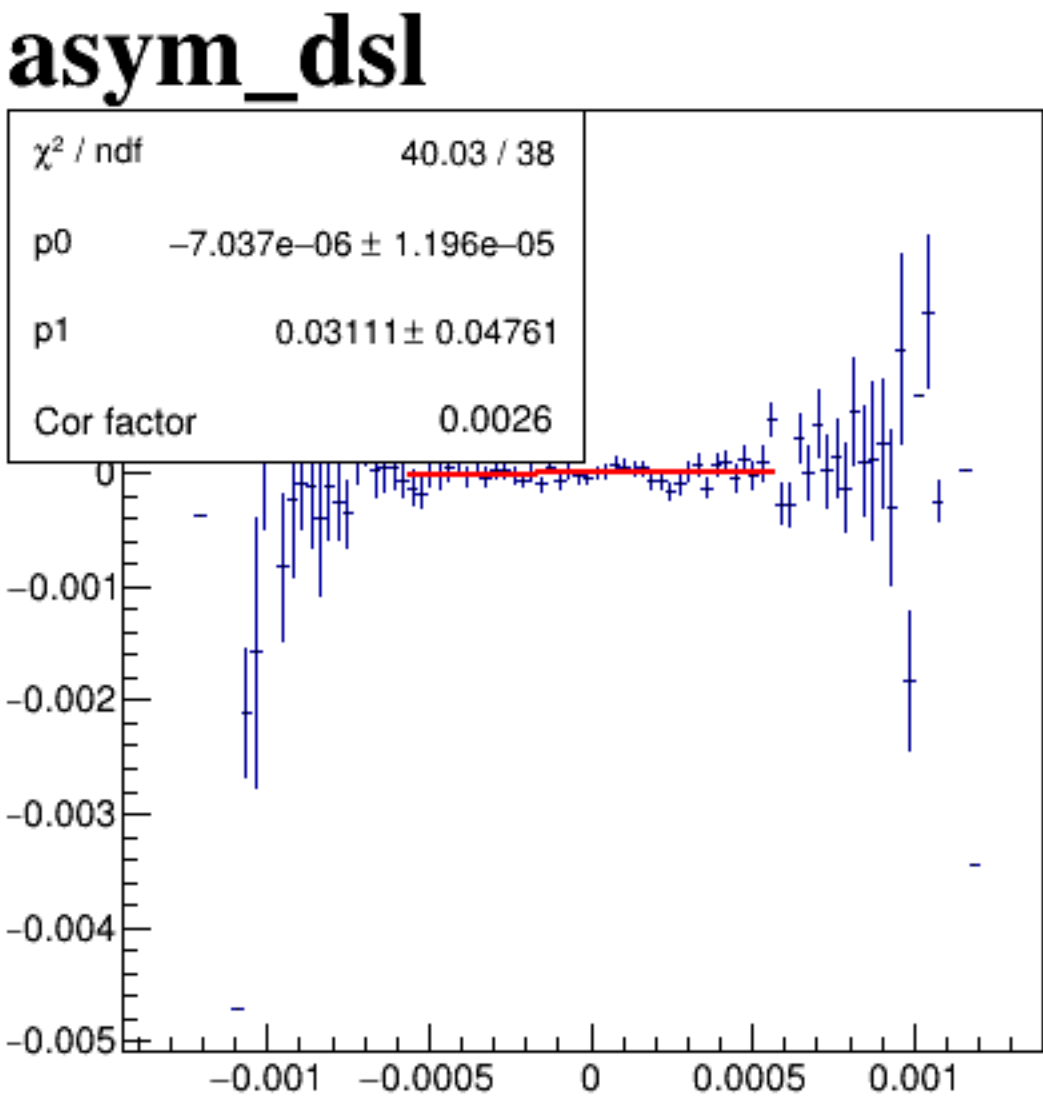
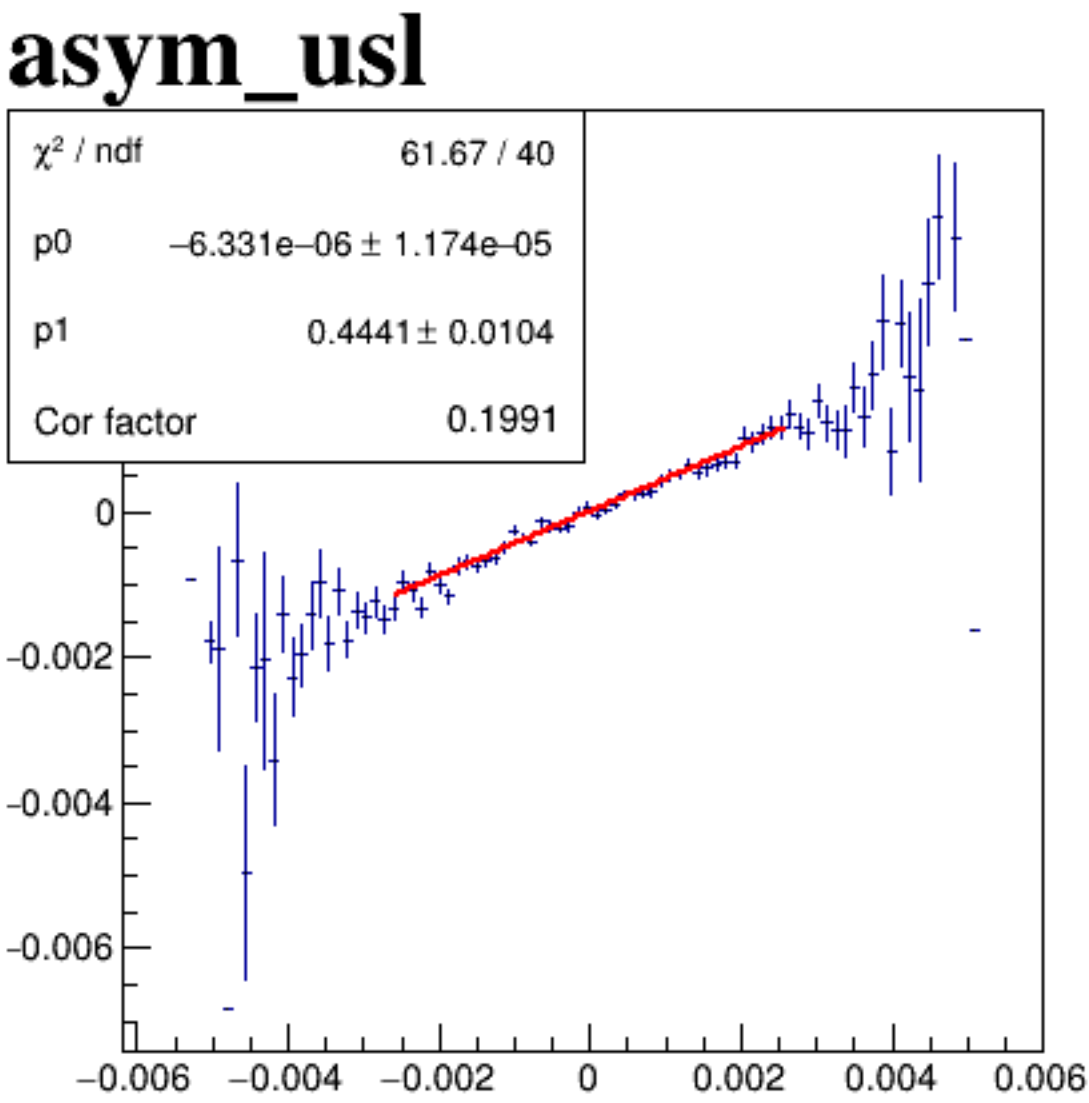
asym_usl



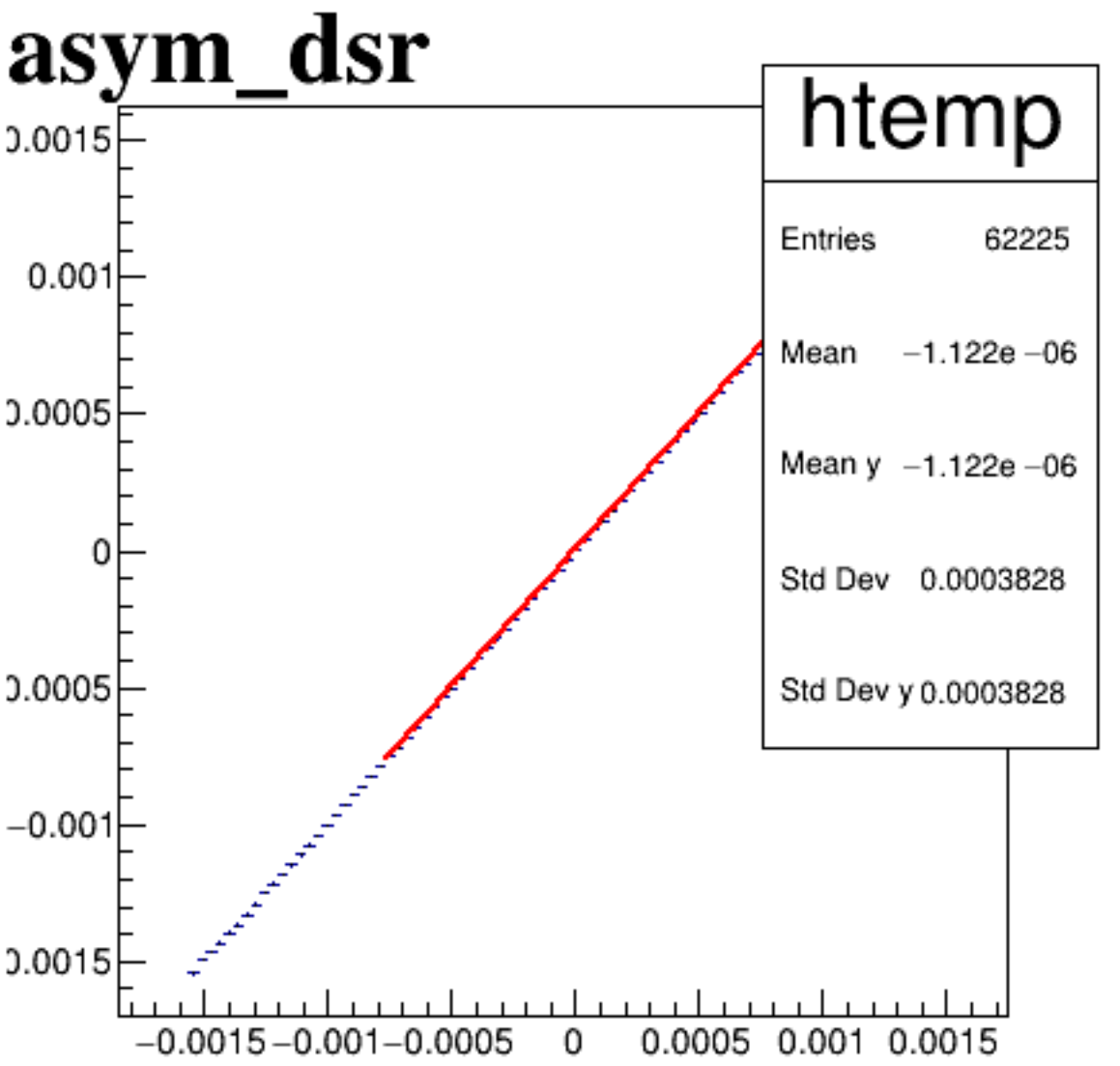
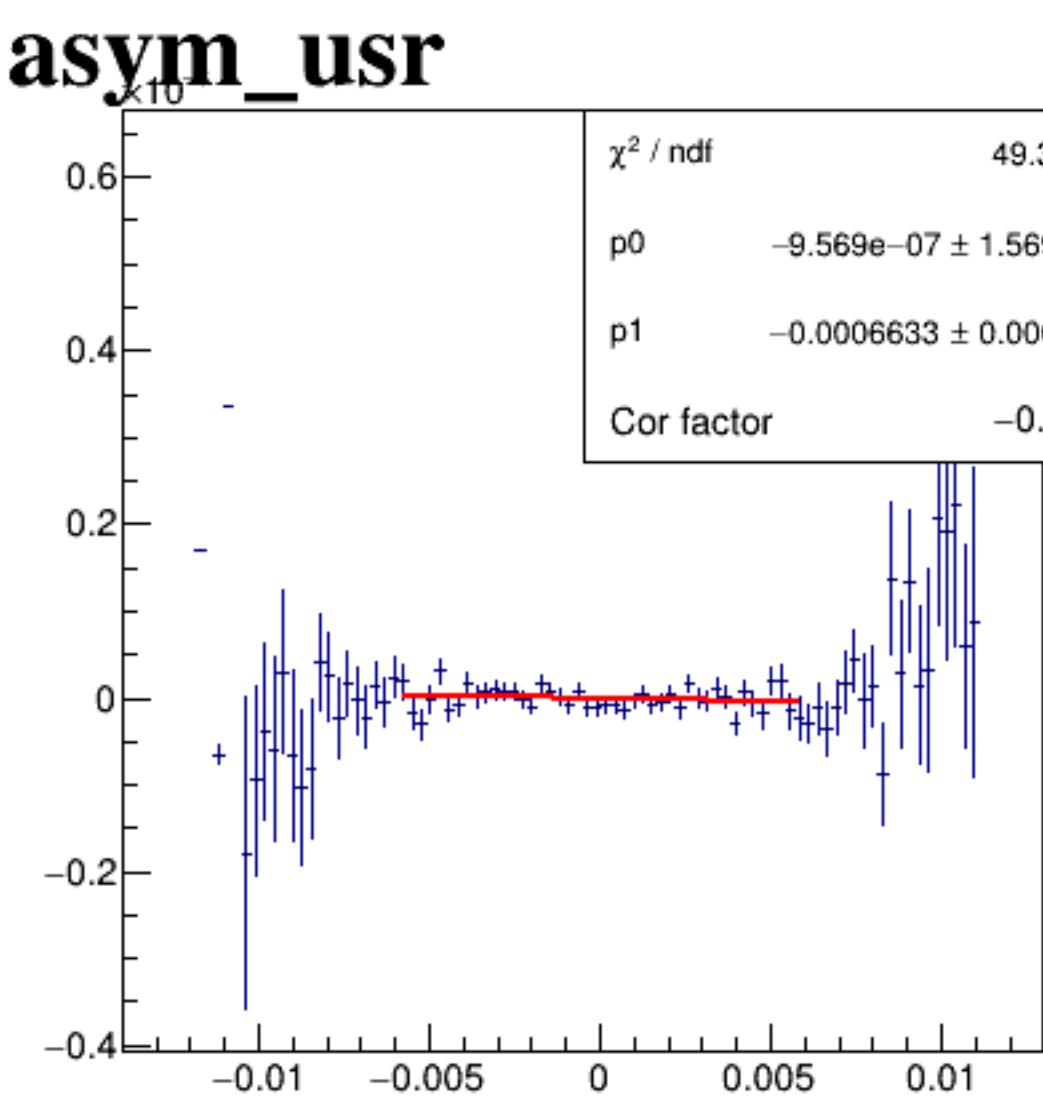
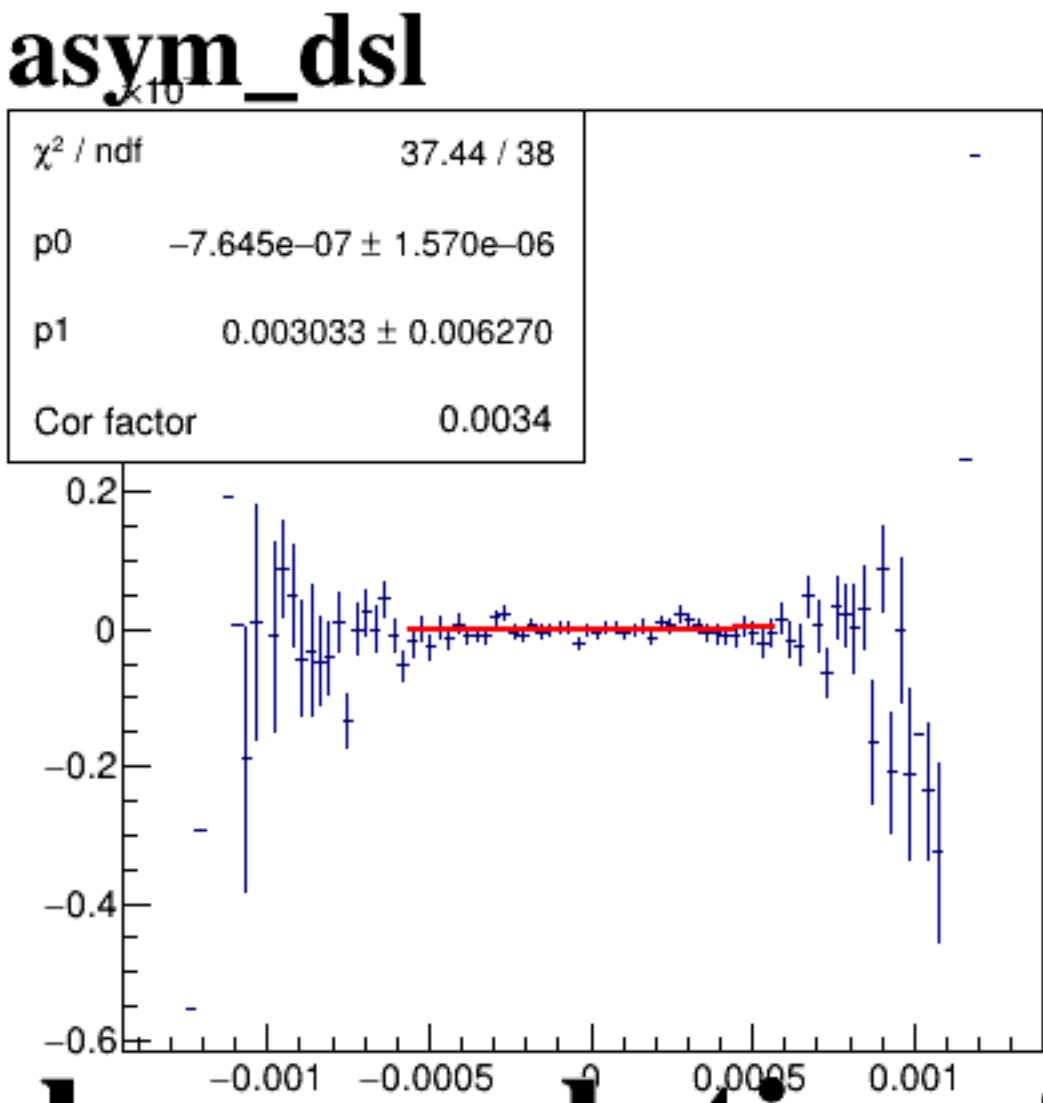
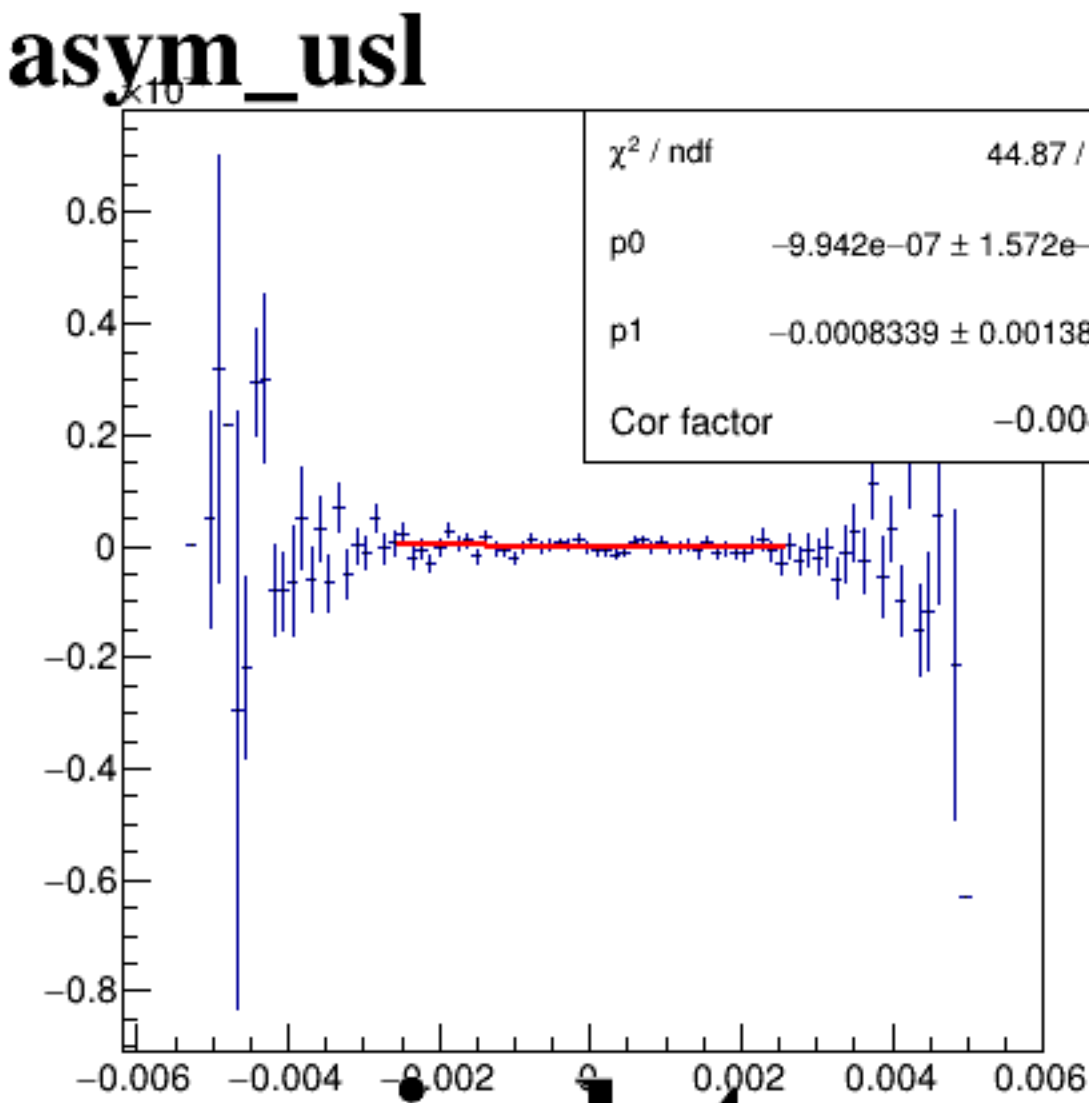
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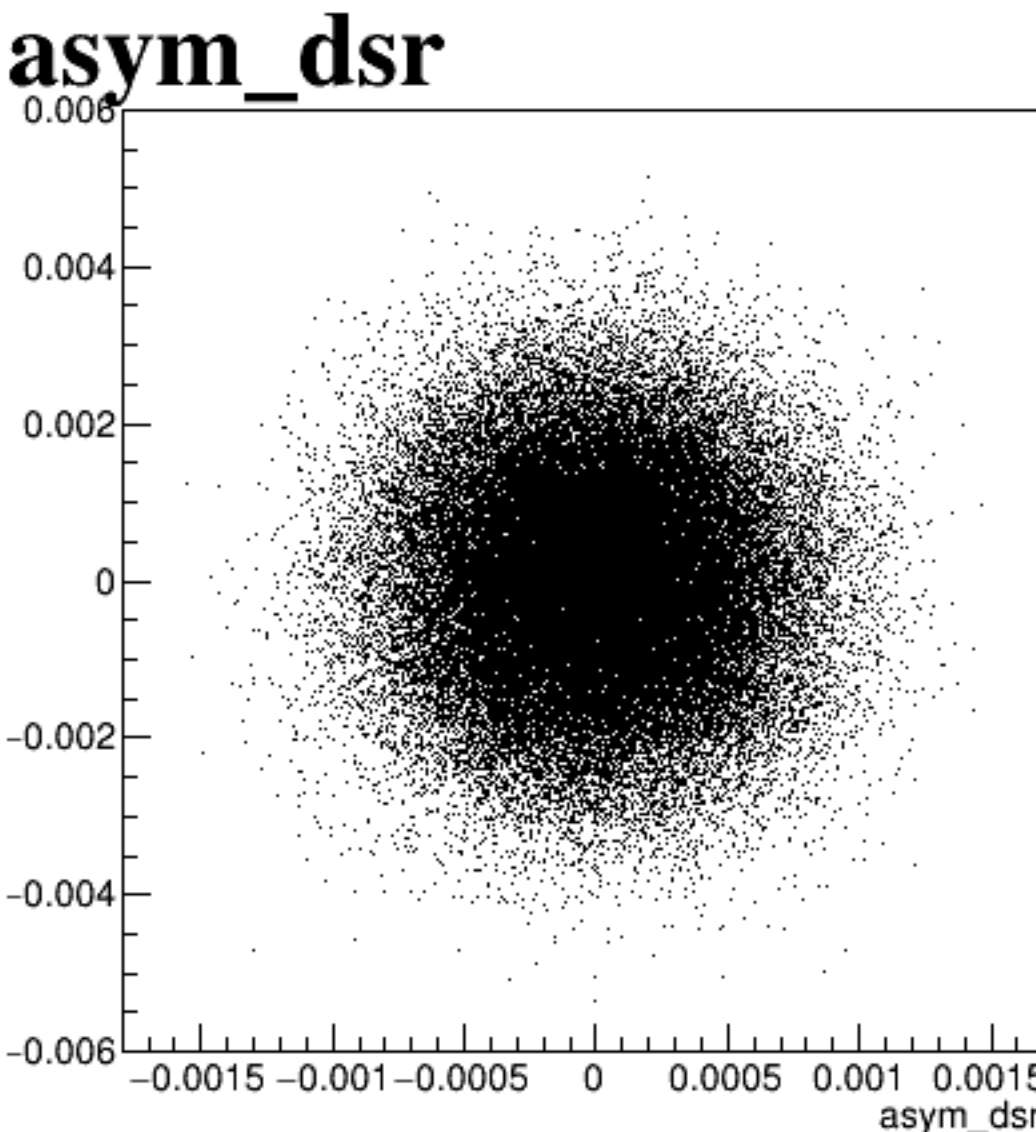
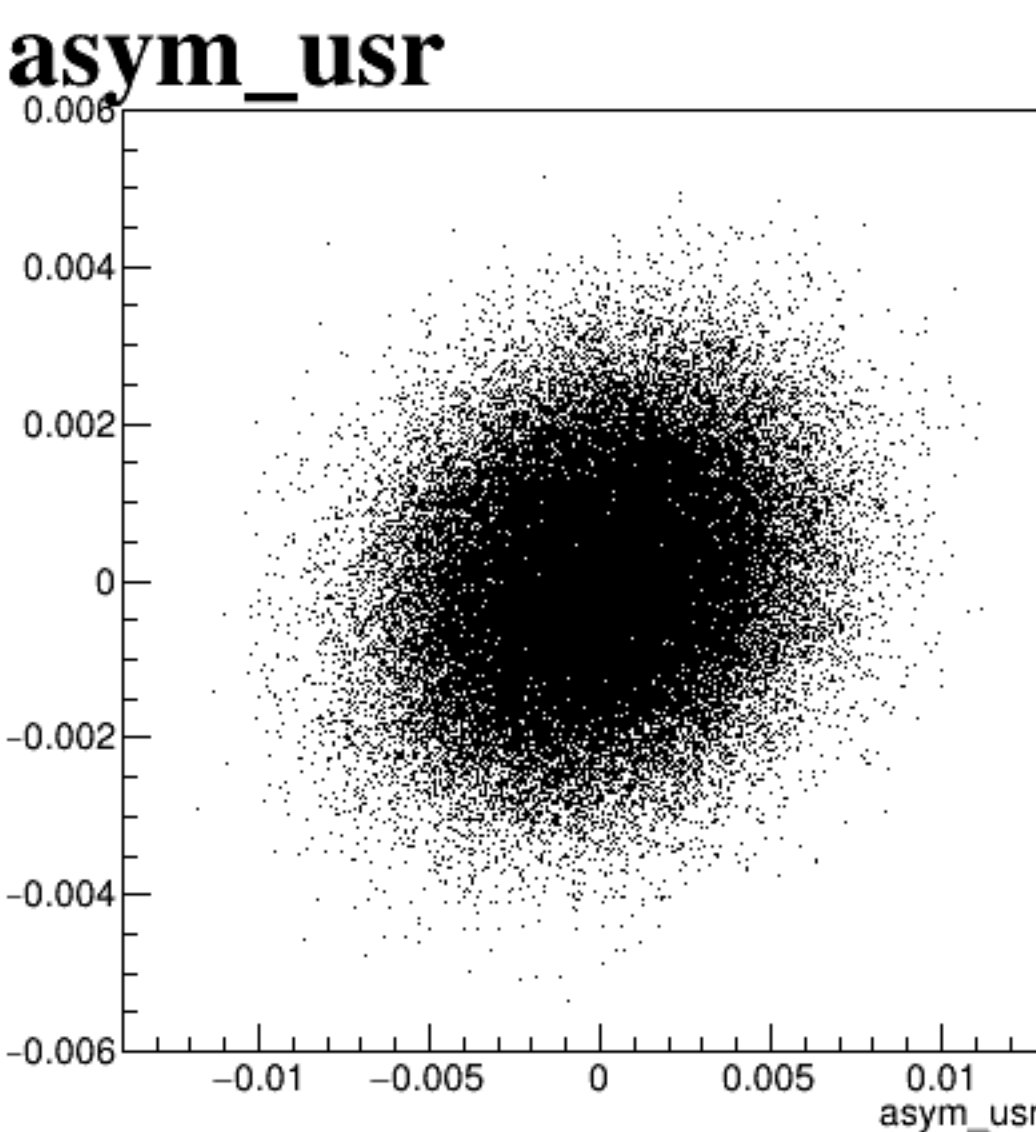
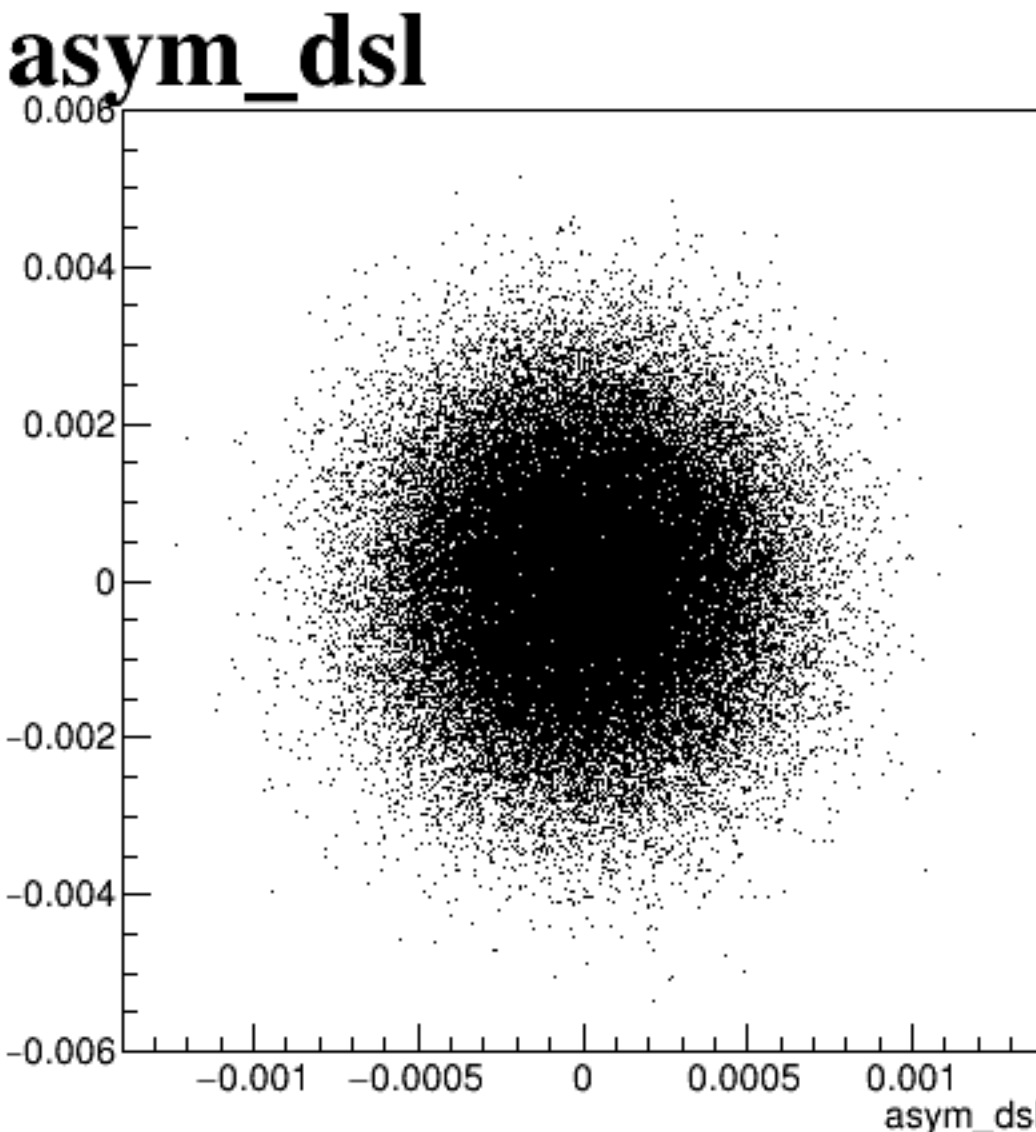
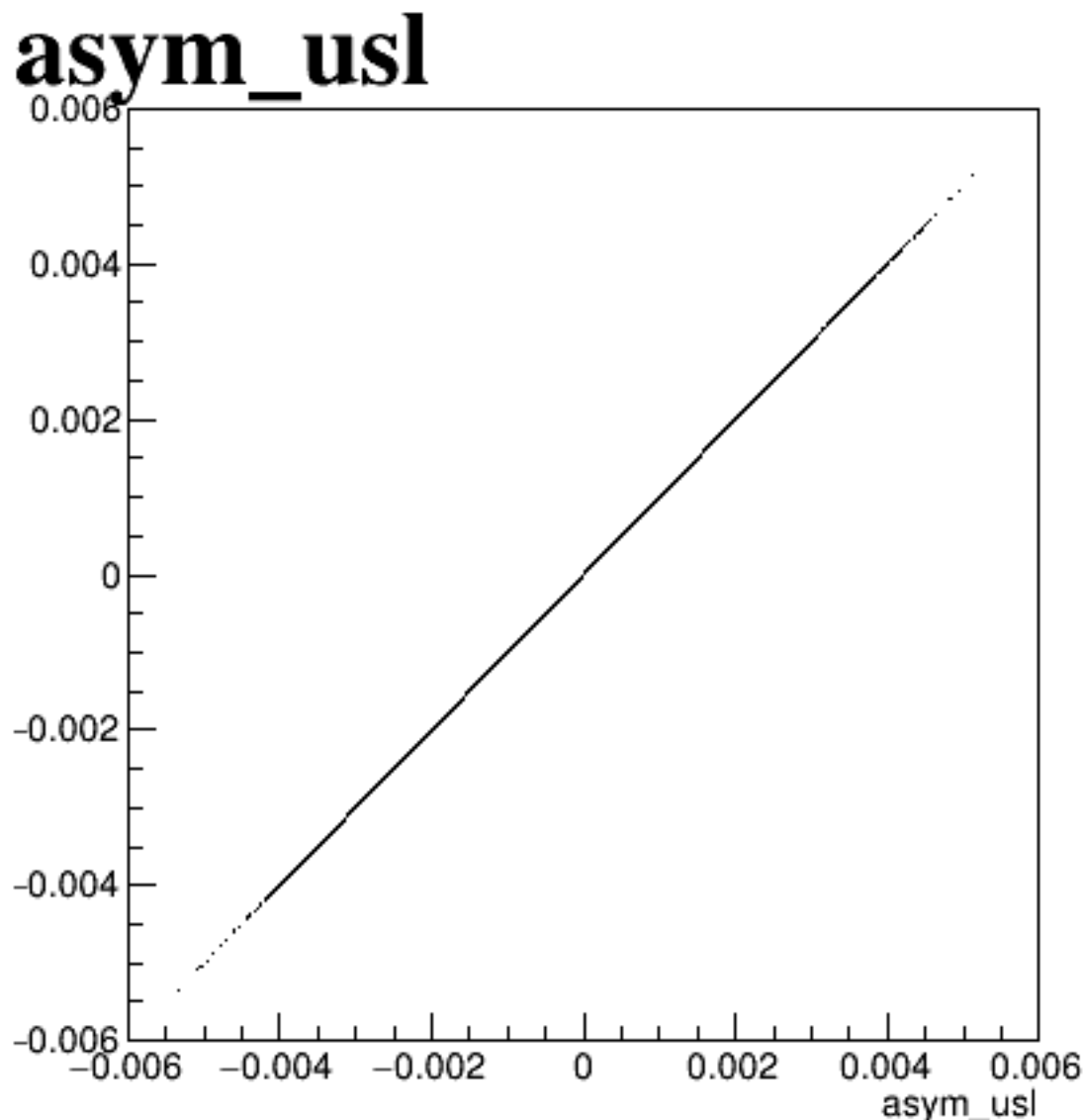
asym_usr



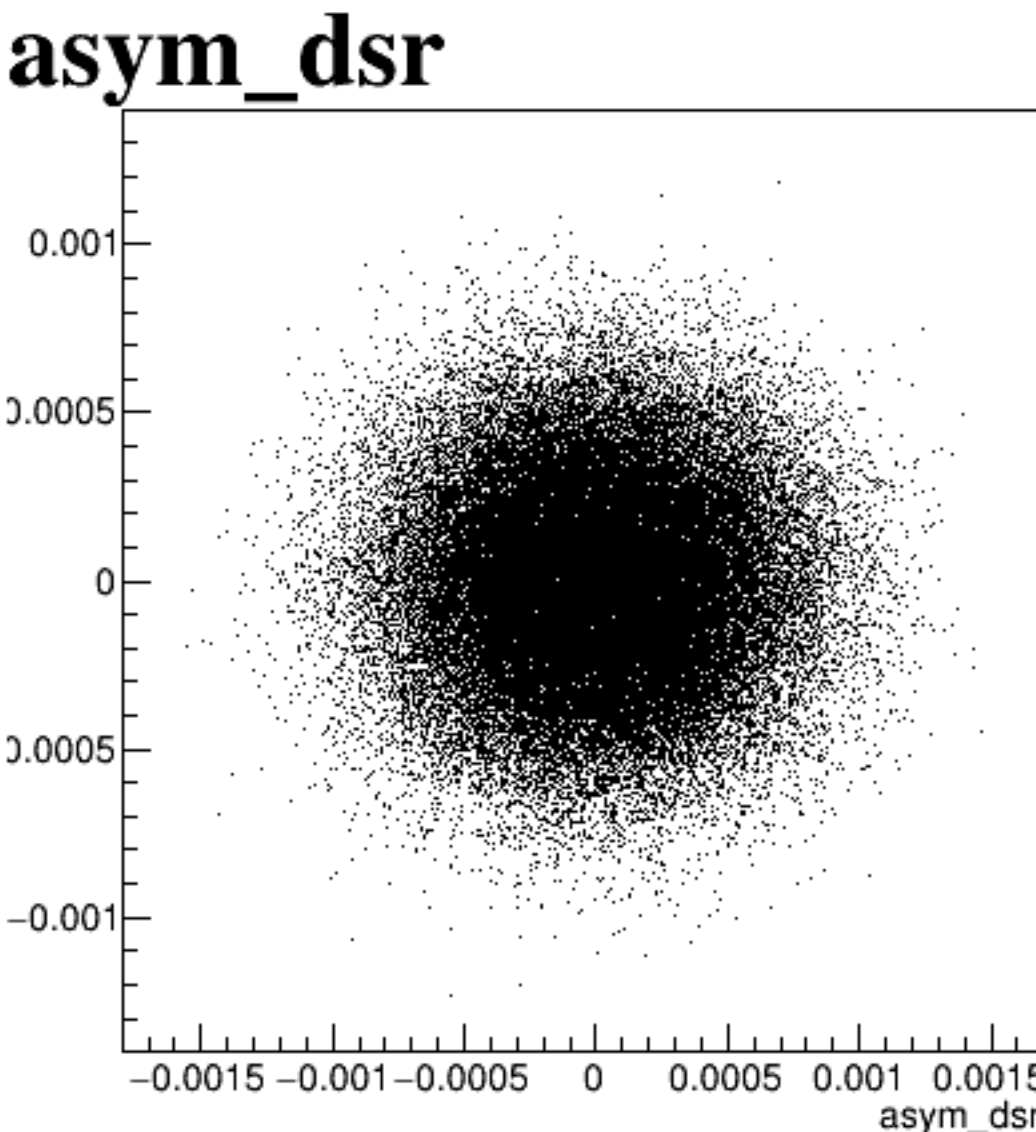
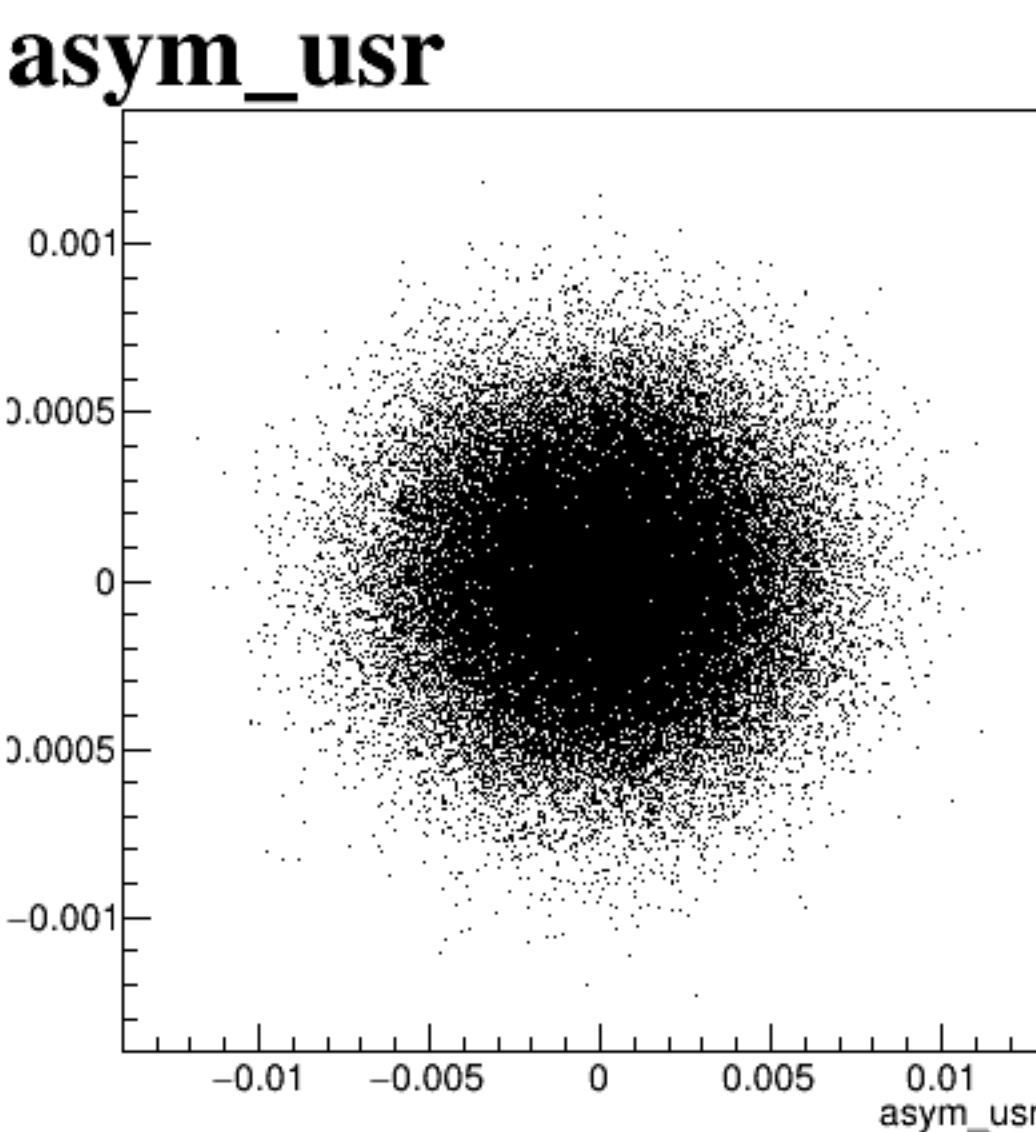
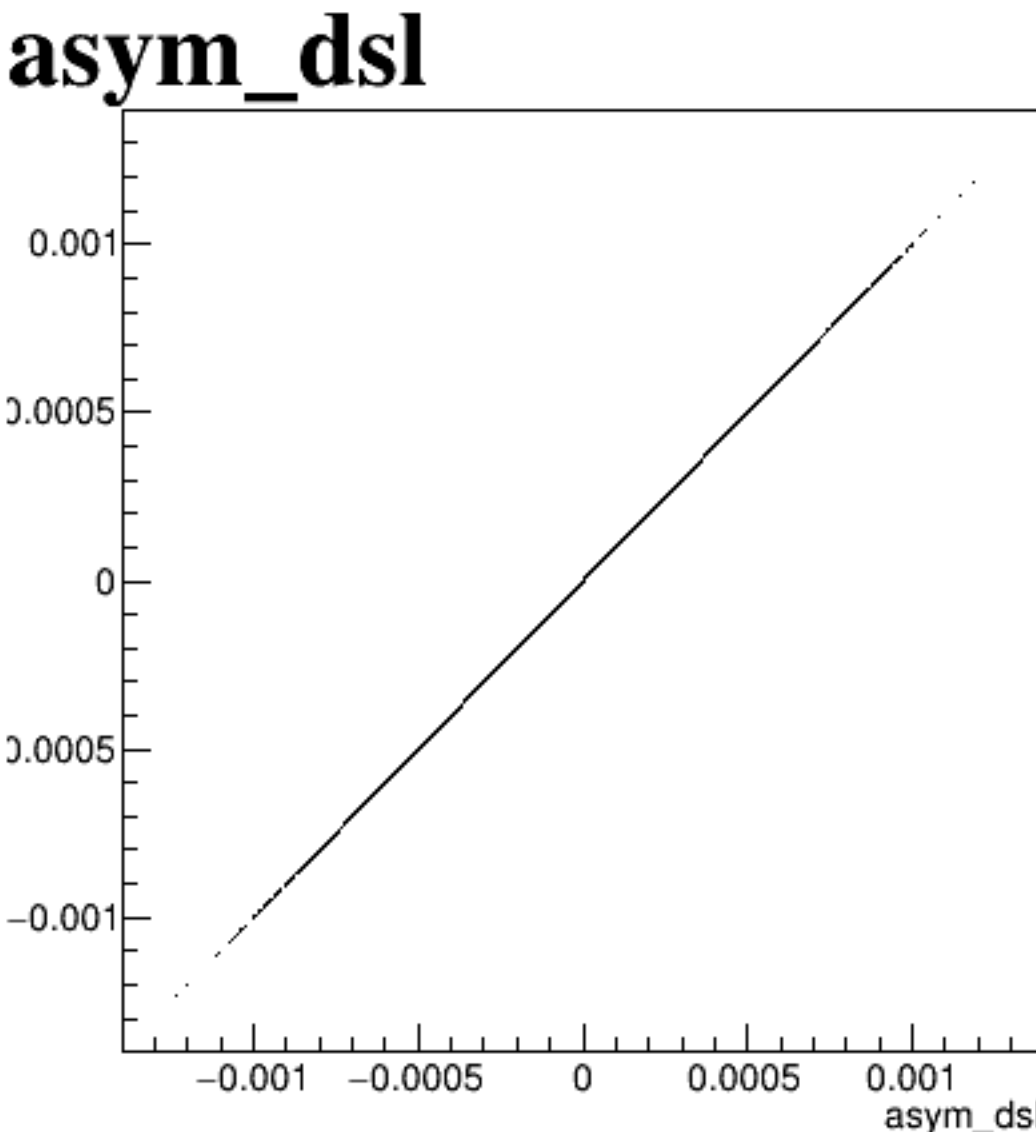
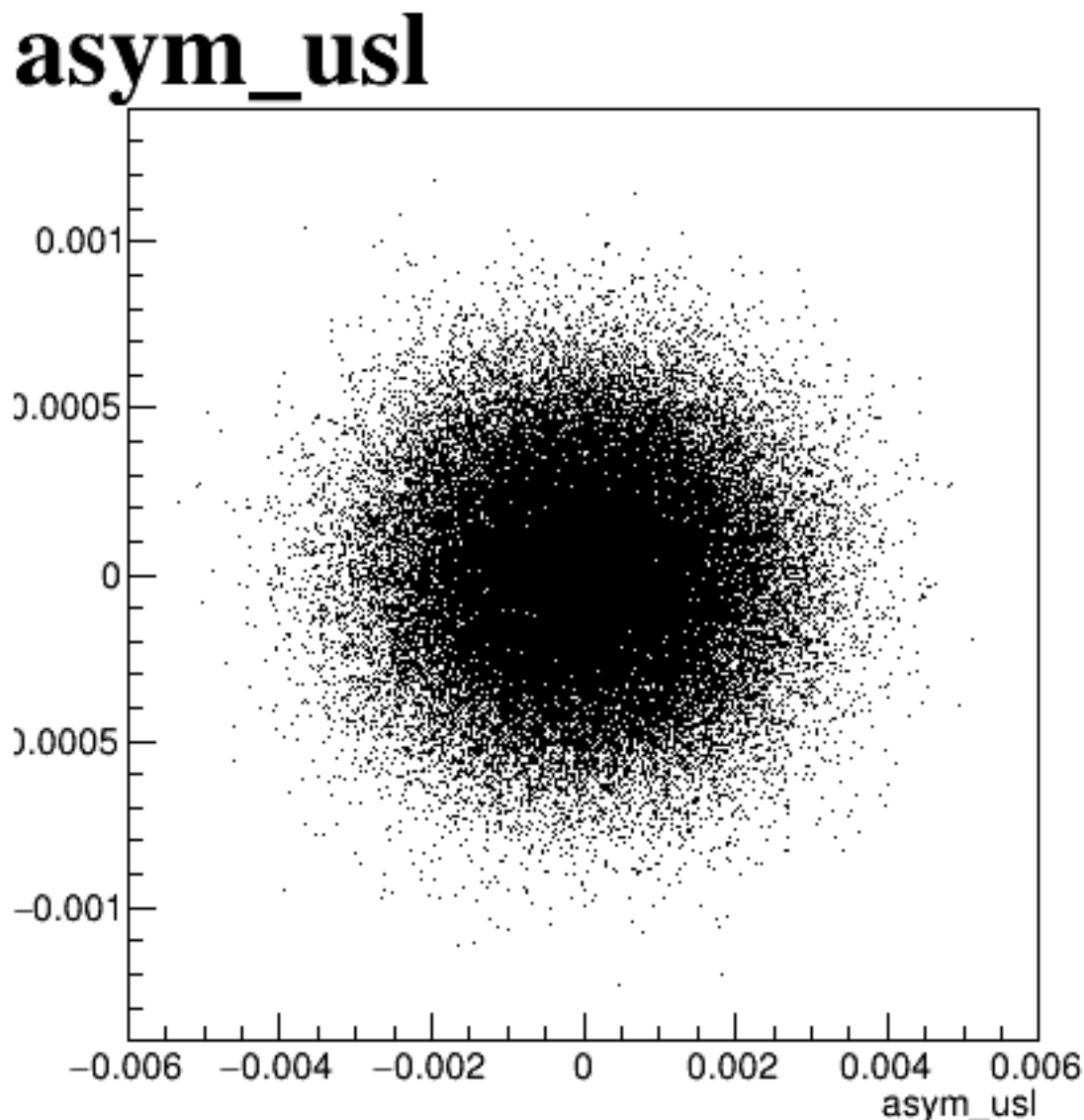
asym_dsr



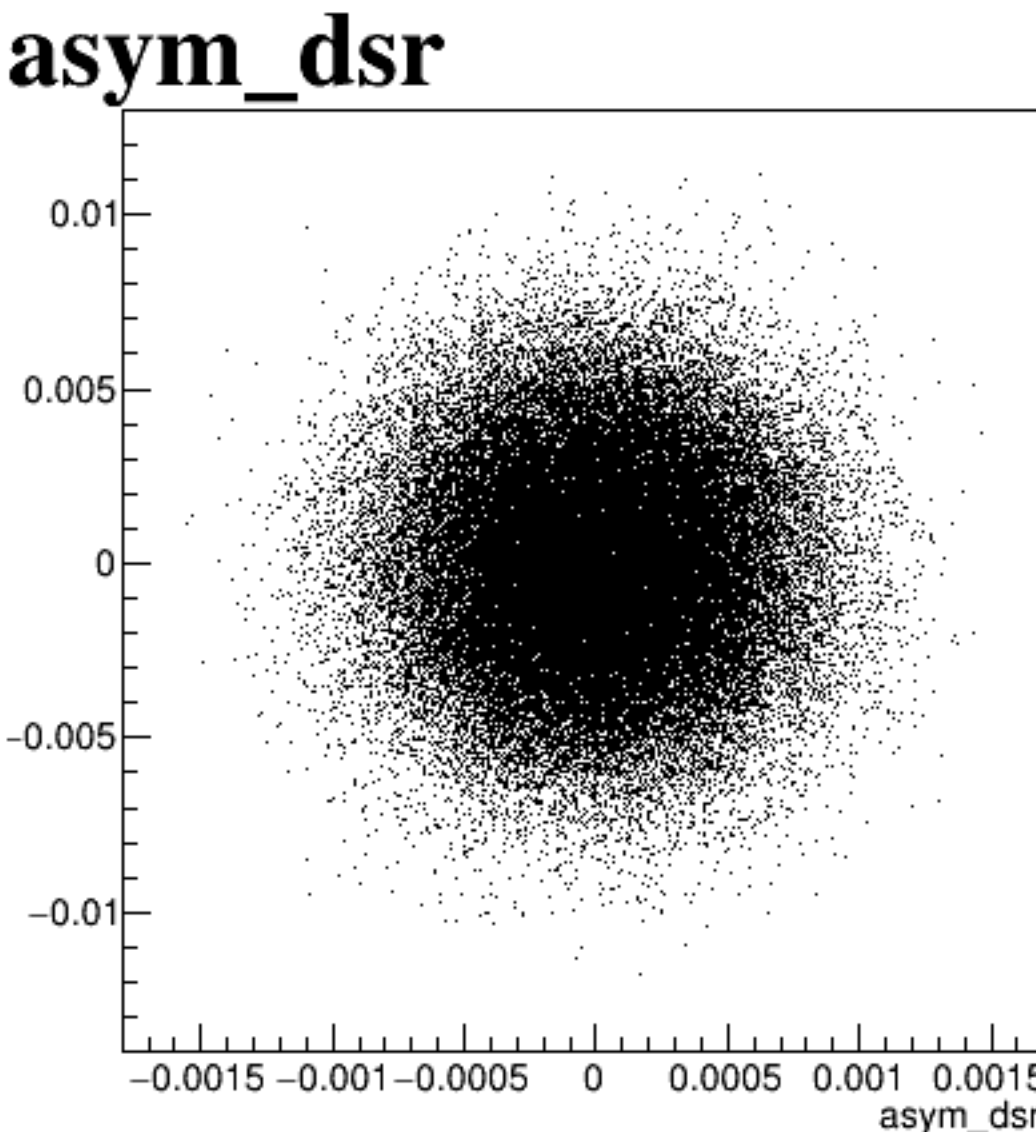
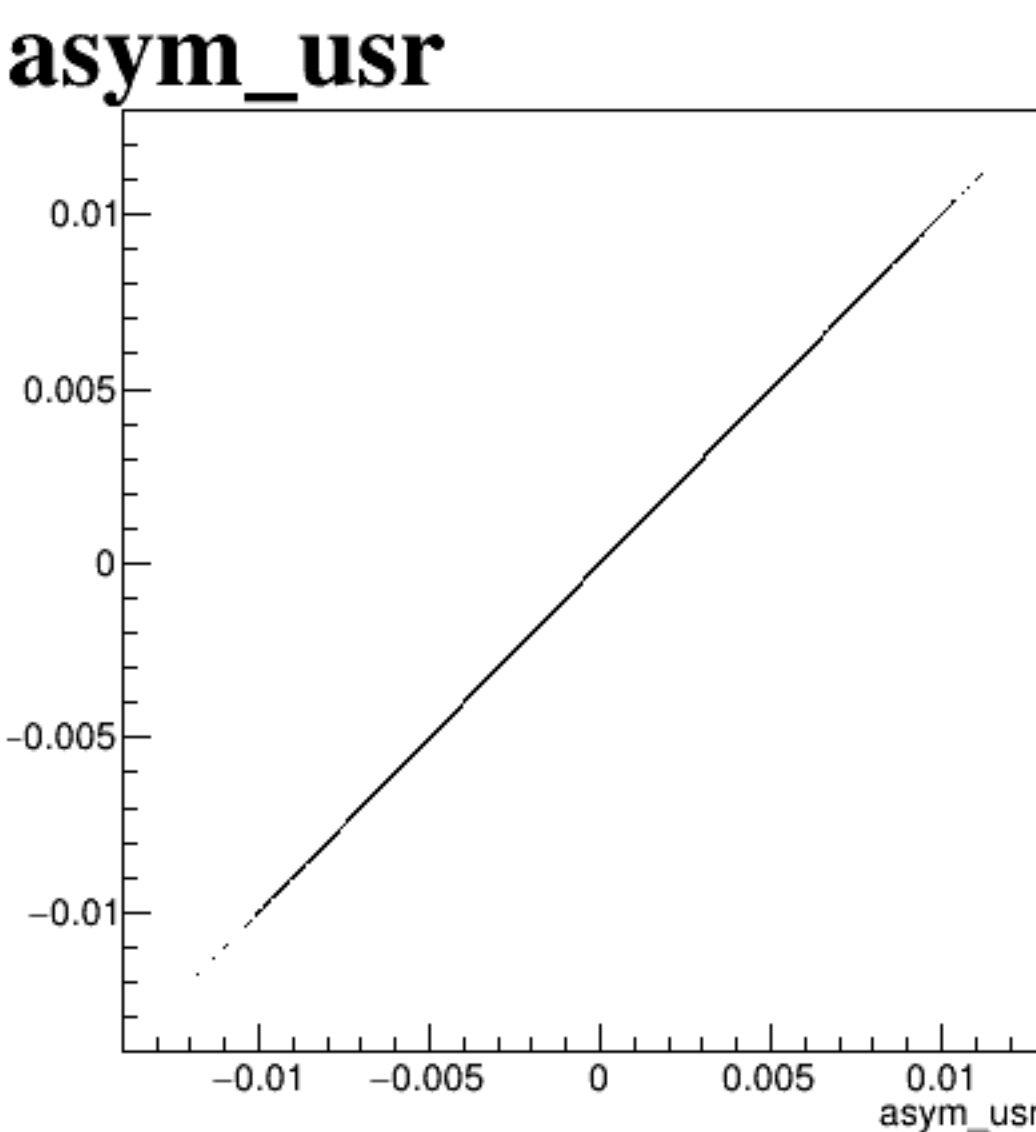
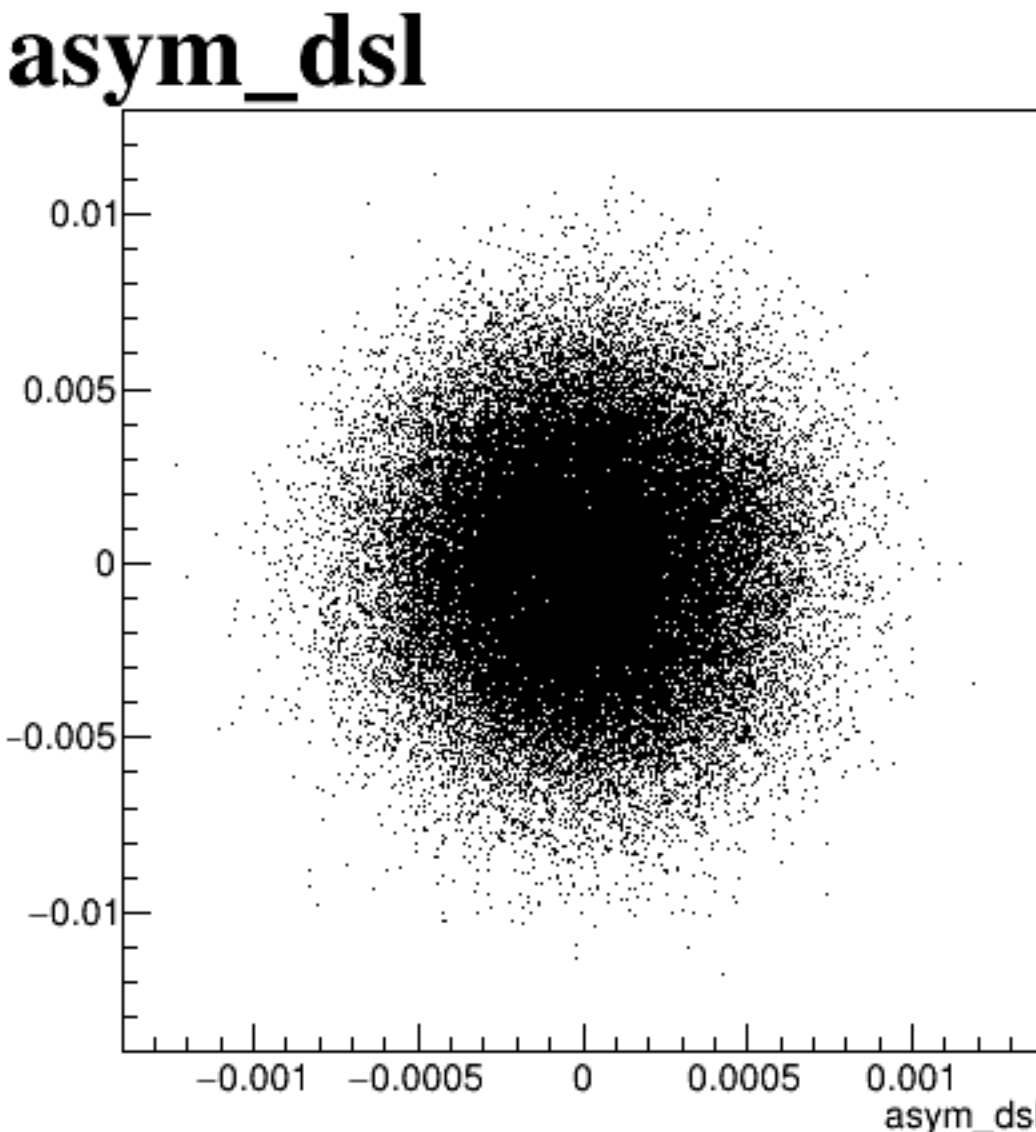
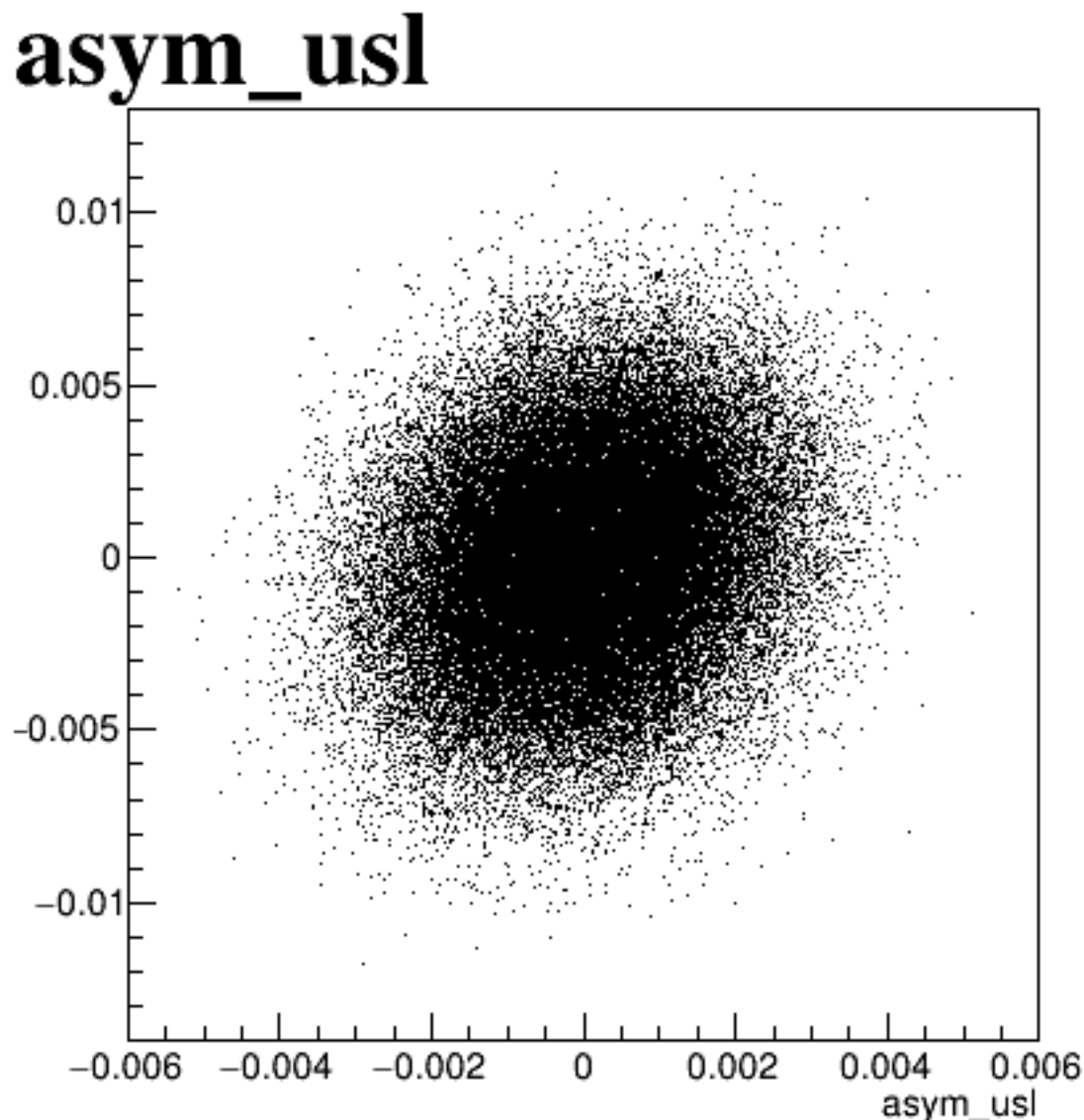
asym_usl



asym_dsl



asym_usr



asym_dsr

