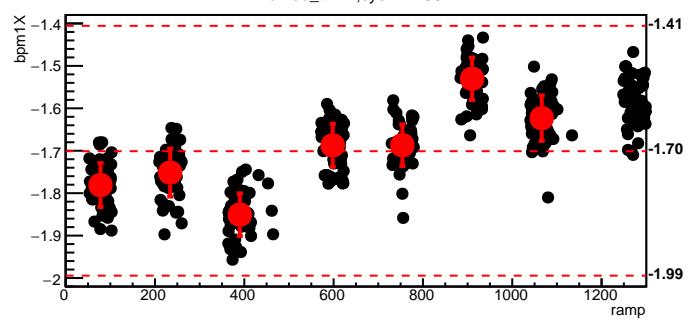
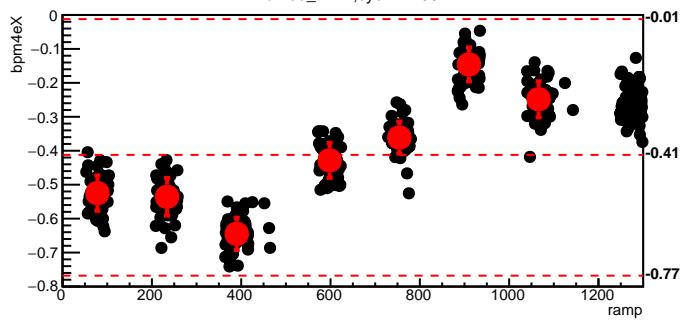


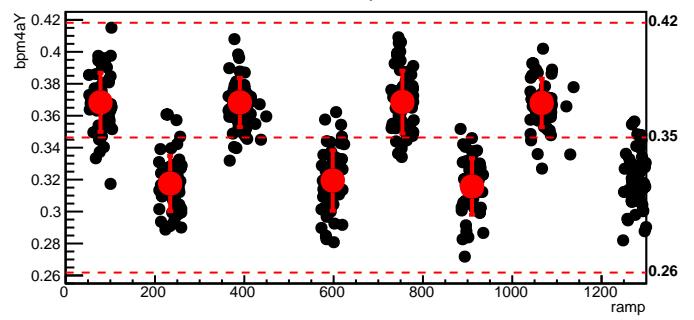
bmod Trim1,cyc=11100



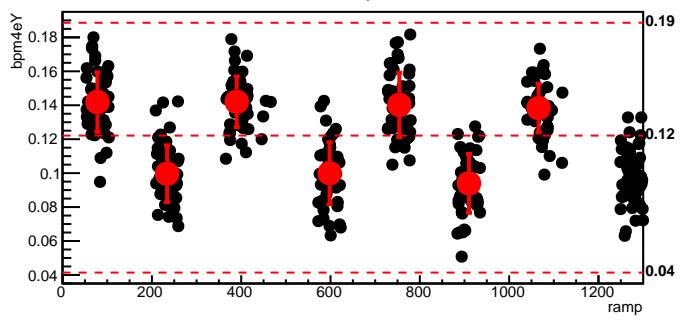
bmod Trim1,cyc=11100



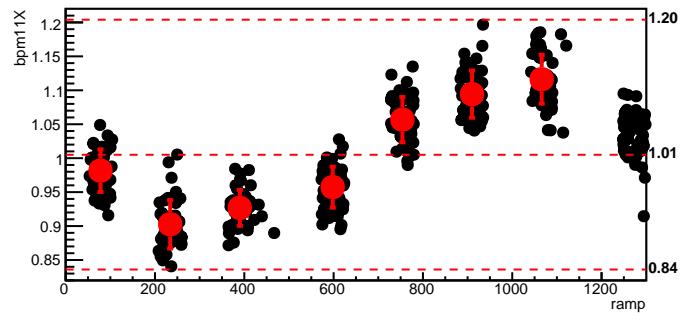
bmod Trim1,cyc=11100



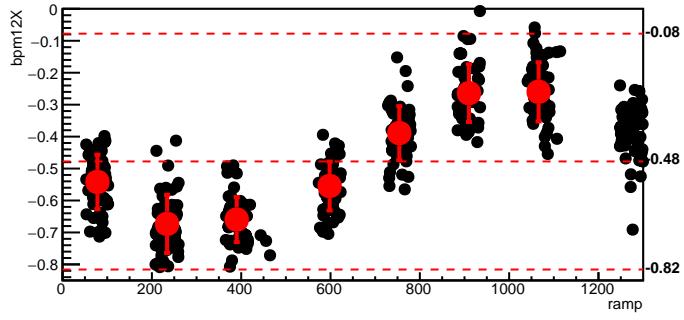
bmod Trim1,cyc=11100



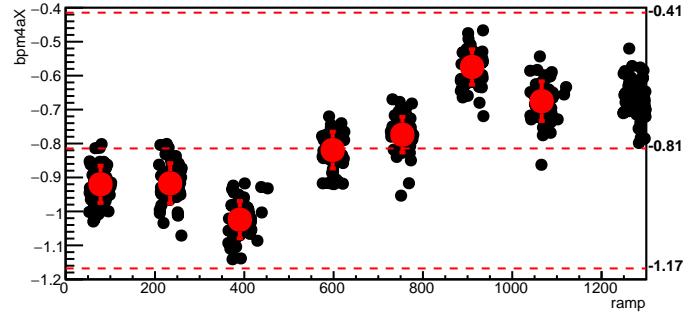
bmod Trim1,cyc=11100



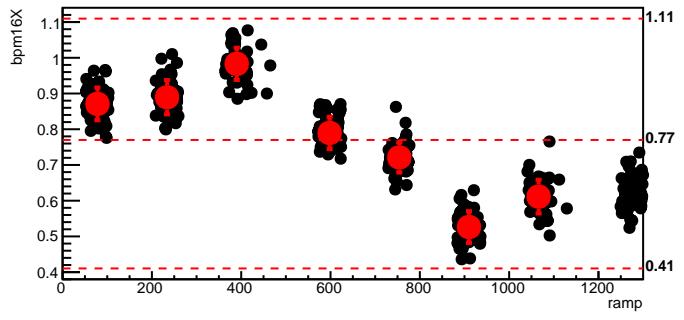
bmod Trim1,cyc=11100



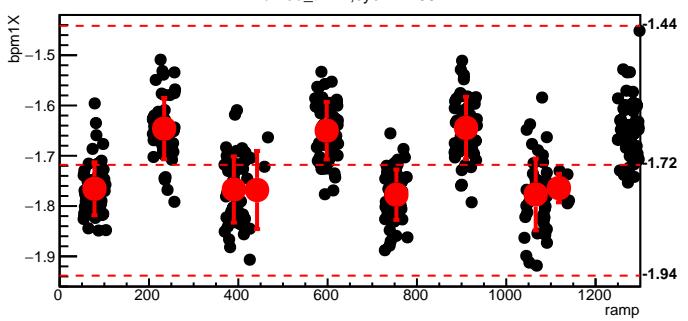
bmod Trim1,cyc=11100



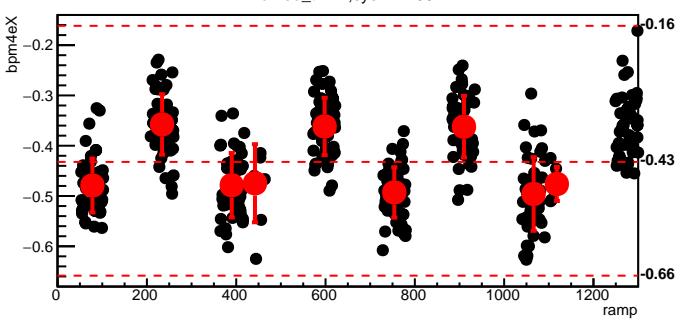
bmod Trim1,cyc=11100



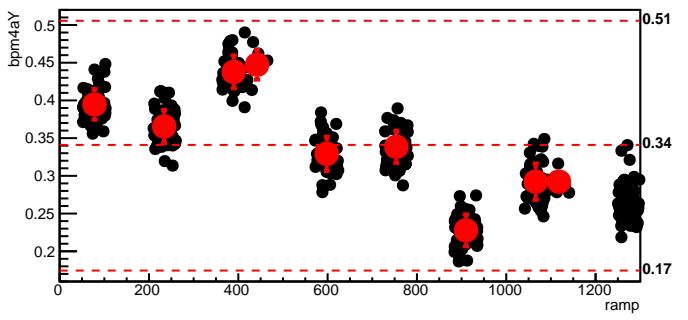
bmod Trim2,cyc=11100



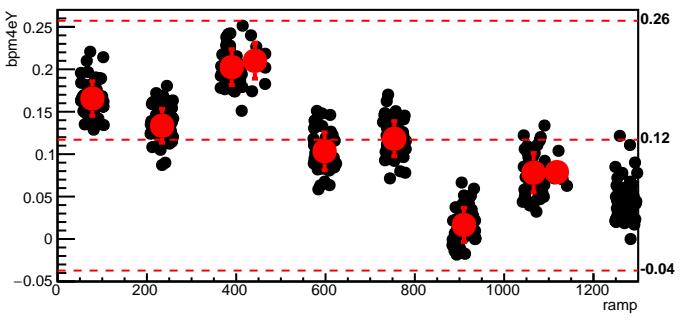
bmod Trim2,cyc=11100



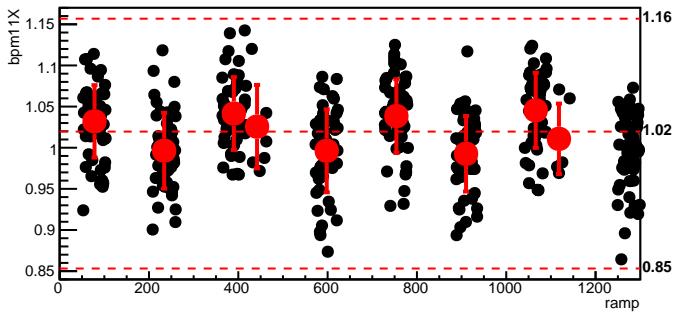
bmod Trim2,cyc=11100



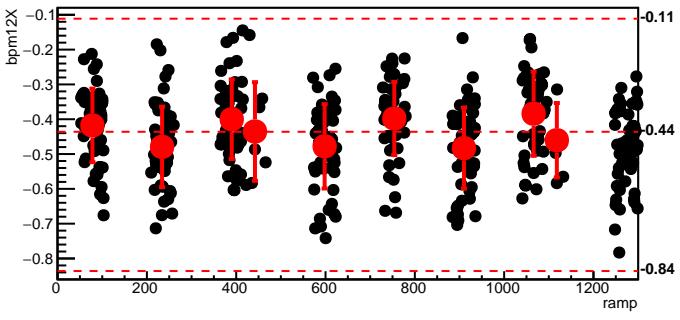
bmod Trim2,cyc=11100



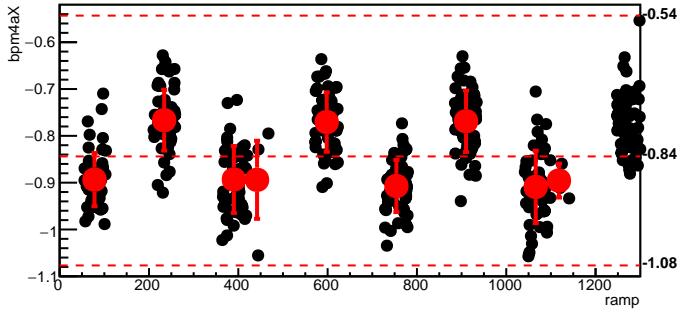
bmod Trim2,cyc=11100



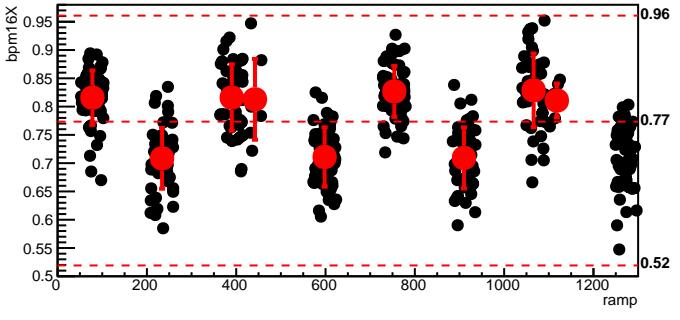
bmod Trim2,cyc=11100



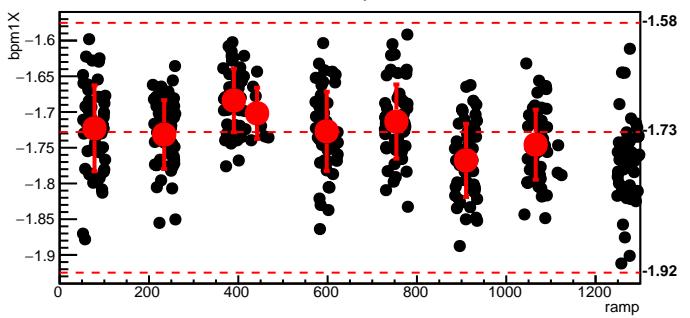
bmod Trim2,cyc=11100



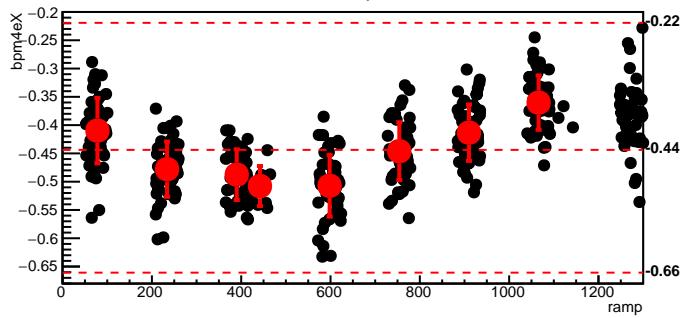
bmod Trim2,cyc=11100



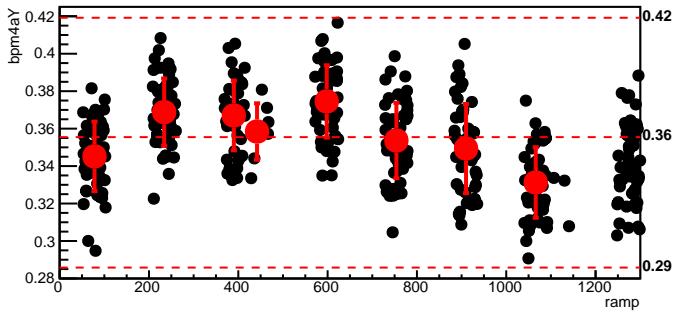
bmod Trim3,cyc=11100



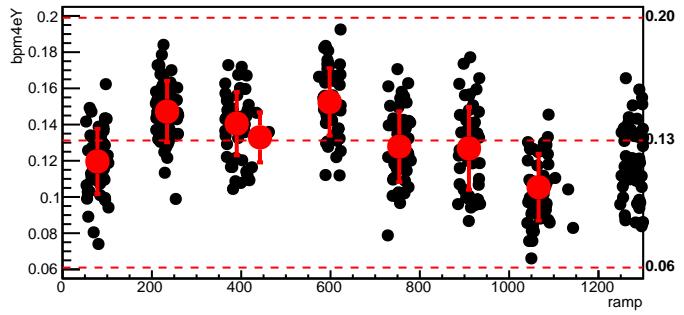
bmod Trim3,cyc=11100



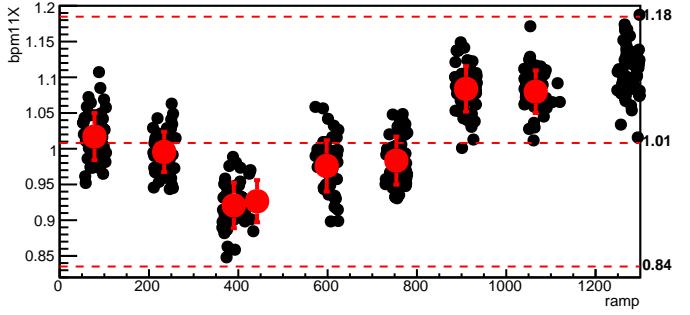
bmod Trim3,cyc=11100



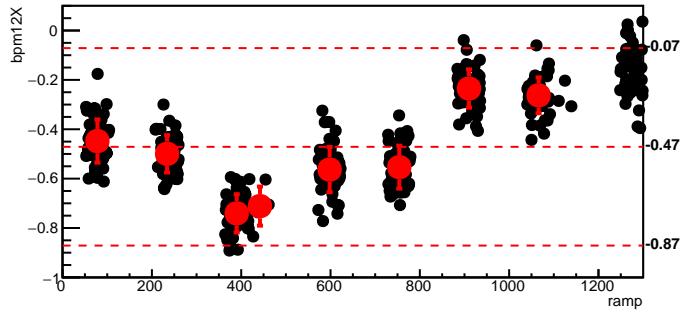
bmod Trim3,cyc=11100



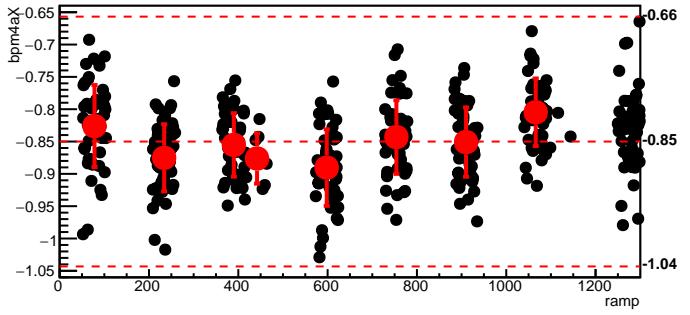
bmod Trim3,cyc=11100



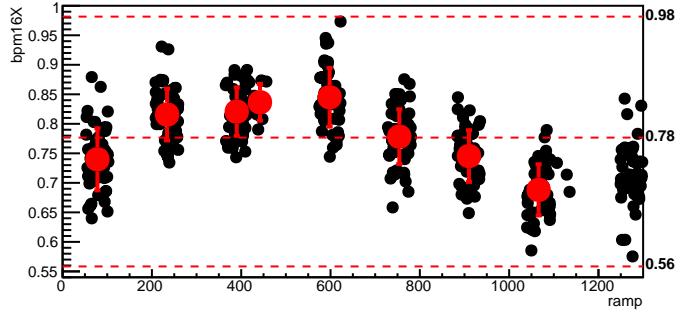
bmod Trim3,cyc=11100



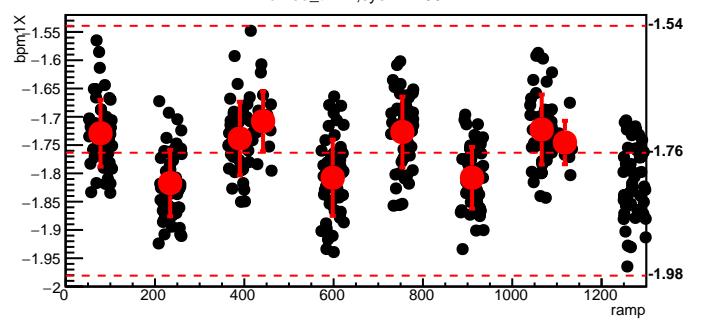
bmod Trim3,cyc=11100



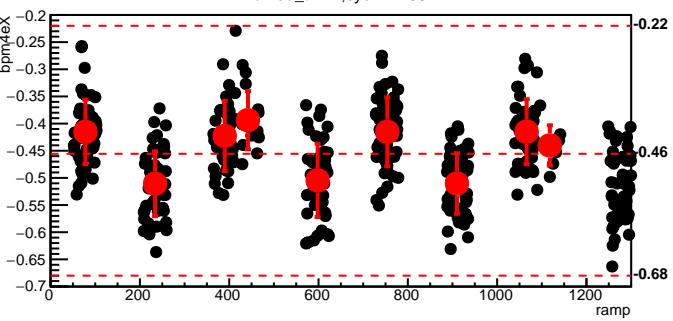
bmod Trim3,cyc=11100



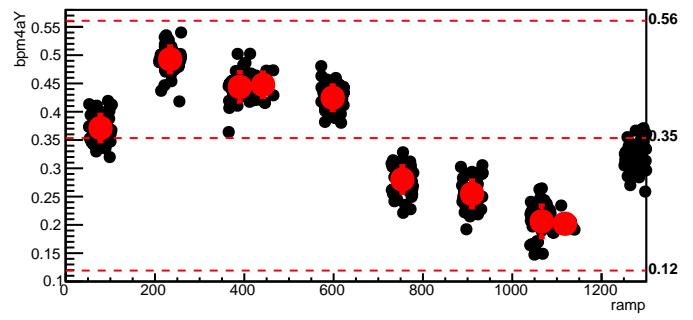
bmod Trim4,cyc=11100



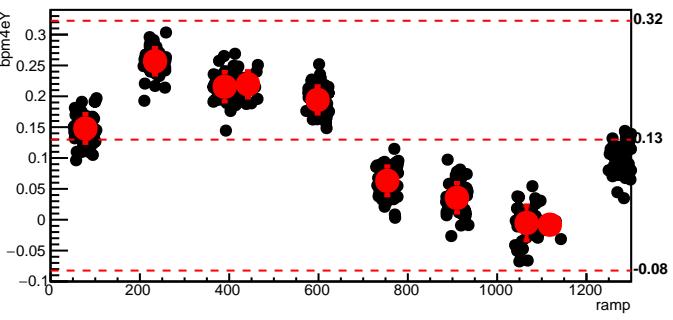
bmod Trim4,cyc=11100



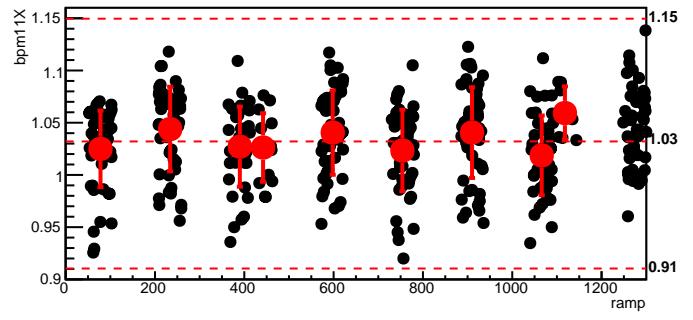
bmod Trim4,cyc=11100



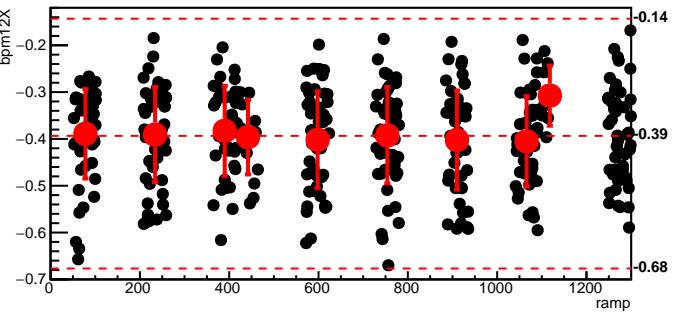
bmod Trim4,cyc=11100



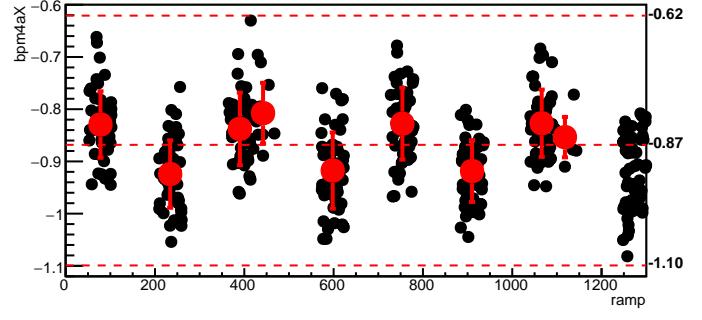
bmod Trim4,cyc=11100



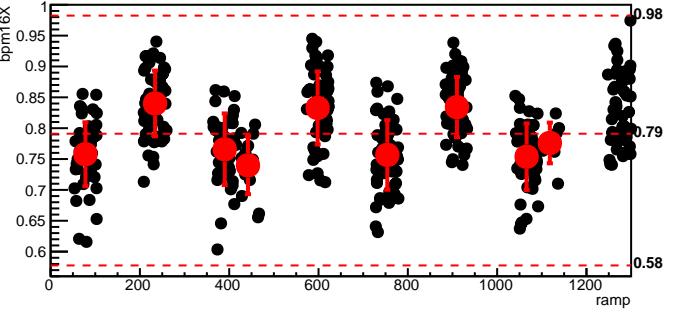
bmod Trim4,cyc=11100



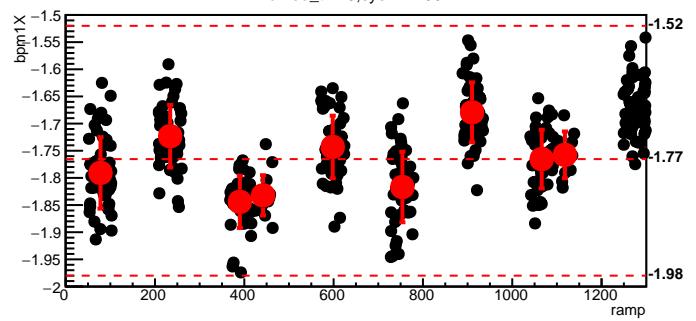
bmod Trim4,cyc=11100



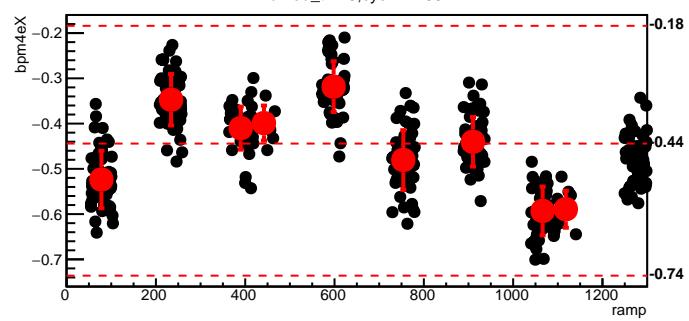
bmod Trim4,cyc=11100



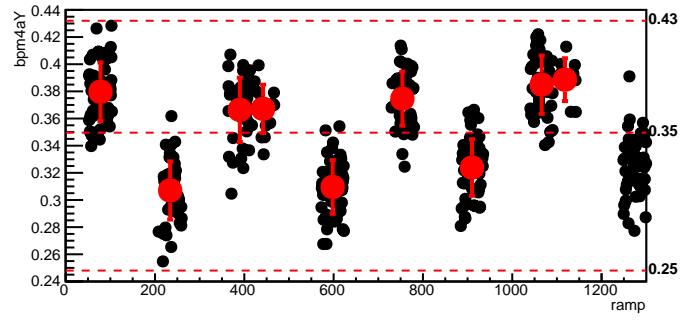
bmod Trim5,cyc=11100



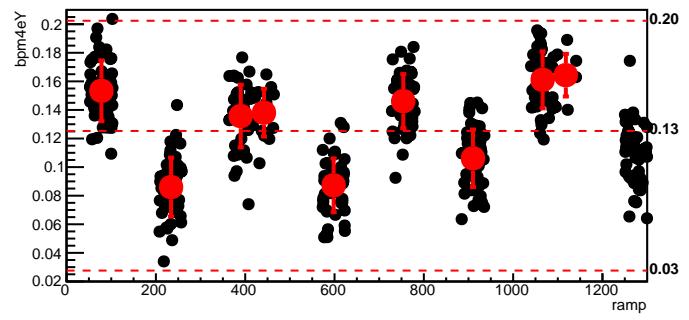
bmod Trim5,cyc=11100



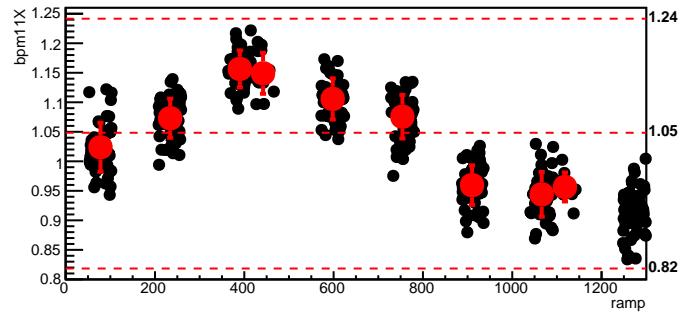
bmod Trim5,cyc=11100



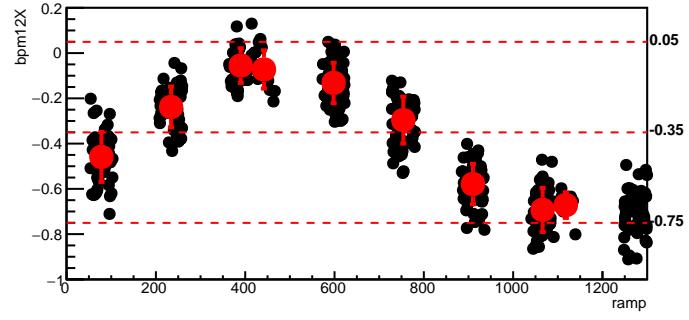
bmod Trim5,cyc=11100



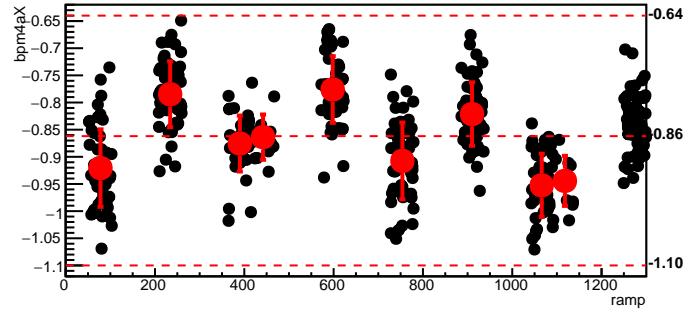
bmod Trim5,cyc=11100



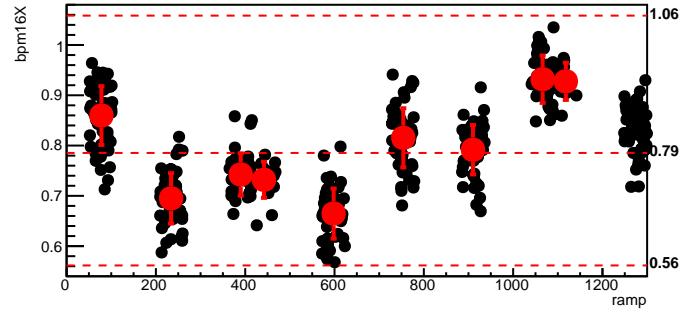
bmod Trim5,cyc=11100



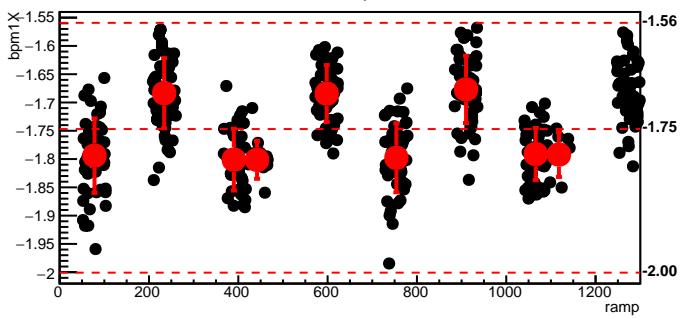
bmod Trim5,cyc=11100



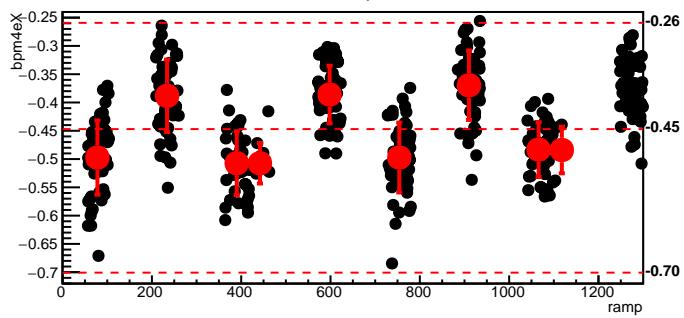
bmod Trim5,cyc=11100



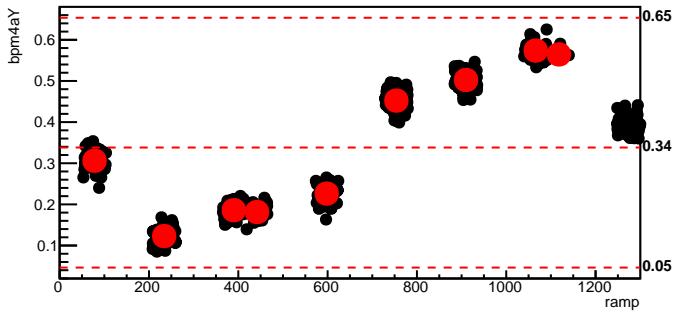
bmod Trim6,cyc=11100



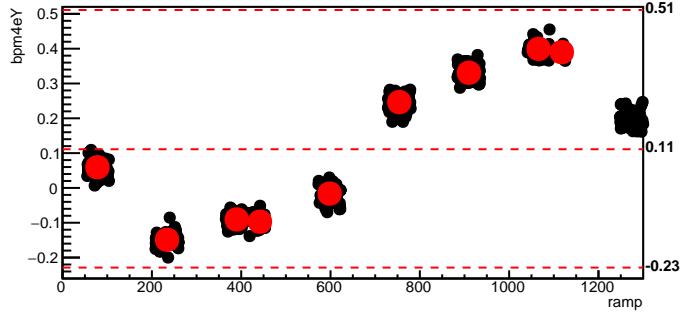
bmod Trim6,cyc=11100



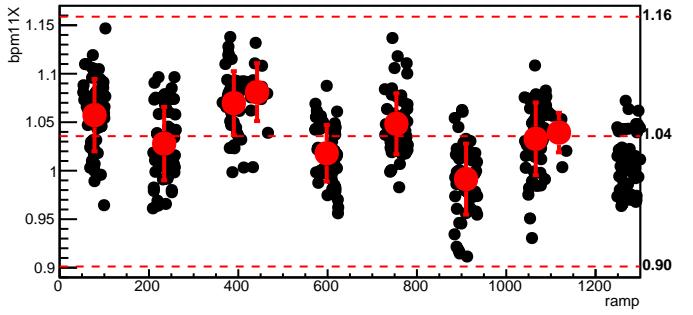
bmod Trim6,cyc=11100



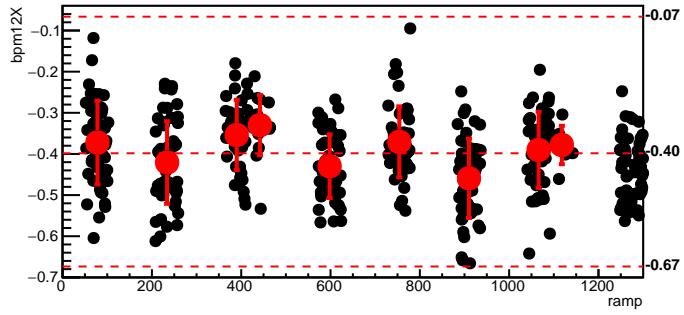
bmod Trim6,cyc=11100



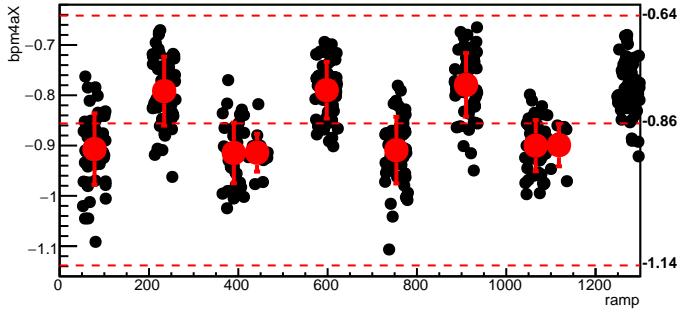
bmod Trim6,cyc=11100



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bmod Trim6,cyc=11100

