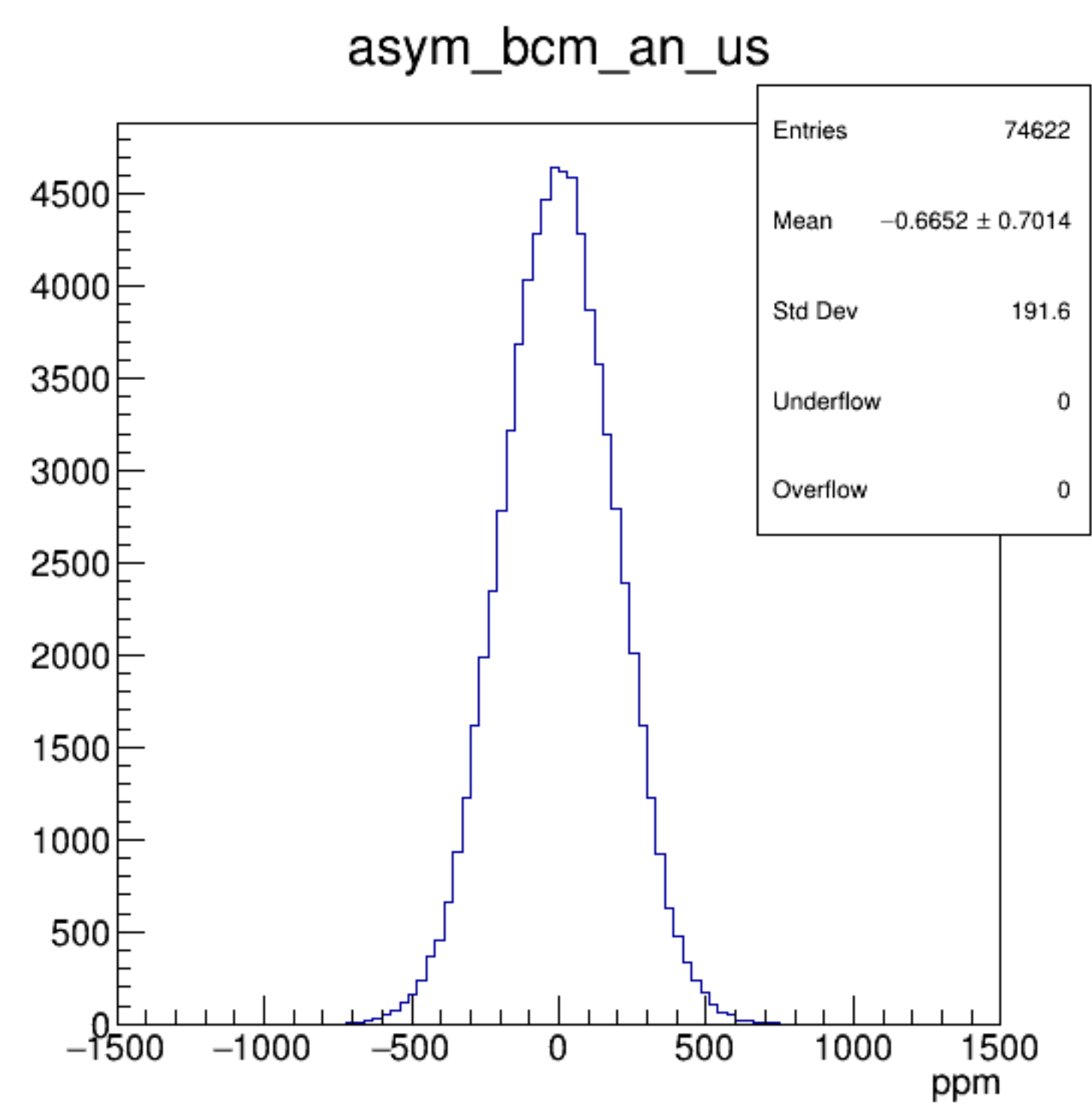
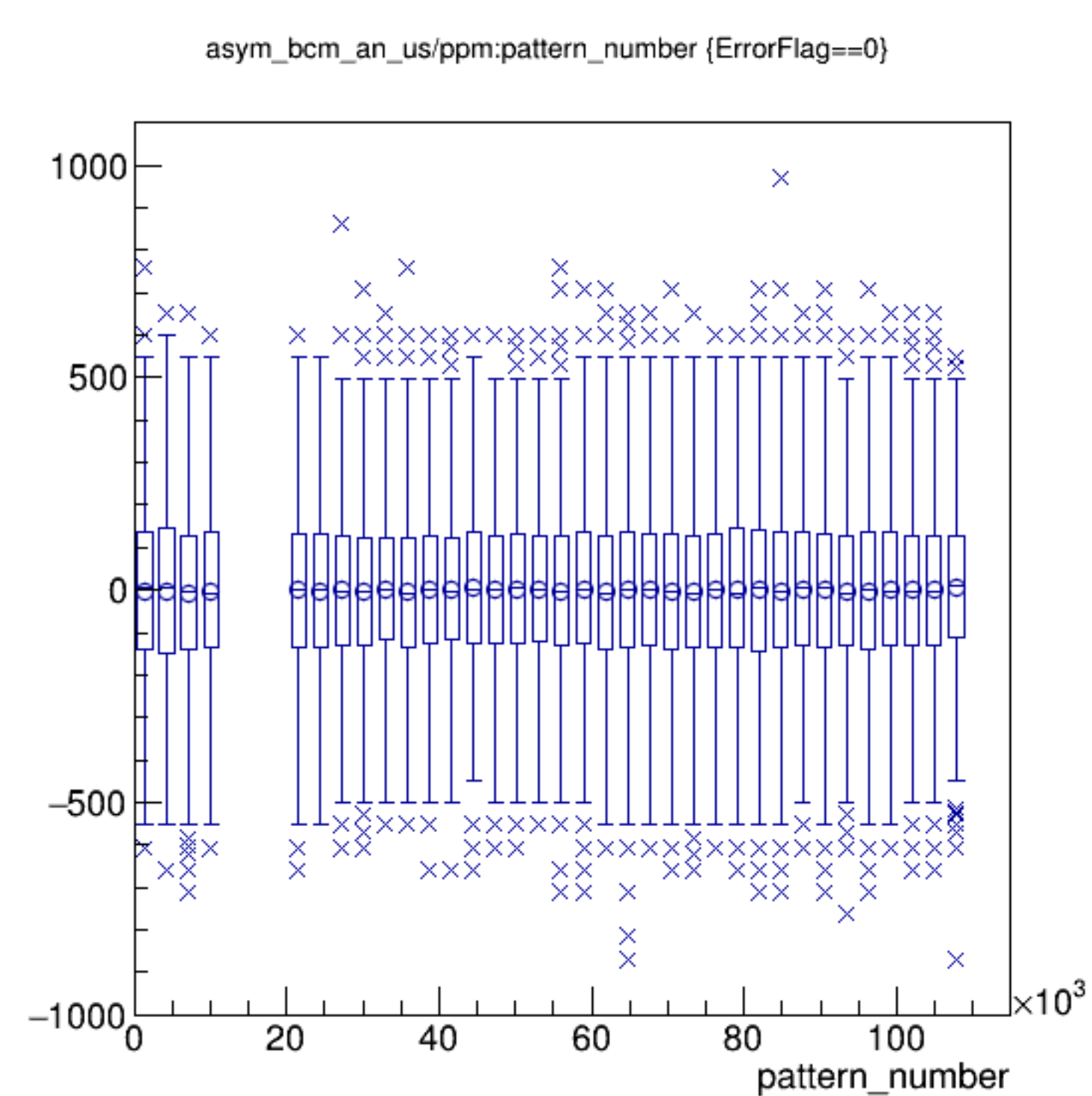
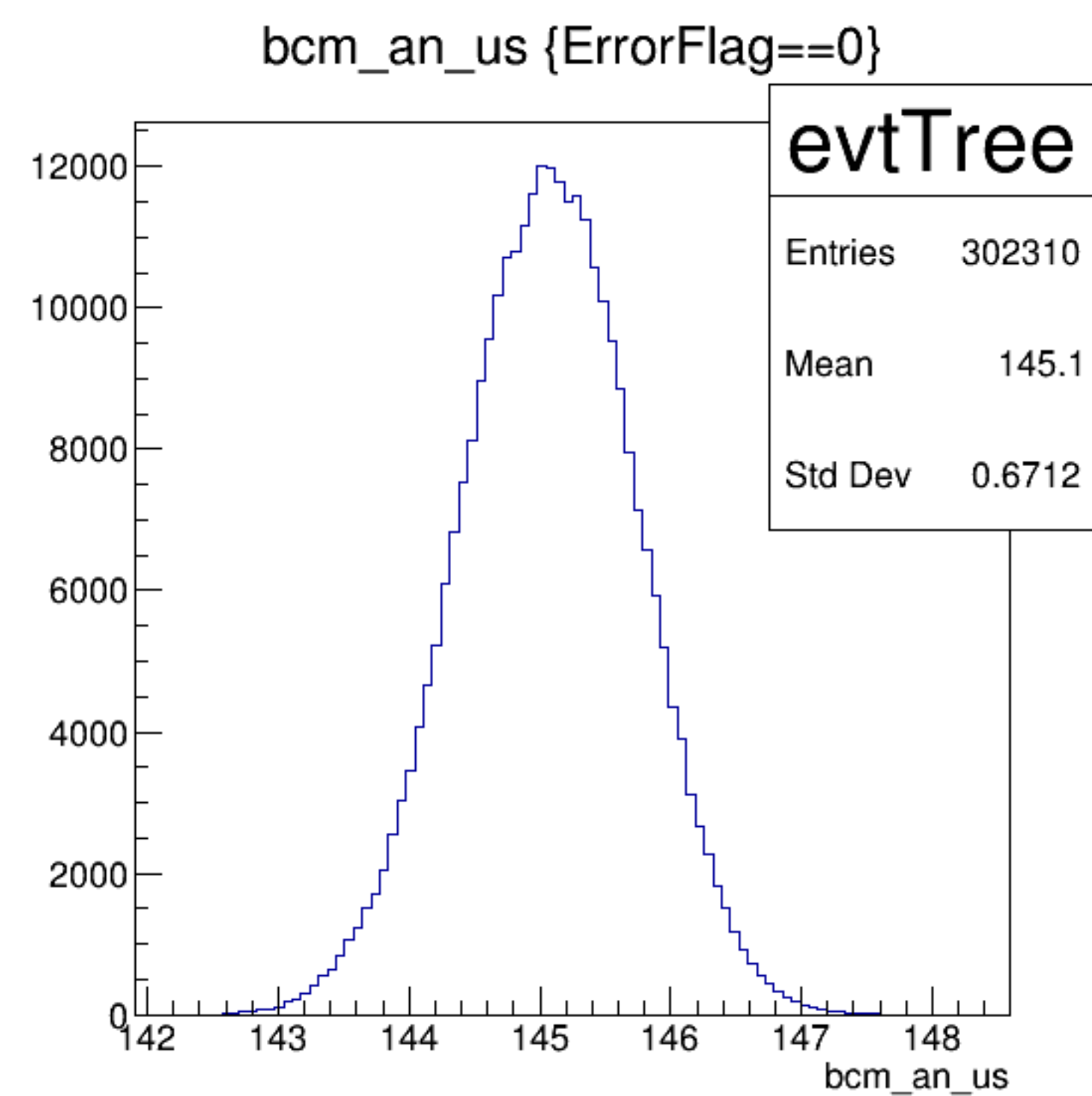
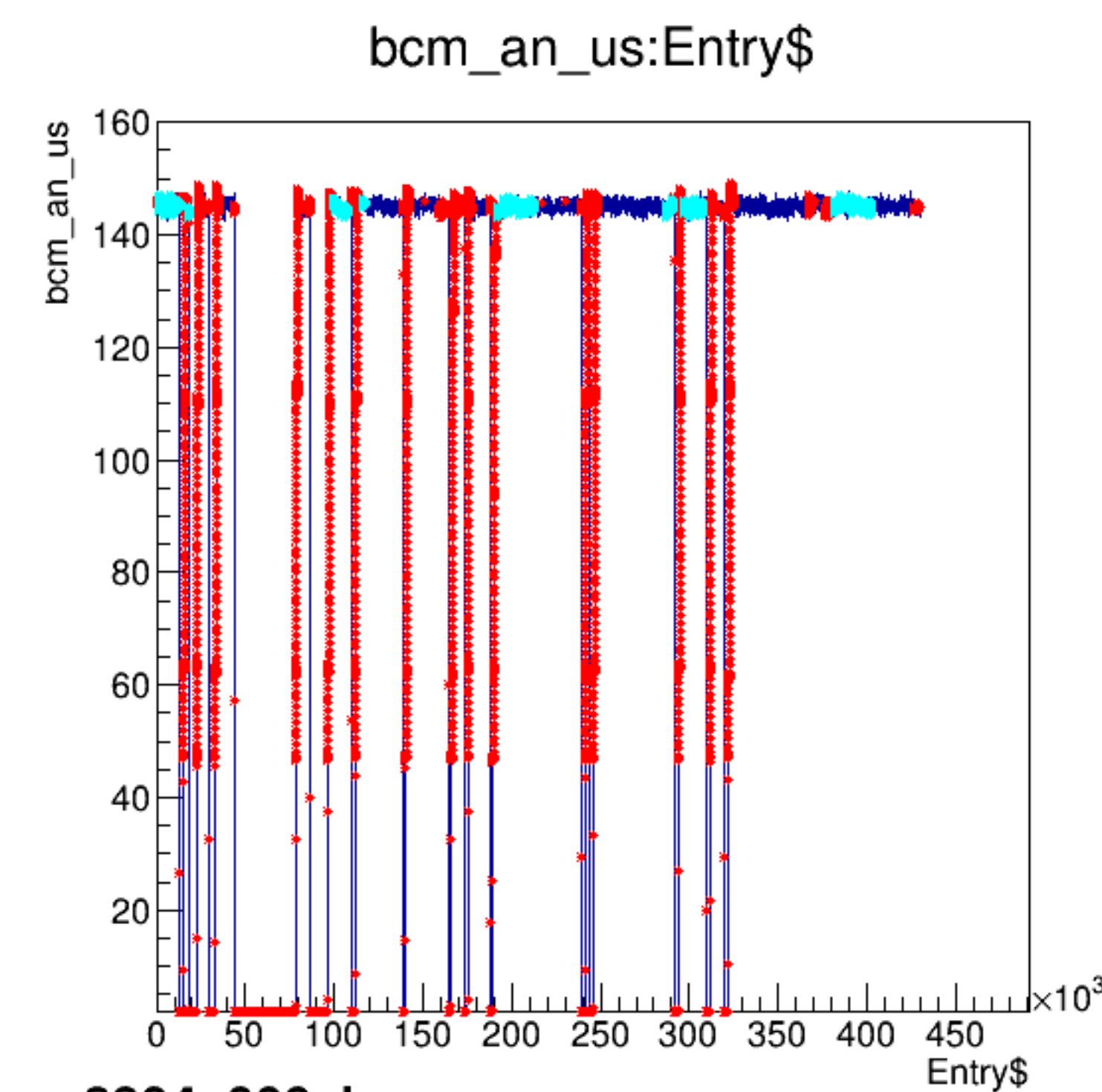
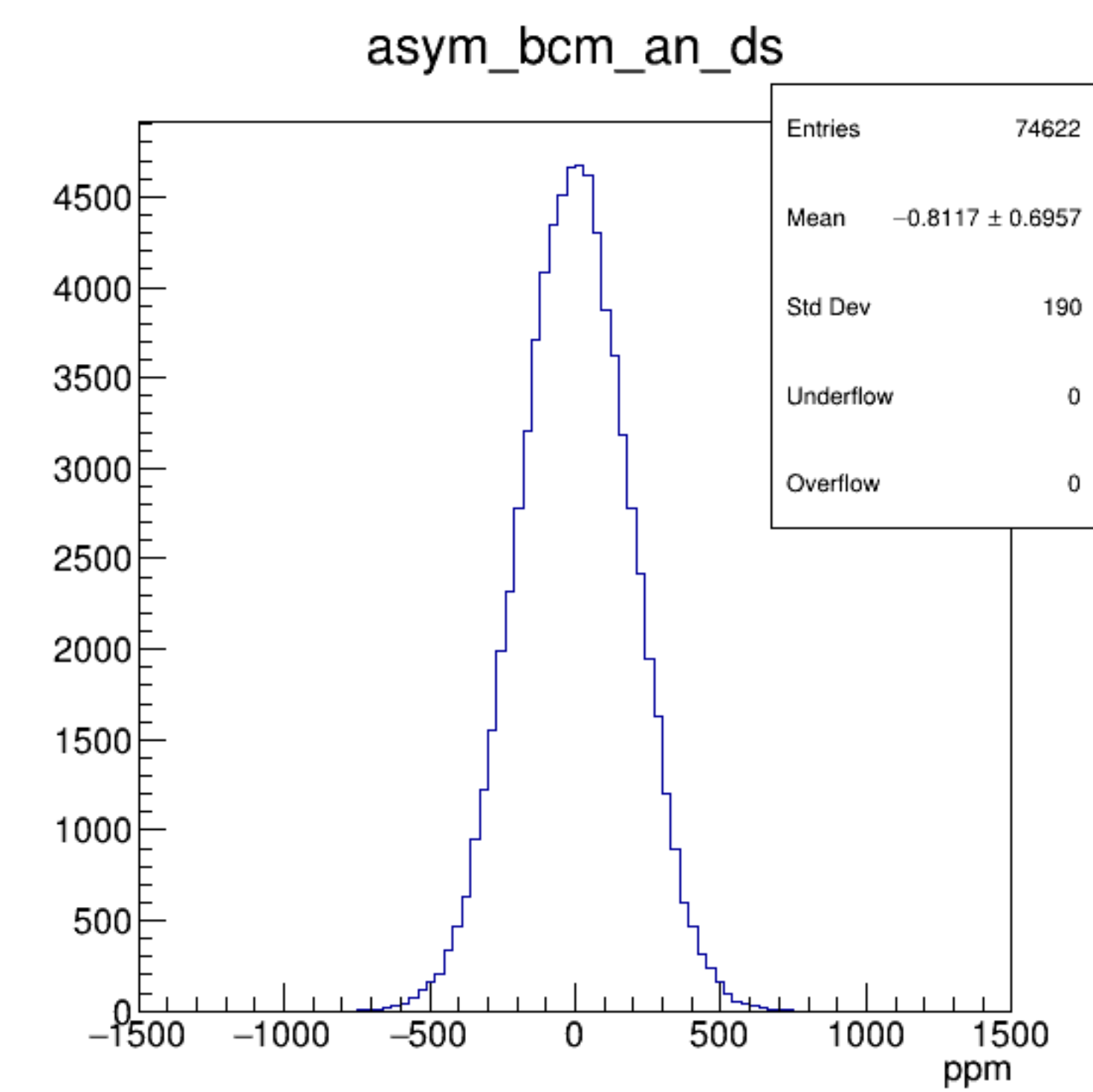
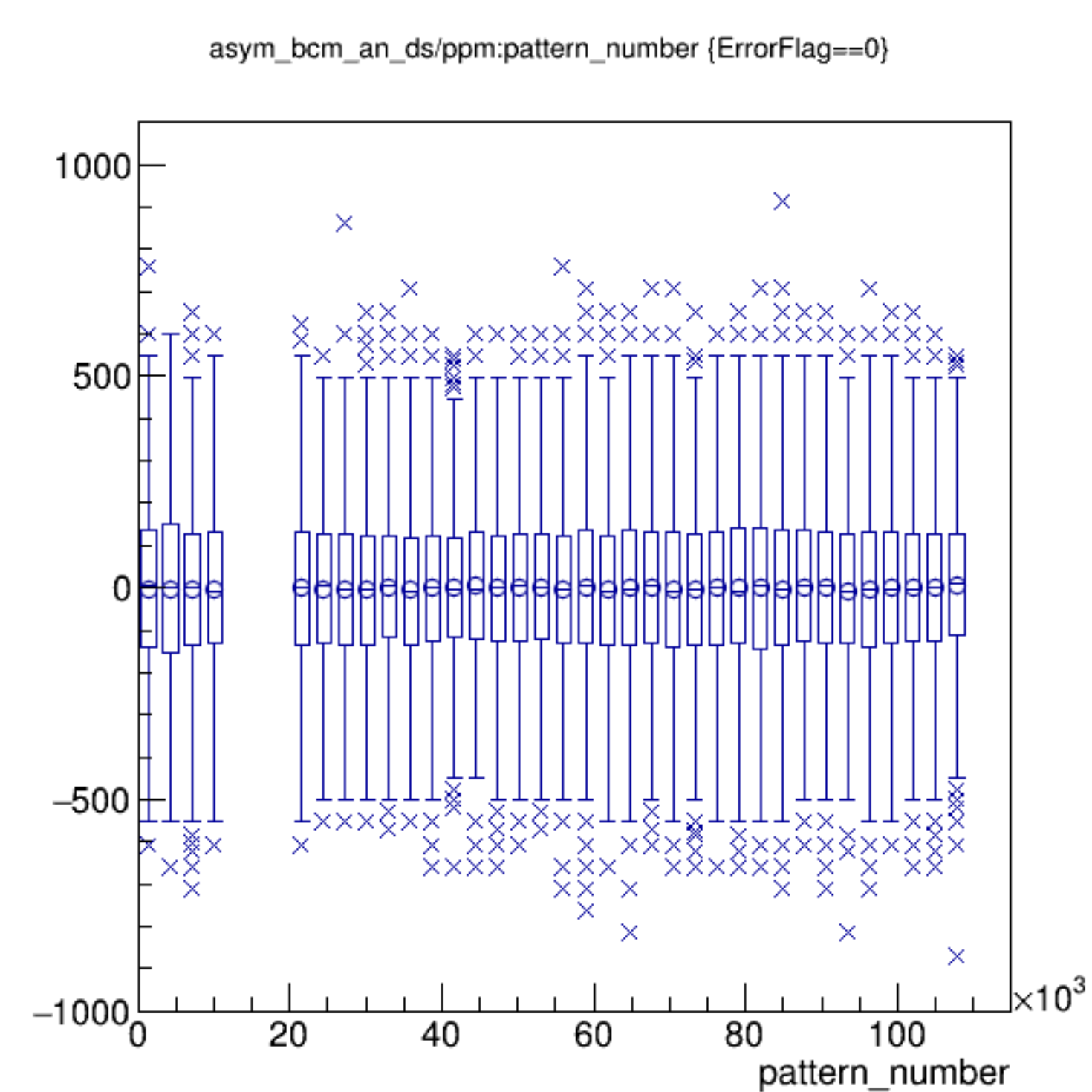
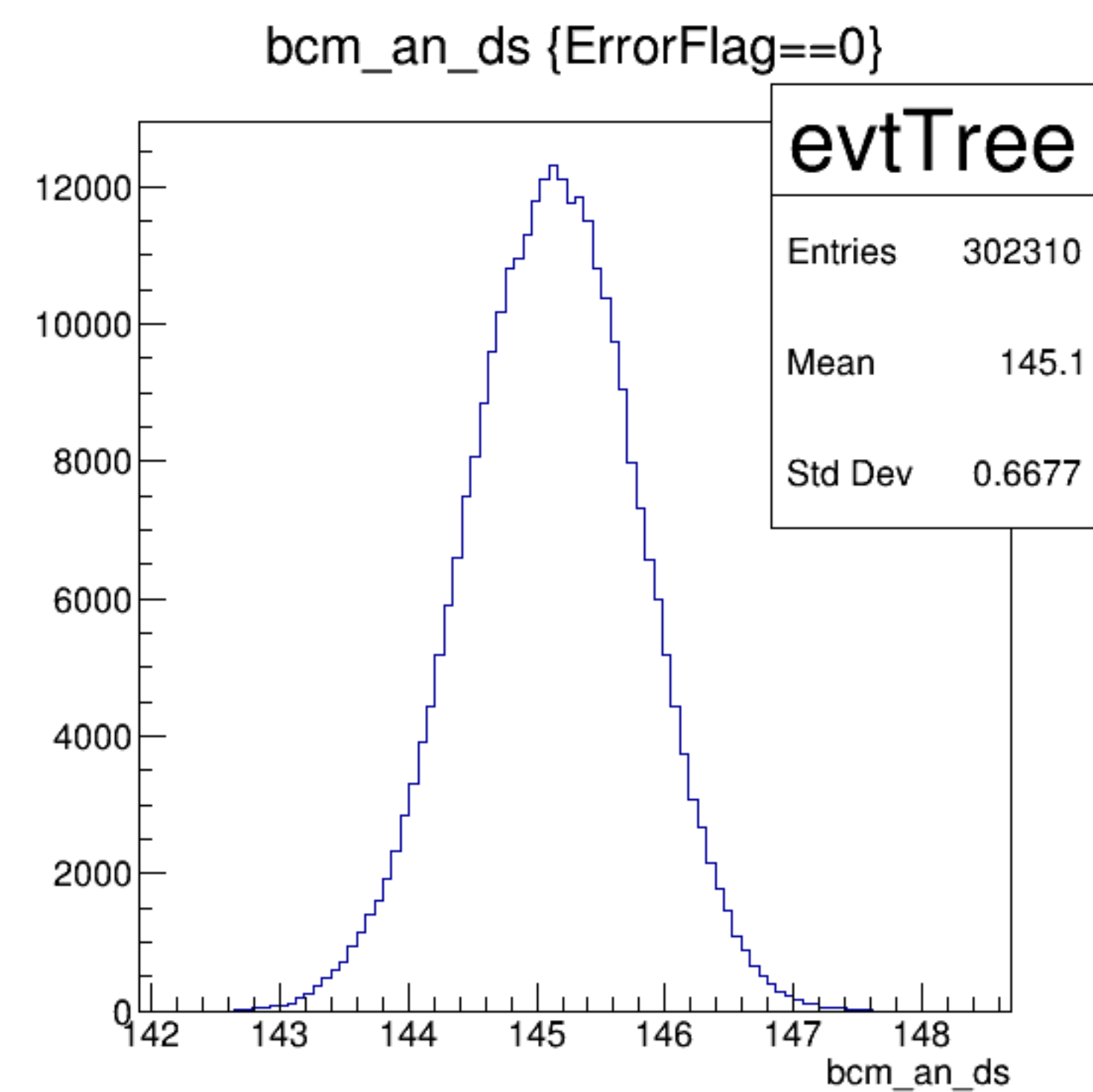
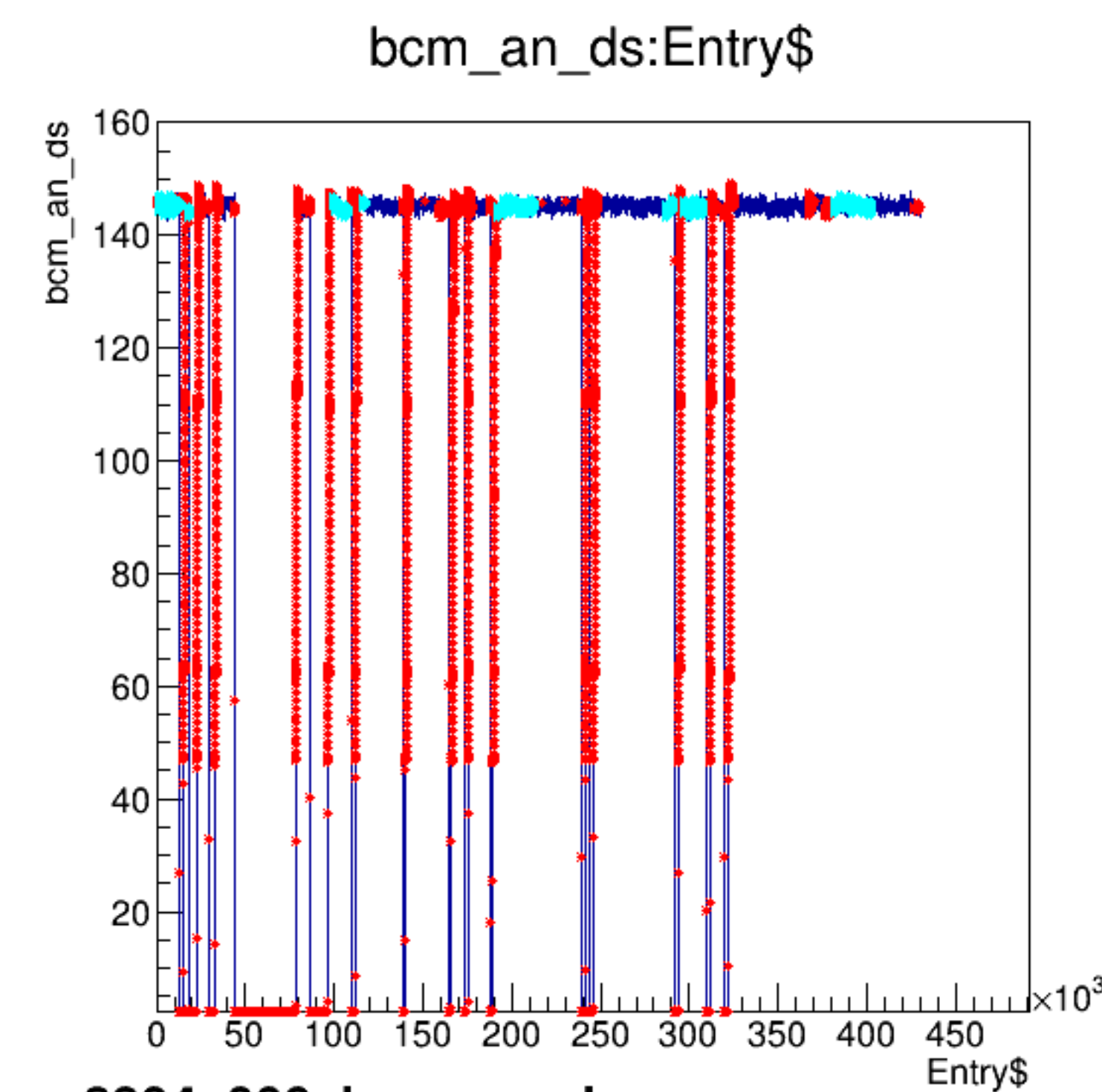
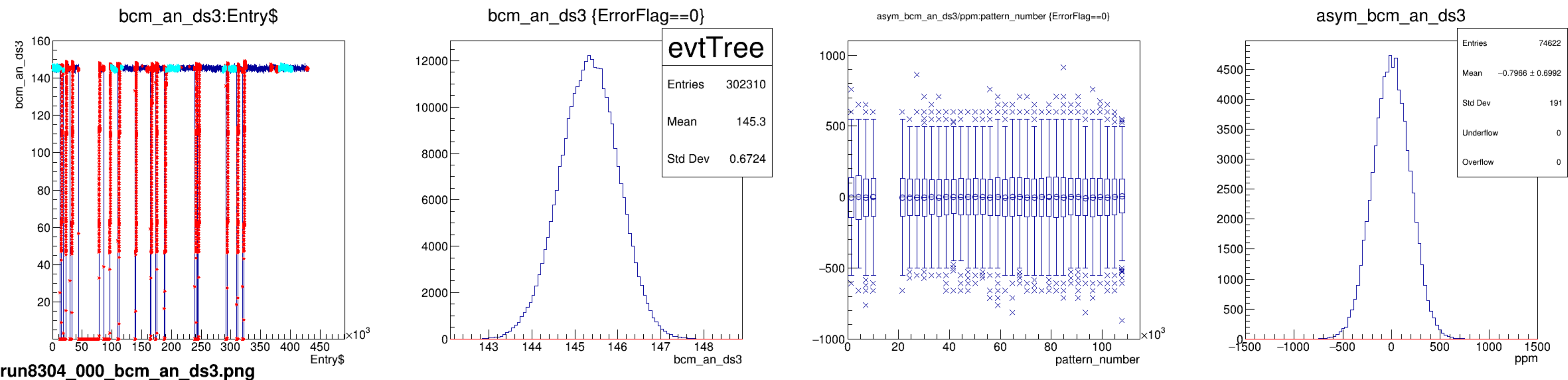
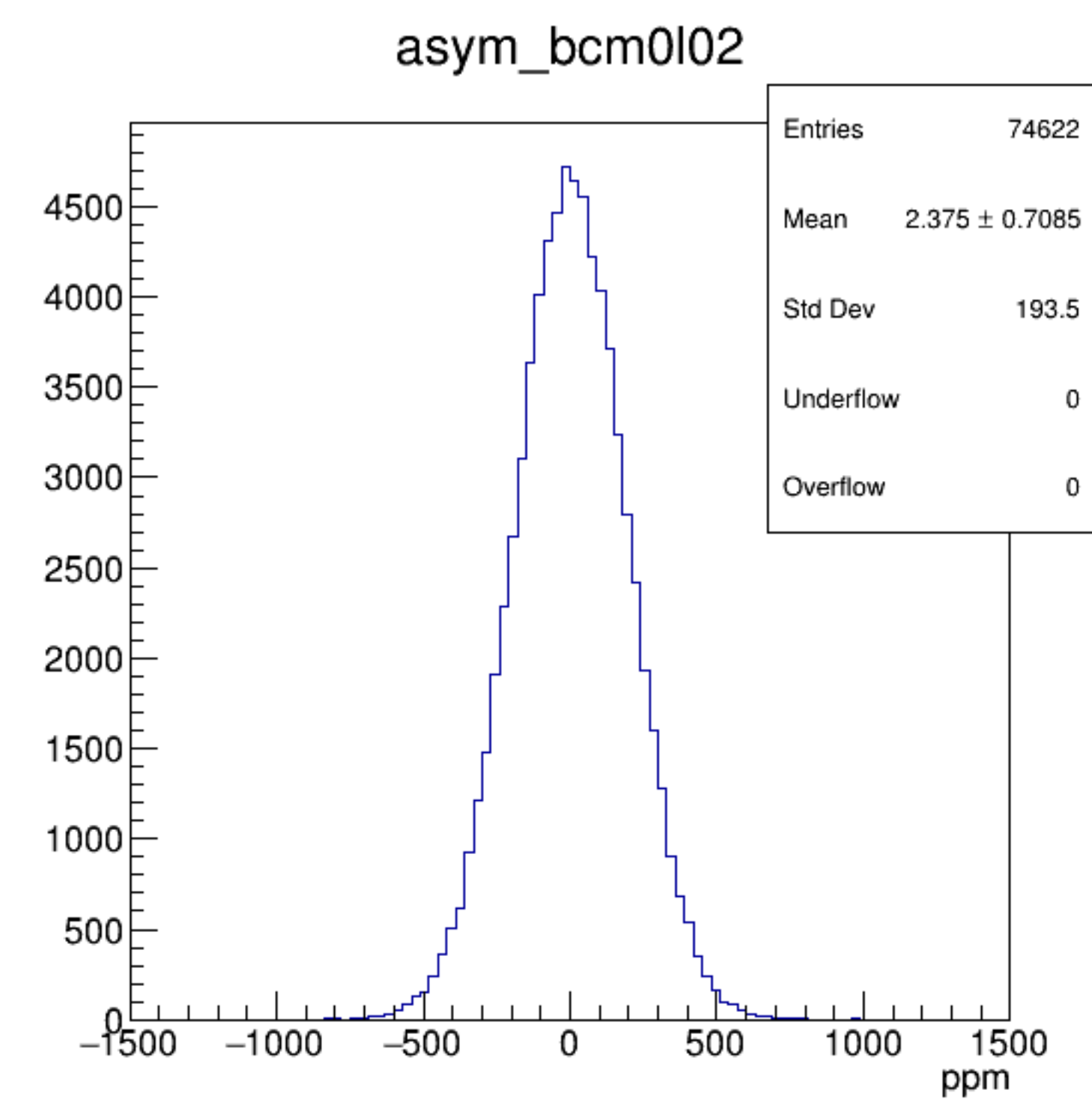
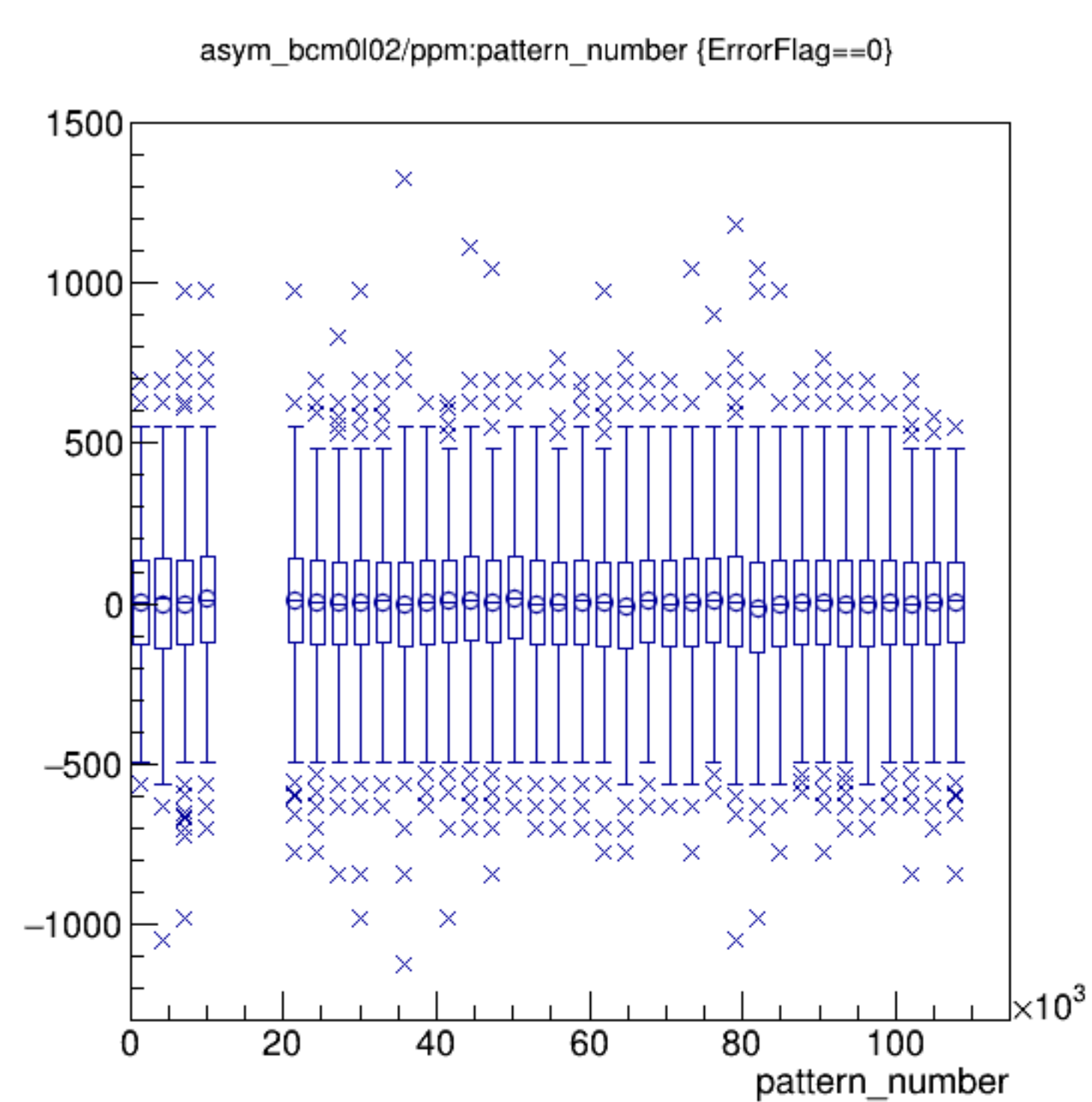
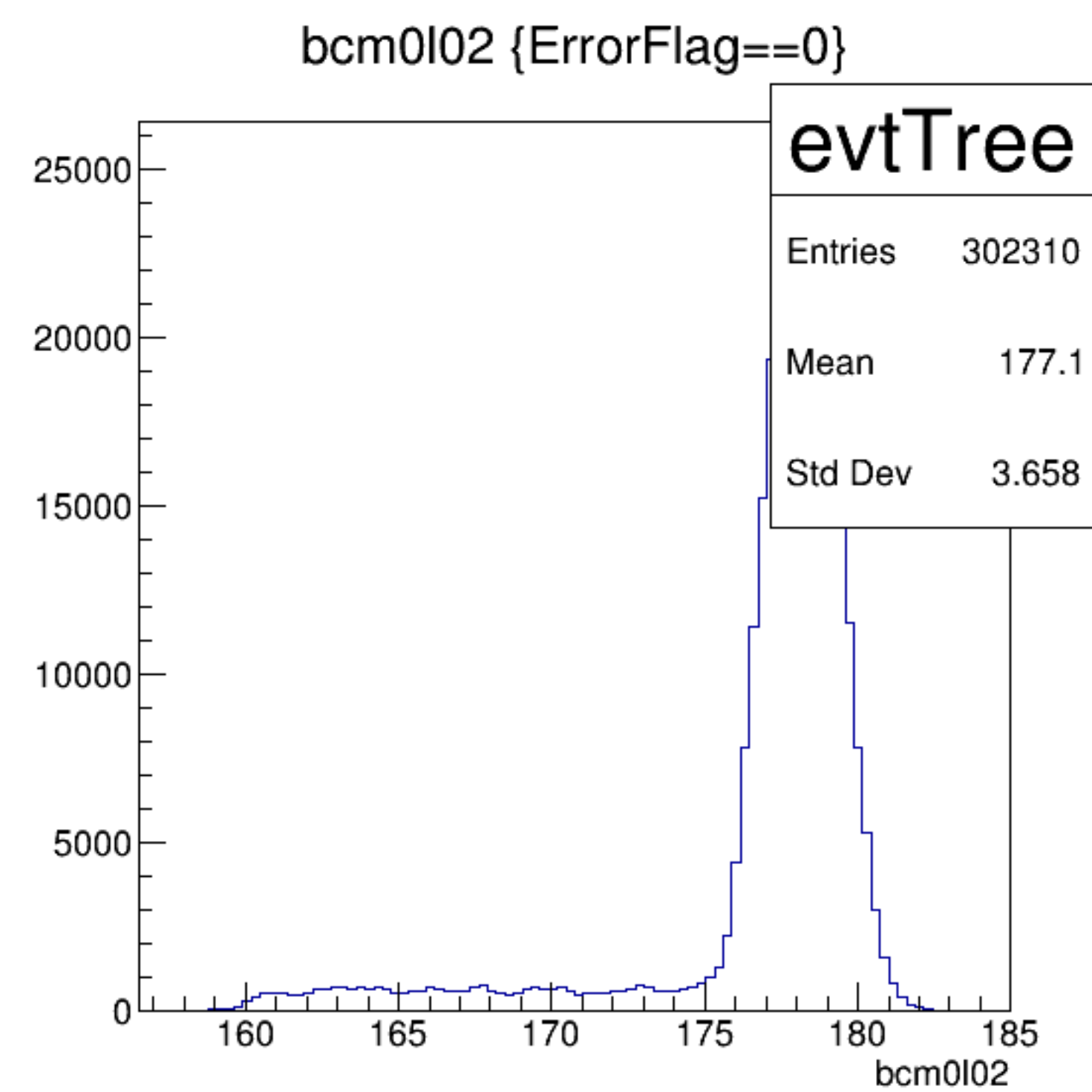
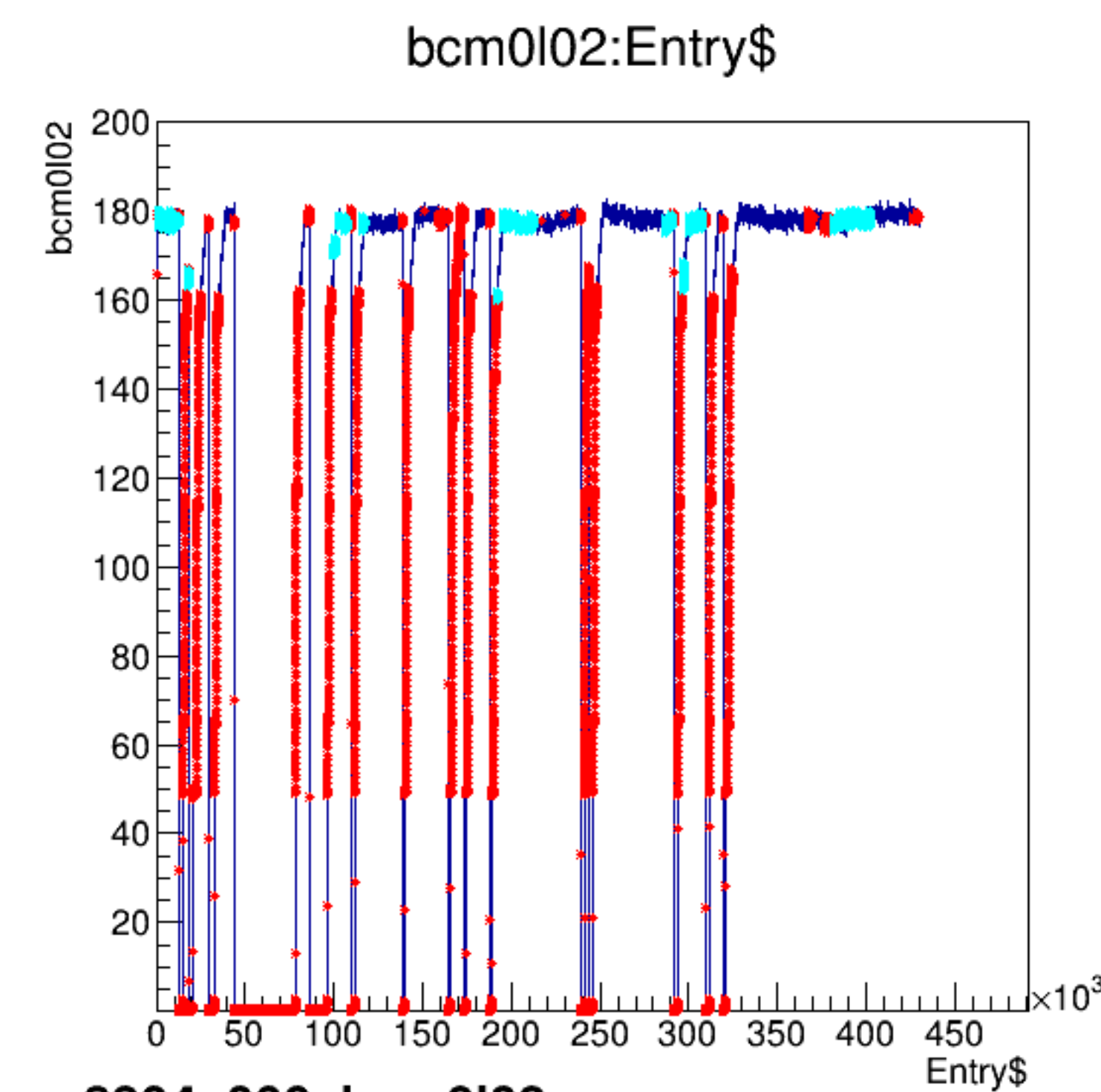


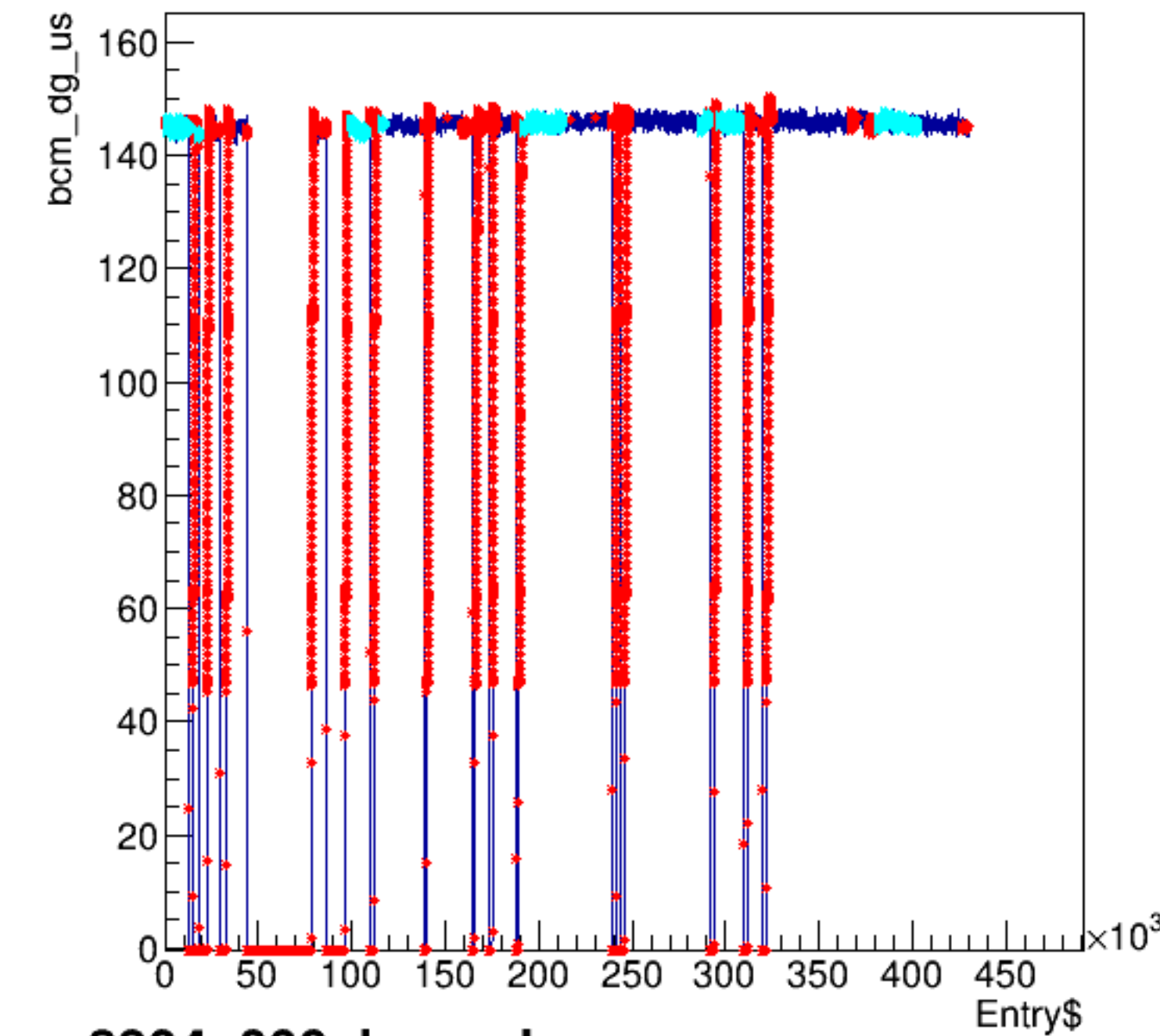






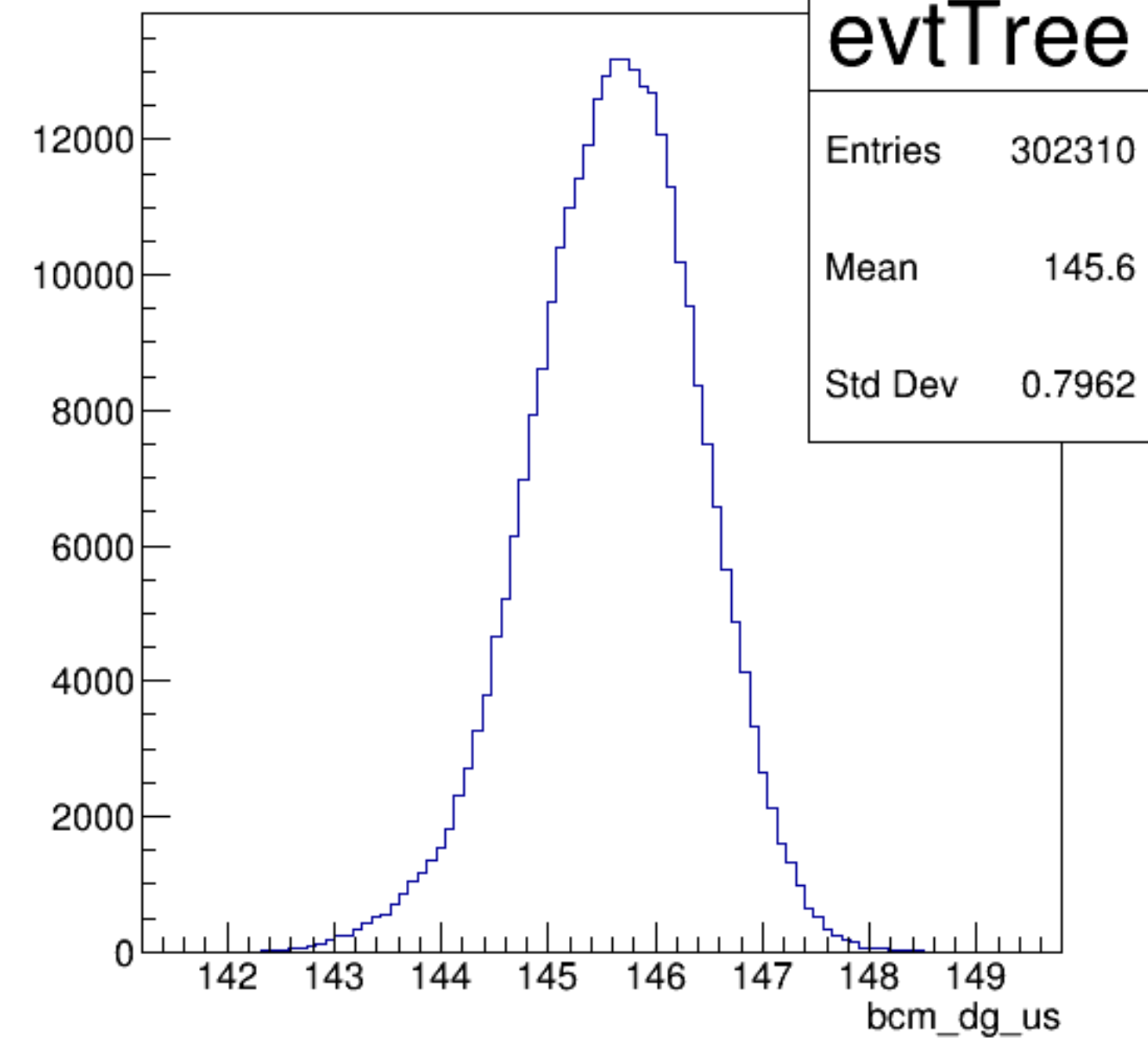


bcm_dg_us:Entry\$

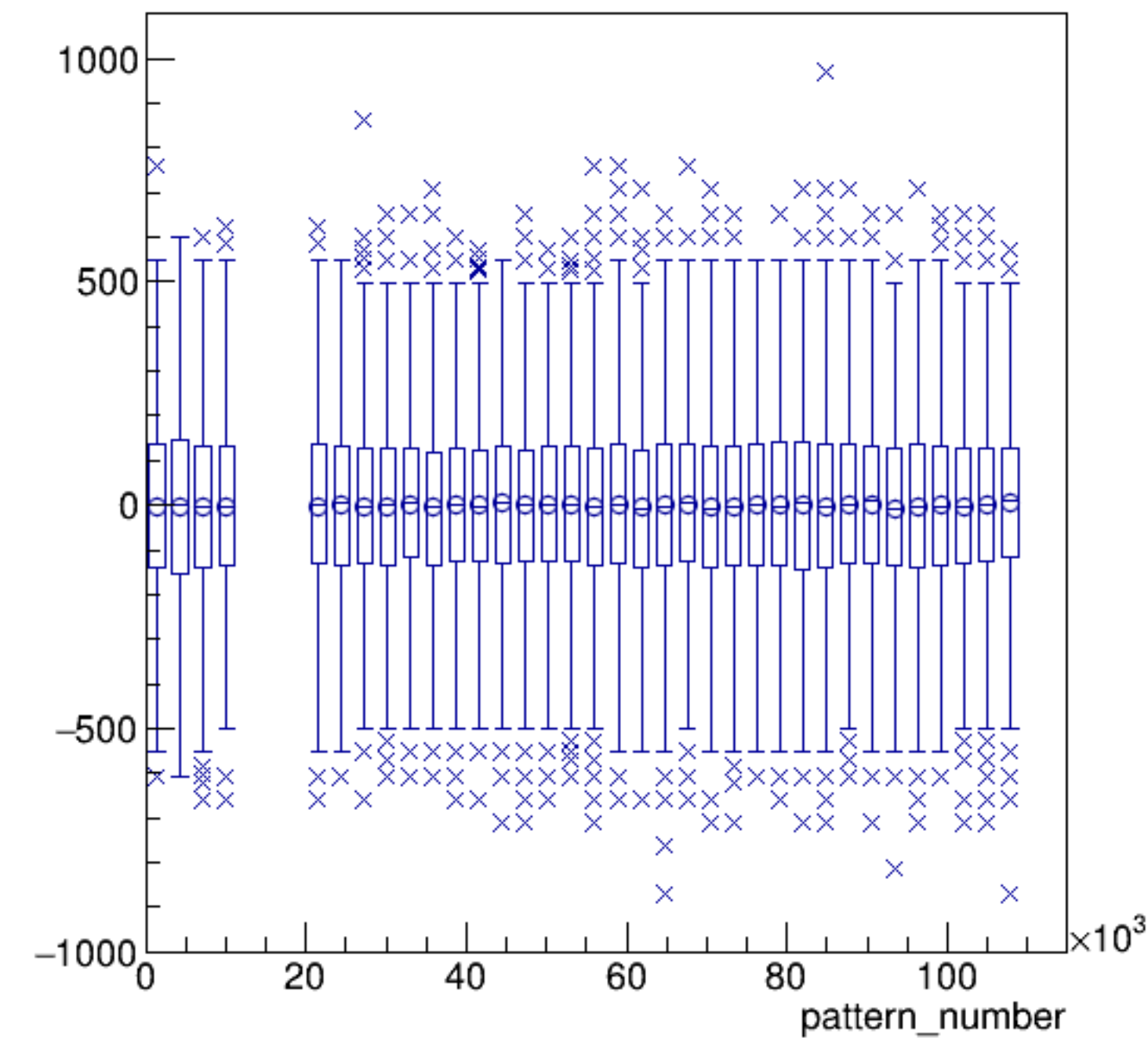


run8304_000_bcm_dg_us.png

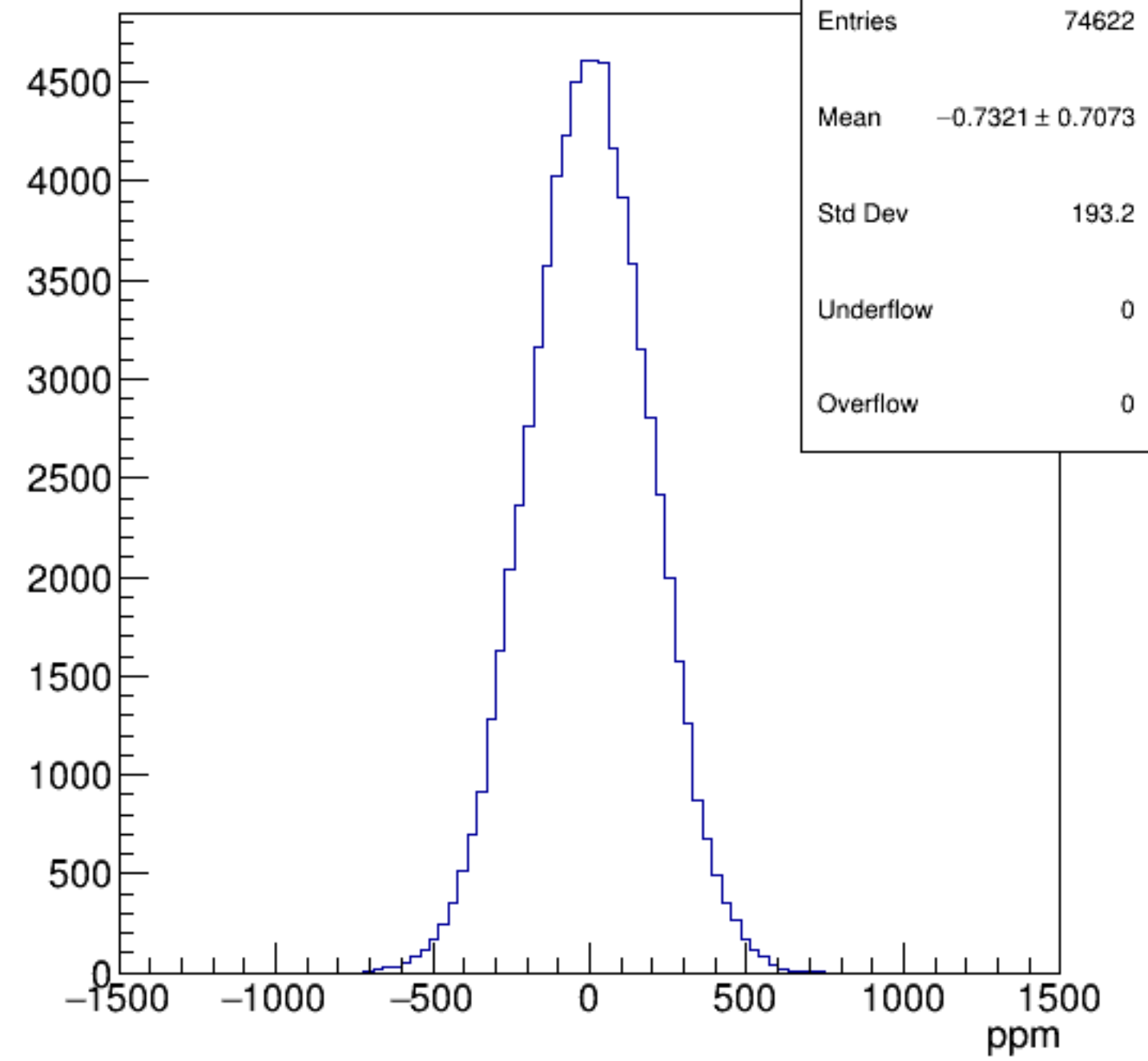
bcm_dg_us {ErrorFlag==0}



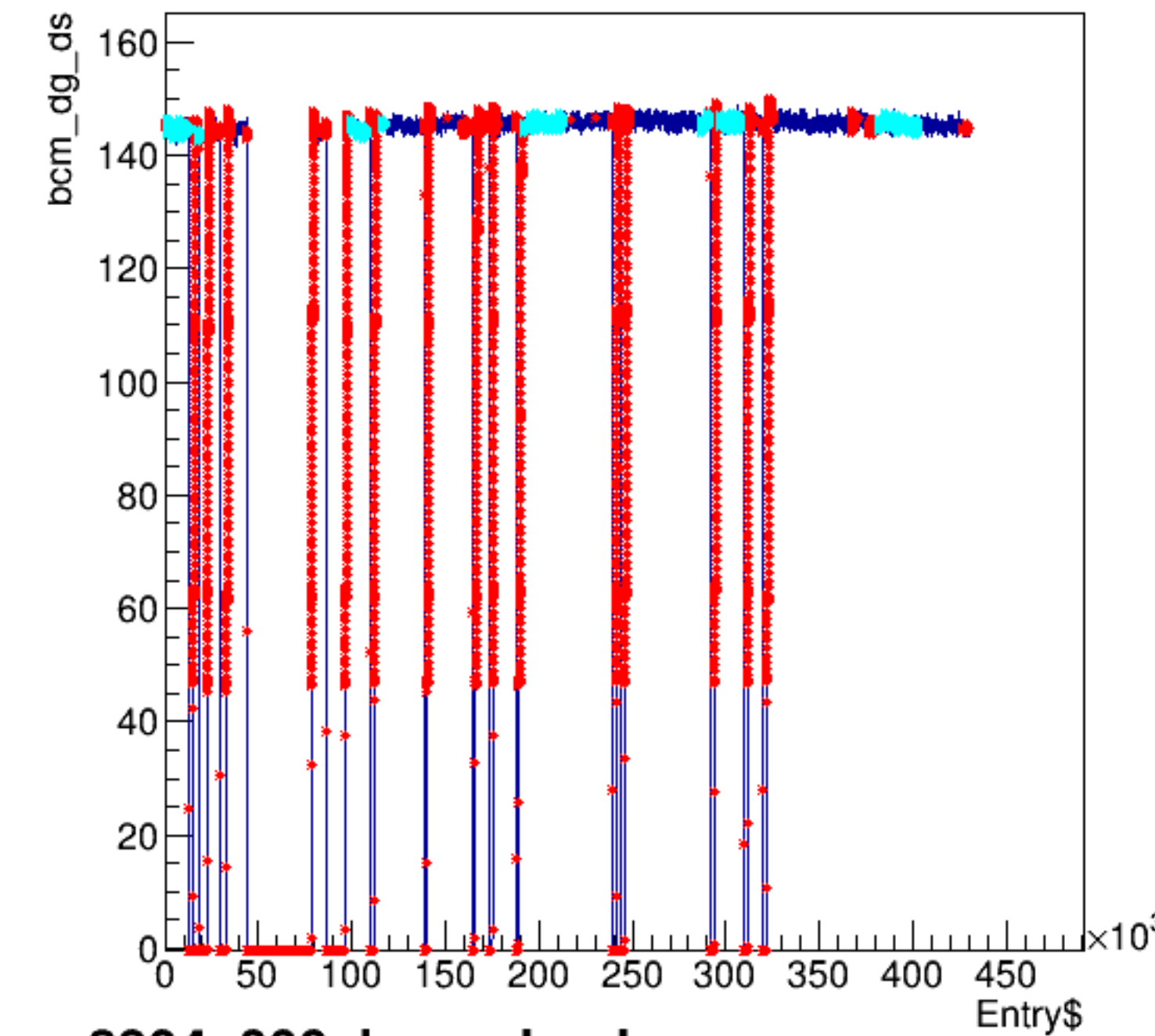
asym_bcm_dg_us/ppm:pattern_number {ErrorFlag==0}



asym_bcm_dg_us

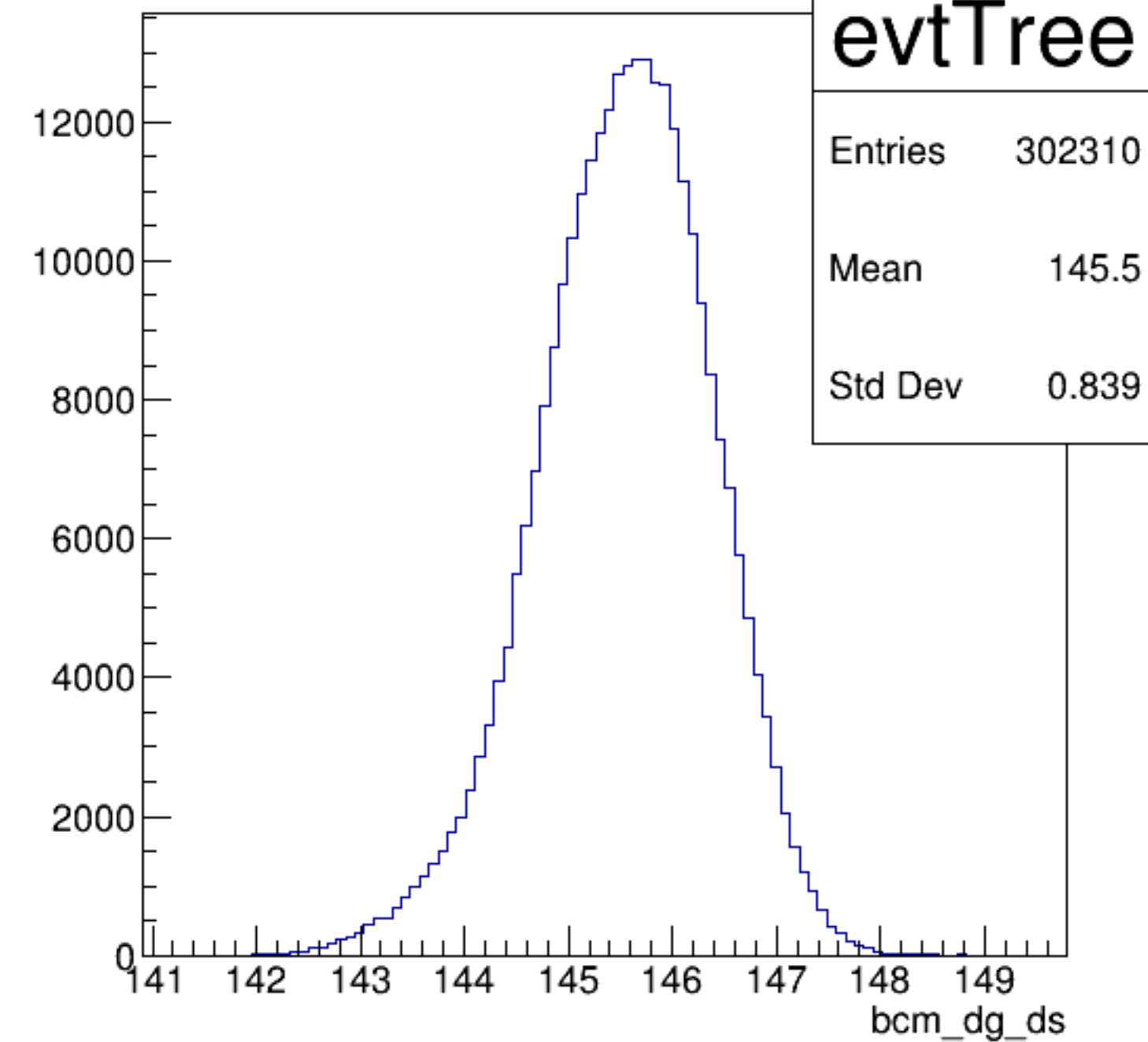


bcm_dg_ds:Entry\$

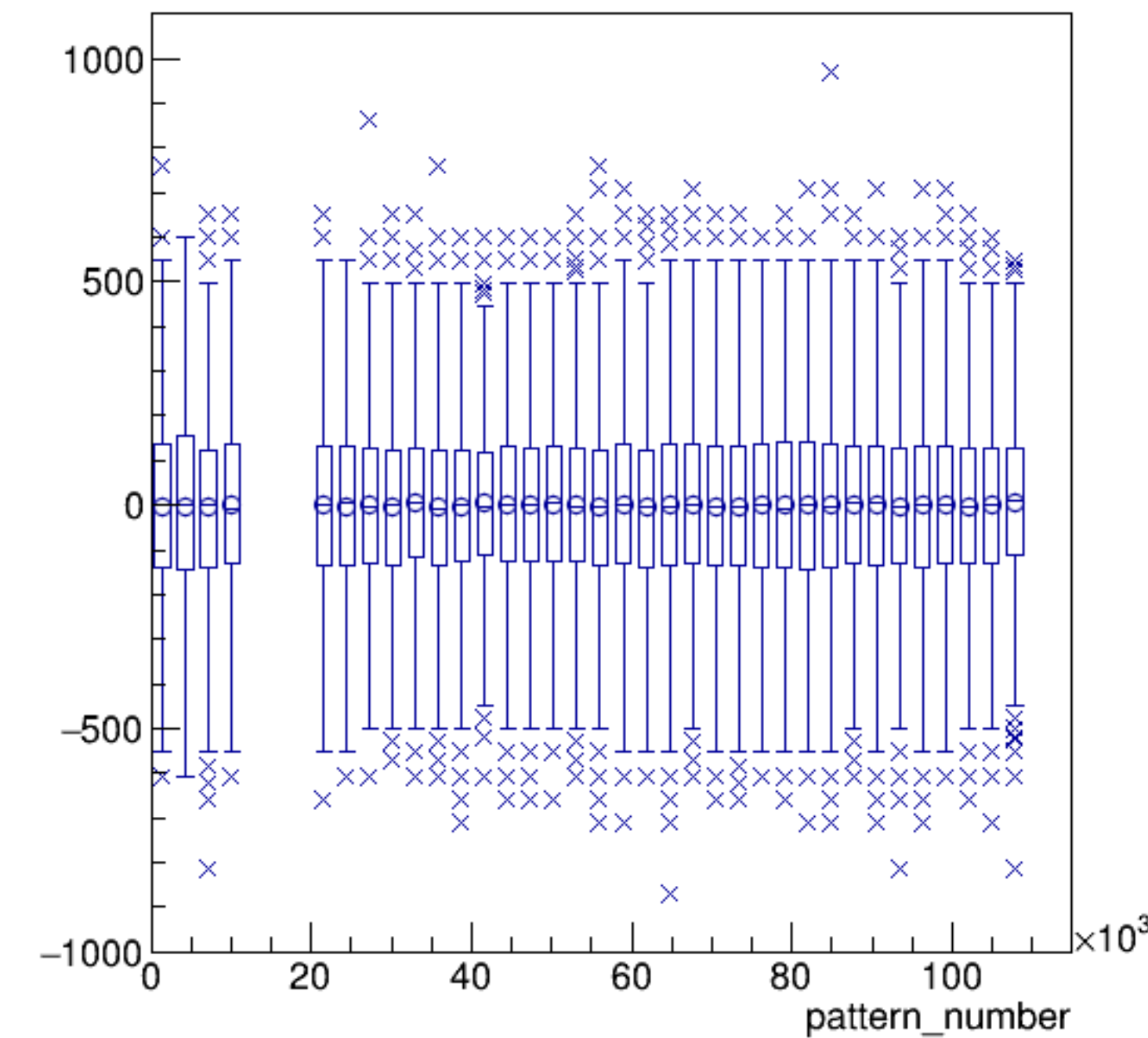


run8304_000_bcm_dg_ds.png

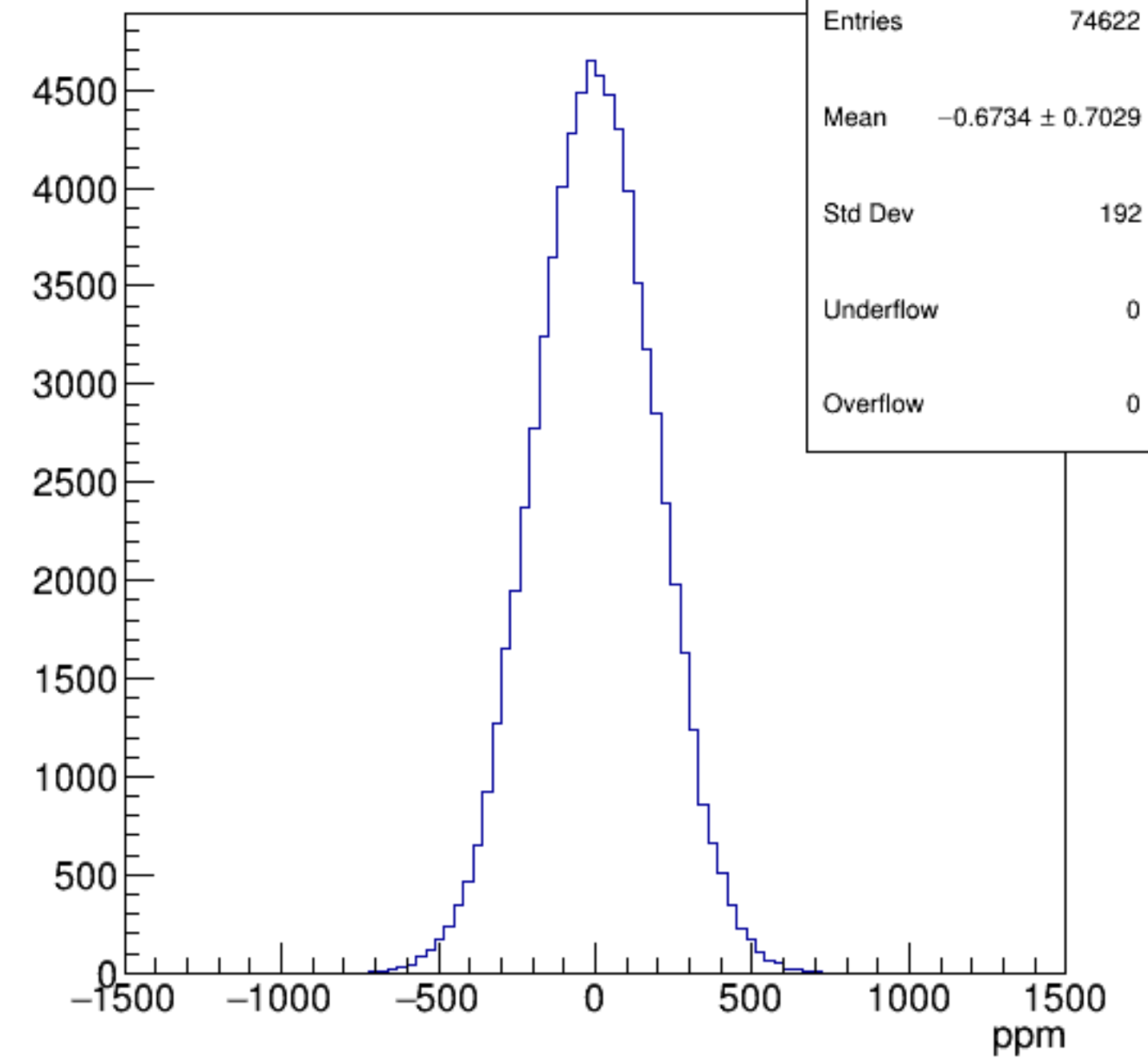
bcm_dg_ds {ErrorFlag==0}



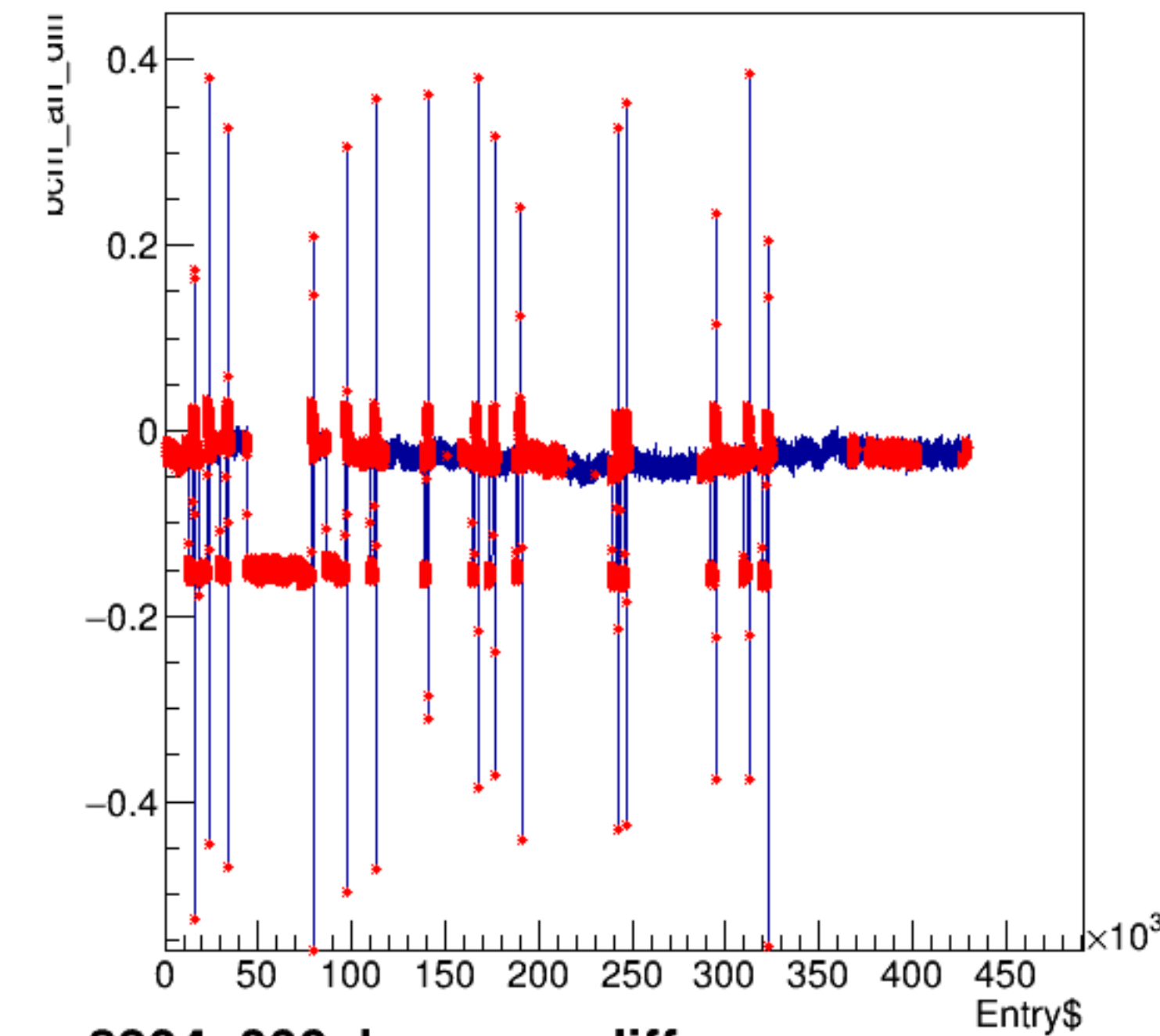
asym_bcm_dg_ds/ppm:pattern_number {ErrorFlag==0}



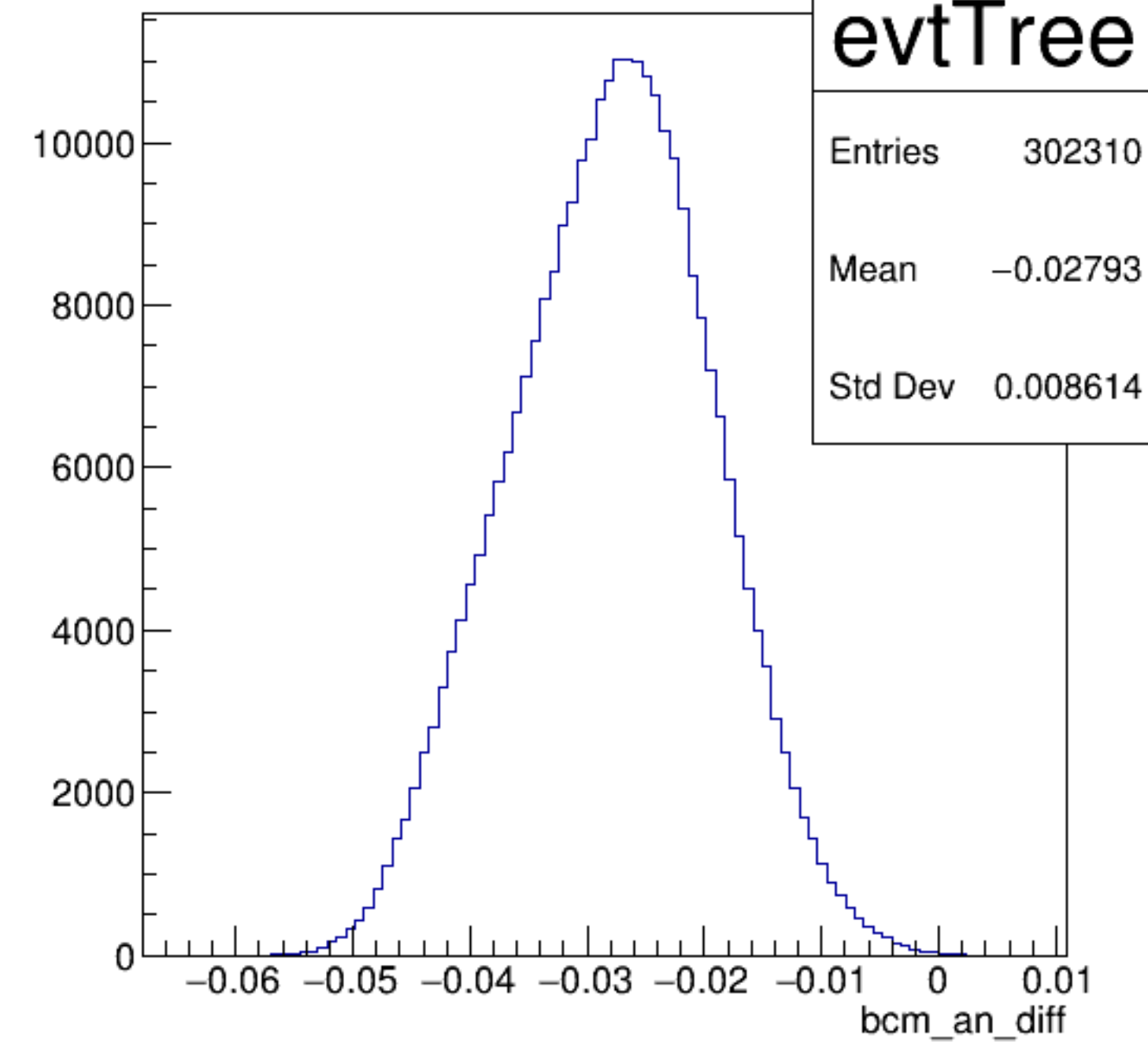
asym_bcm_dg_ds



bcm_an_diff:Entry\$



bcm_an_diff {ErrorFlag==0}



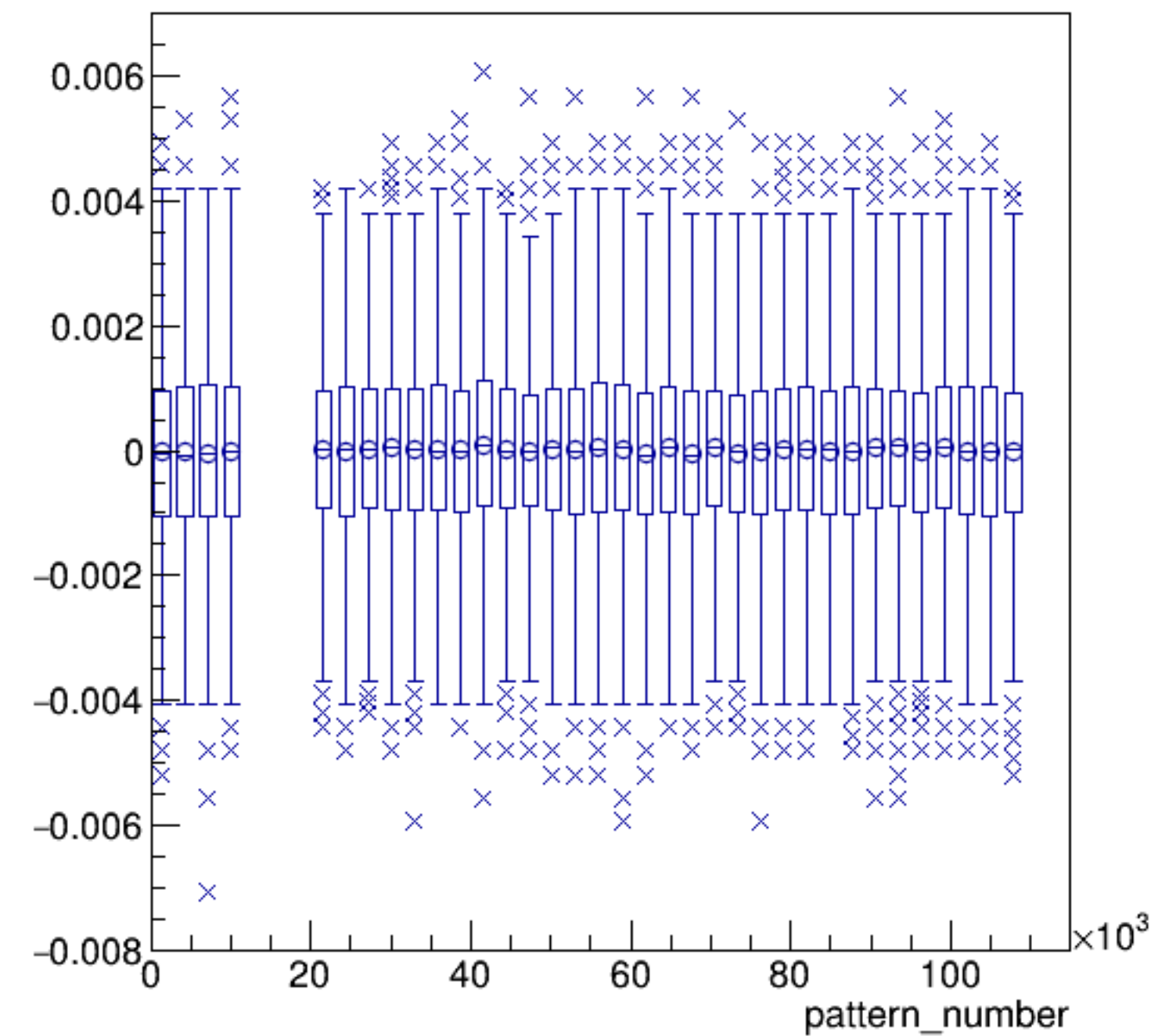
evtTree

Entries 302310

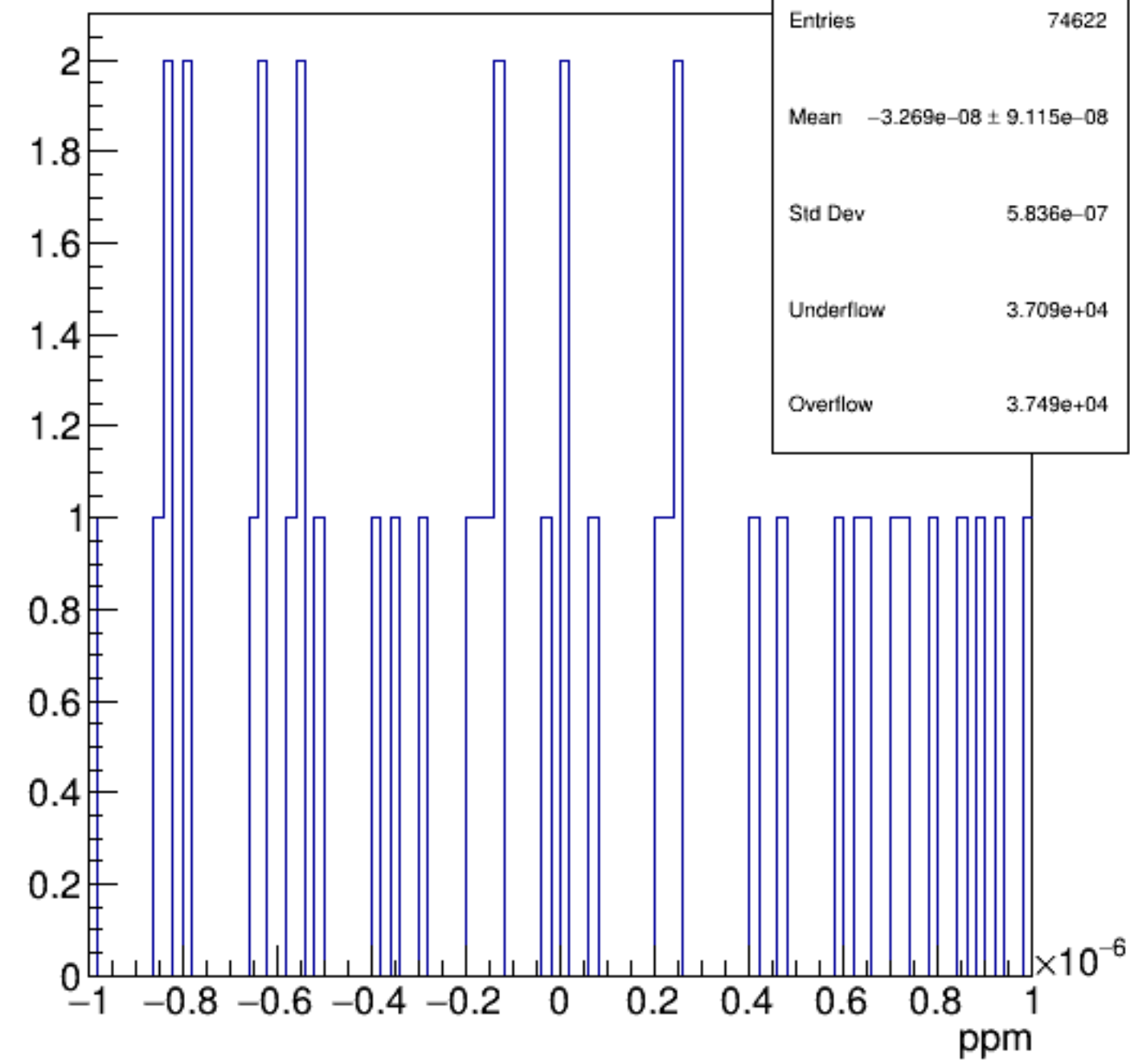
Mean -0.02793

Std Dev 0.008614

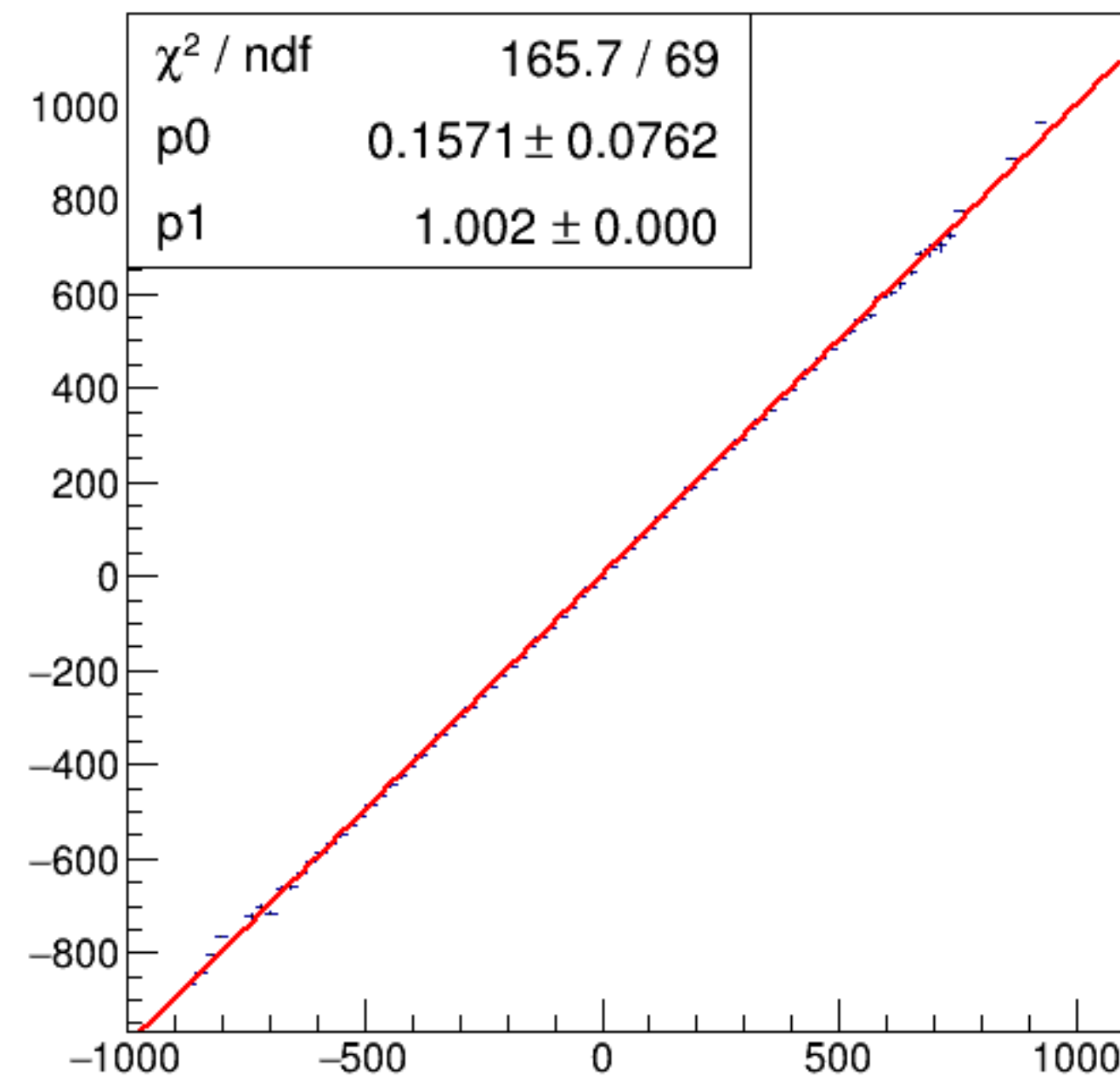
asym_bcm_an_diff*yield_bcm_an_diff:pattern_number {ErrorFlag==0}



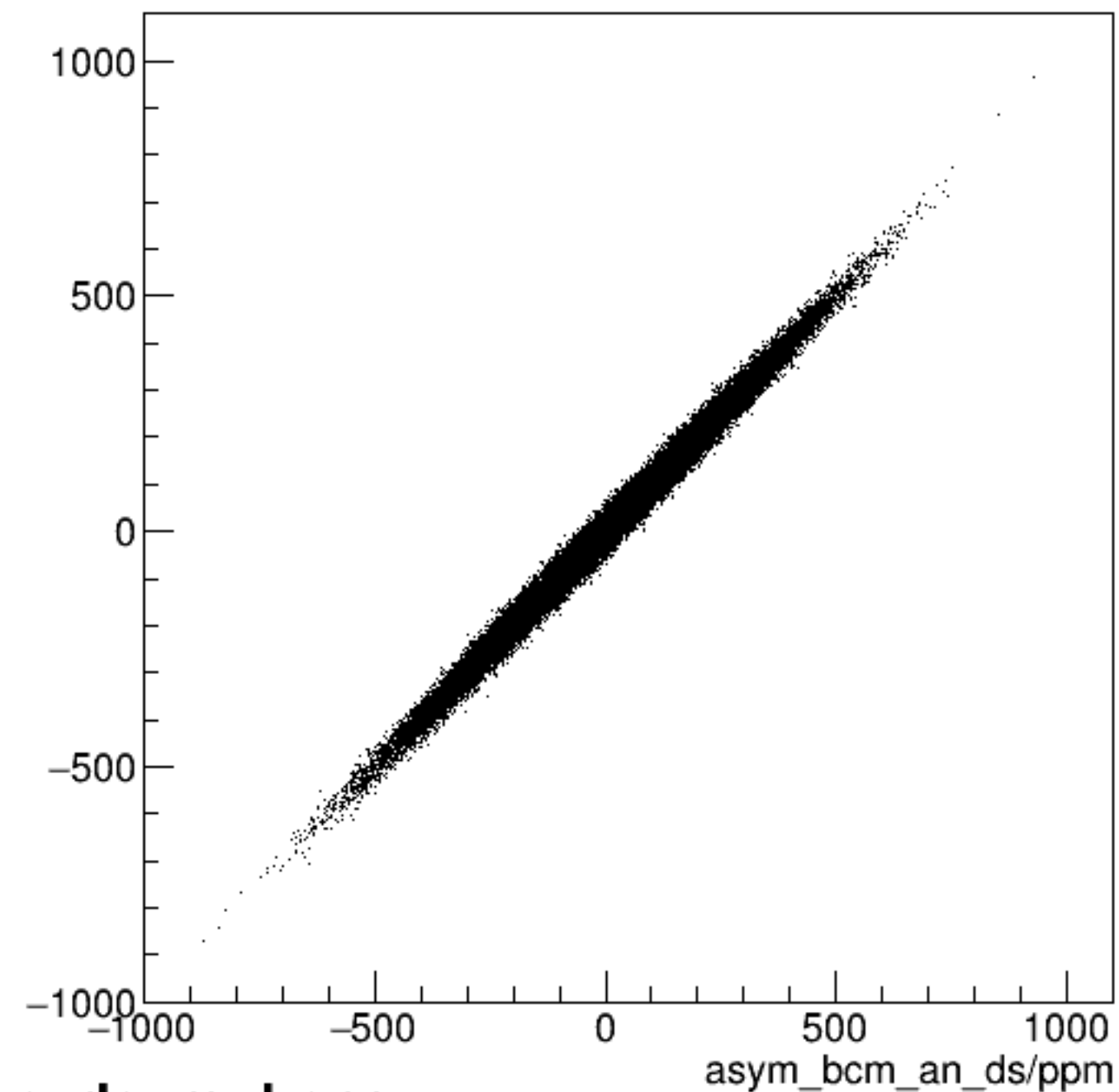
asym_bcm_an_diff



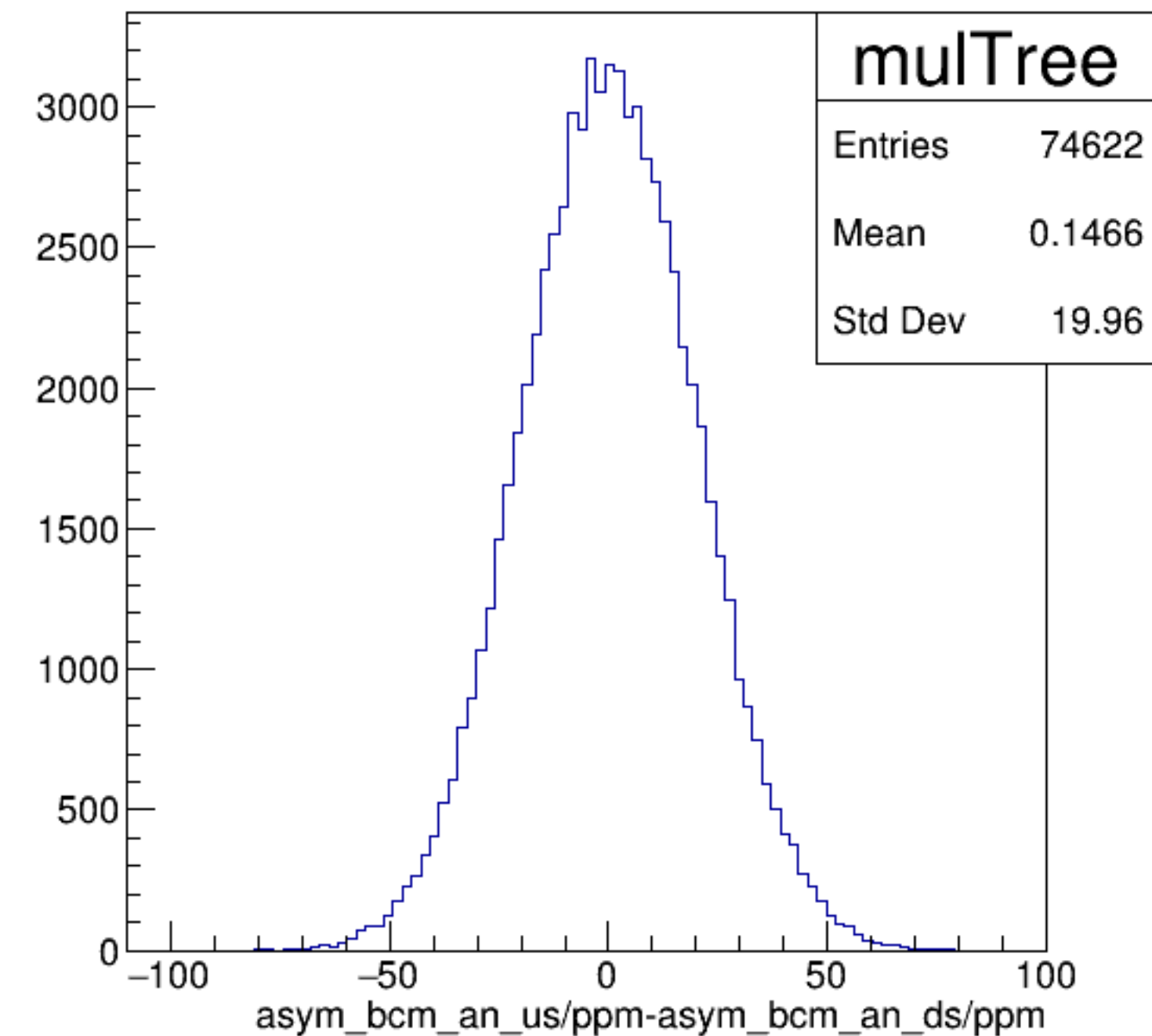
asym_bcm_an_us/ppm:asym_bcm_an_ds/ppm {ErrorFlag==0}



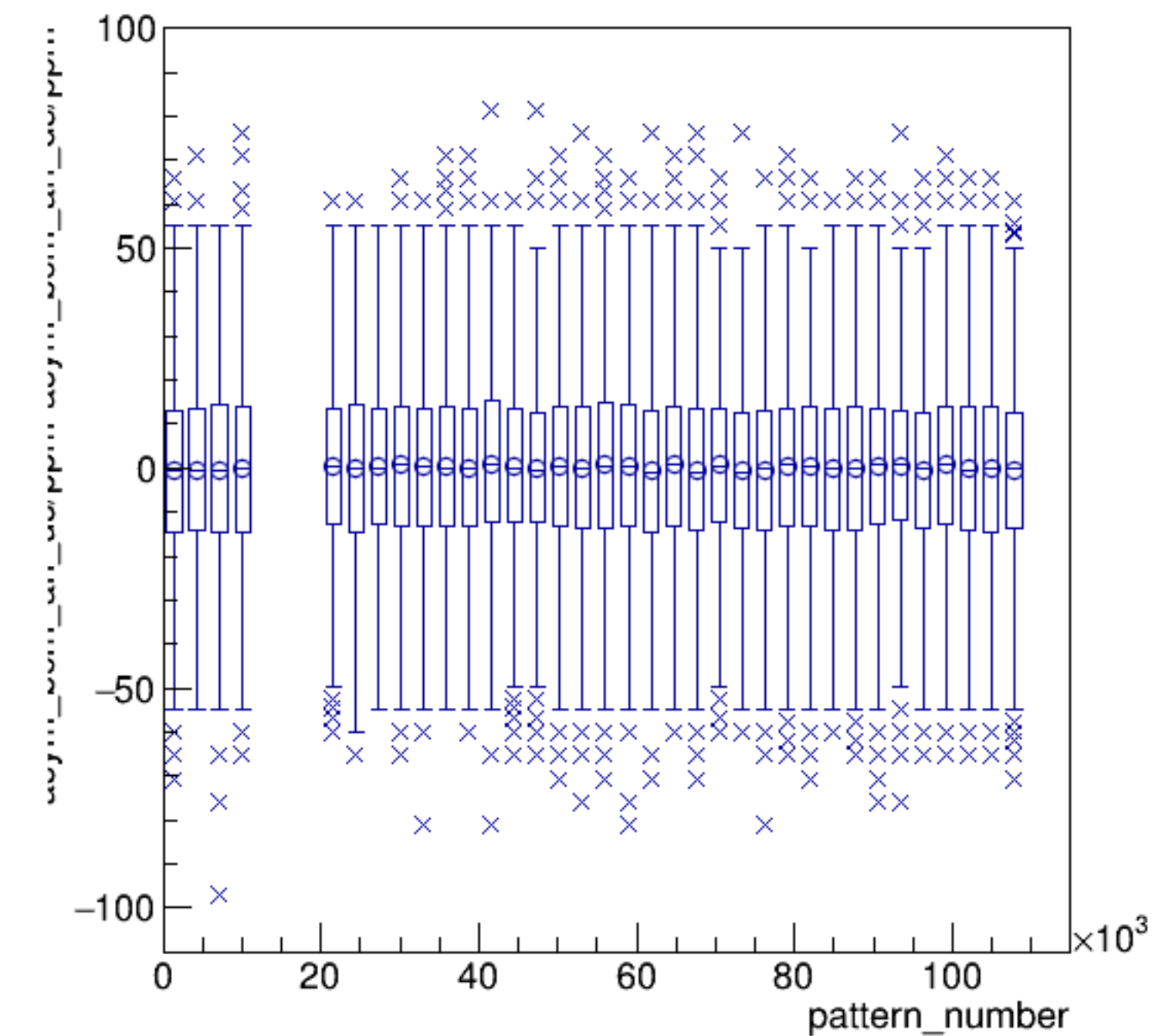
asym_bcm_an_us/ppm:asym_bcm_an_ds/ppm {ErrorFlag==0}



asym_bcm_an_us/ppm-asym_bcm_an_ds/ppm {ErrorFlag==0}

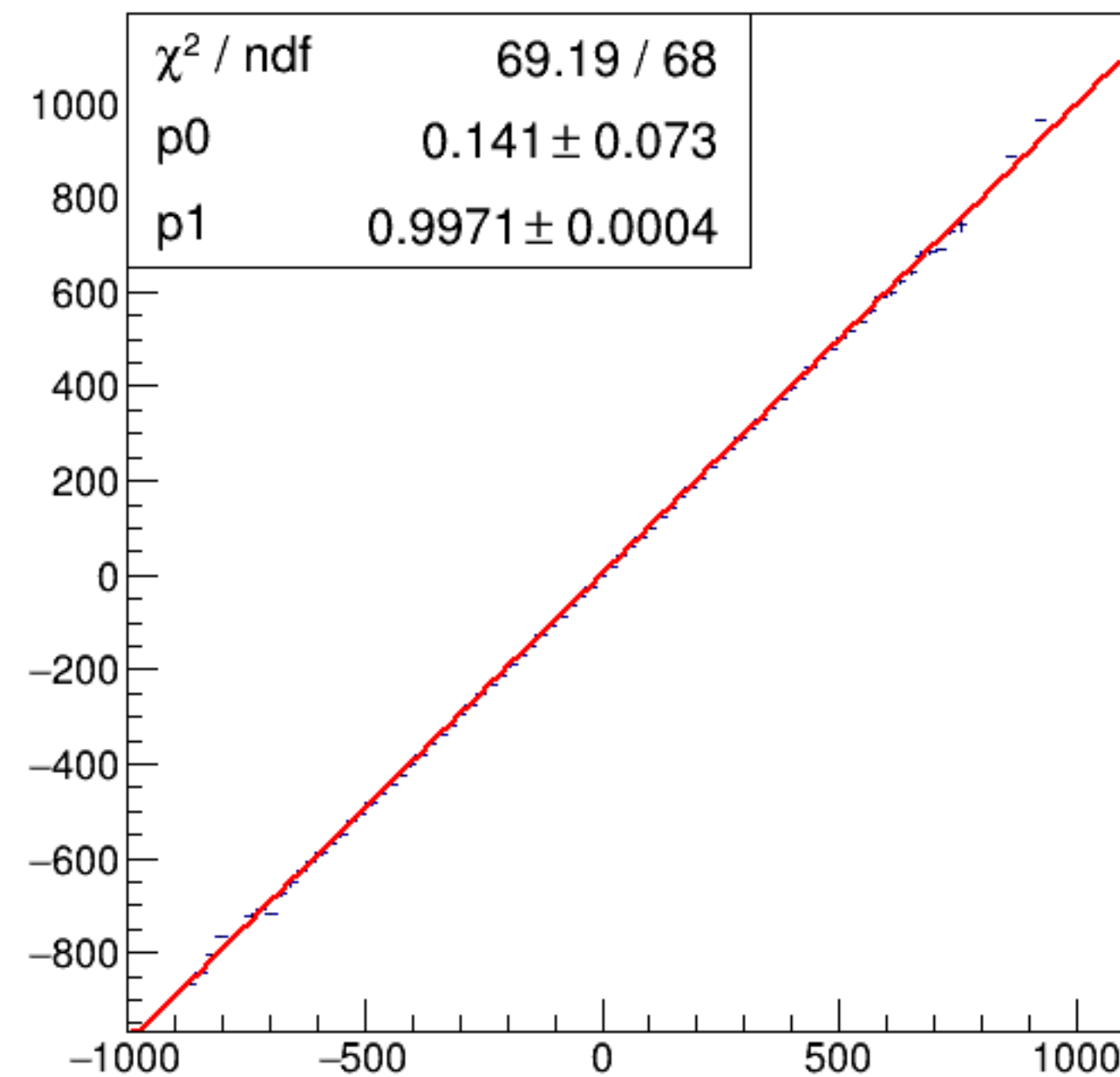


asym_bcm_an_us/ppm-asym_bcm_an_ds/ppm:pattern_number {ErrorFlag==0}

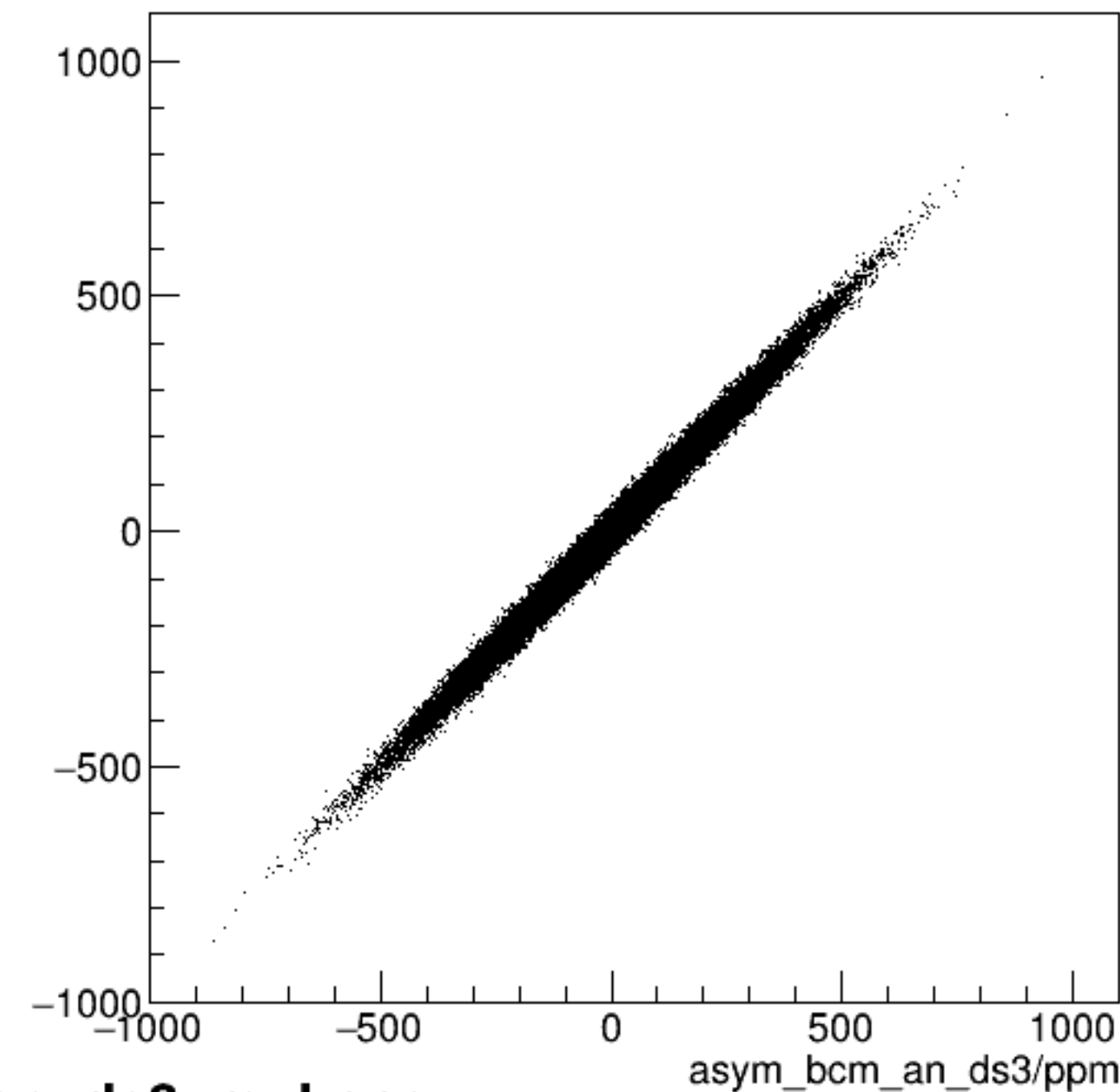


run8304_000_asym_bcm_an_us_vs_asym_bcm_an_ds_mul.png

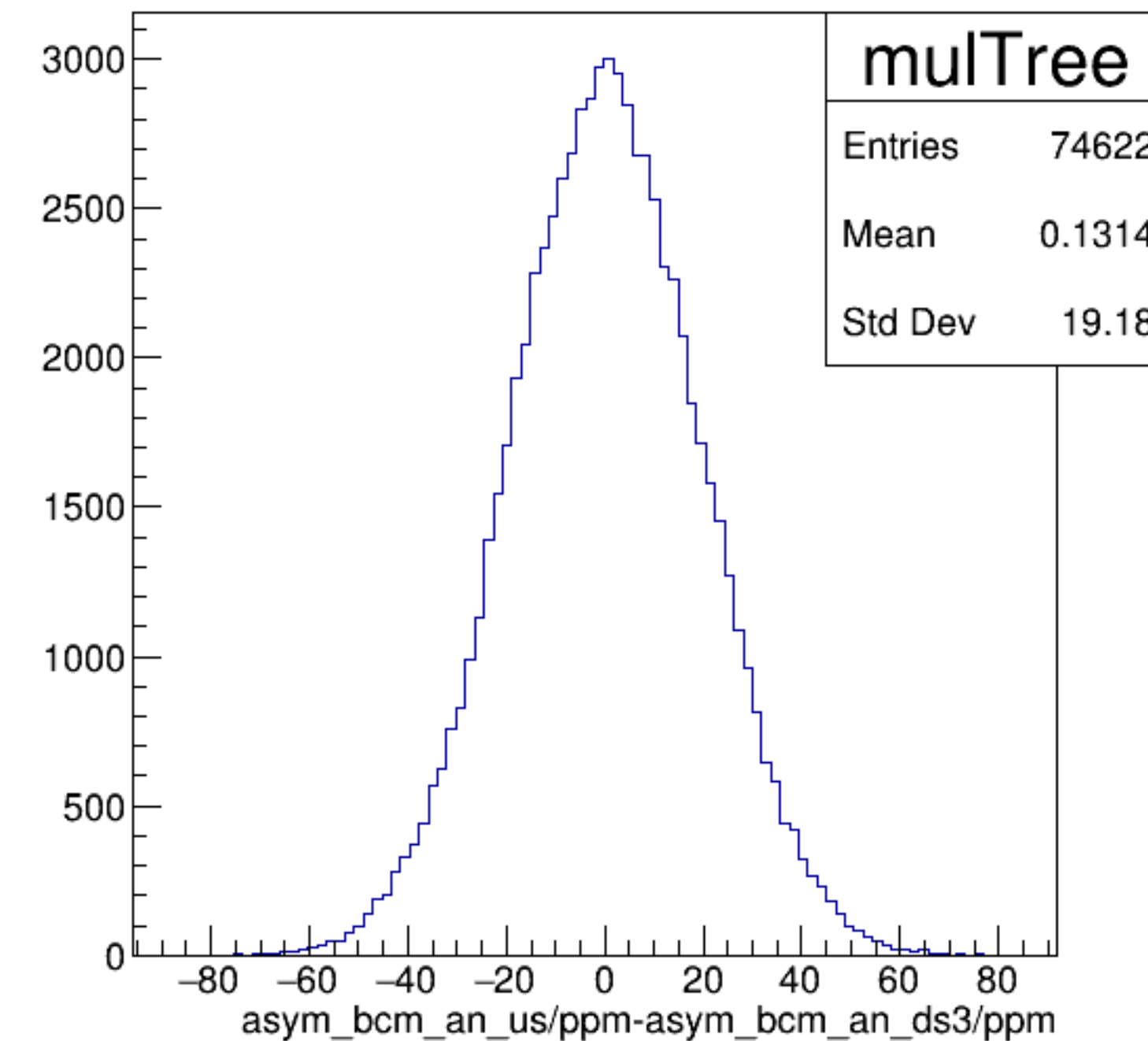
asym_bcm_an_us/ppm:asym_bcm_an_ds3/ppm {ErrorFlag==0}



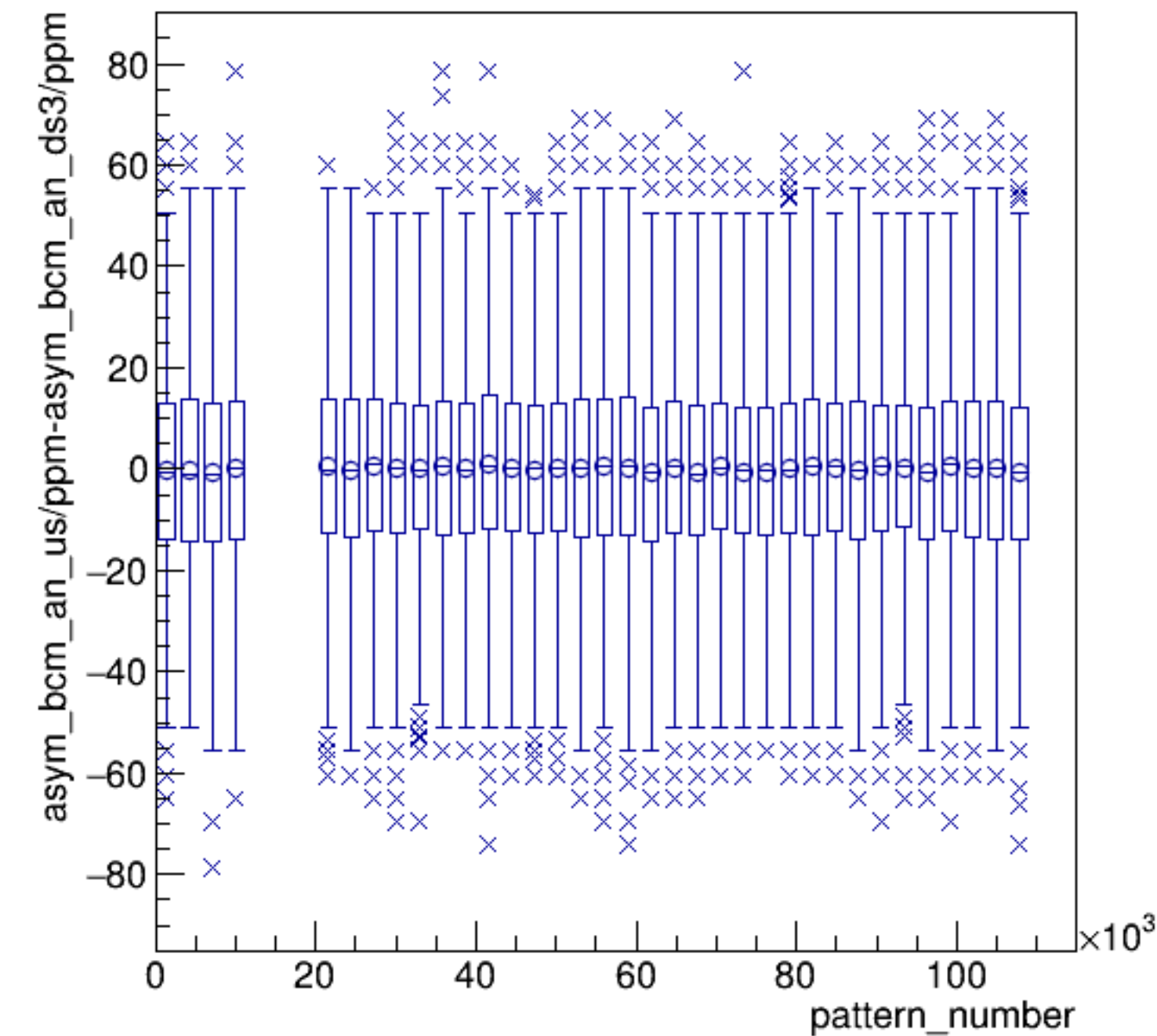
asym_bcm_an_us/ppm:asym_bcm_an_ds3/ppm {ErrorFlag==0}



asym_bcm_an_us/ppm-asym_bcm_an_ds3/ppm {ErrorFlag==0}

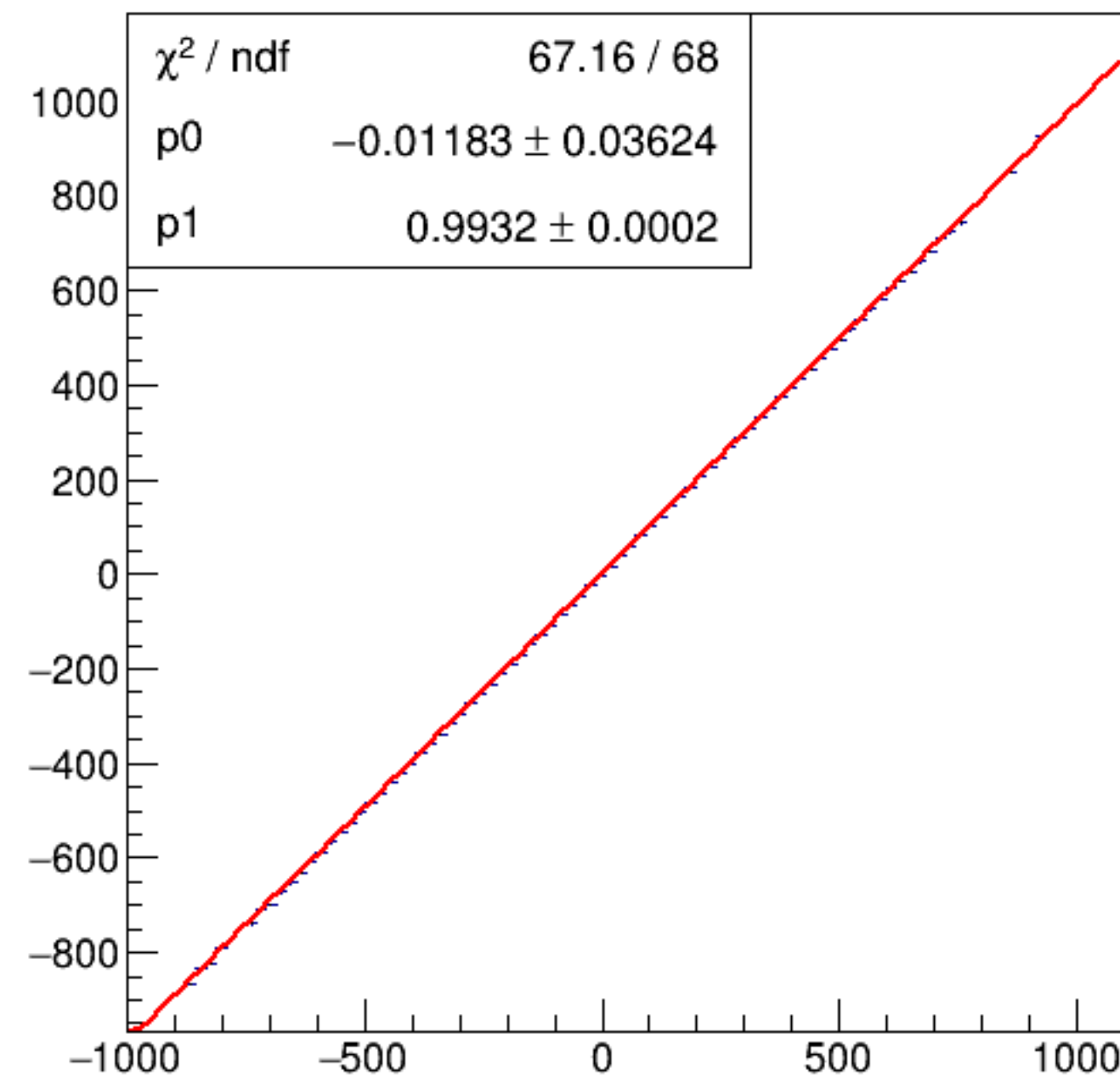


asym_bcm_an_us/ppm-asym_bcm_an_ds3/ppm:pattern_number {ErrorFlag==0}

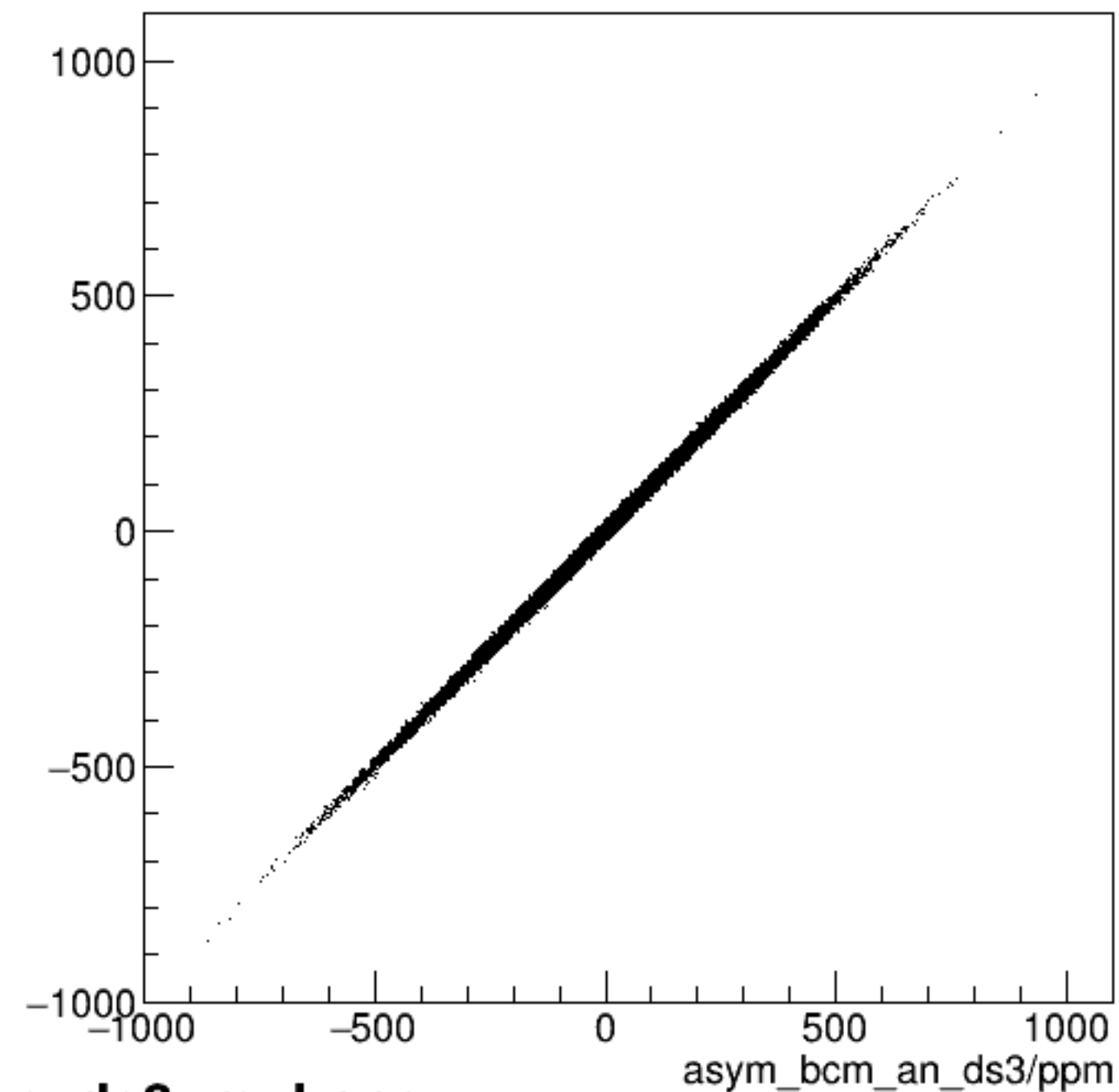


run8304_000_asym_bcm_an_us_vs_asym_bcm_an_ds3_mul.png

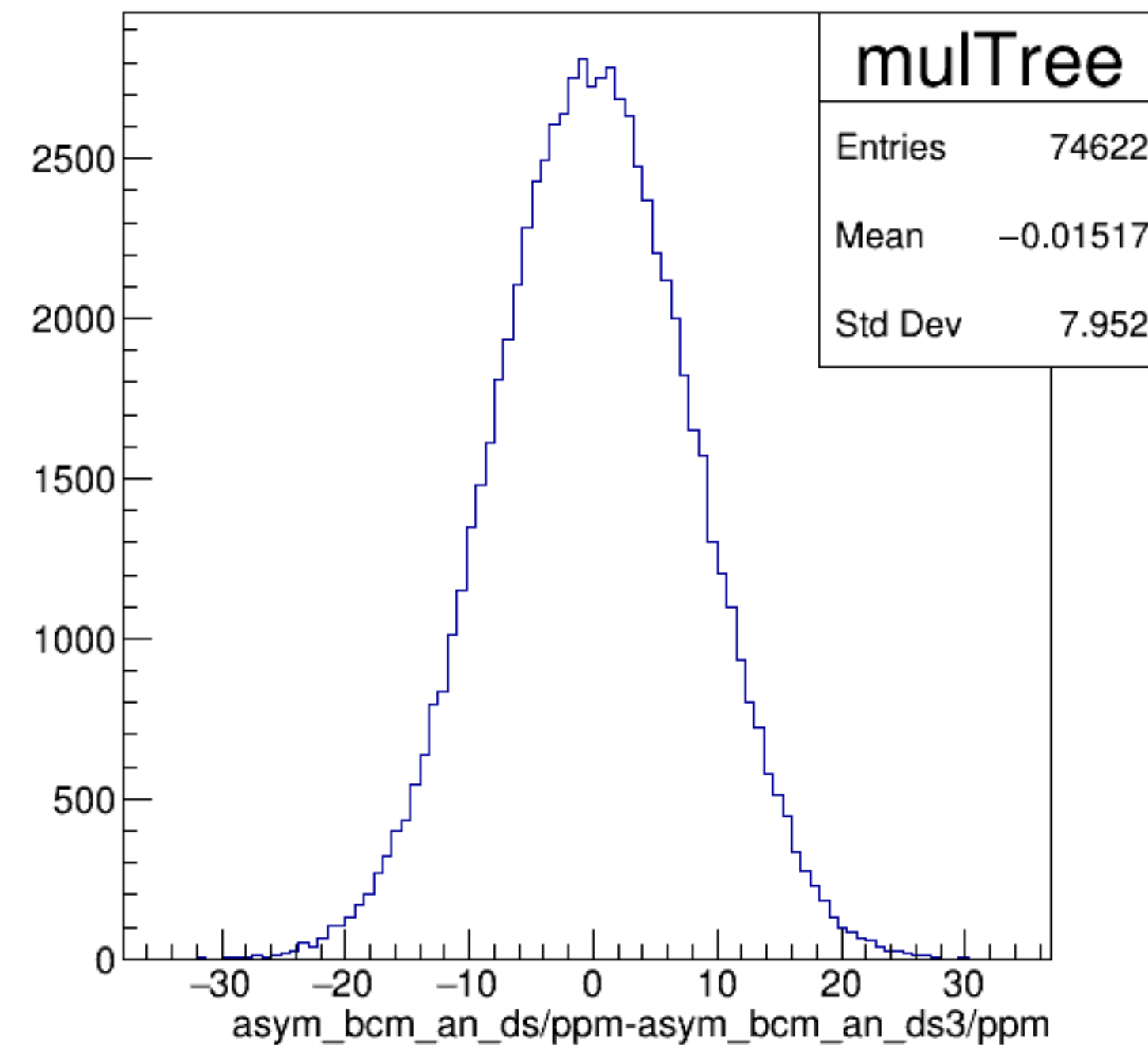
asym_bcm_an_ds/ppm:asym_bcm_an_ds3/ppm {ErrorFlag==0}



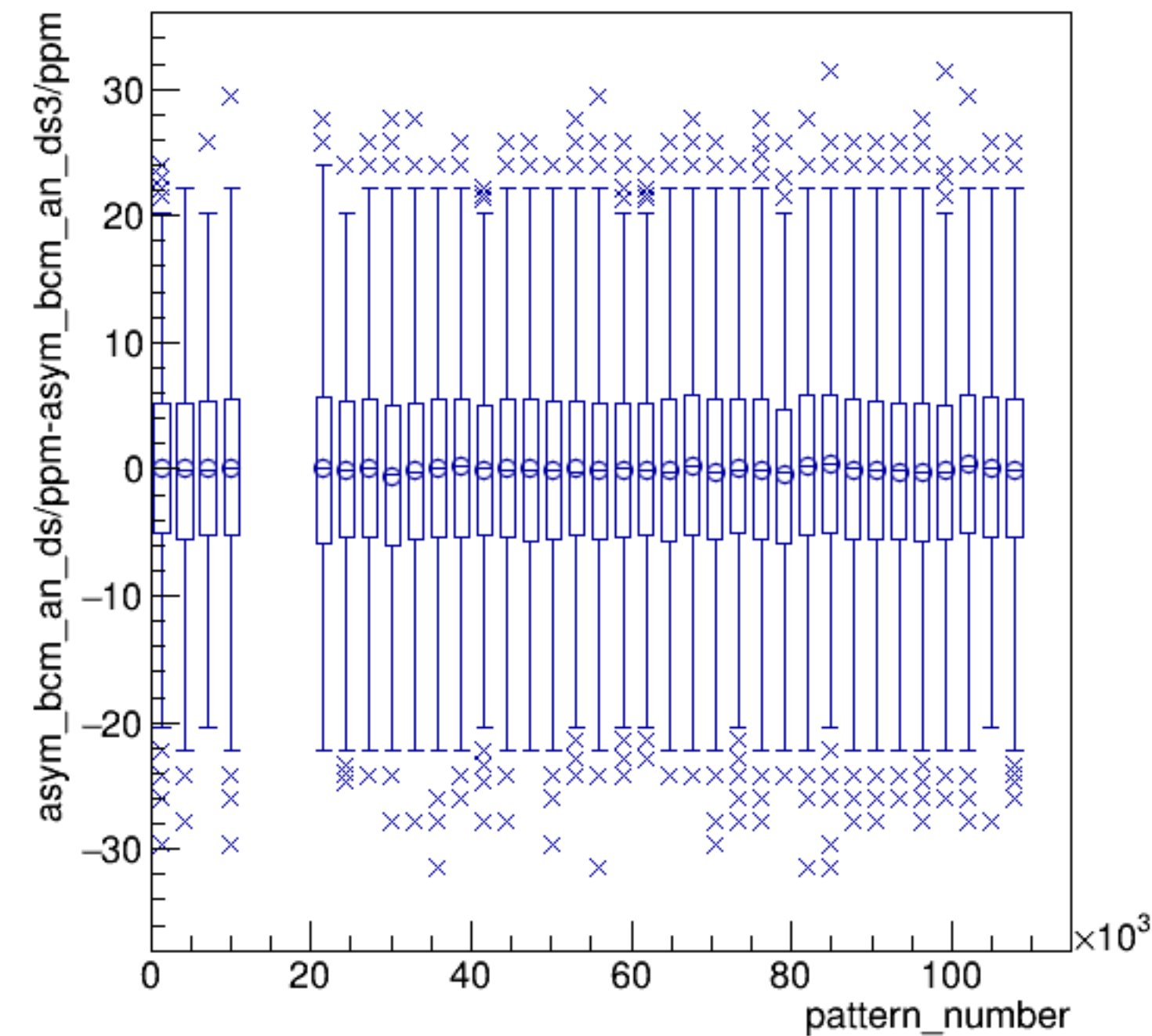
asym_bcm_an_ds/ppm:asym_bcm_an_ds3/ppm {ErrorFlag==0}



asym_bcm_an_ds/ppm-asym_bcm_an_ds3/ppm {ErrorFlag==0}

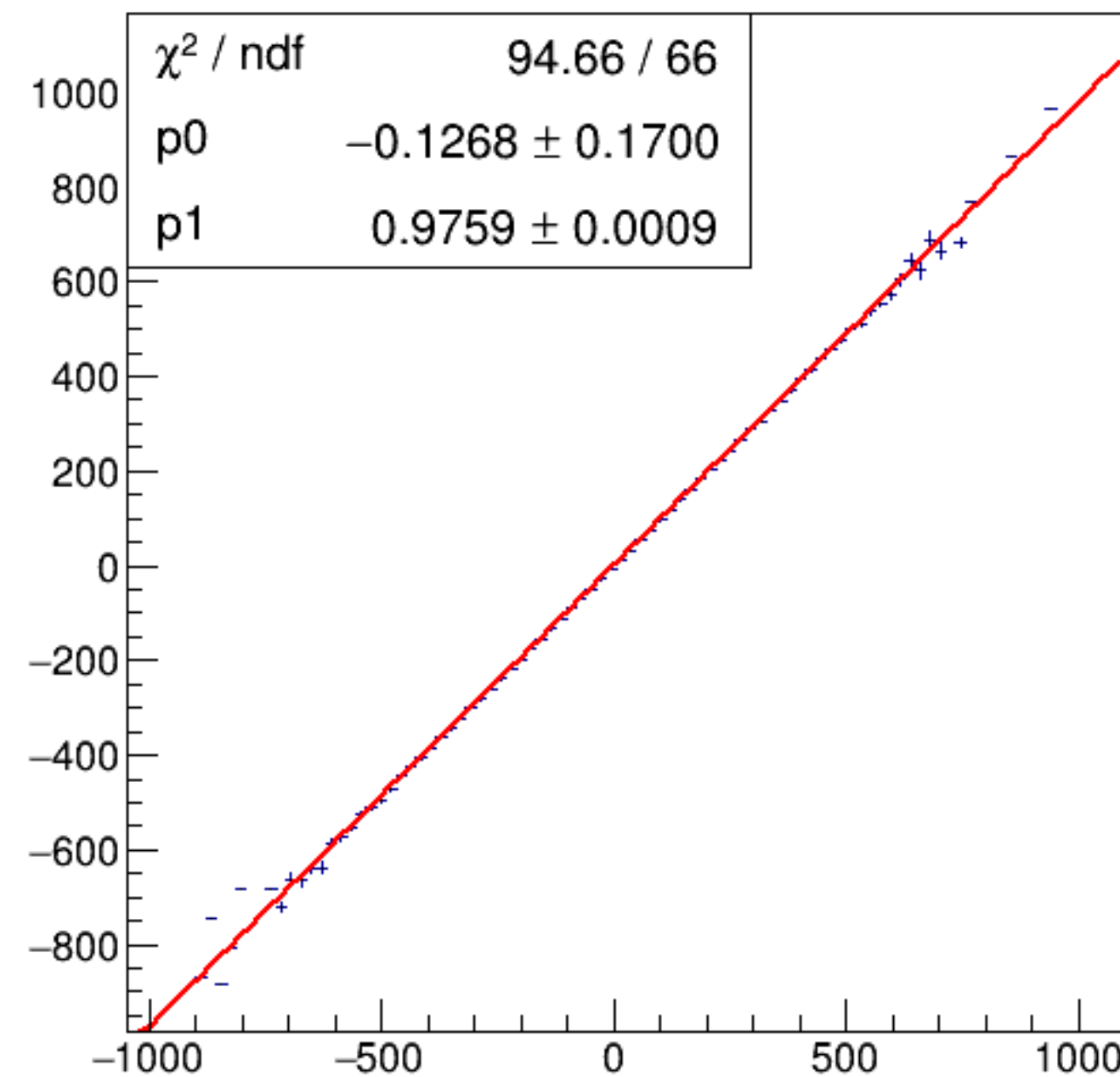


asym_bcm_an_ds/ppm-asym_bcm_an_ds3/ppm:pattern_number {ErrorFlag==0}

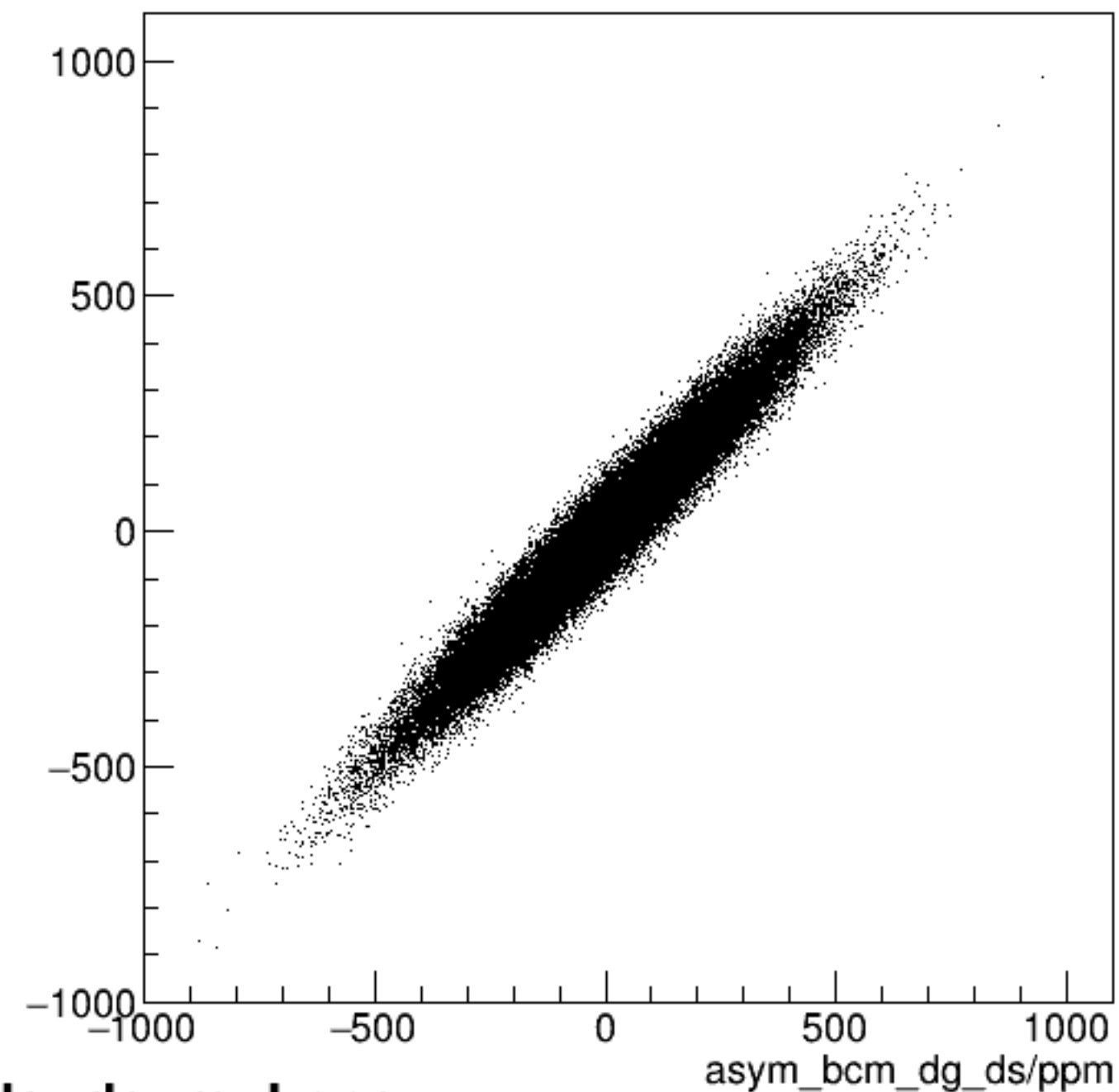


run8304_000_asym_bcm_an_ds_vs_asym_bcm_an_ds3_mul.png

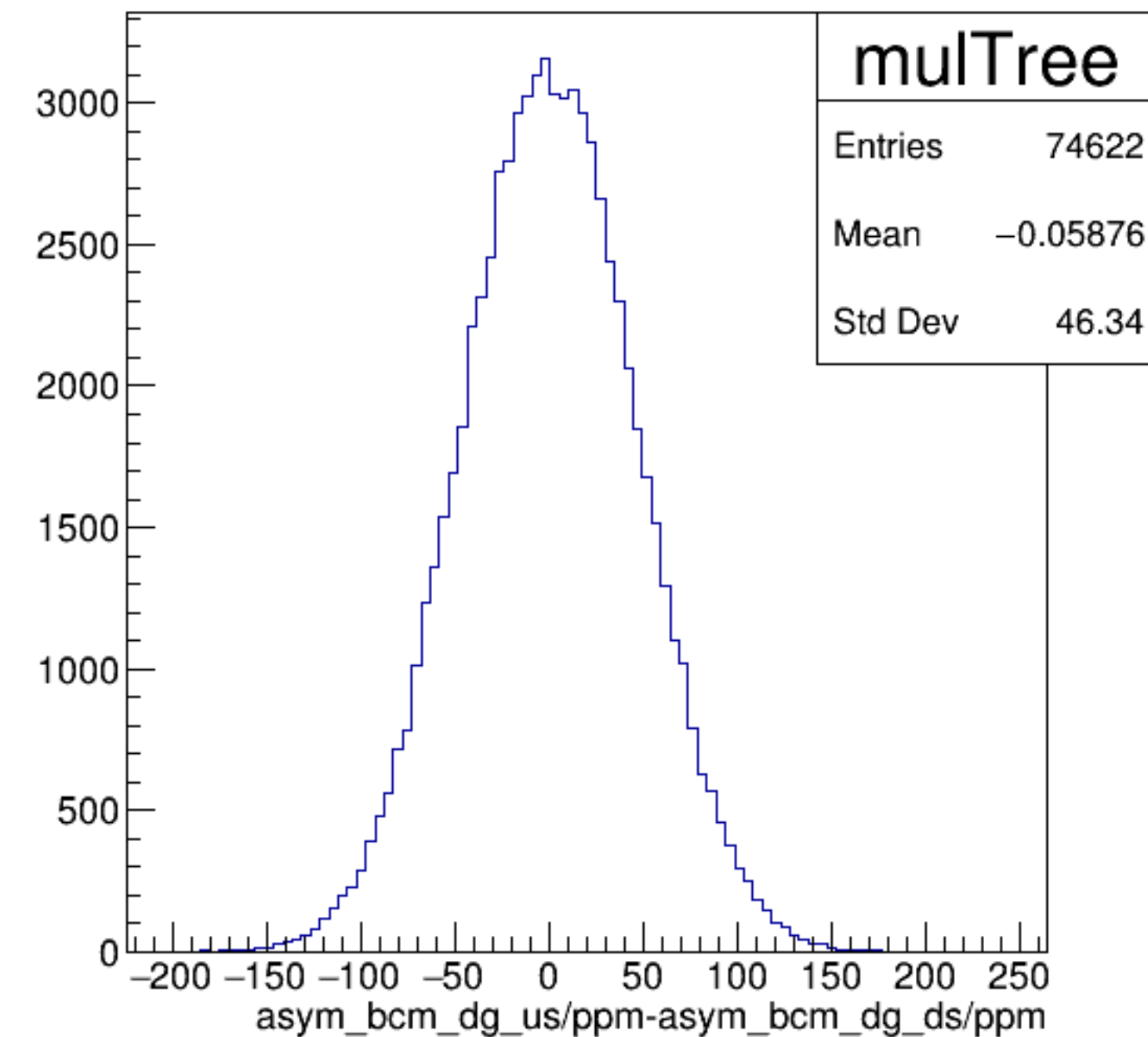
asym_bcm_dg_us/ppm:asym_bcm_dg_ds/ppm {ErrorFlag==0}



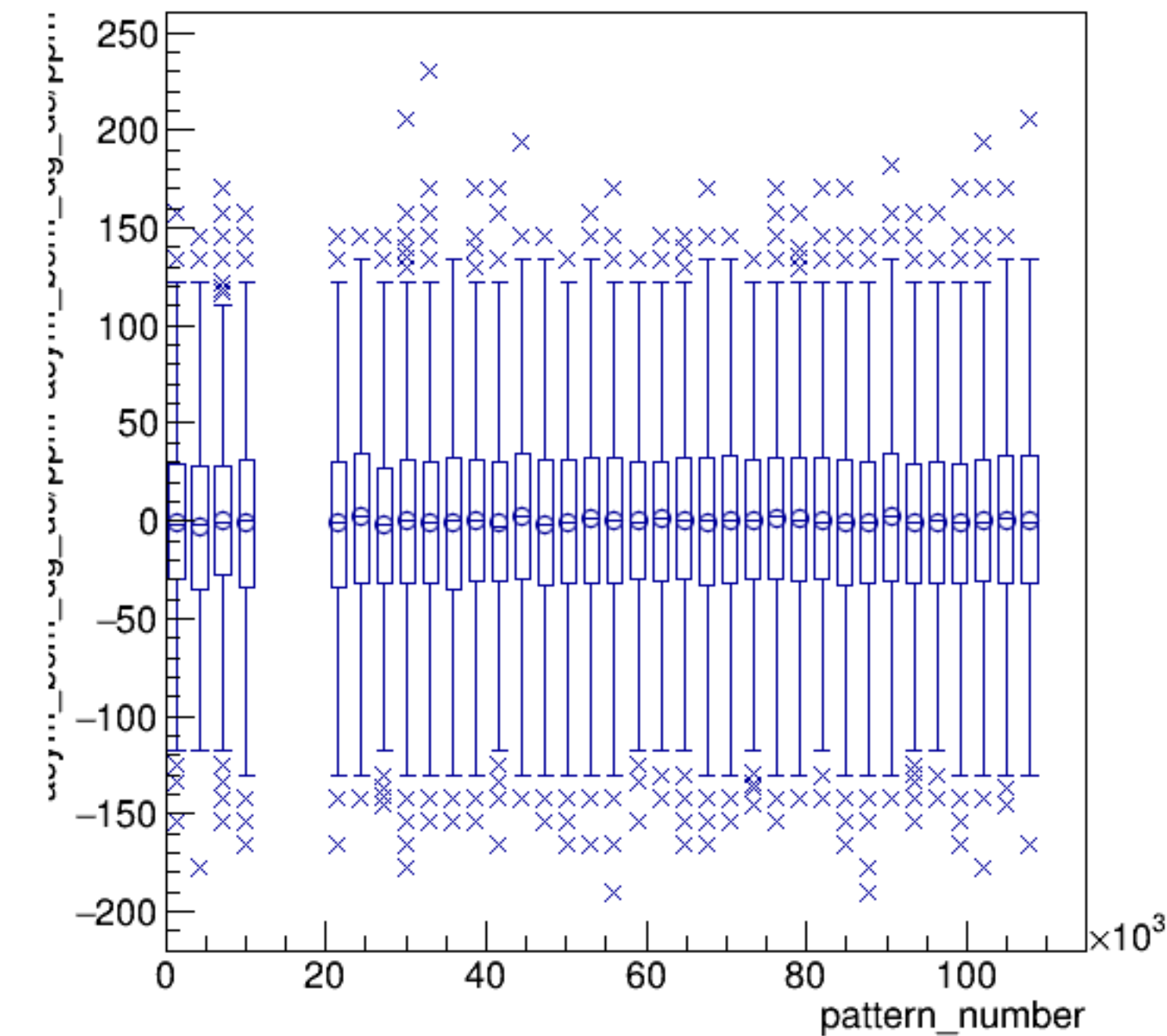
asym_bcm_dg_us/ppm:asym_bcm_dg_ds/ppm {ErrorFlag==0}



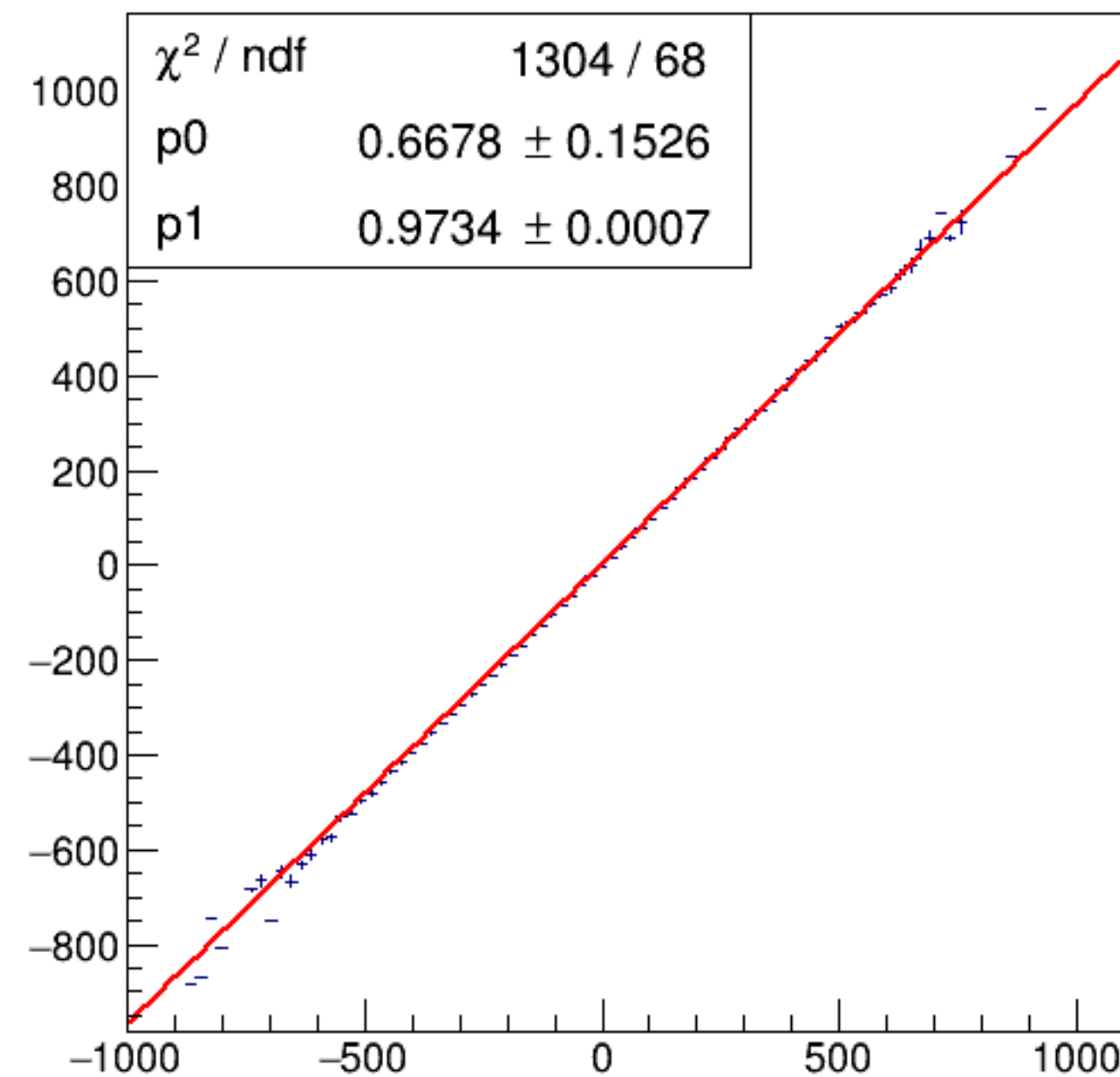
asym_bcm_dg_us/ppm-asym_bcm_dg_ds/ppm {ErrorFlag==0}



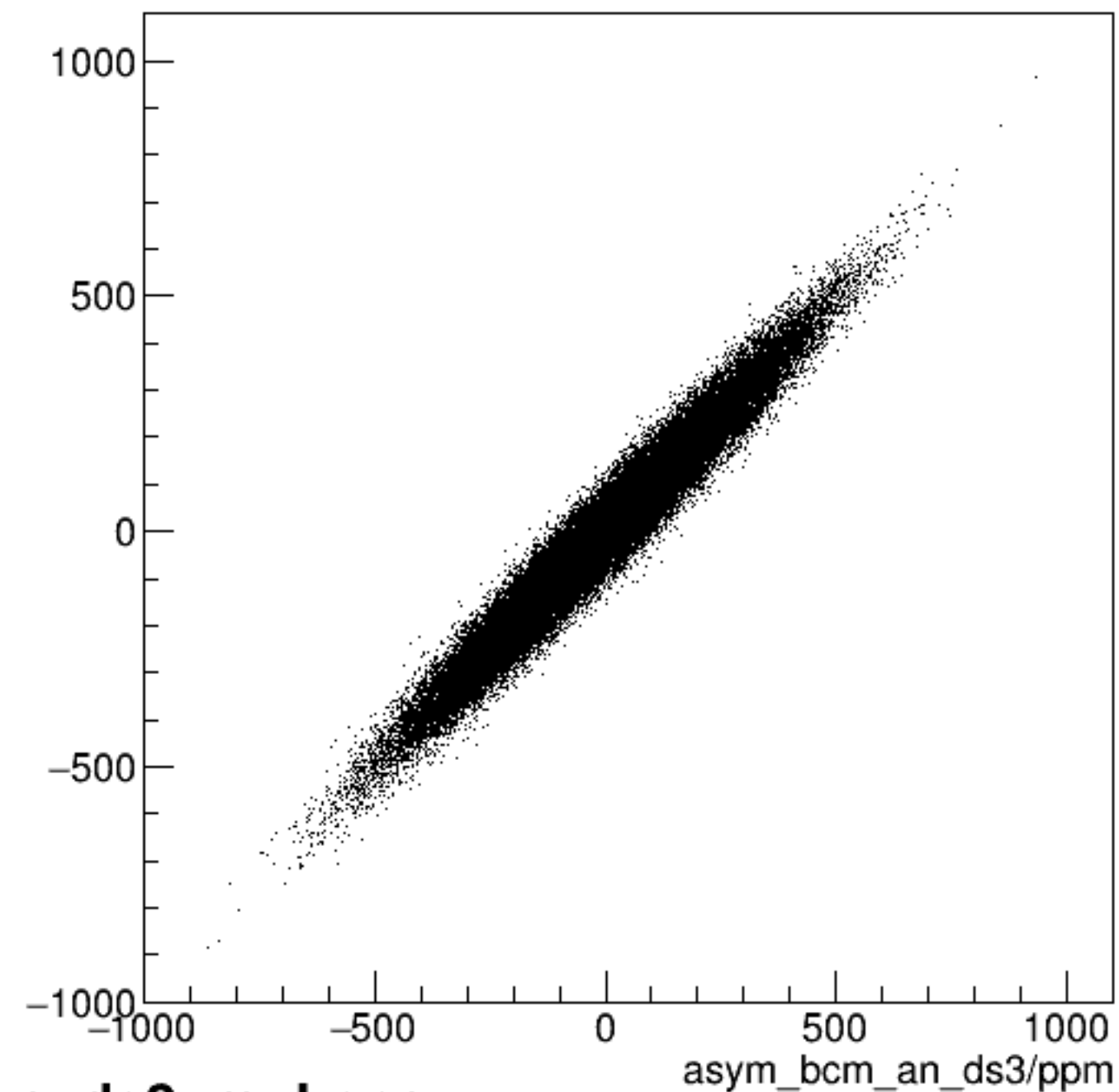
asym_bcm_dg_us/ppm-asym_bcm_dg_ds/ppm:pattern_number {ErrorFlag==0}



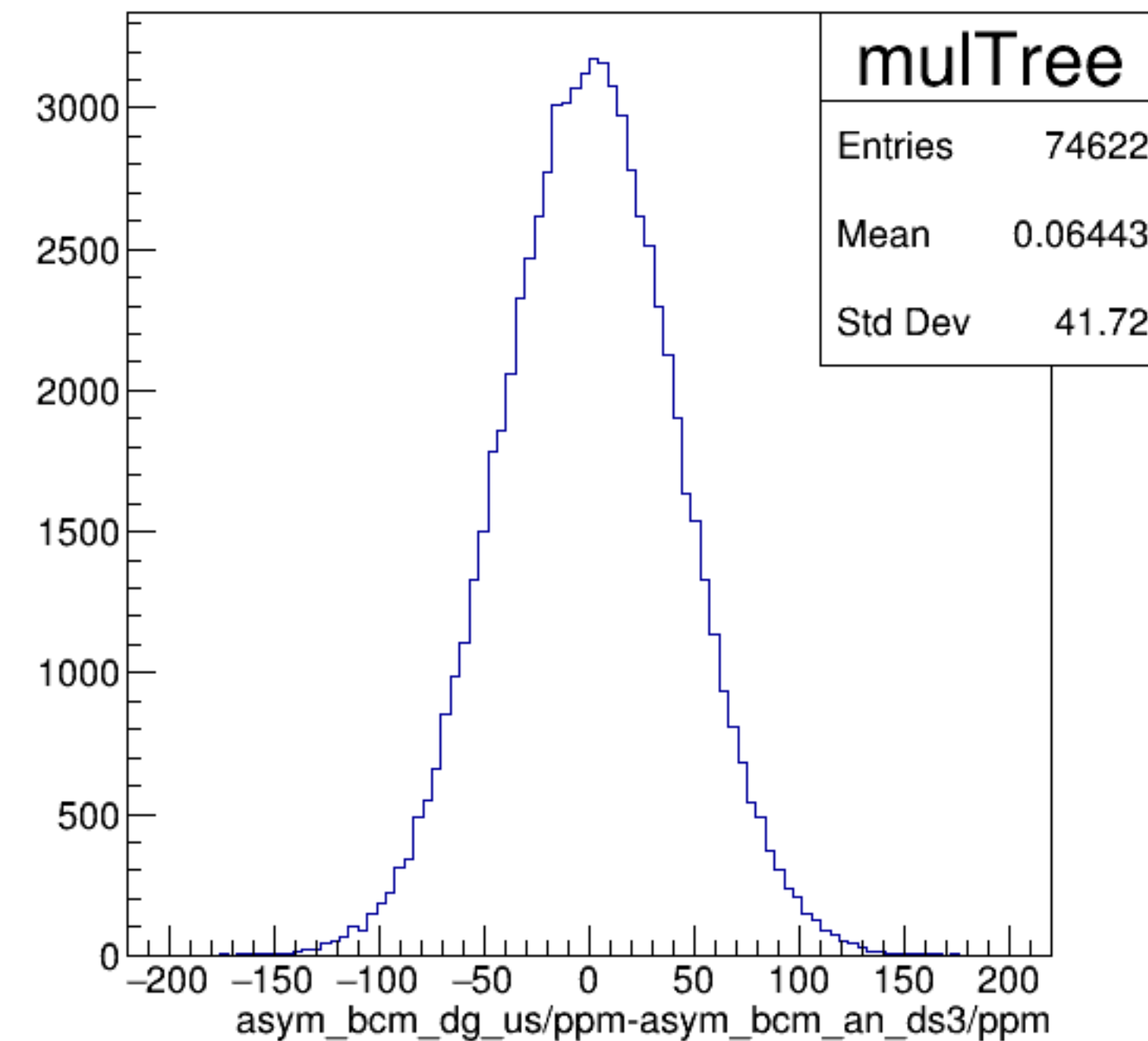
asym_bcm_dg_us/ppm:asym_bcm_an_ds3/ppm {ErrorFlag==0}



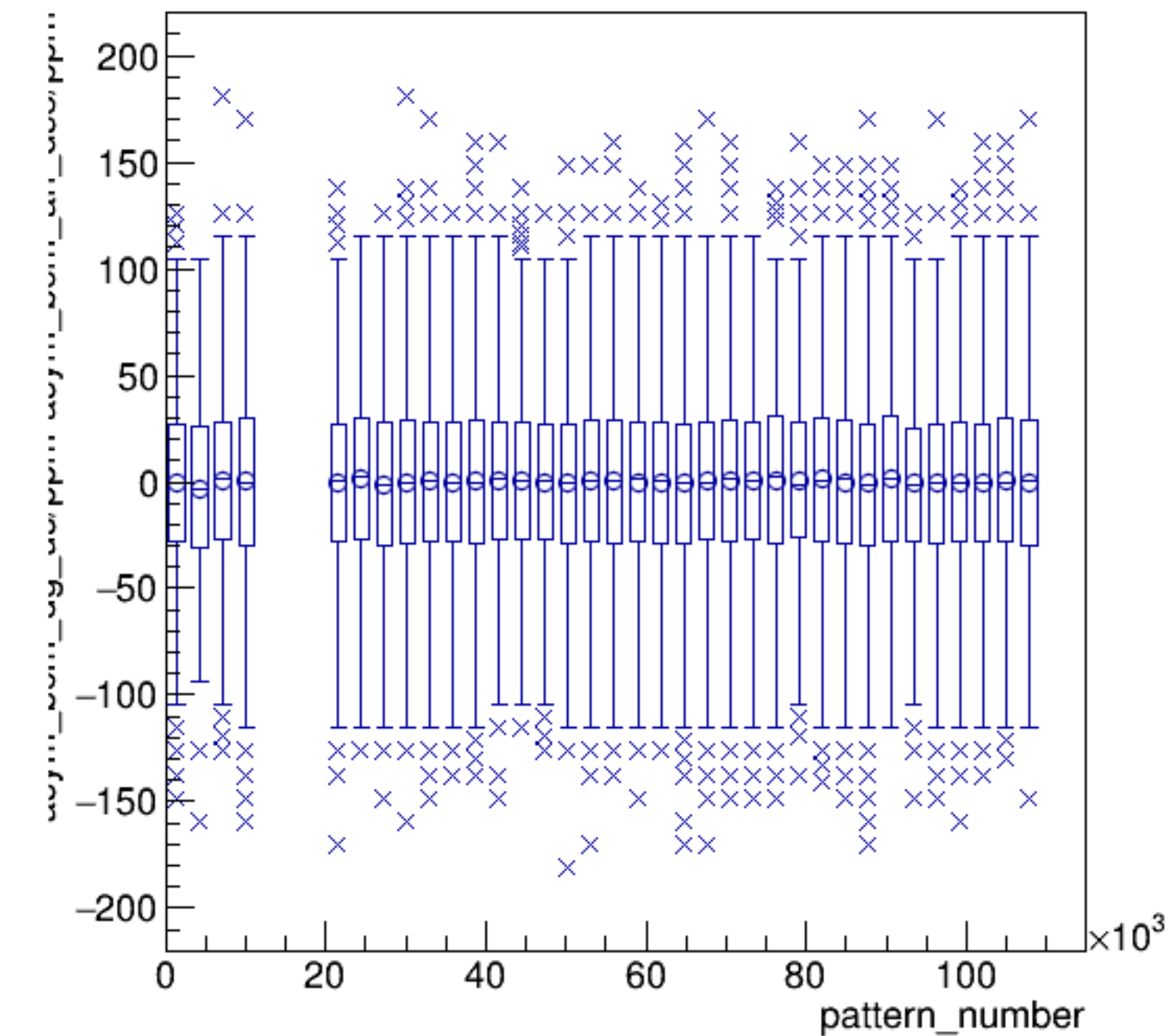
asym_bcm_dg_us/ppm:asym_bcm_an_ds3/ppm {ErrorFlag==0}



asym_bcm_dg_us/ppm-asym_bcm_an_ds3/ppm {ErrorFlag==0}

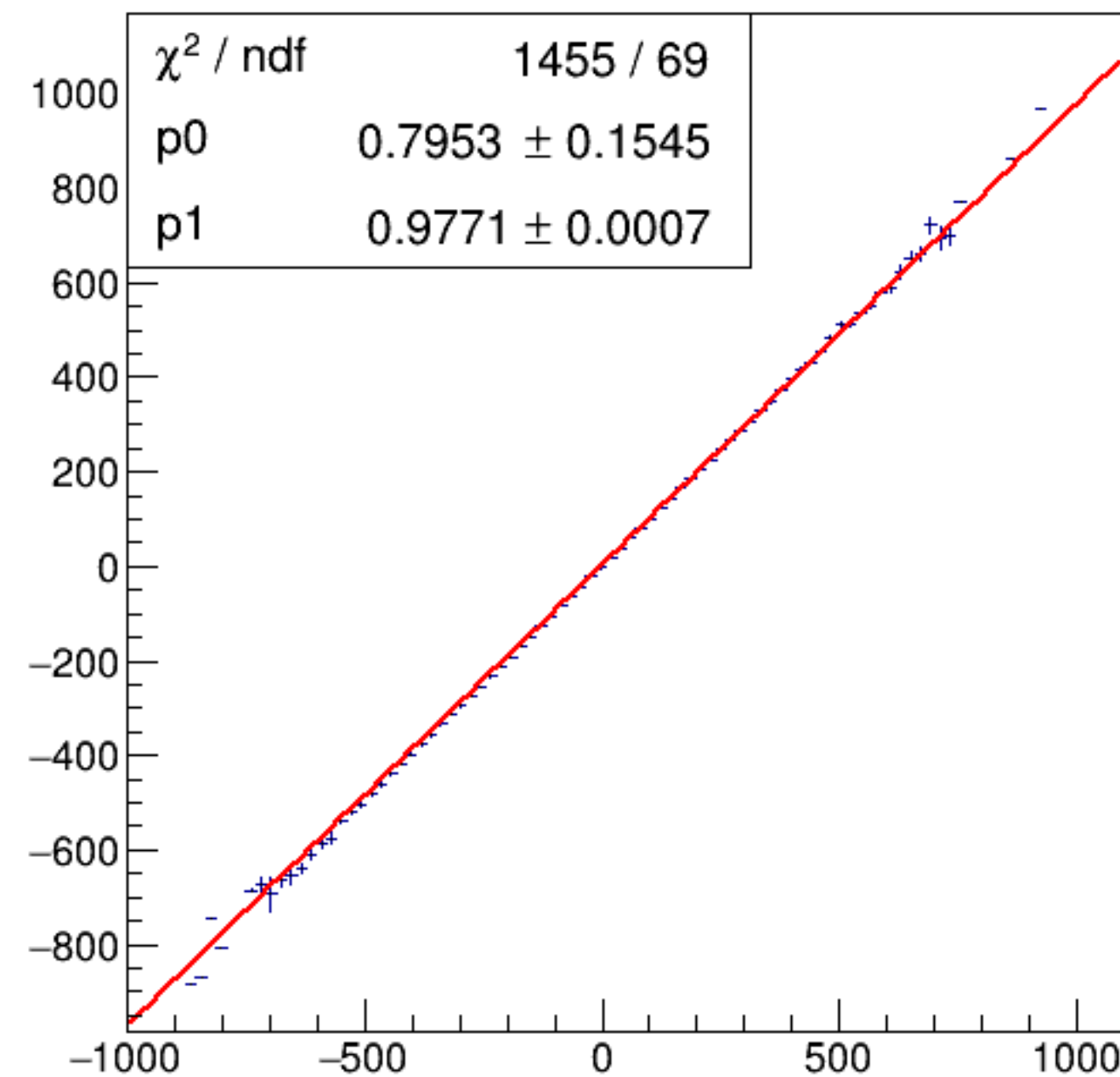


asym_bcm_dg_us/ppm-asym_bcm_an_ds3/ppm:pattern_number {ErrorFlag==0}

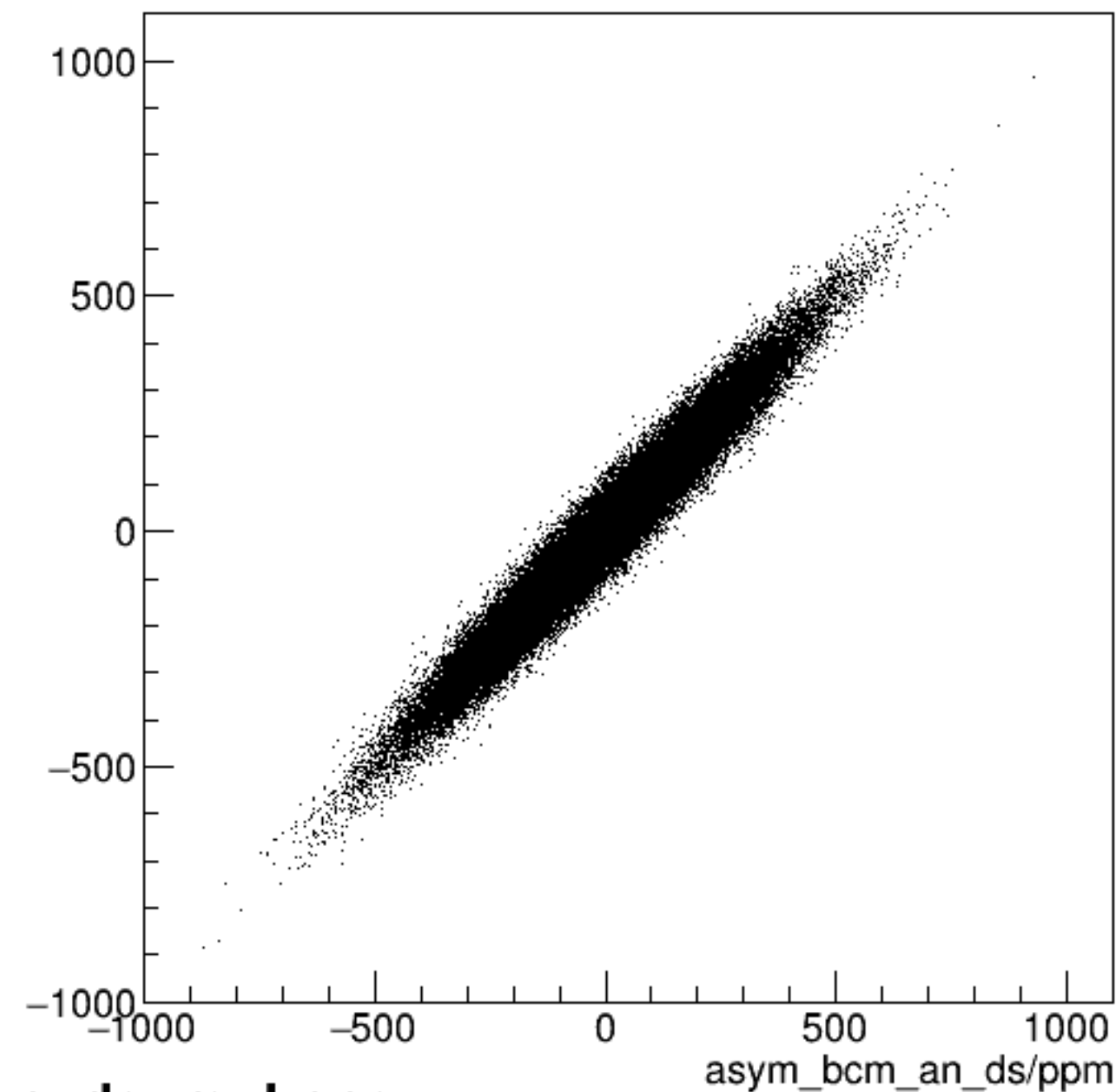


run8304_000_asym_bcm_dg_us_vs_asym_bcm_an_ds3_mul.png

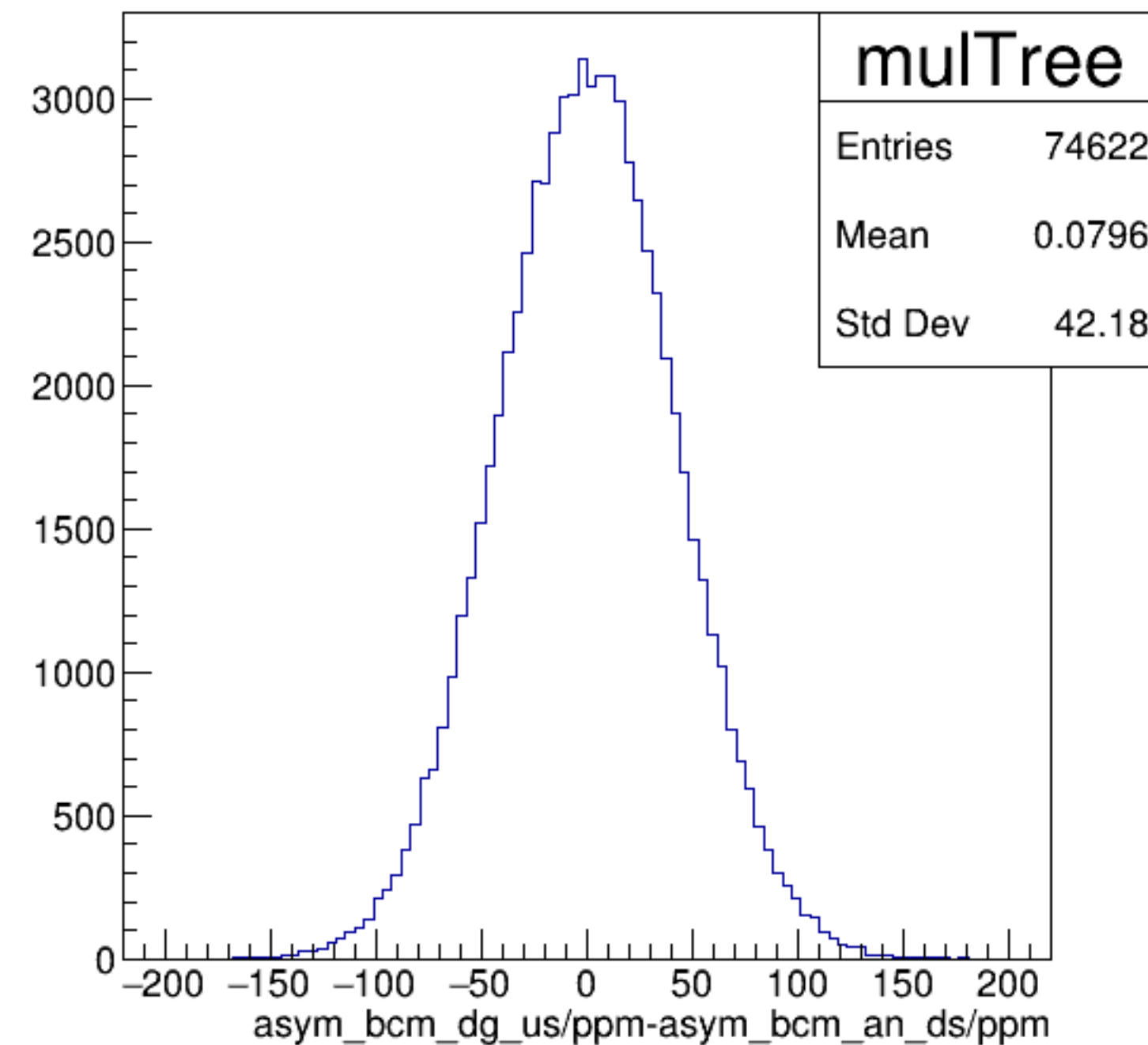
asym_bcm_dg_us/ppm:asym_bcm_an_ds/ppm {ErrorFlag==0}



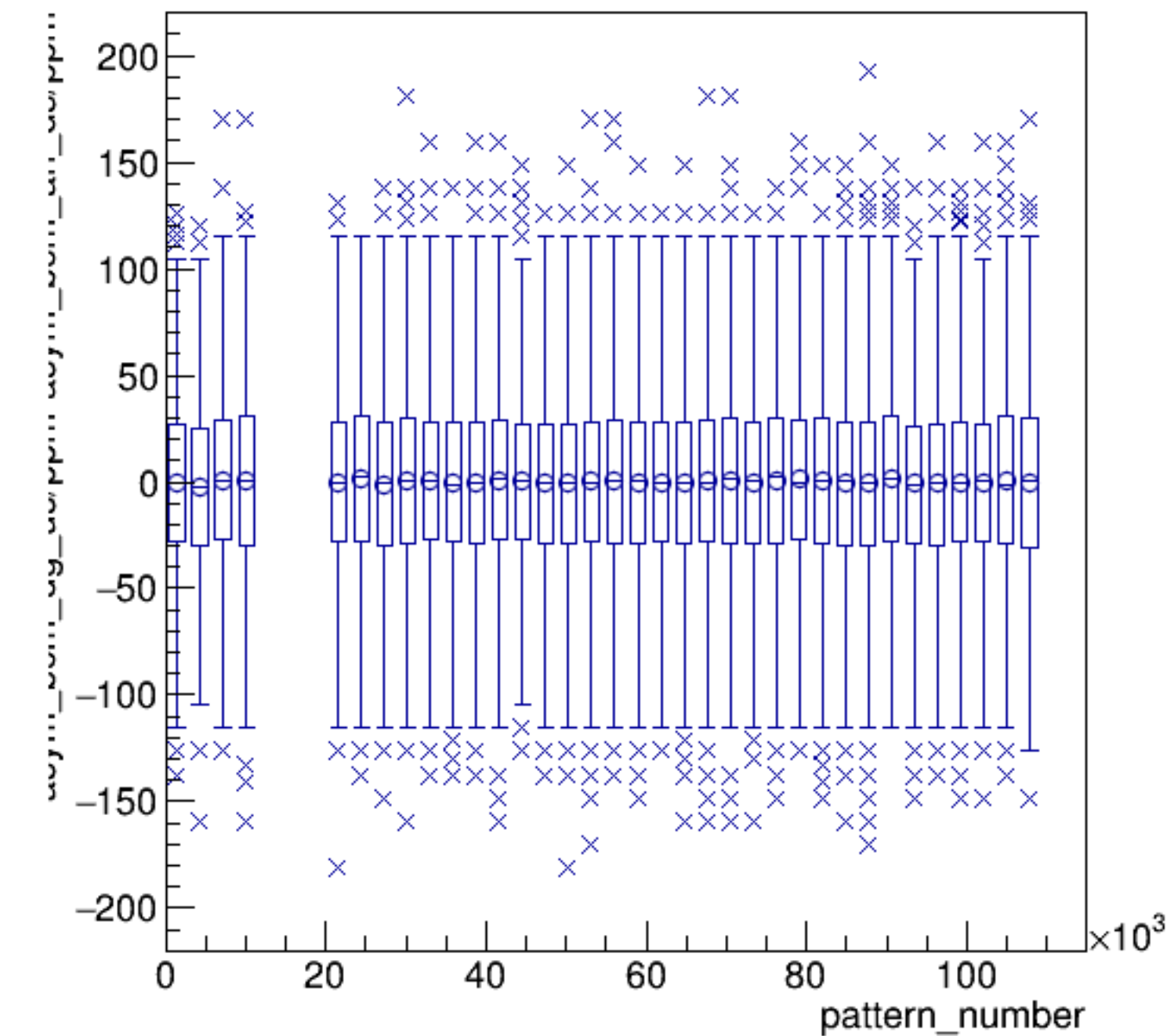
asym_bcm_dg_us/ppm:asym_bcm_an_ds/ppm {ErrorFlag==0}



asym_bcm_dg_us/ppm-asym_bcm_an_ds/ppm {ErrorFlag==0}

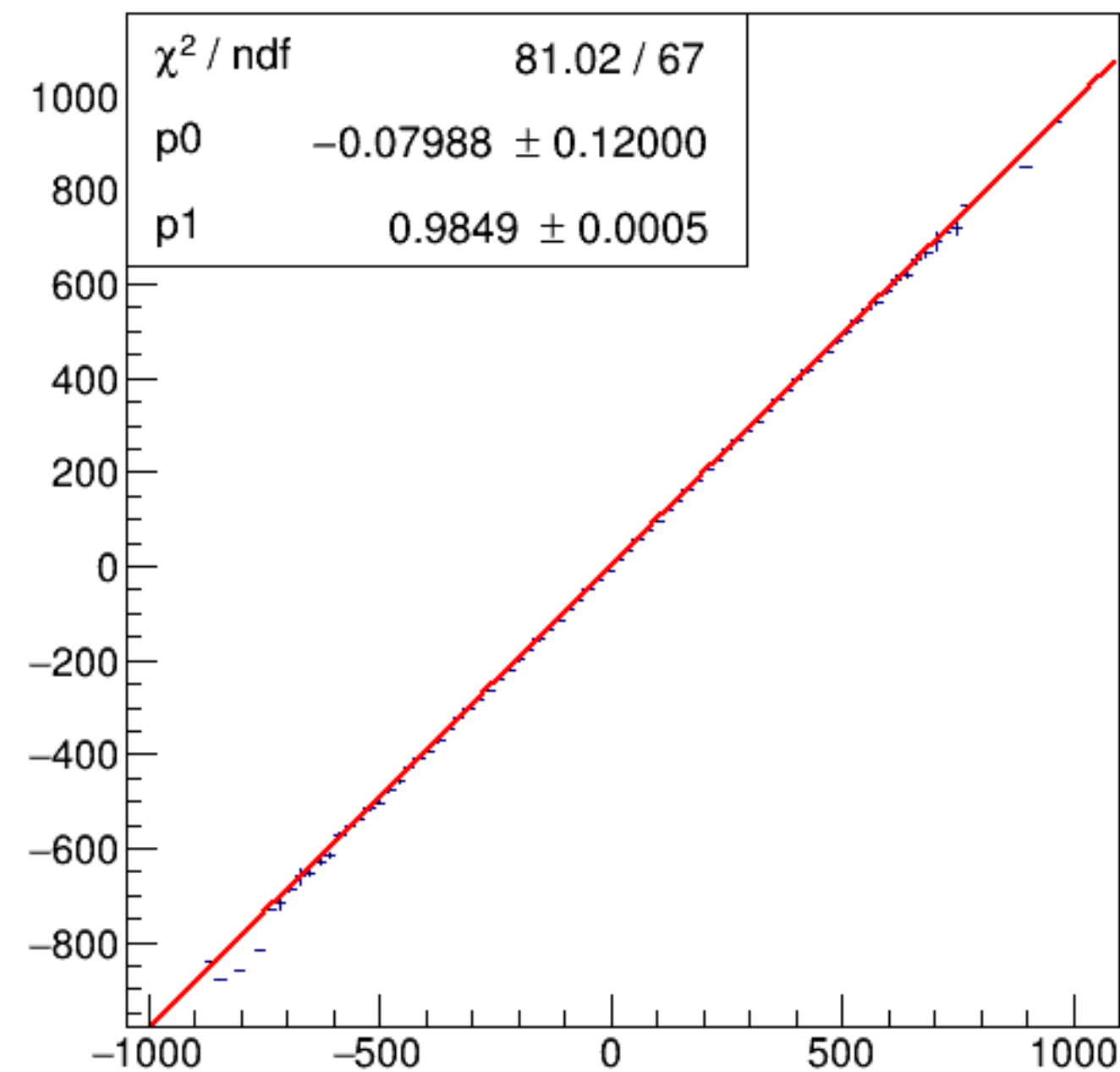


asym_bcm_dg_us/ppm-asym_bcm_an_ds/ppm:pattern_number {ErrorFlag==0}

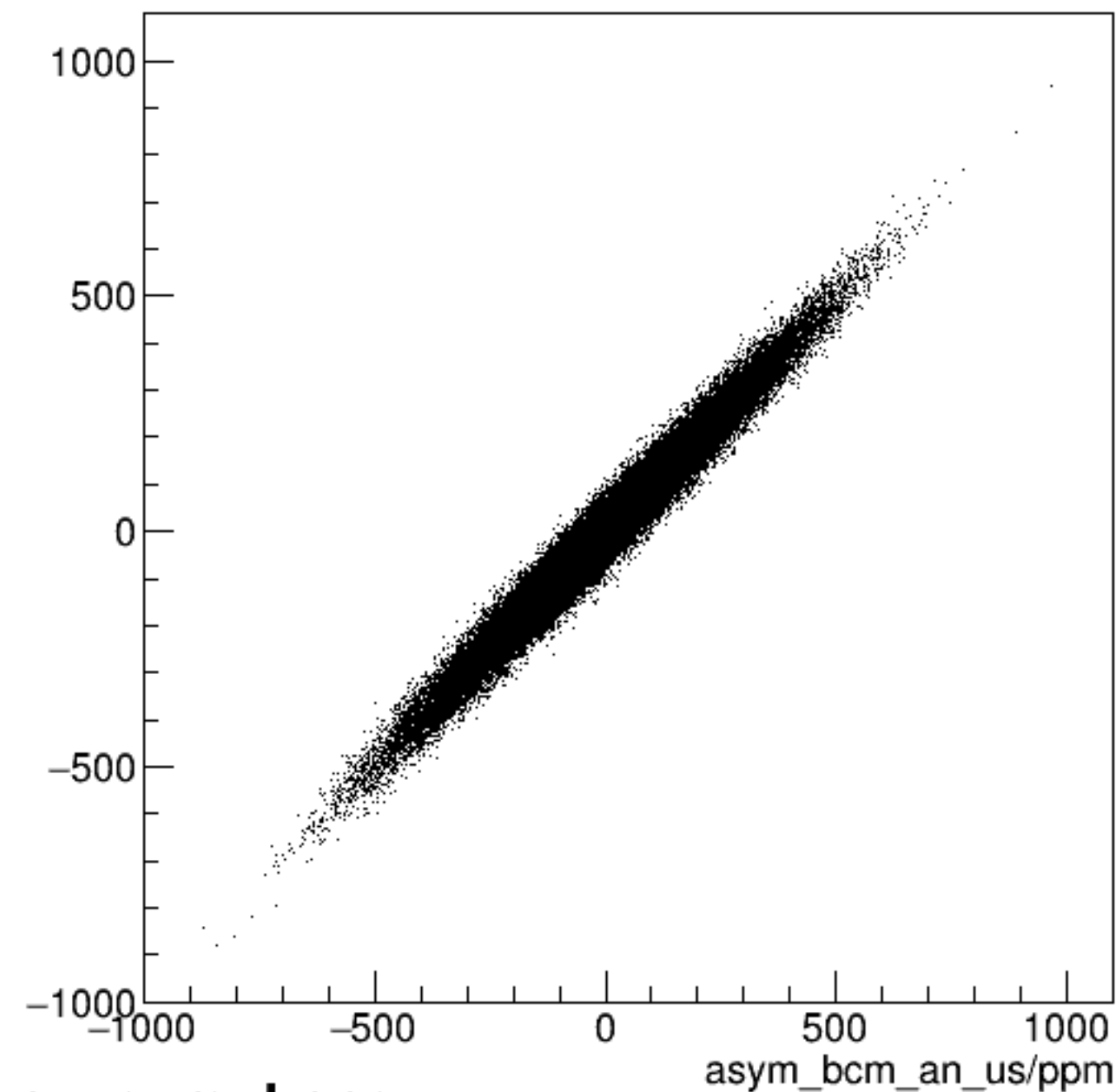


run8304_000_asym_bcm_dg_us_vs_asym_bcm_an_ds_mul.png

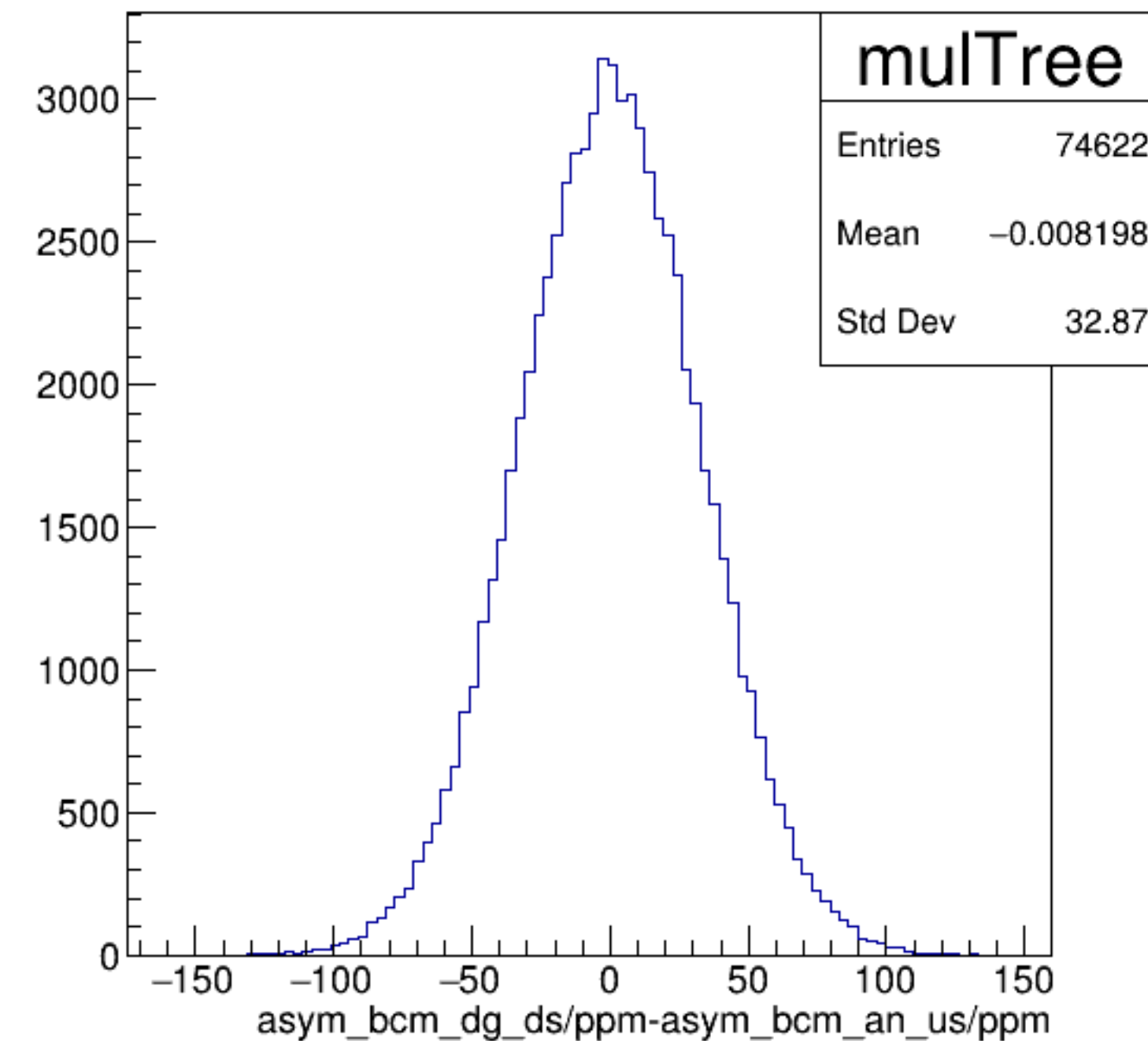
asym_bcm_dg_ds/ppm:asym_bcm_an_us/ppm {ErrorFlag==0}



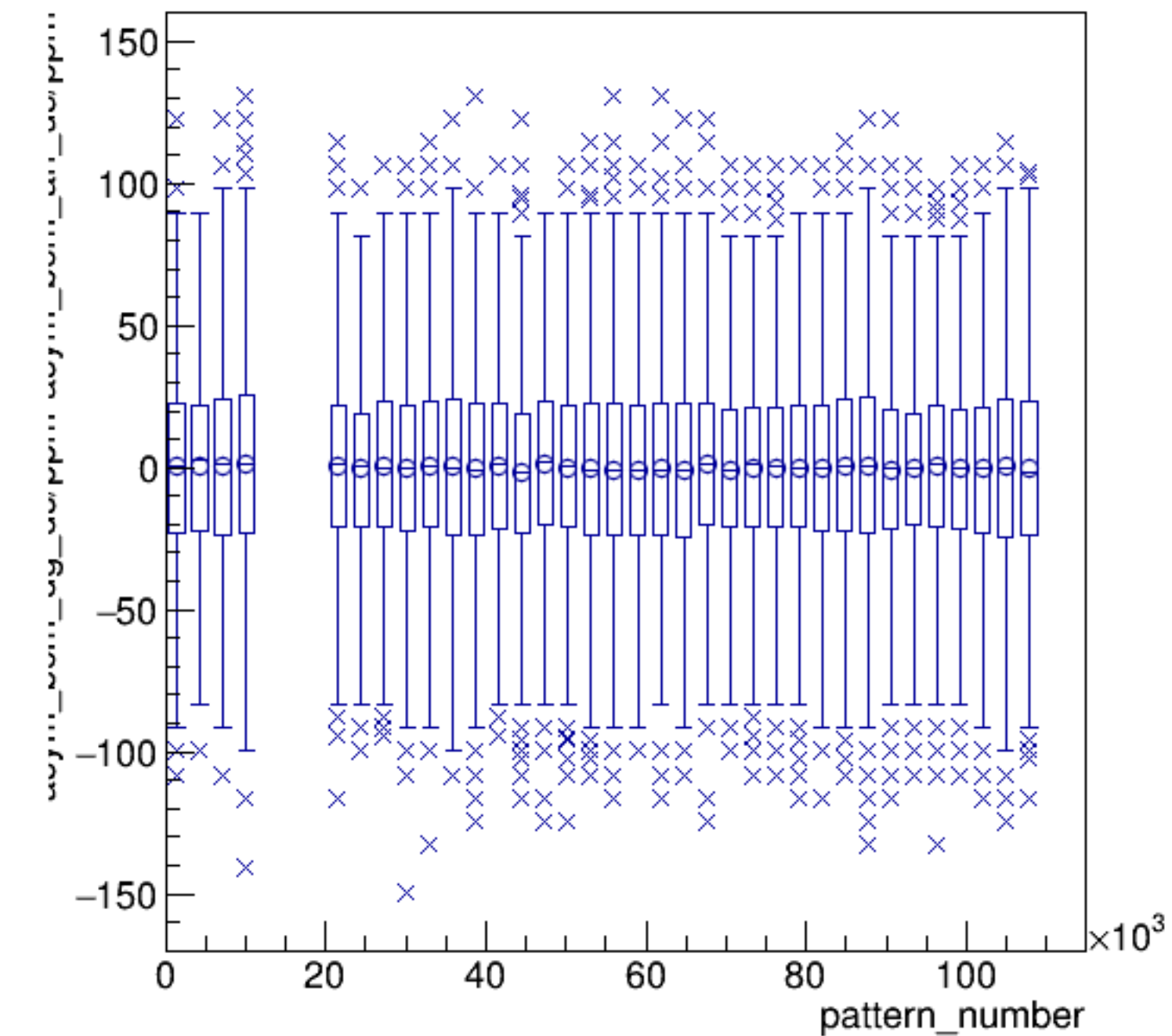
asym_bcm_dg_ds/ppm:asym_bcm_an_us/ppm {ErrorFlag==0}

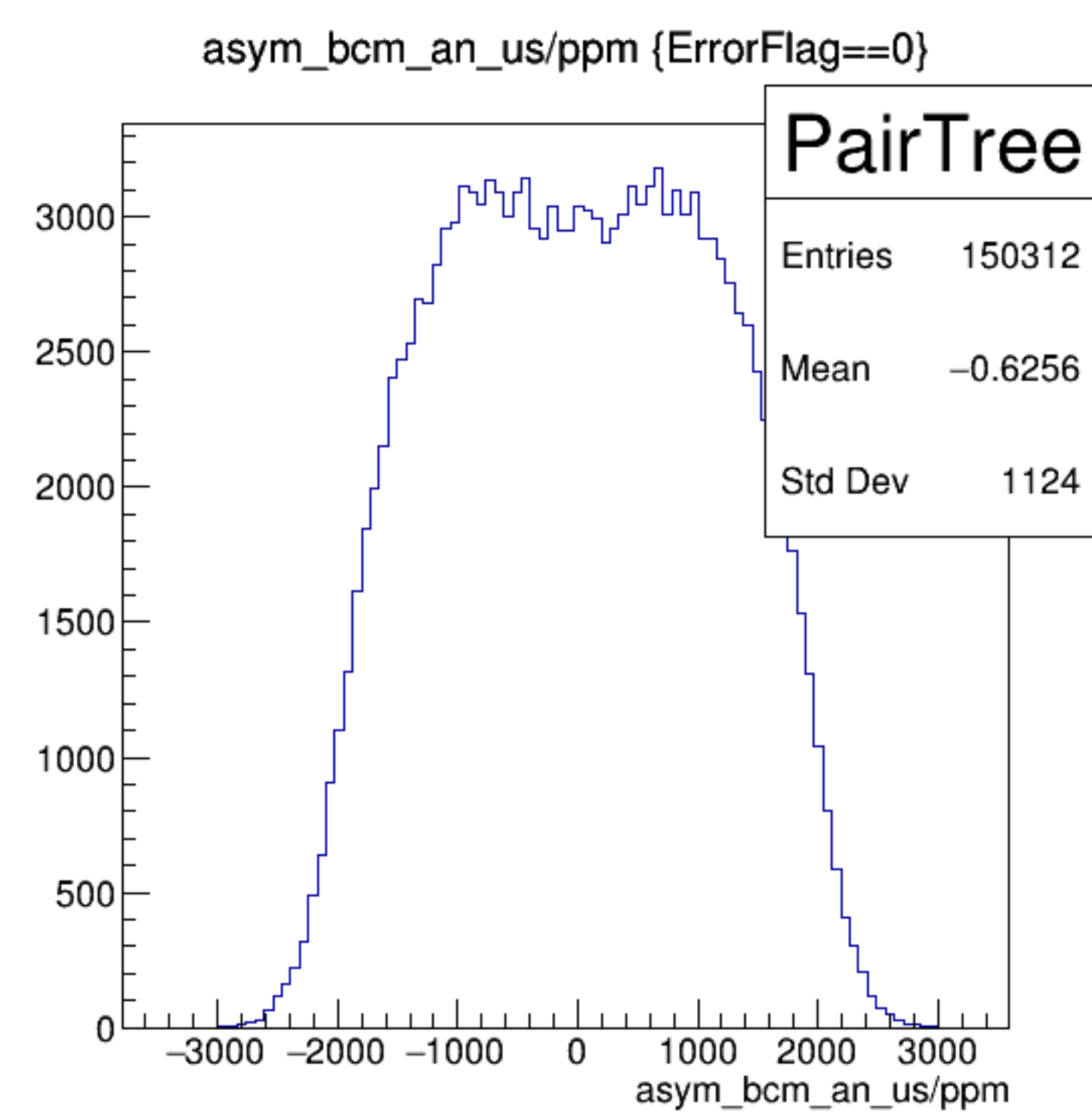
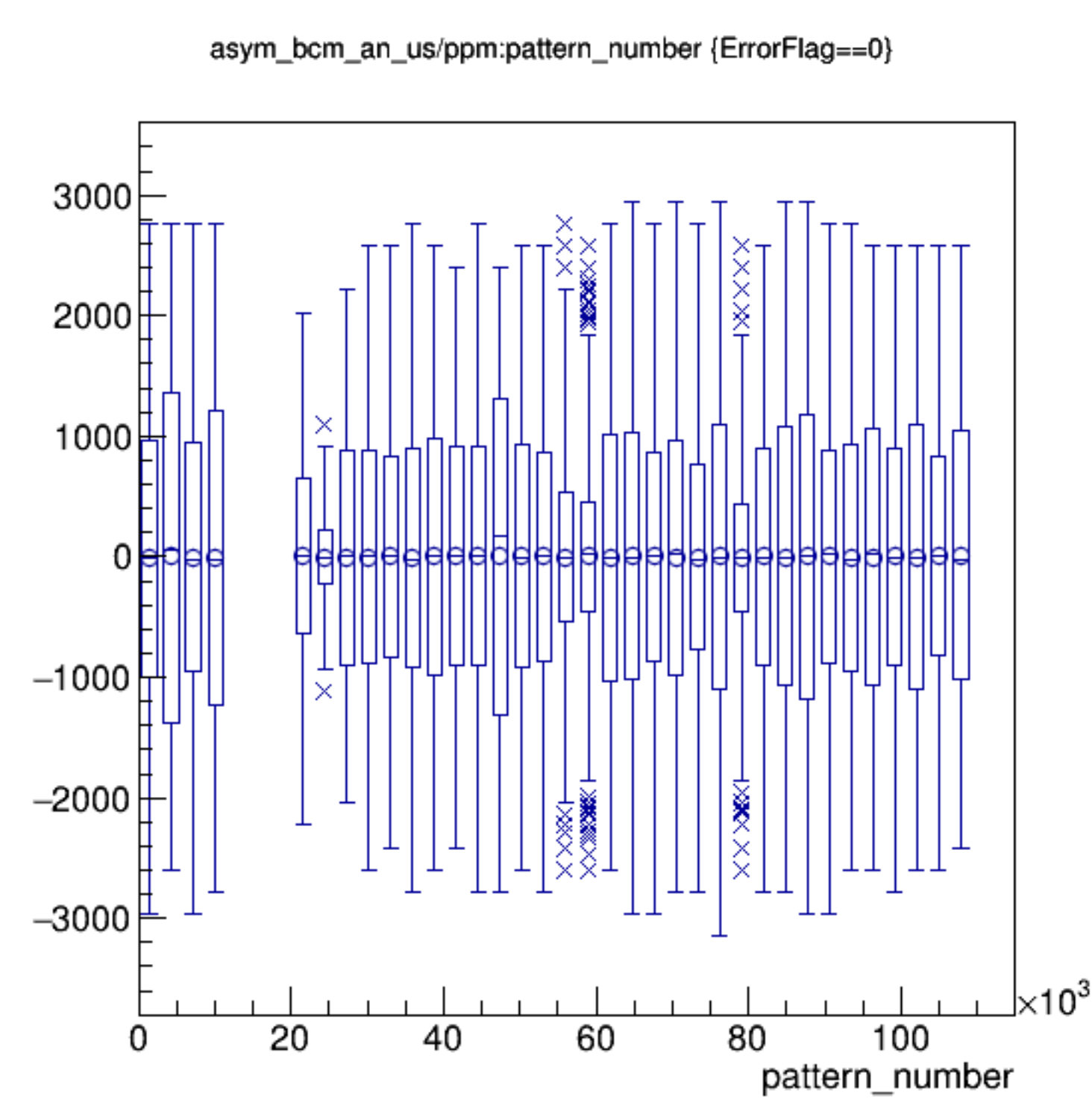
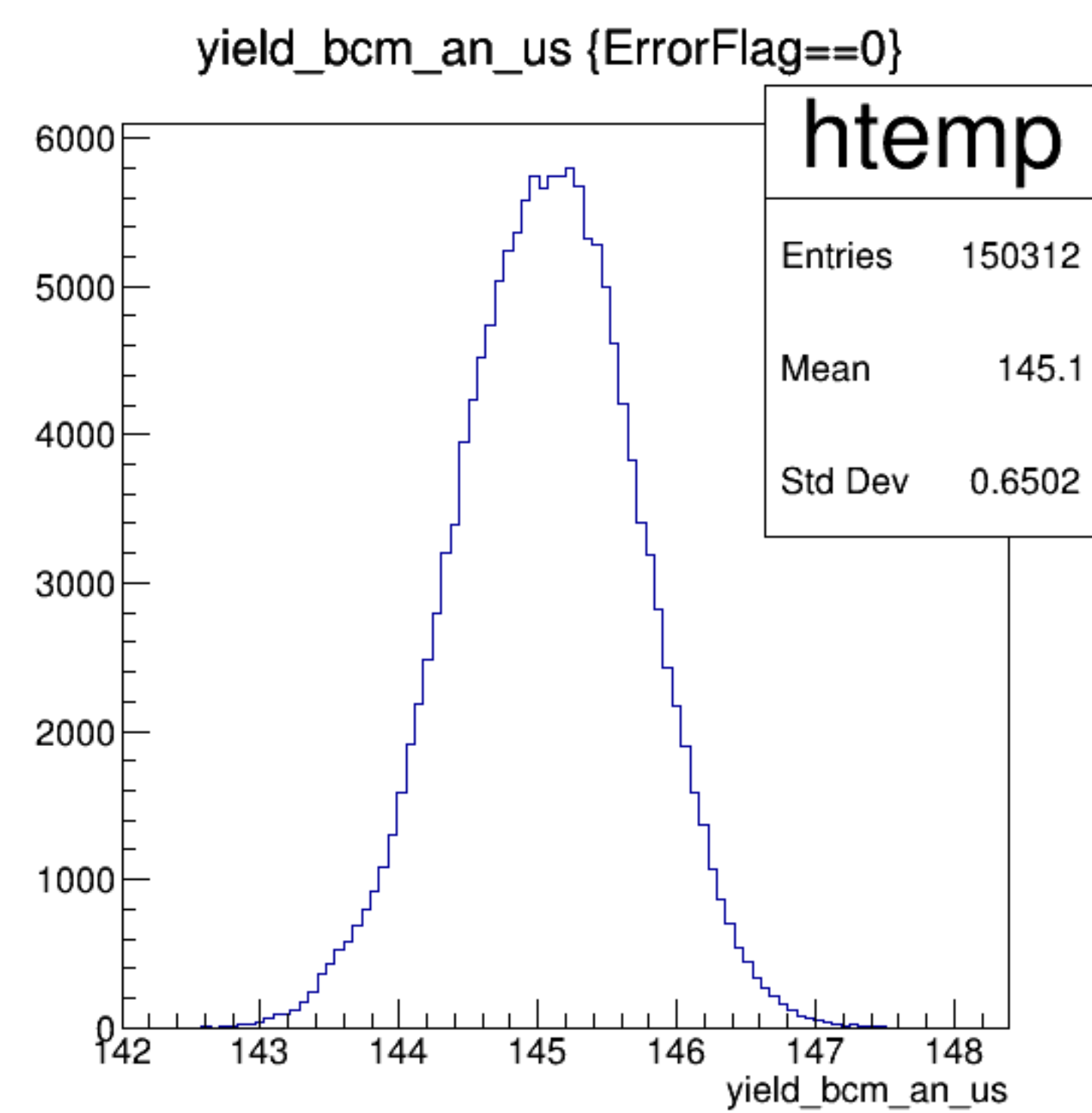
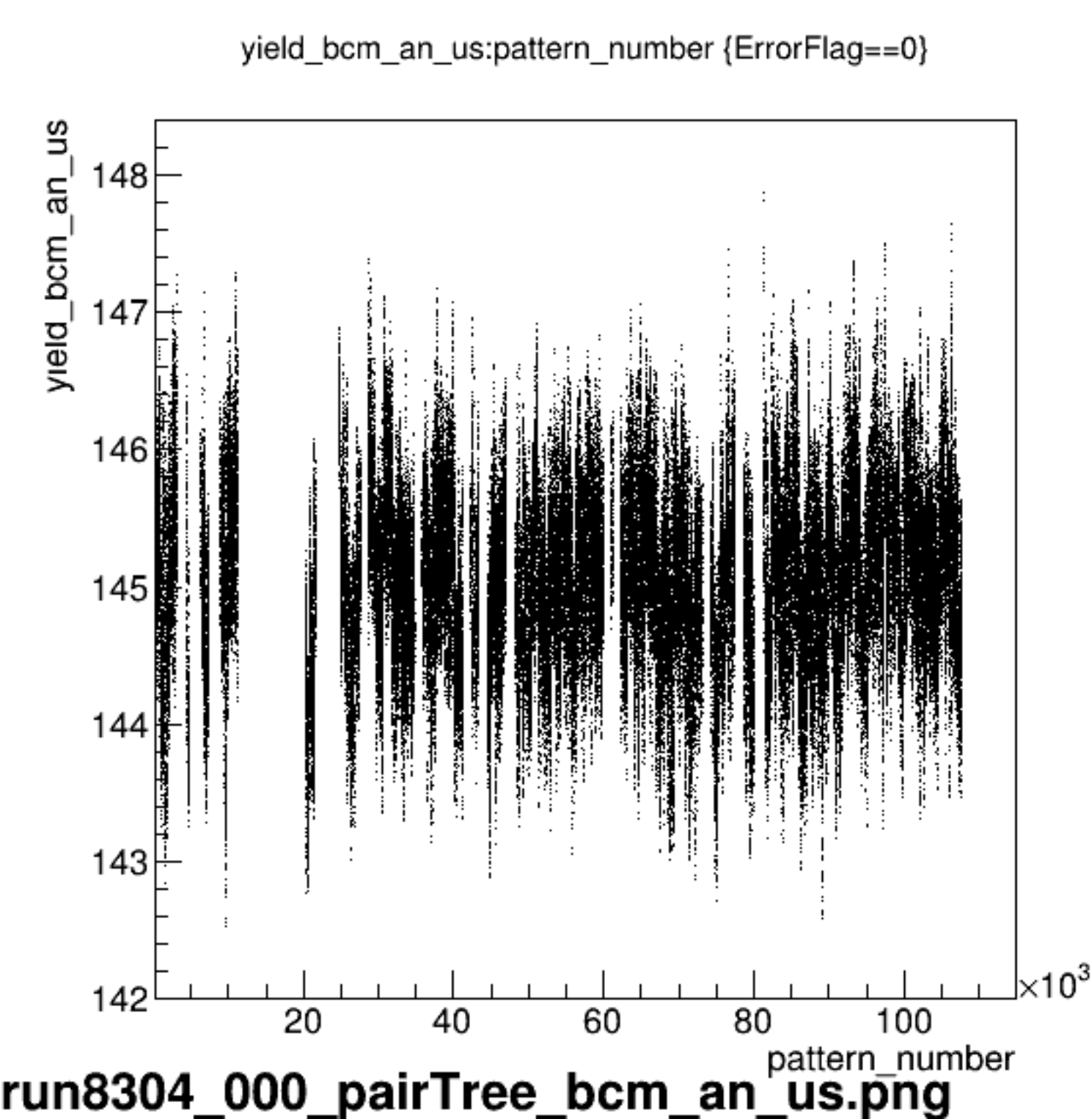


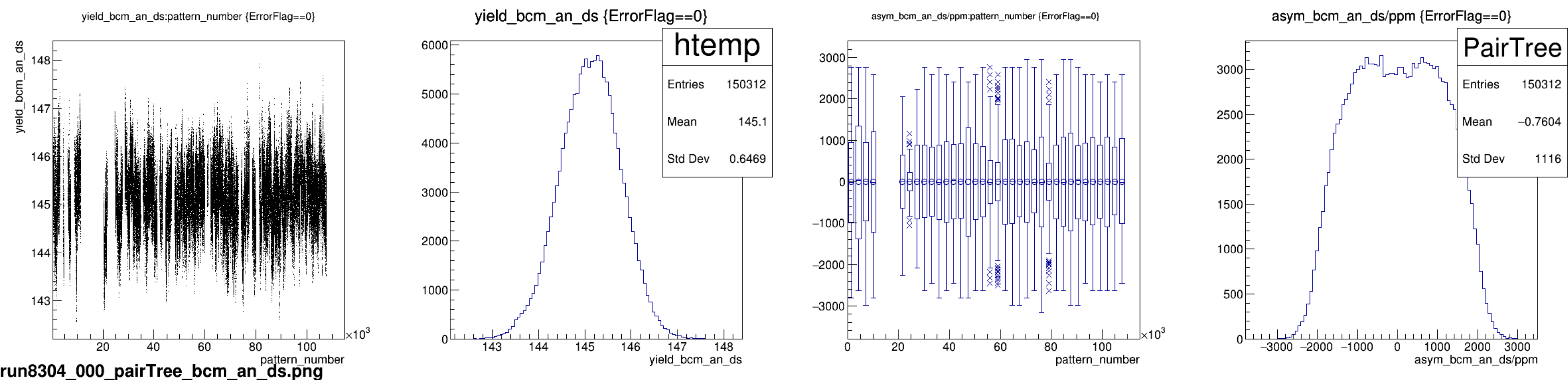
asym_bcm_dg_ds/ppm-asym_bcm_an_us/ppm {ErrorFlag==0}

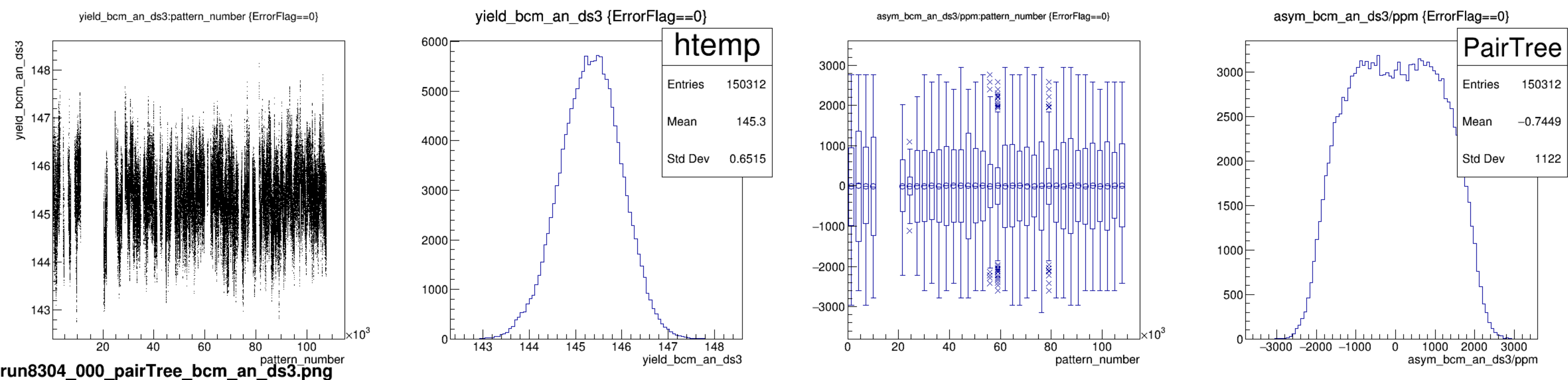


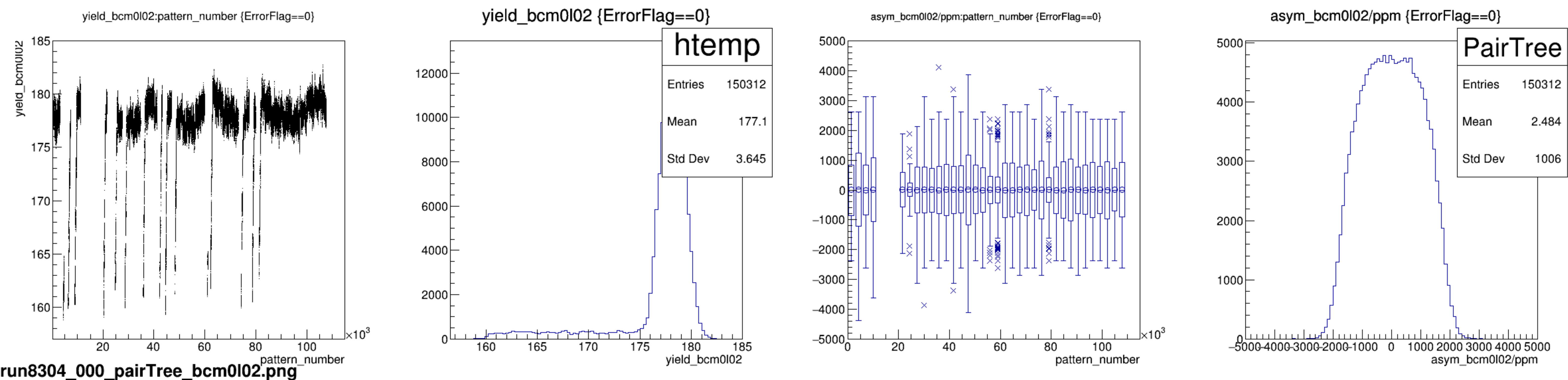
asym_bcm_dg_ds/ppm-asym_bcm_an_us/ppm:pattern_number {ErrorFlag==0}

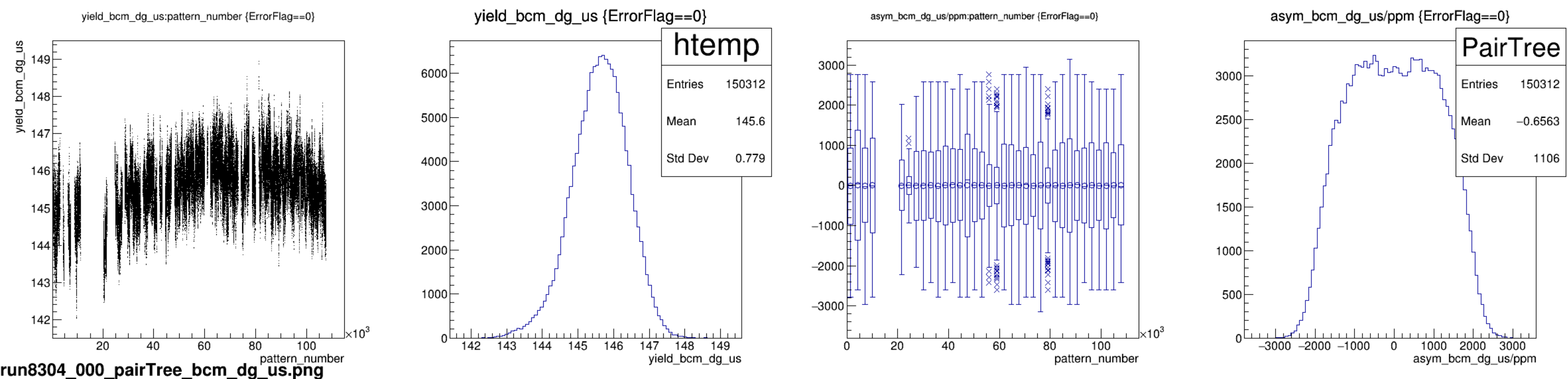




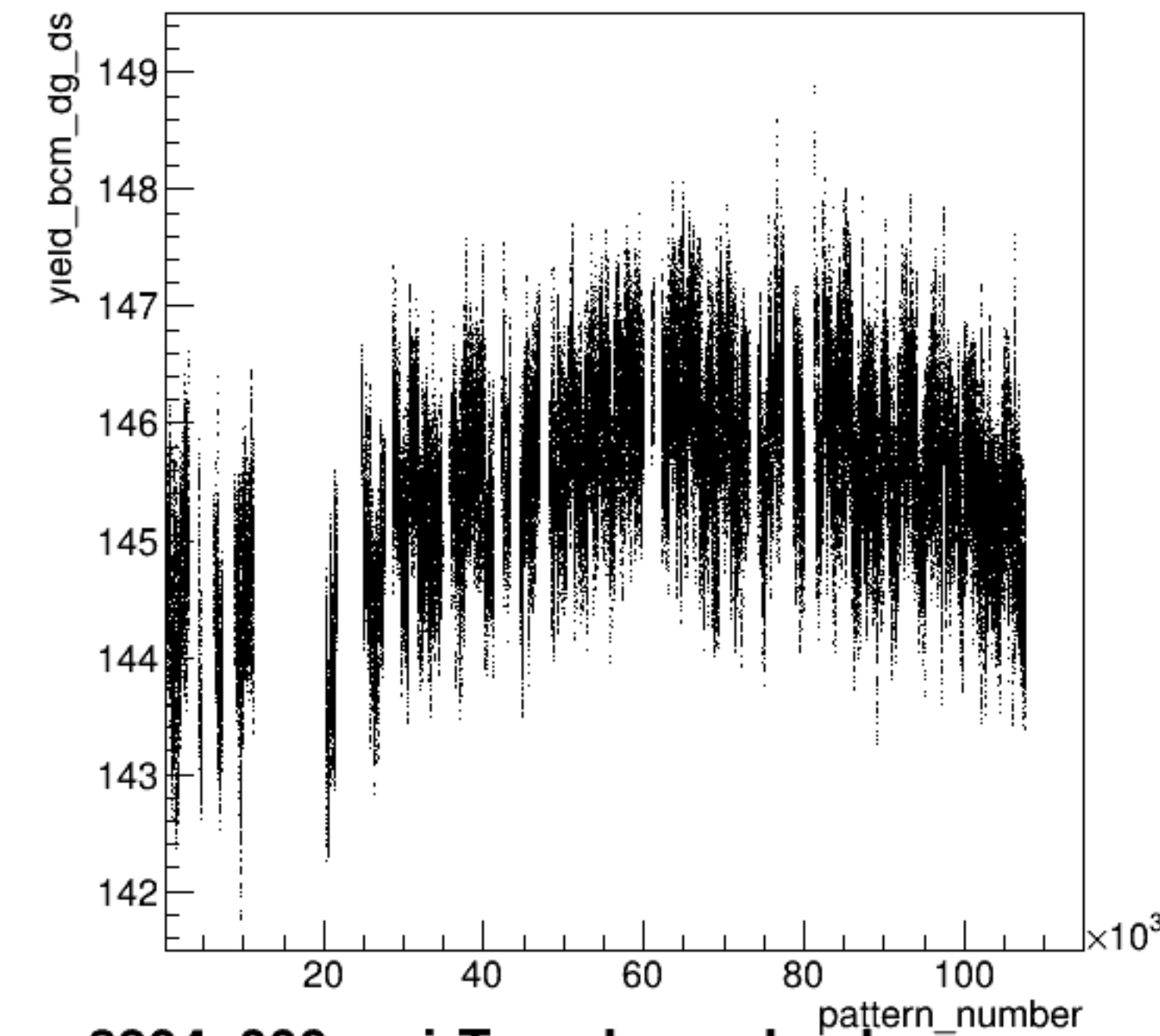






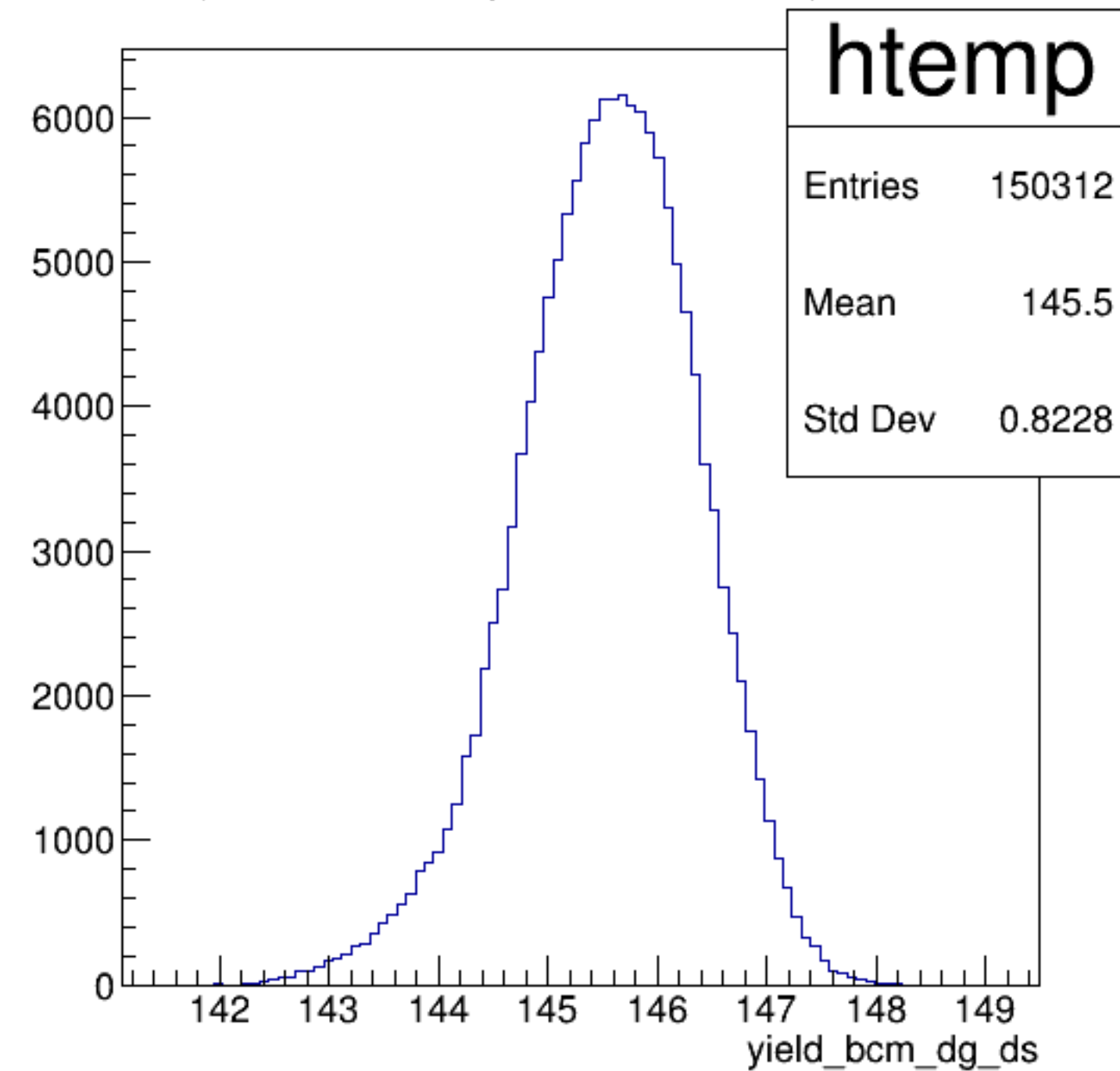


yield_bcm_dg_ds:pattern_number {ErrorFlag==0}

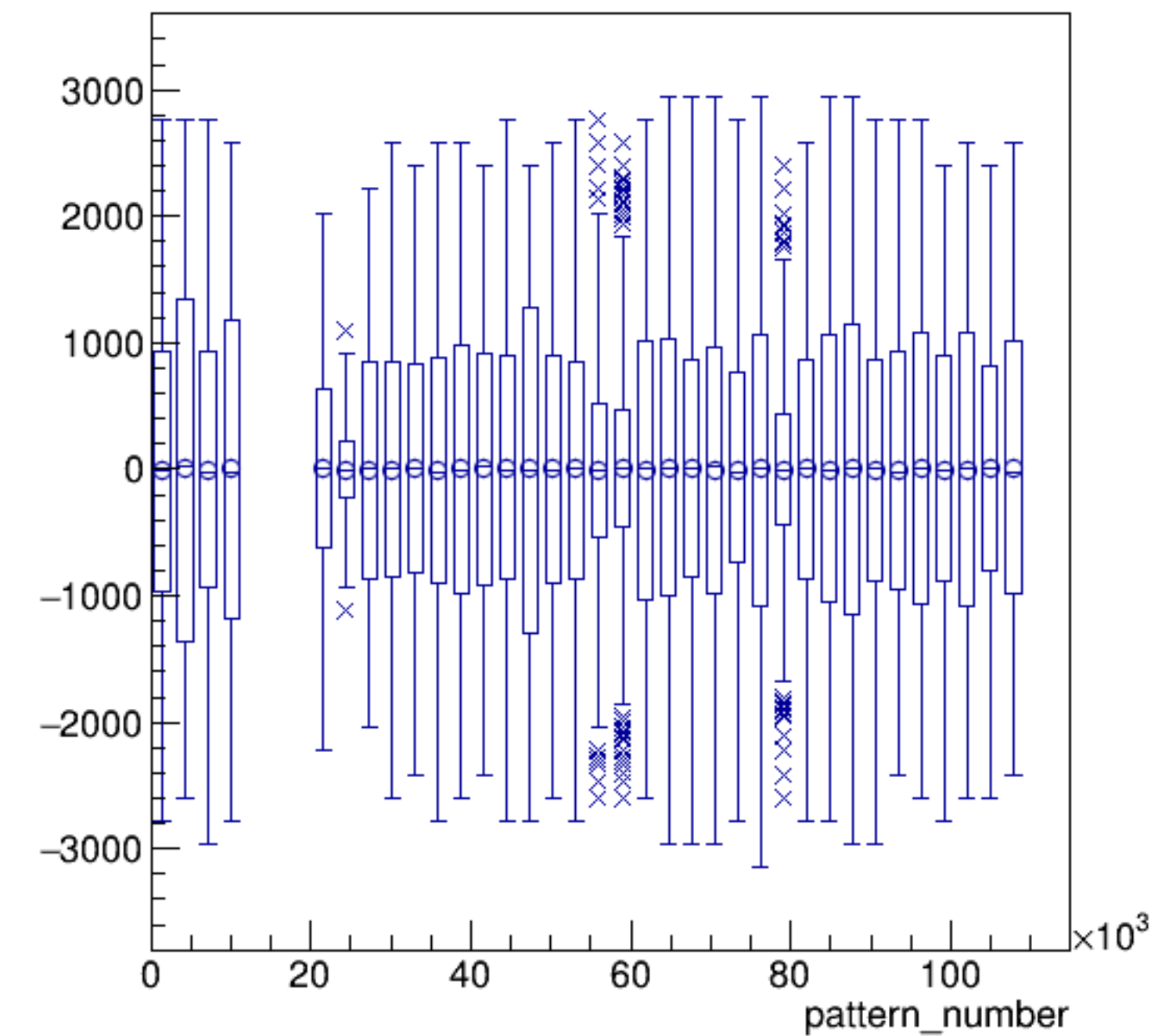


run8304_000_pairTree_bcm_dg_ds.png

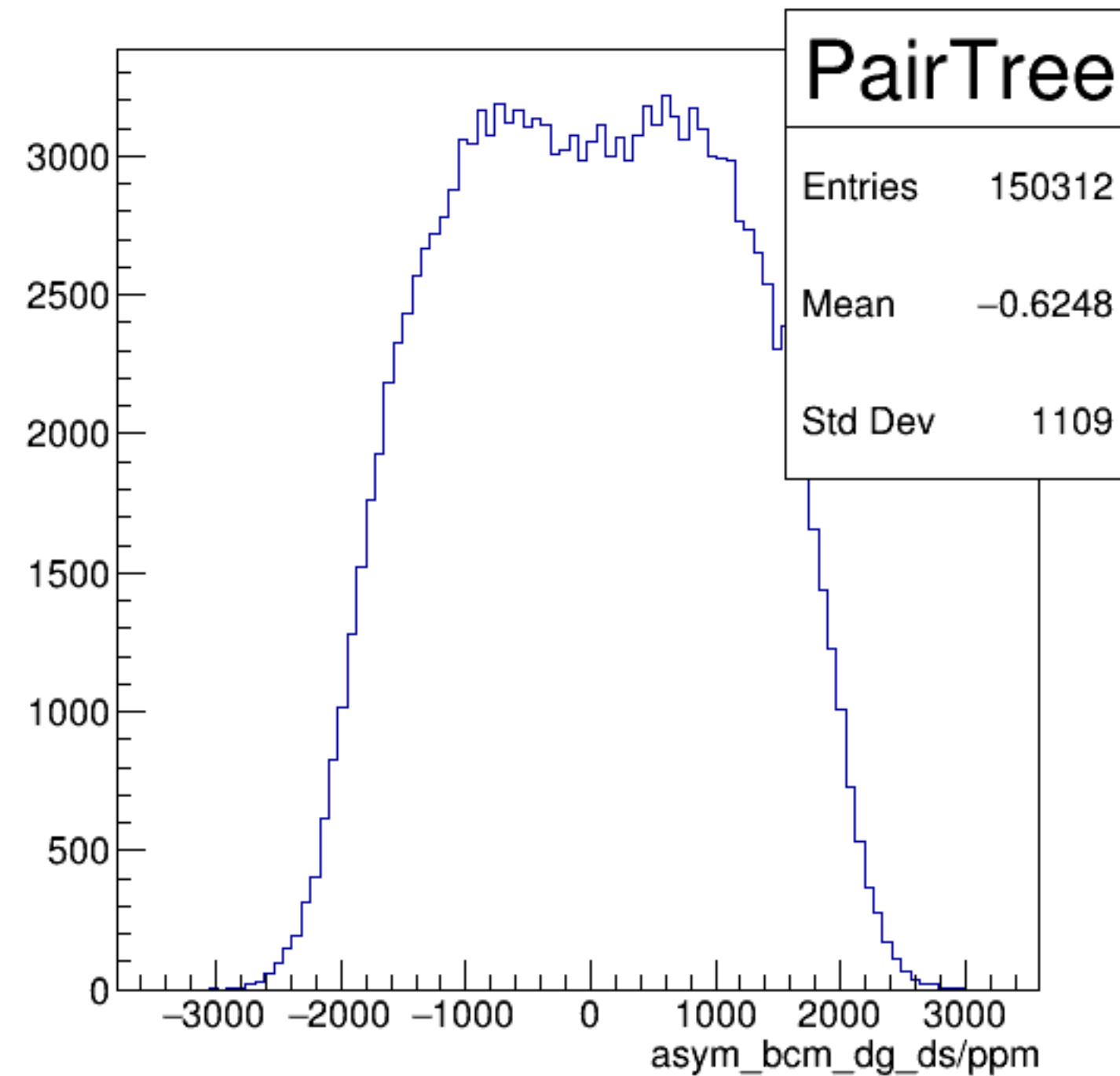
yield_bcm_dg_ds {ErrorFlag==0}



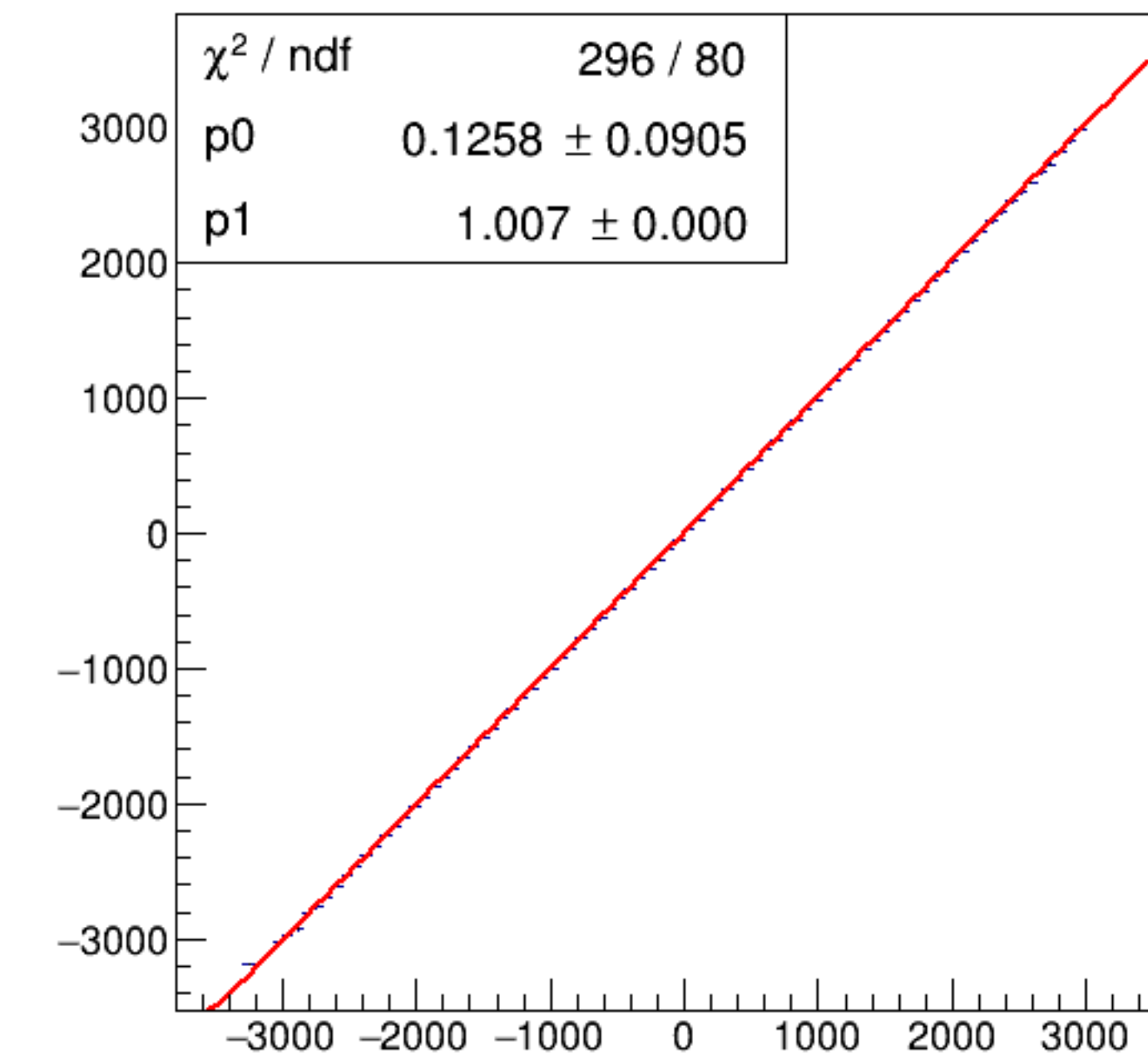
asym_bcm_dg_ds/ppm:pattern_number {ErrorFlag==0}



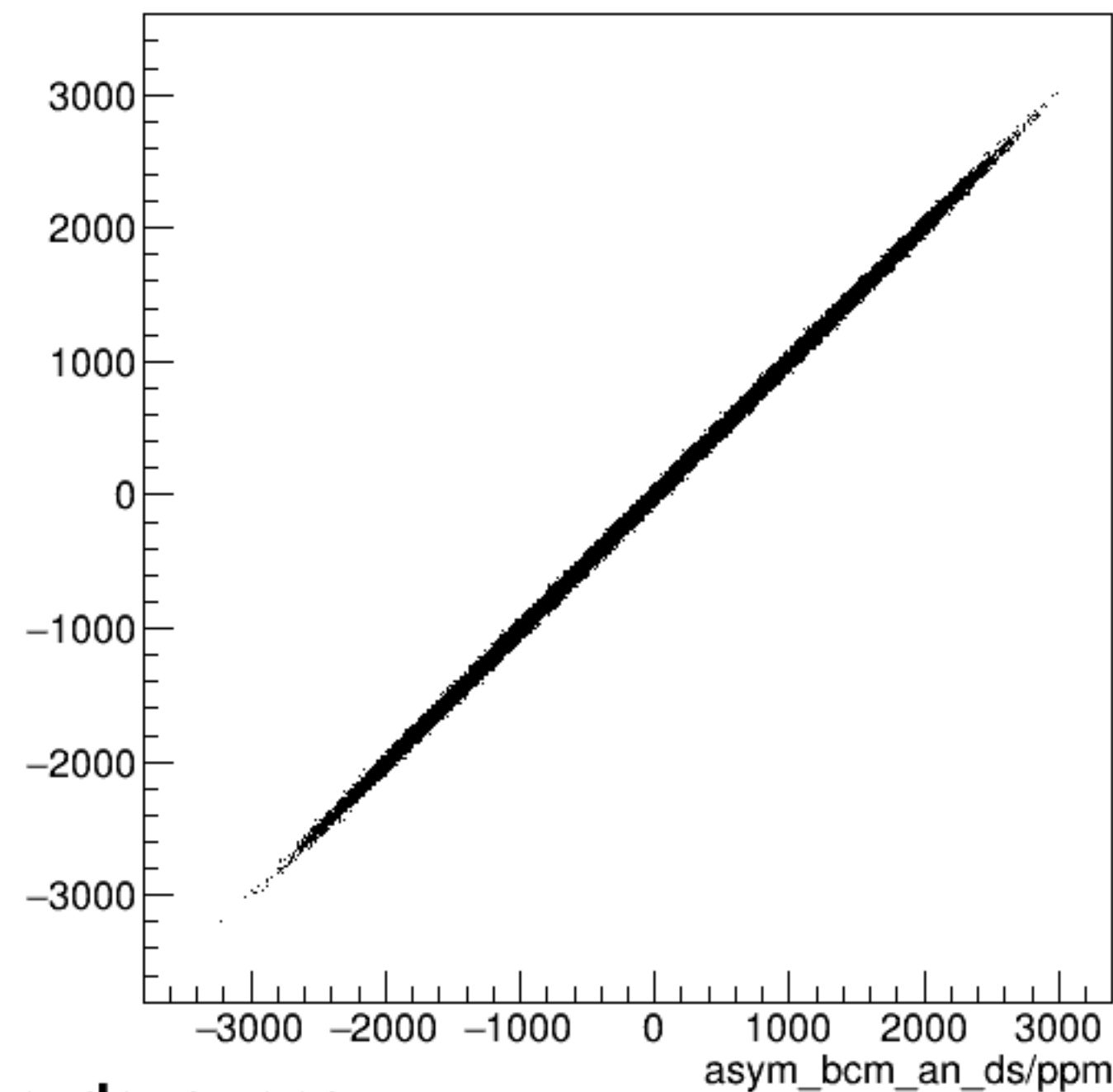
asym_bcm_dg_ds/ppm {ErrorFlag==0}



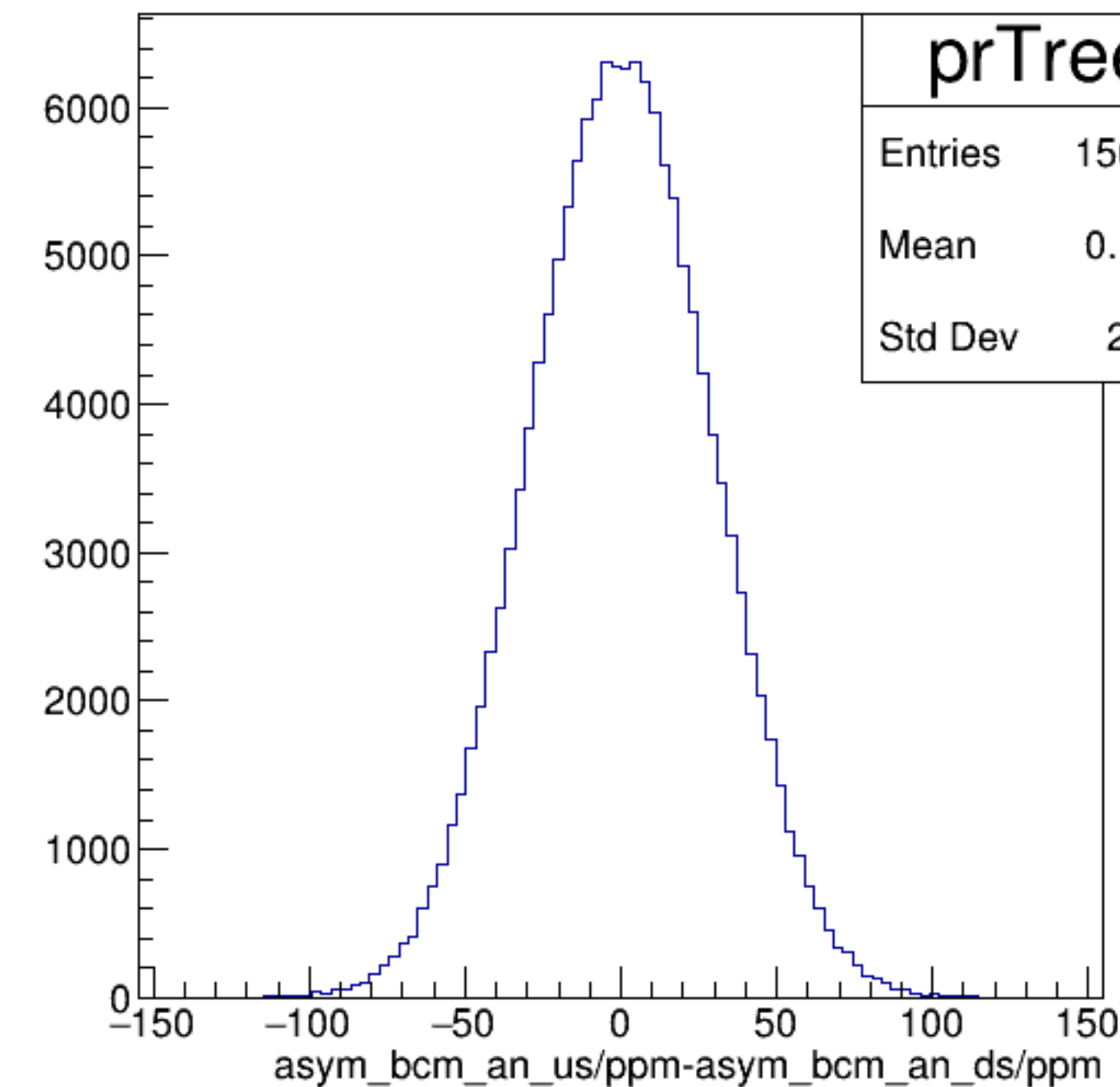
asym_bcm_an_us/ppm:asym_bcm_an_ds/ppm {ErrorFlag==0}



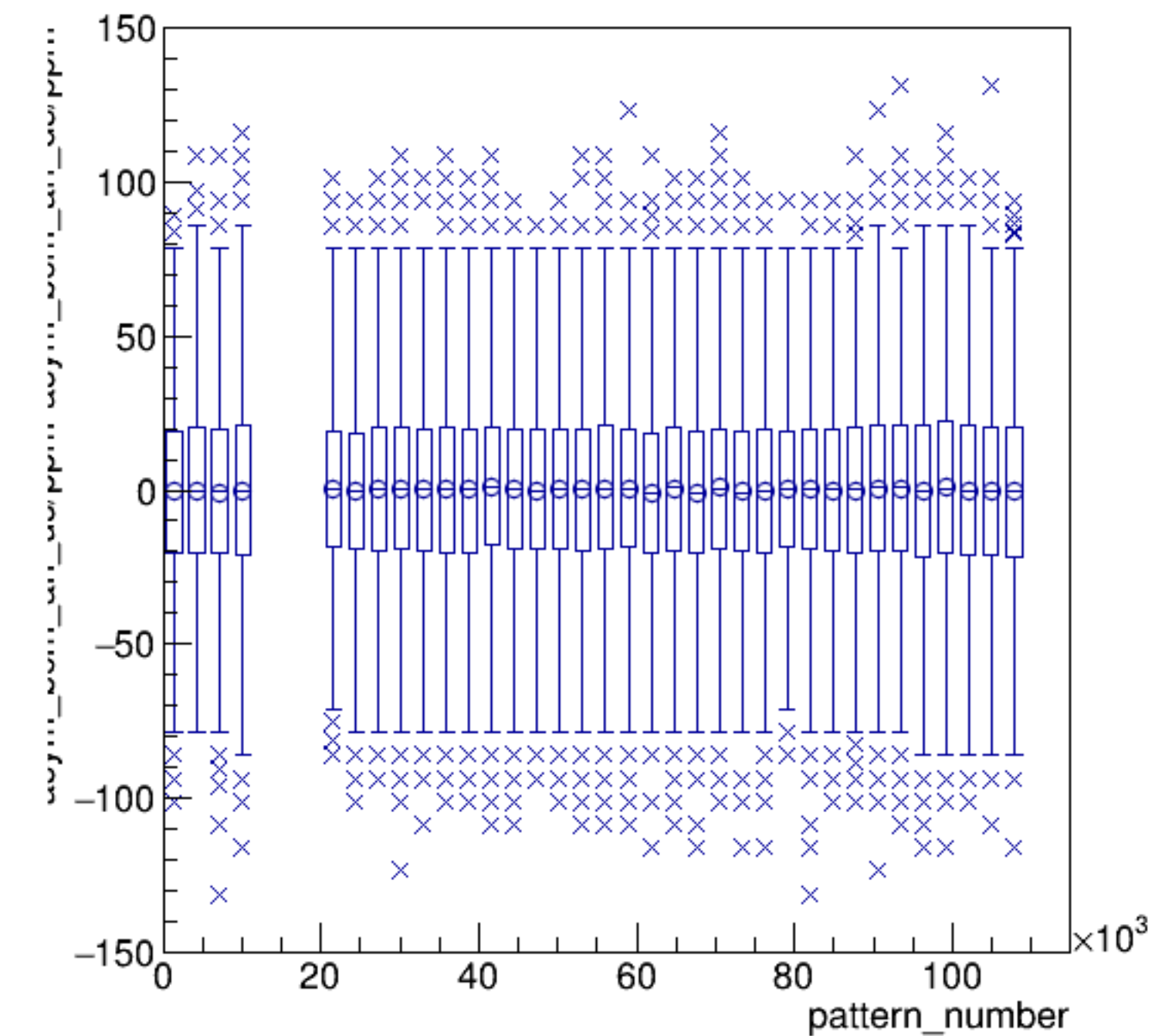
asym_bcm_an_us/ppm:asym_bcm_an_ds/ppm {ErrorFlag==0}



asym_bcm_an_us/ppm-asym_bcm_an_ds/ppm {ErrorFlag==0}

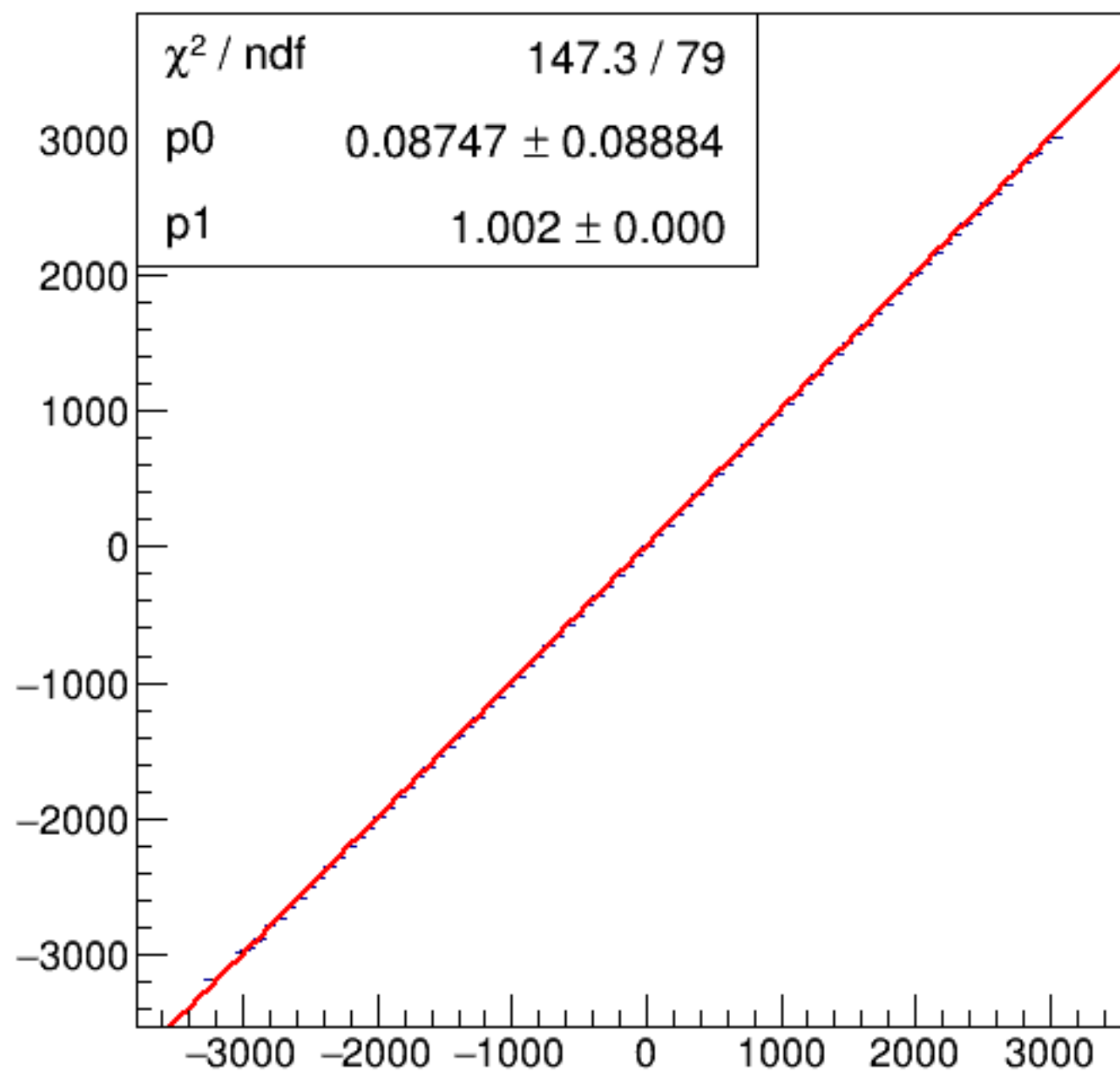


asym_bcm_an_us/ppm-asym_bcm_an_ds/ppm:pattern_number {ErrorFlag==0}

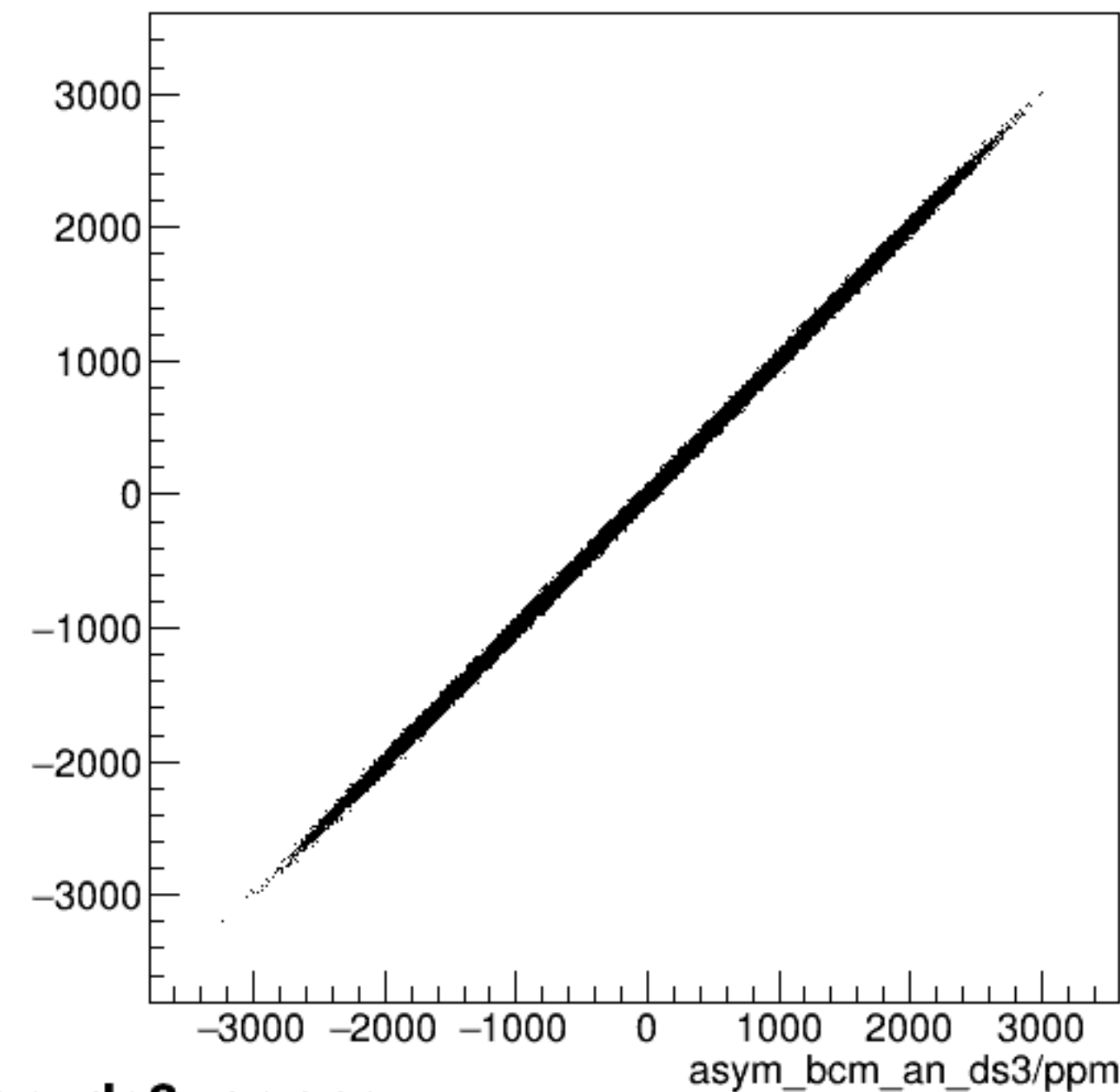


run8304_000_asym_bcm_an_us_vs_asym_bcm_an_ds_pr.png

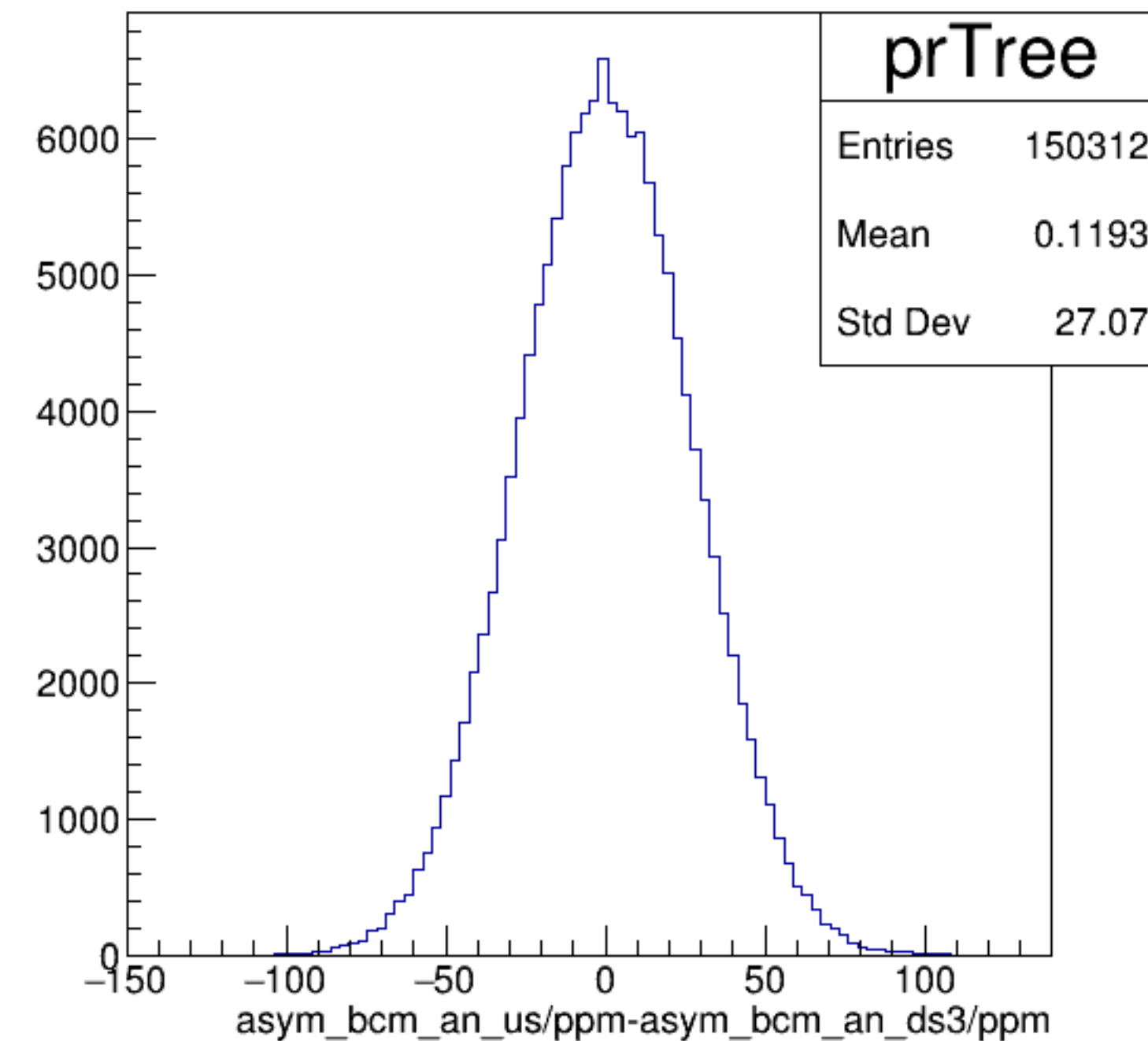
asym_bcm_an_us/ppm:asym_bcm_an_ds3/ppm {ErrorFlag==0}



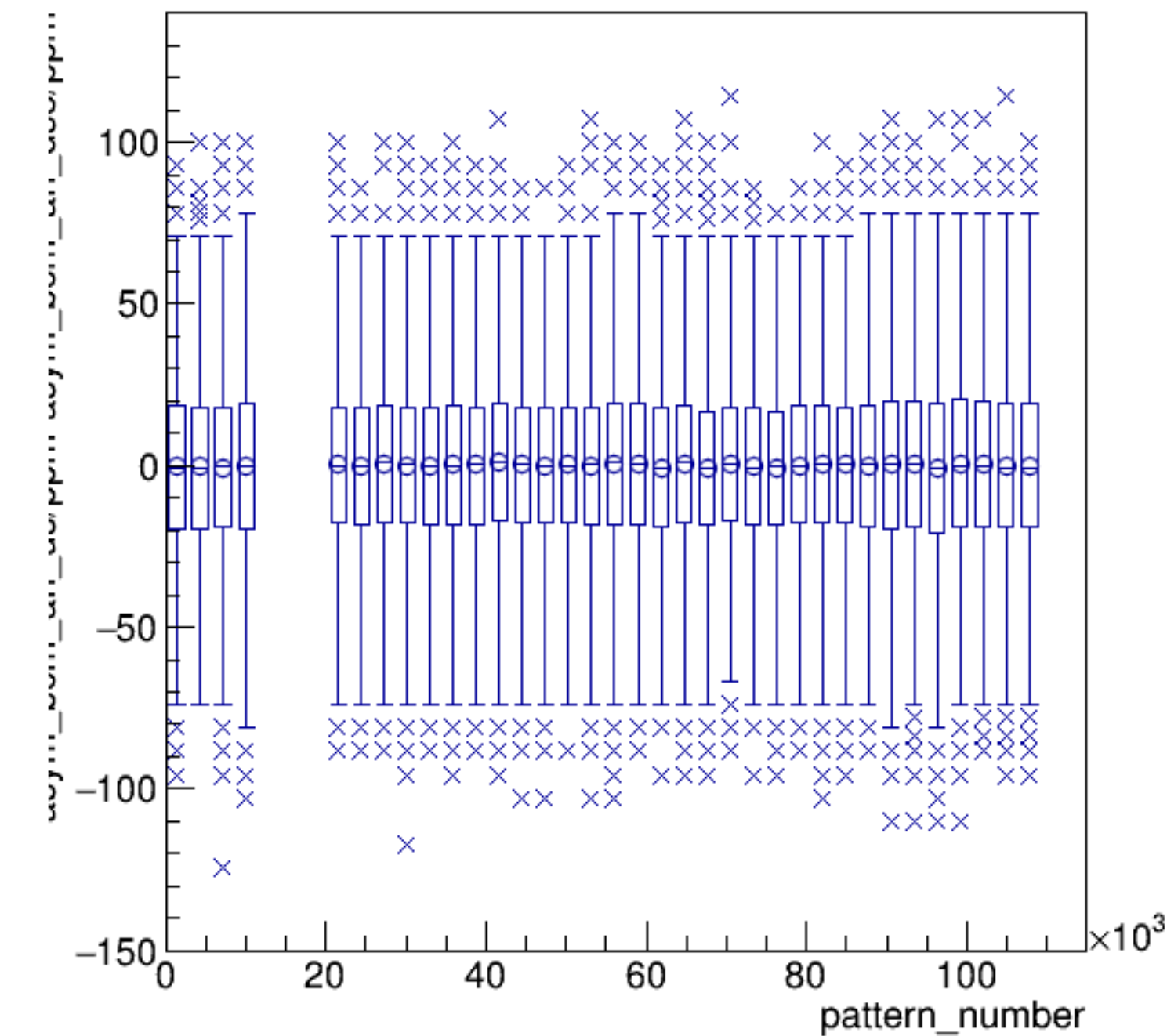
asym_bcm_an_us/ppm:asym_bcm_an_ds3/ppm {ErrorFlag==0}



asym_bcm_an_us/ppm-asym_bcm_an_ds3/ppm {ErrorFlag==0}

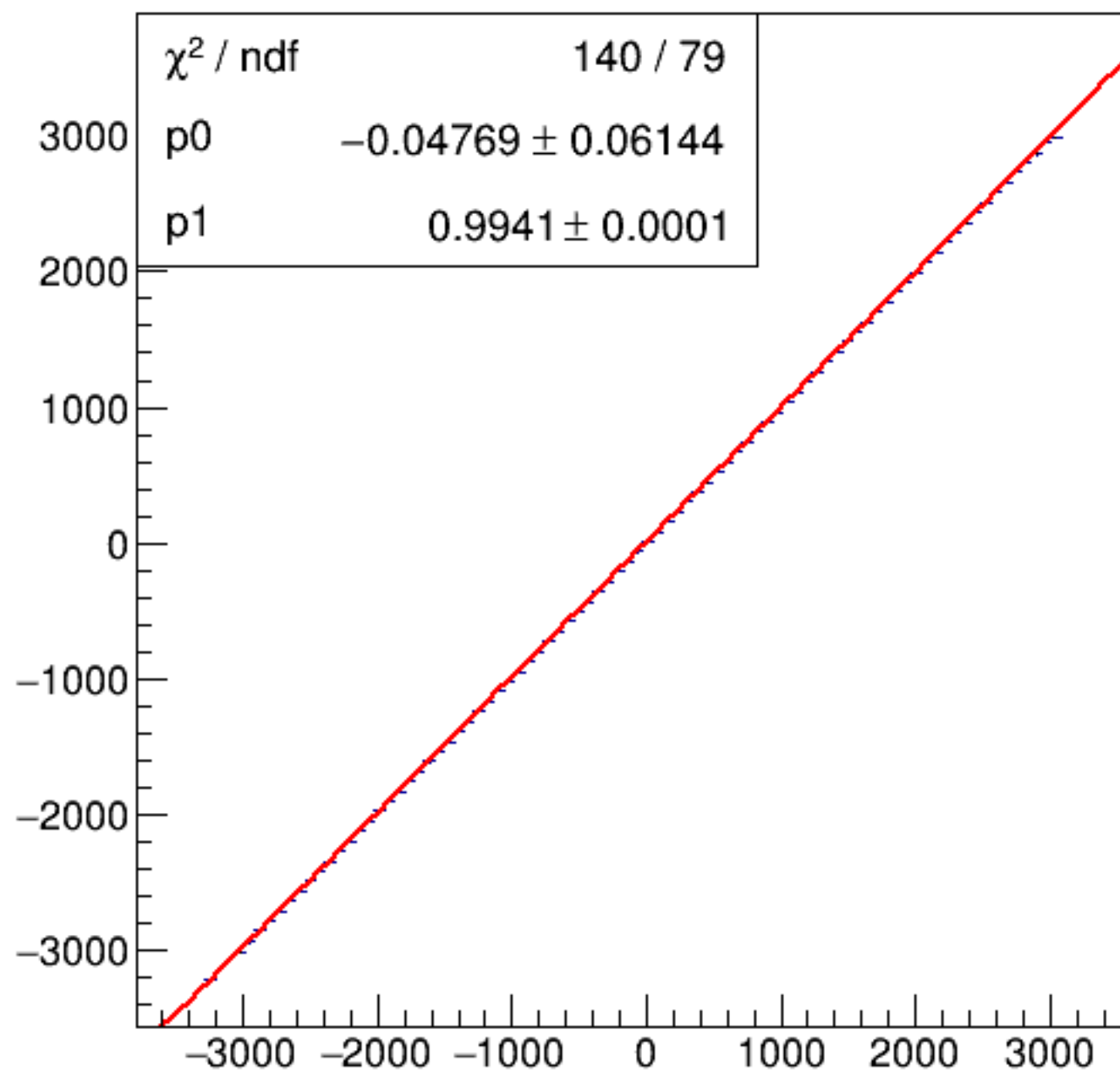


asym_bcm_an_us/ppm-asym_bcm_an_ds3/ppm:pattern_number {ErrorFlag==0}

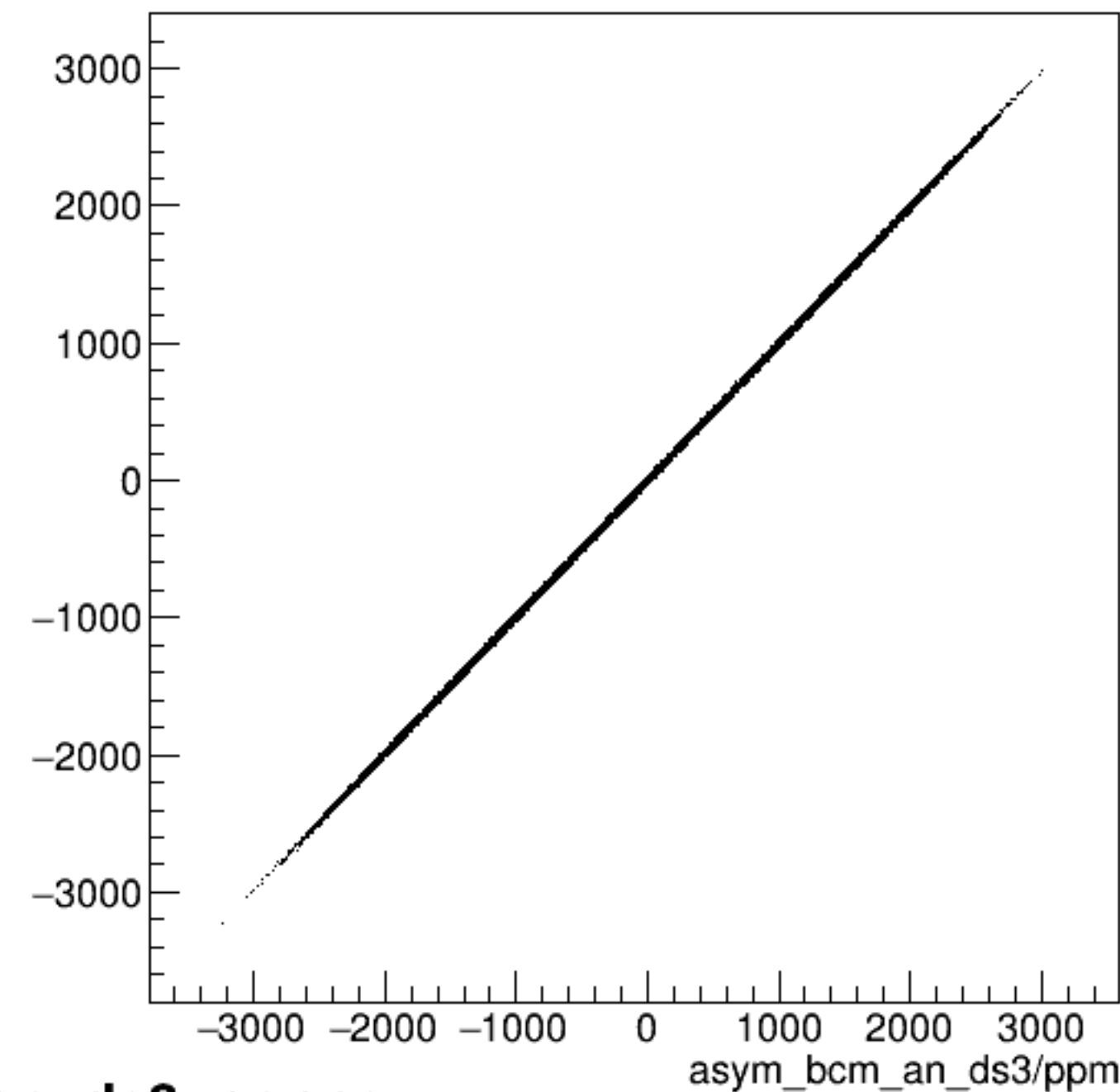


run8304_000_asym_bcm_an_us_vs_asym_bcm_an_ds3_pr.png

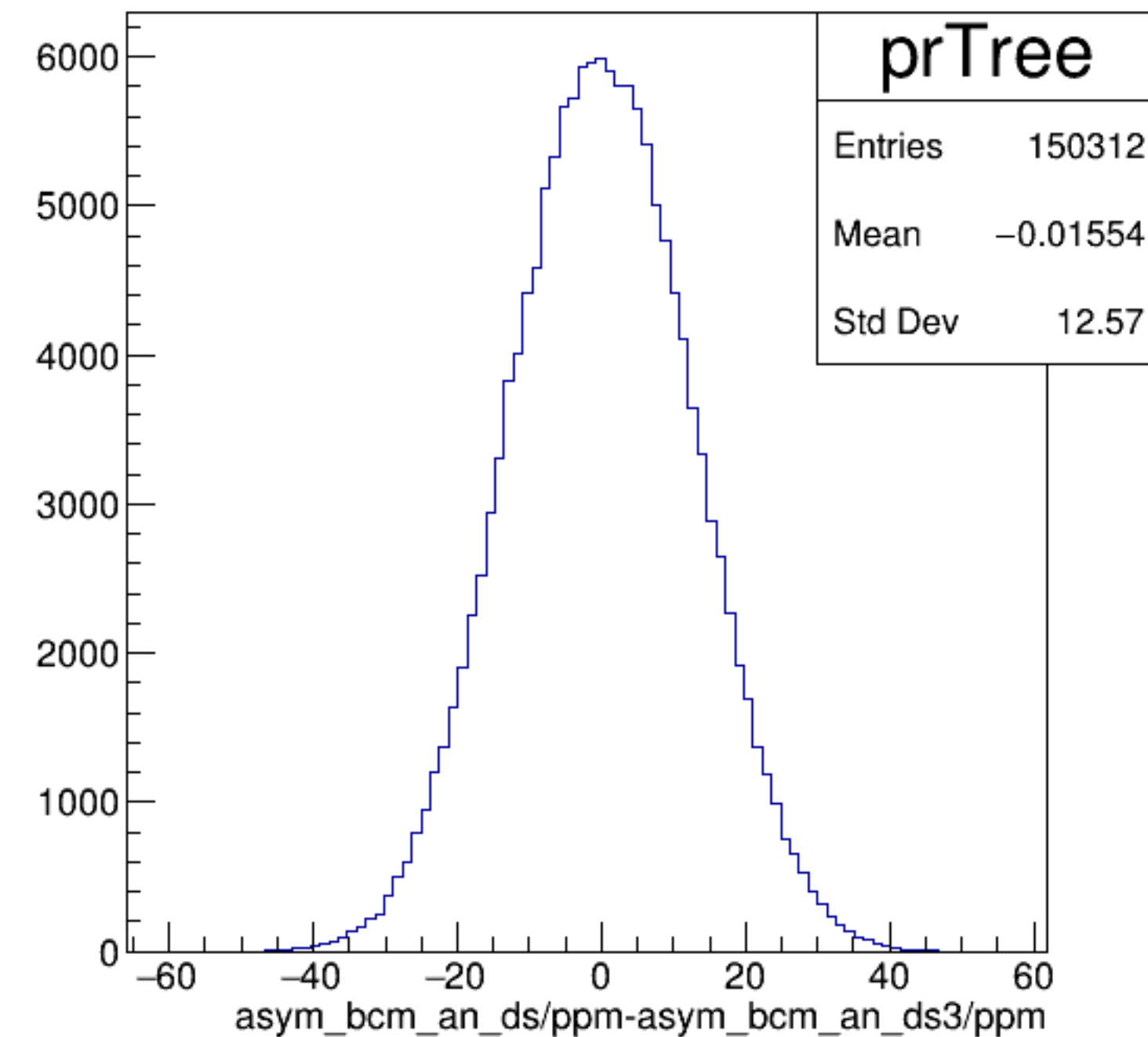
asym_bcm_an_ds/ppm:asym_bcm_an_ds3/ppm {ErrorFlag==0}



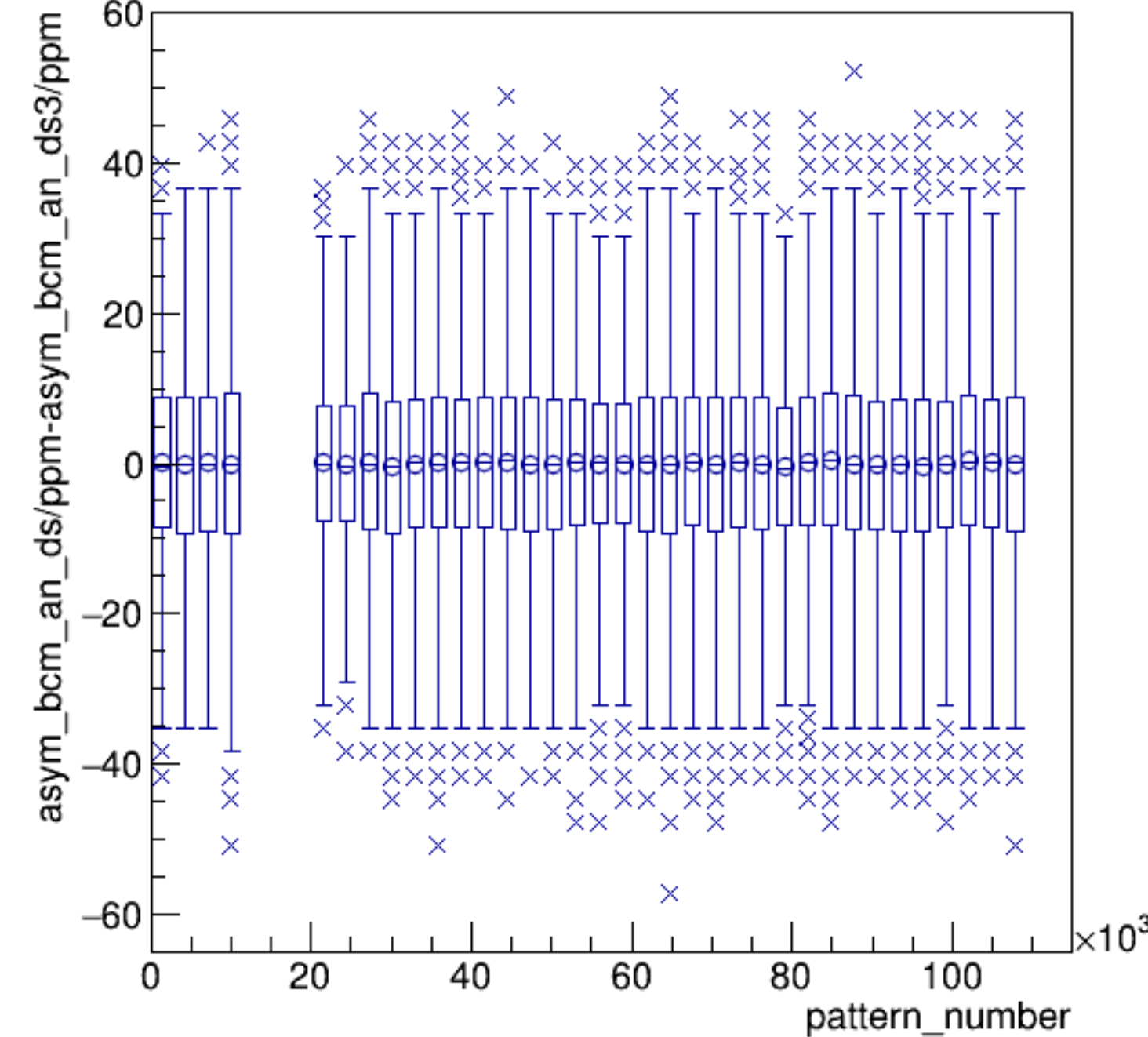
asym_bcm_an_ds/ppm:asym_bcm_an_ds3/ppm {ErrorFlag==0}



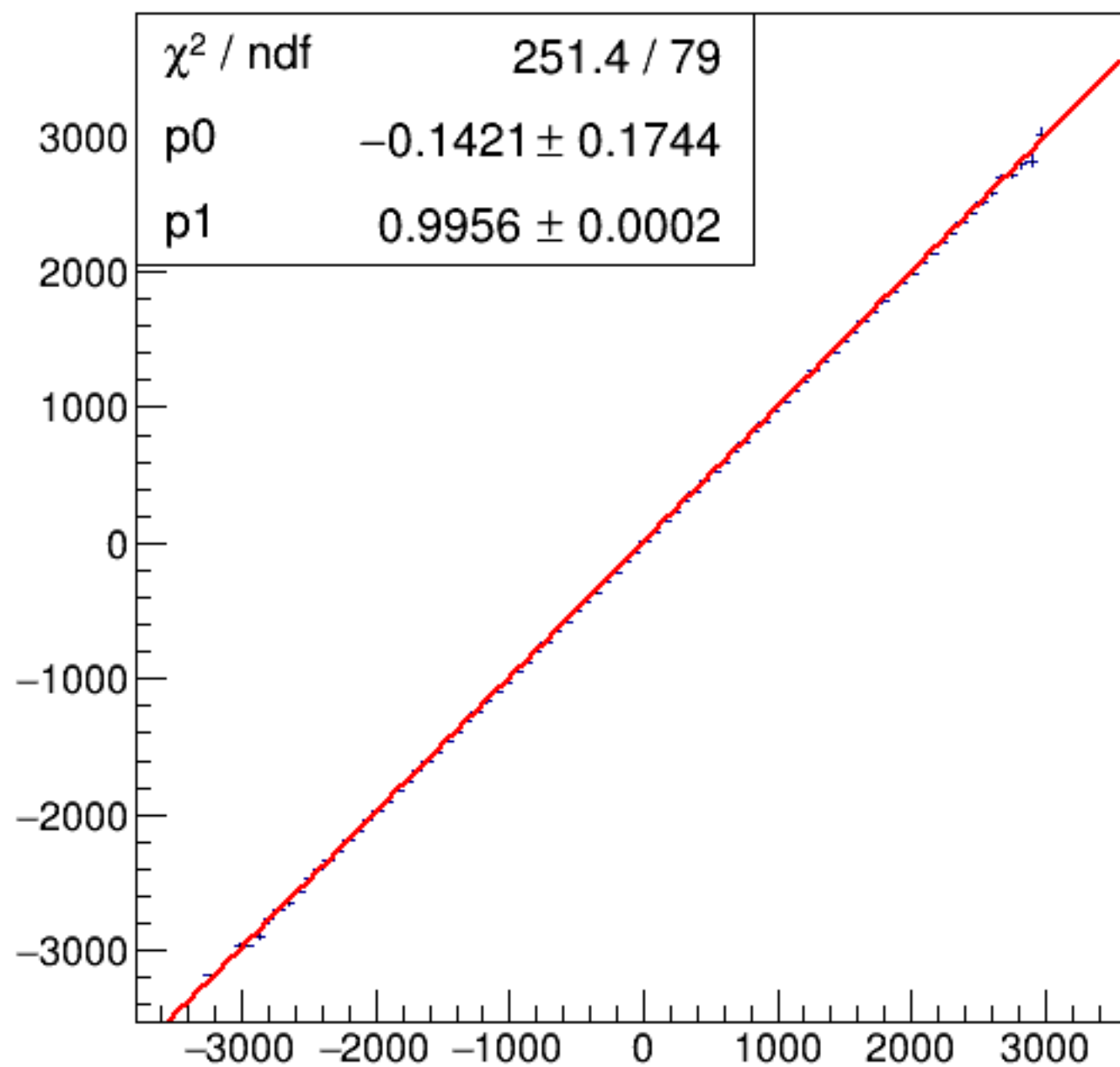
asym_bcm_an_ds/ppm-asym_bcm_an_ds3/ppm {ErrorFlag==0}



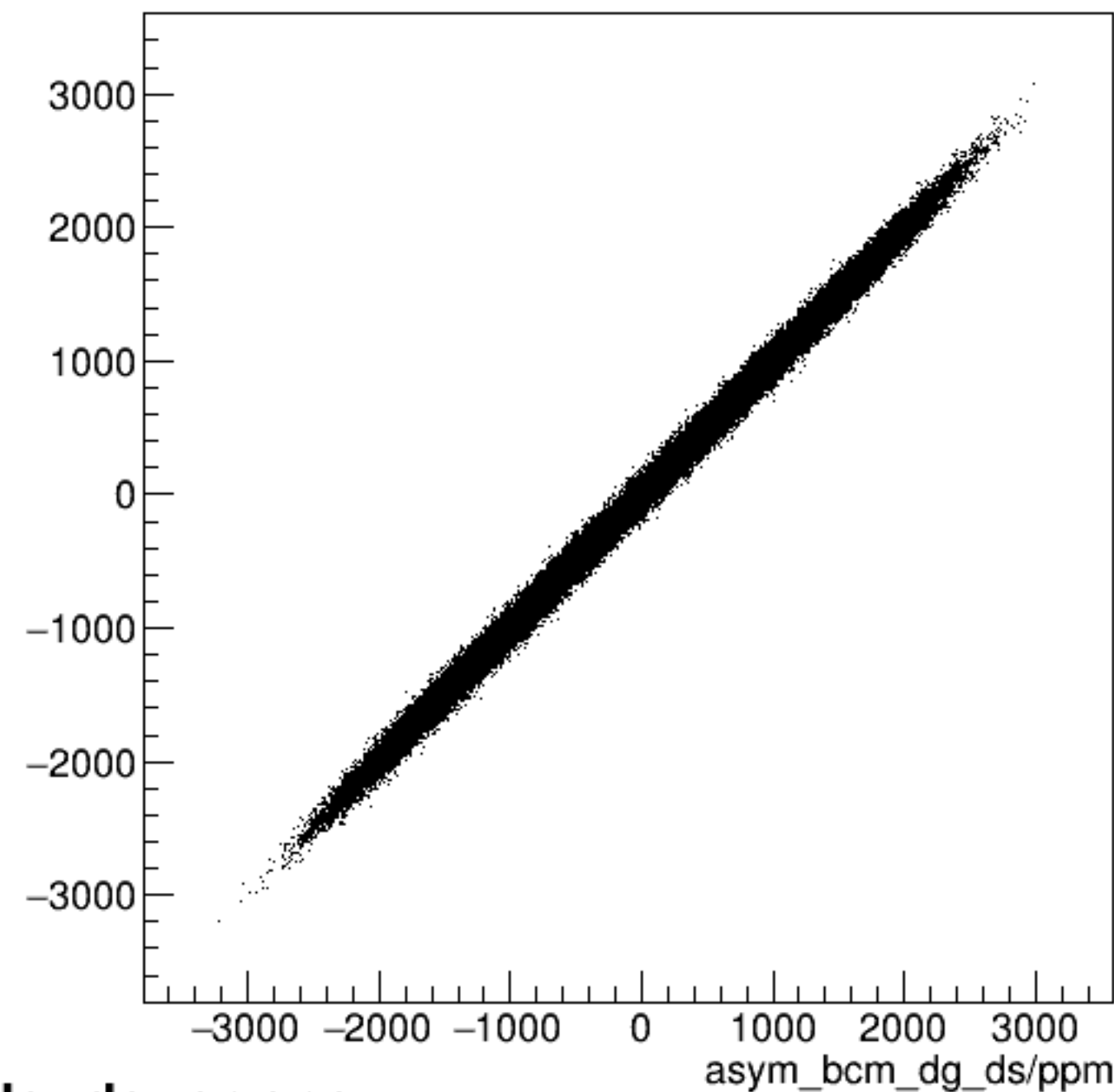
asym_bcm_an_ds/ppm-asym_bcm_an_ds3/ppm:pattern_number {ErrorFlag==0}



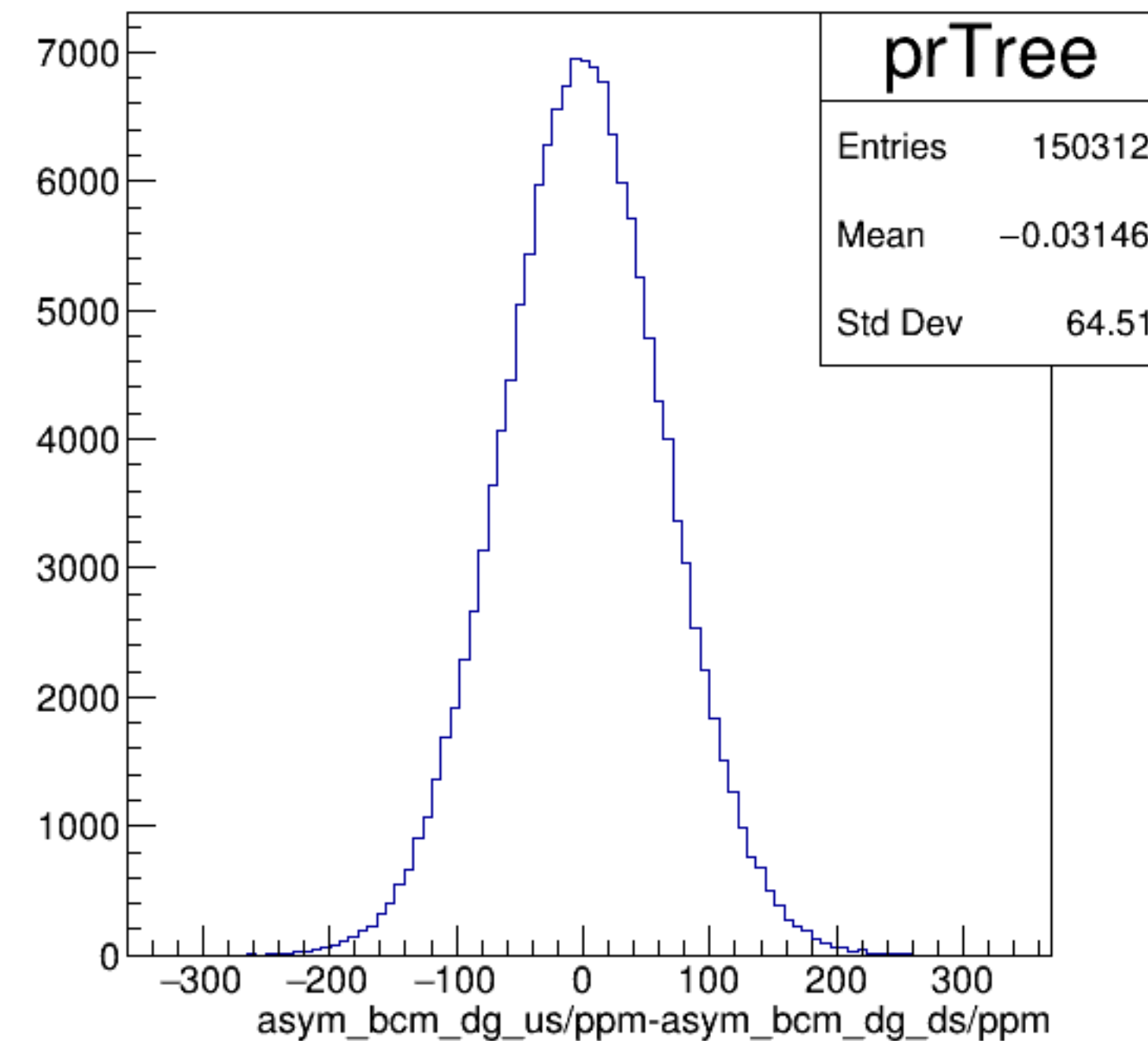
asym_bcm_dg_us/ppm:asym_bcm_dg_ds/ppm {ErrorFlag==0}



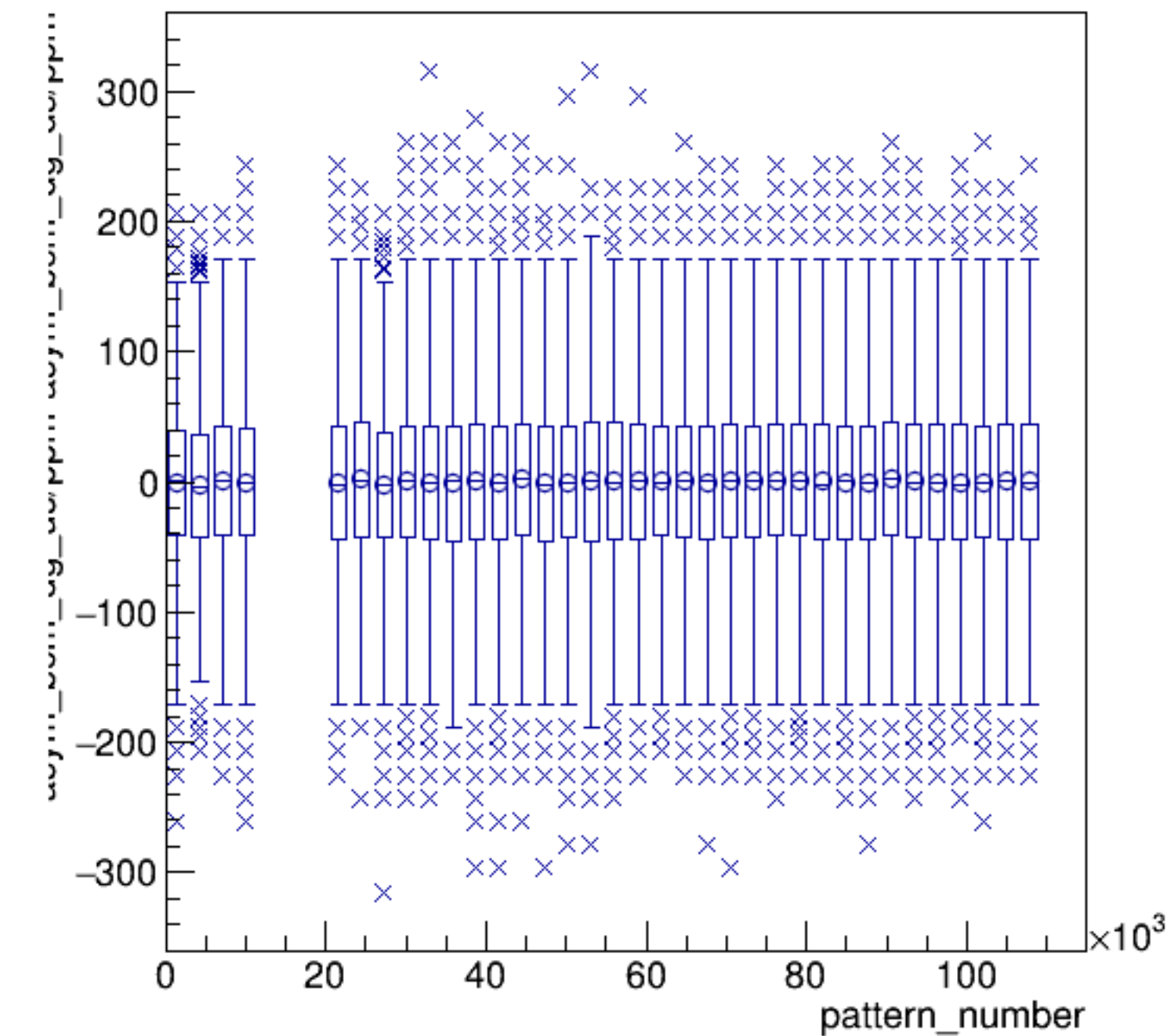
asym_bcm_dg_us/ppm:asym_bcm_dg_ds/ppm {ErrorFlag==0}



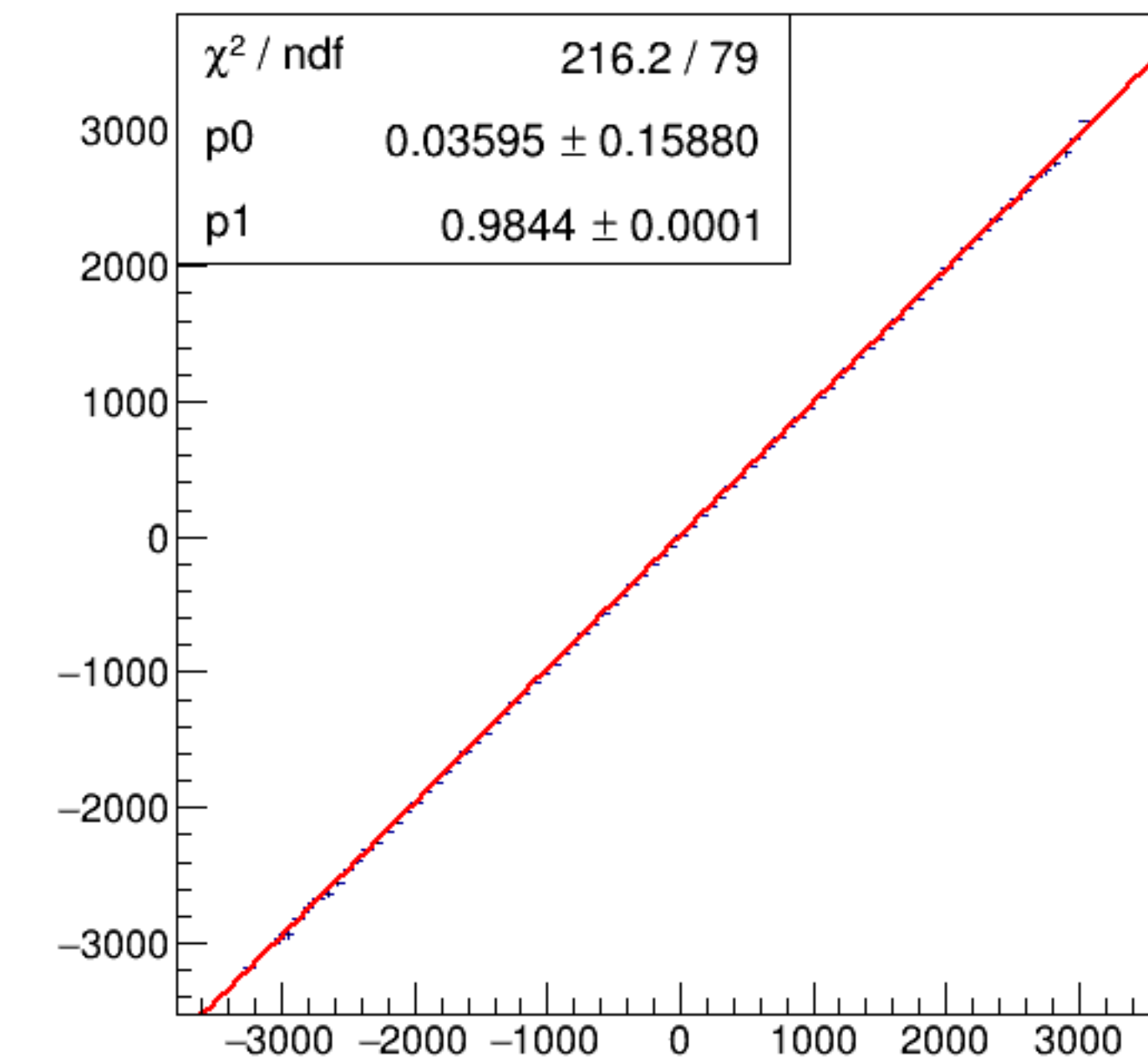
asym_bcm_dg_us/ppm-asym_bcm_dg_ds/ppm {ErrorFlag==0}



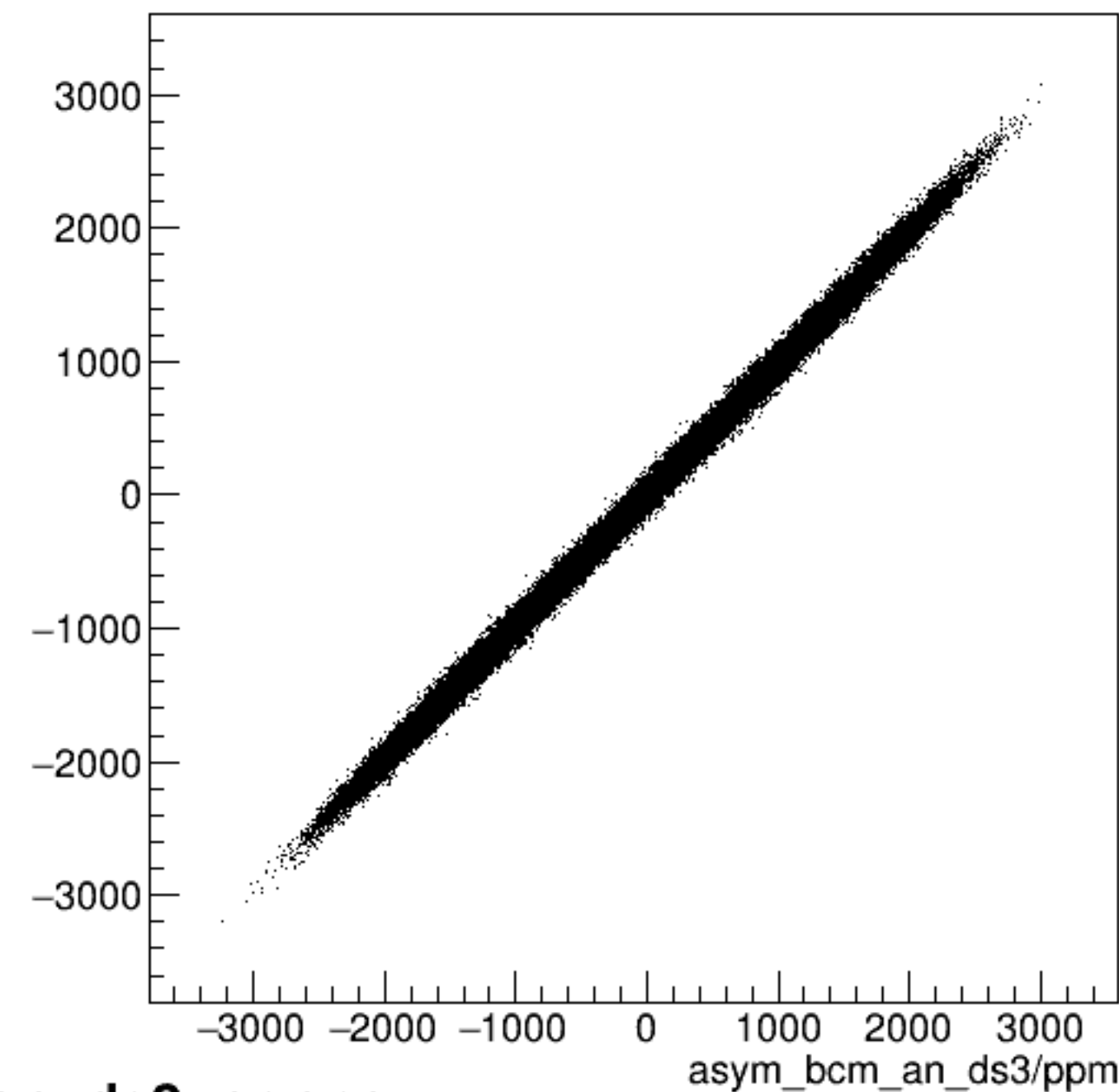
asym_bcm_dg_us/ppm-asym_bcm_dg_ds/ppm:pattern_number {ErrorFlag==0}



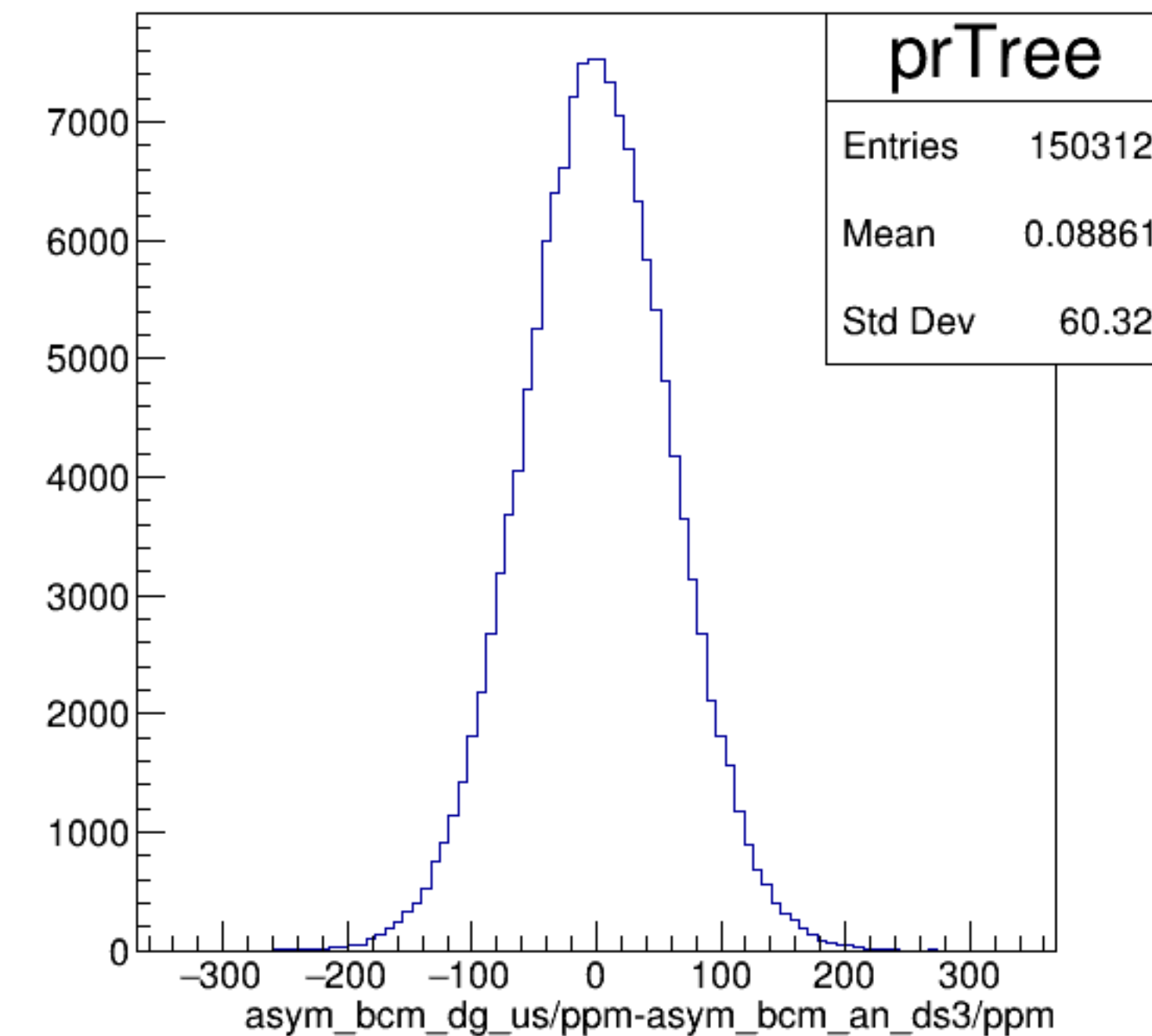
asym_bcm_dg_us/ppm:asym_bcm_an_ds3/ppm {ErrorFlag==0}



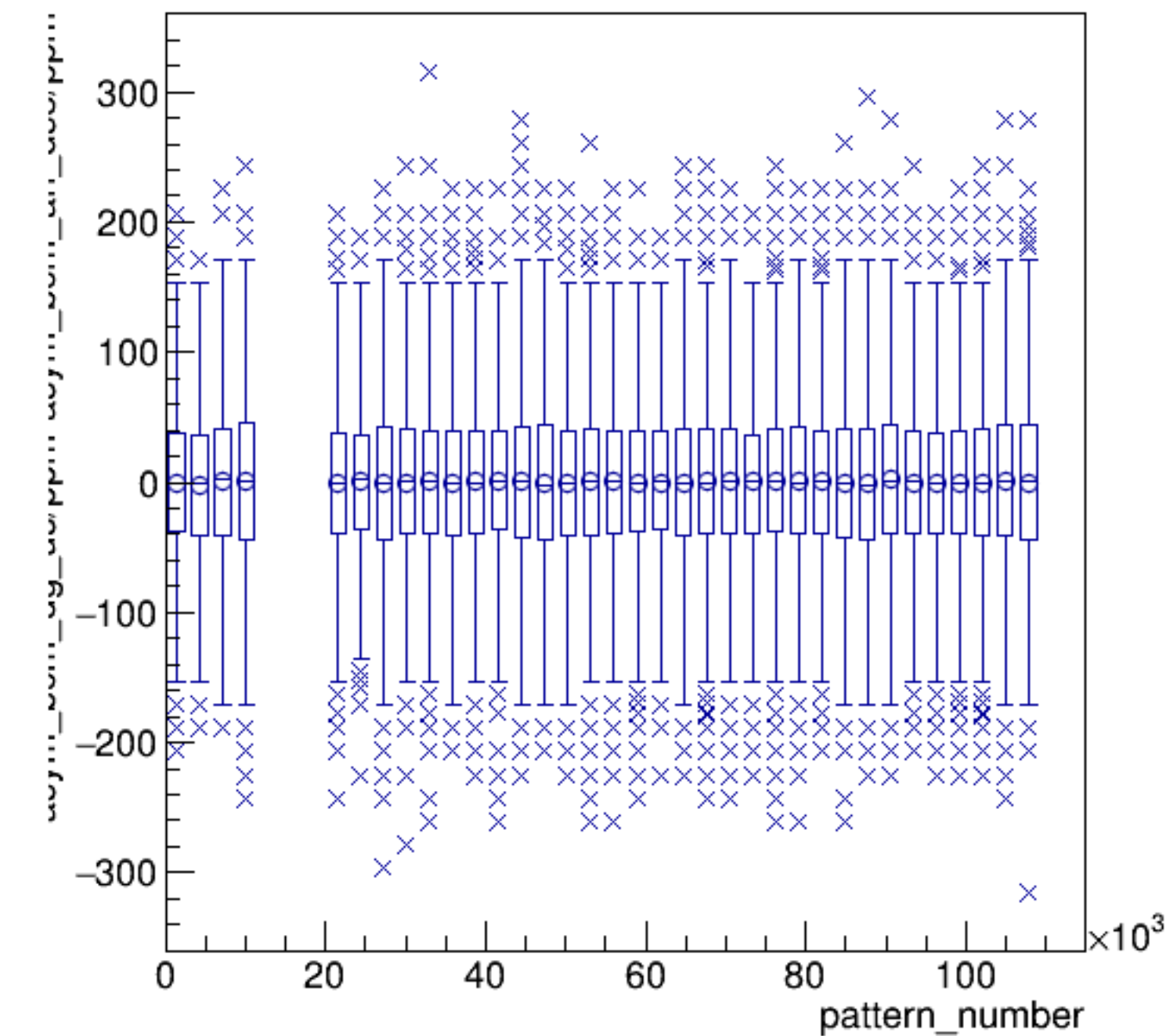
asym_bcm_dg_us/ppm:asym_bcm_an_ds3/ppm {ErrorFlag==0}



asym_bcm_dg_us/ppm-asym_bcm_an_ds3/ppm {ErrorFlag==0}

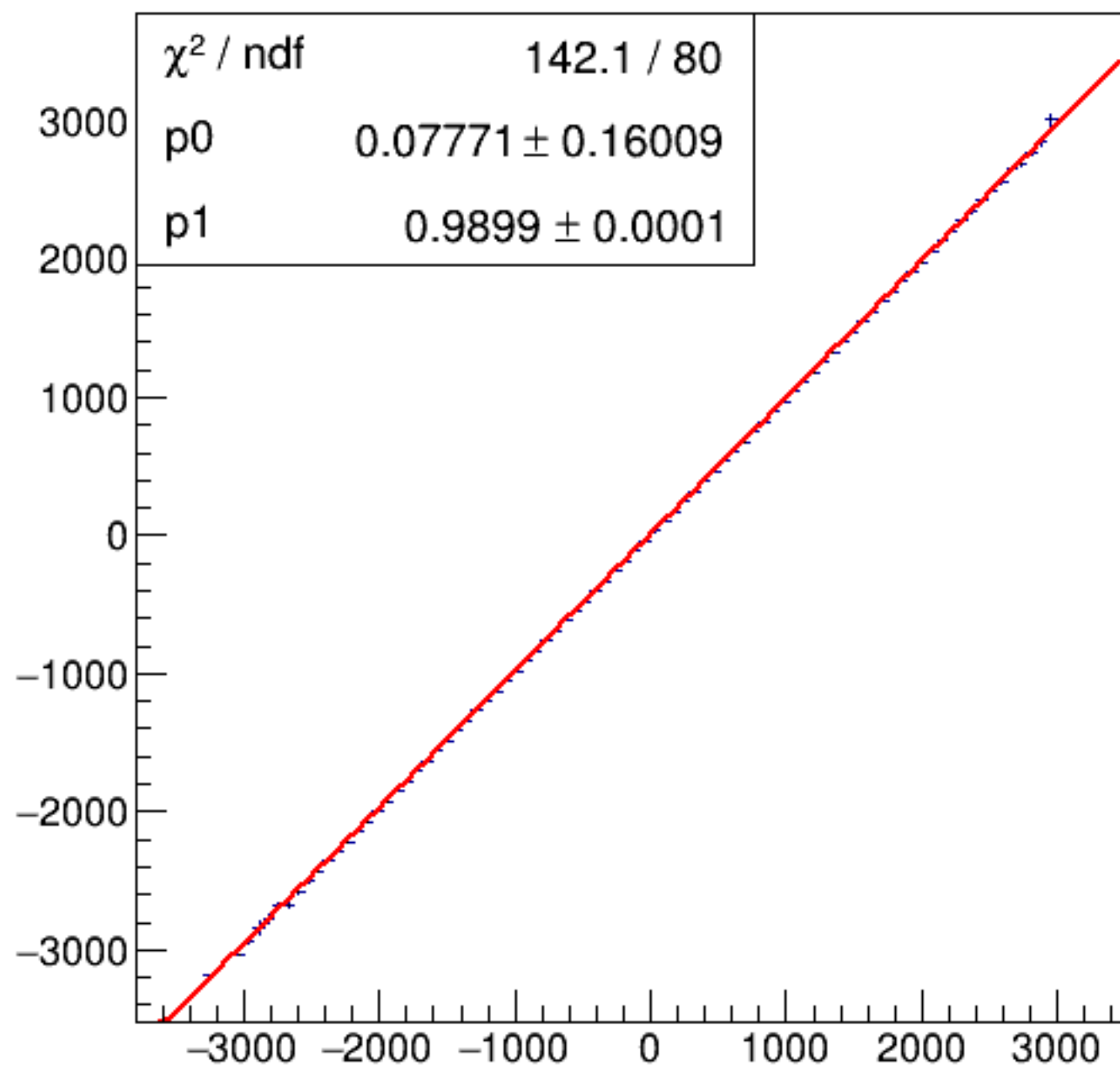


asym_bcm_dg_us/ppm-asym_bcm_an_ds3/ppm:pattern_number {ErrorFlag==0}



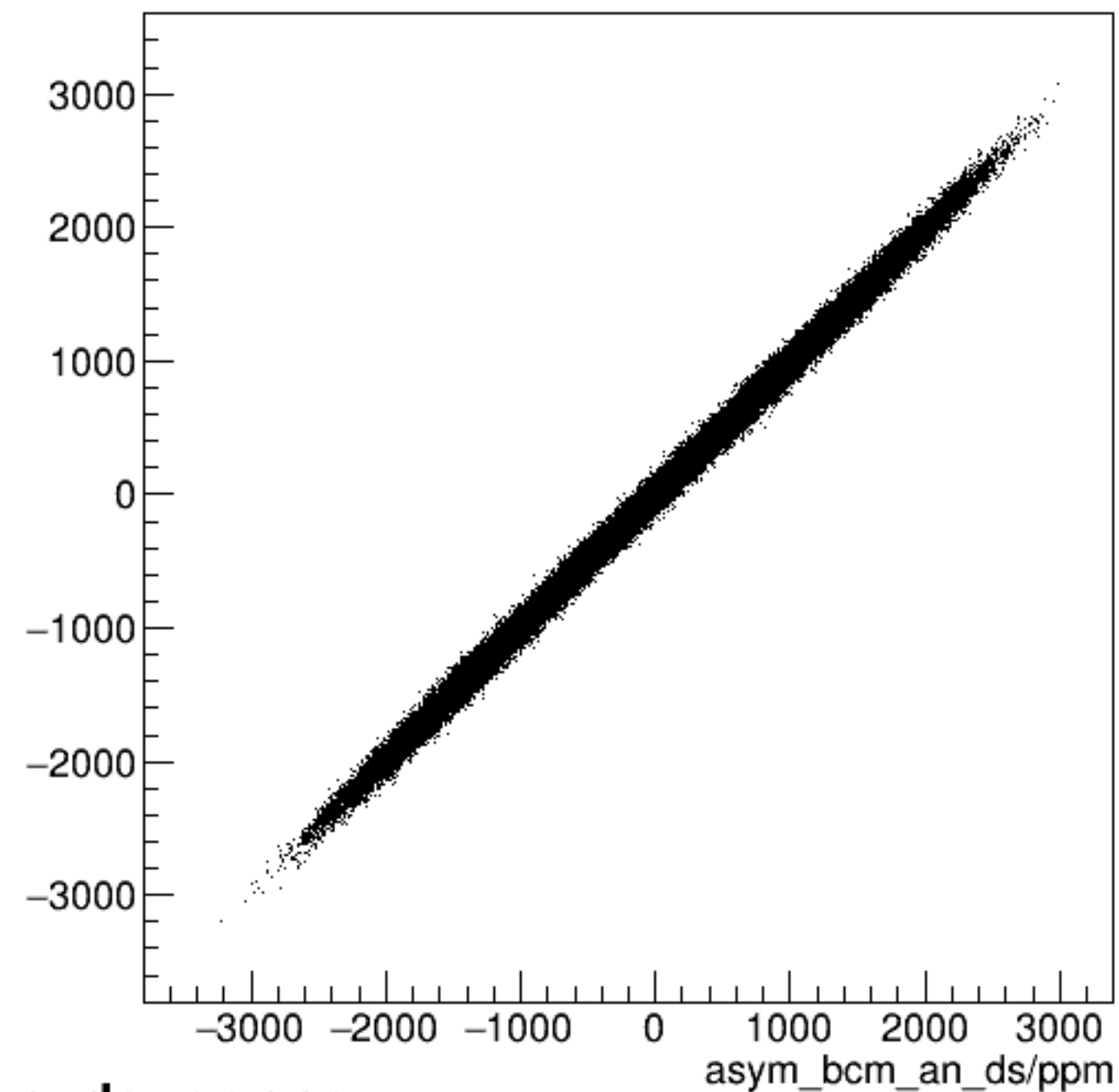
run8304_000_asym_bcm_dg_us_vs_asym_bcm_an_ds3_pr.png

asym_bcm_dg_us/ppm:asym_bcm_an_ds/ppm {ErrorFlag==0}

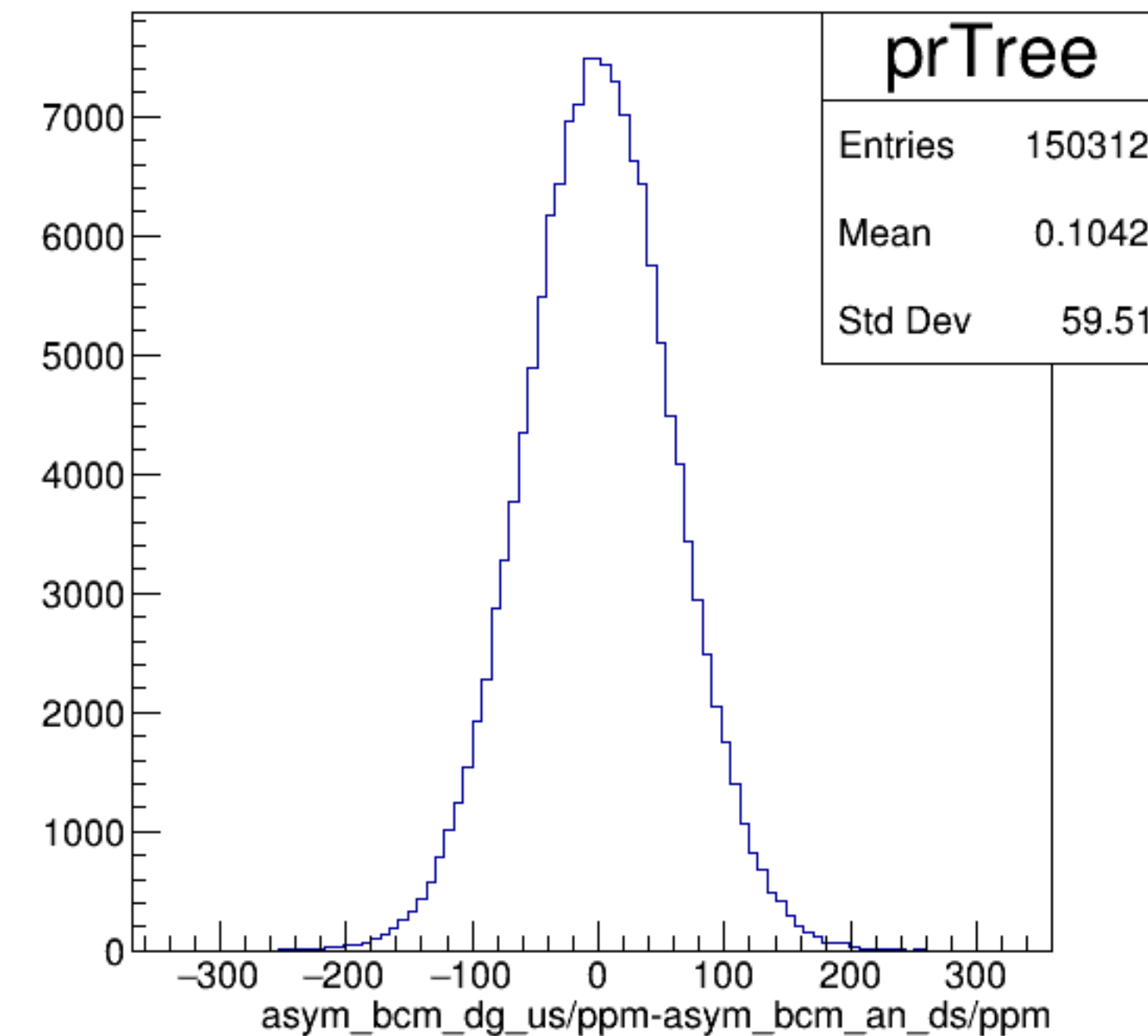


run8304_000_asym_bcm_dg_us_vs_asym_bcm_an_ds_pr.png

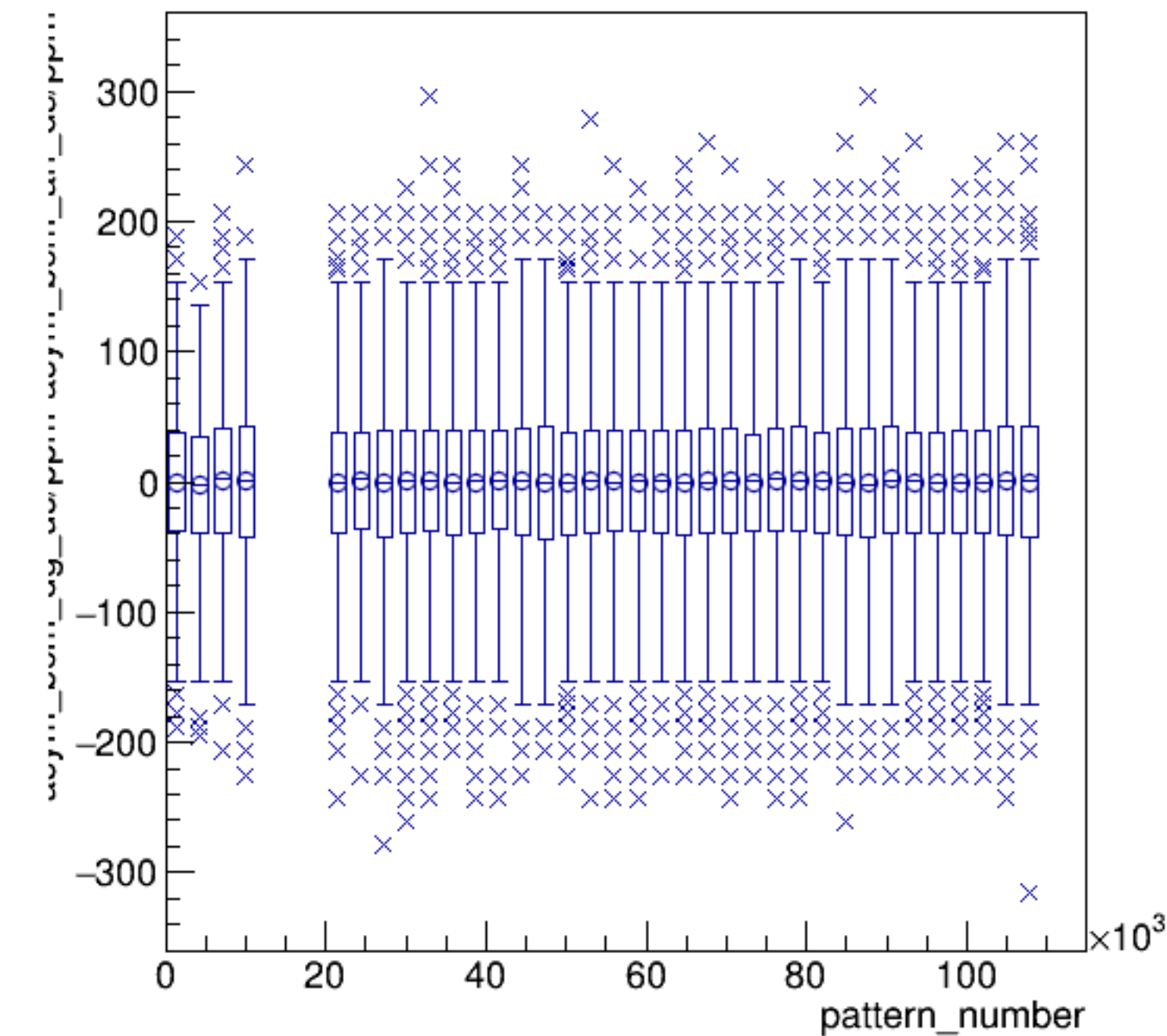
asym_bcm_dg_us/ppm:asym_bcm_an_ds/ppm {ErrorFlag==0}



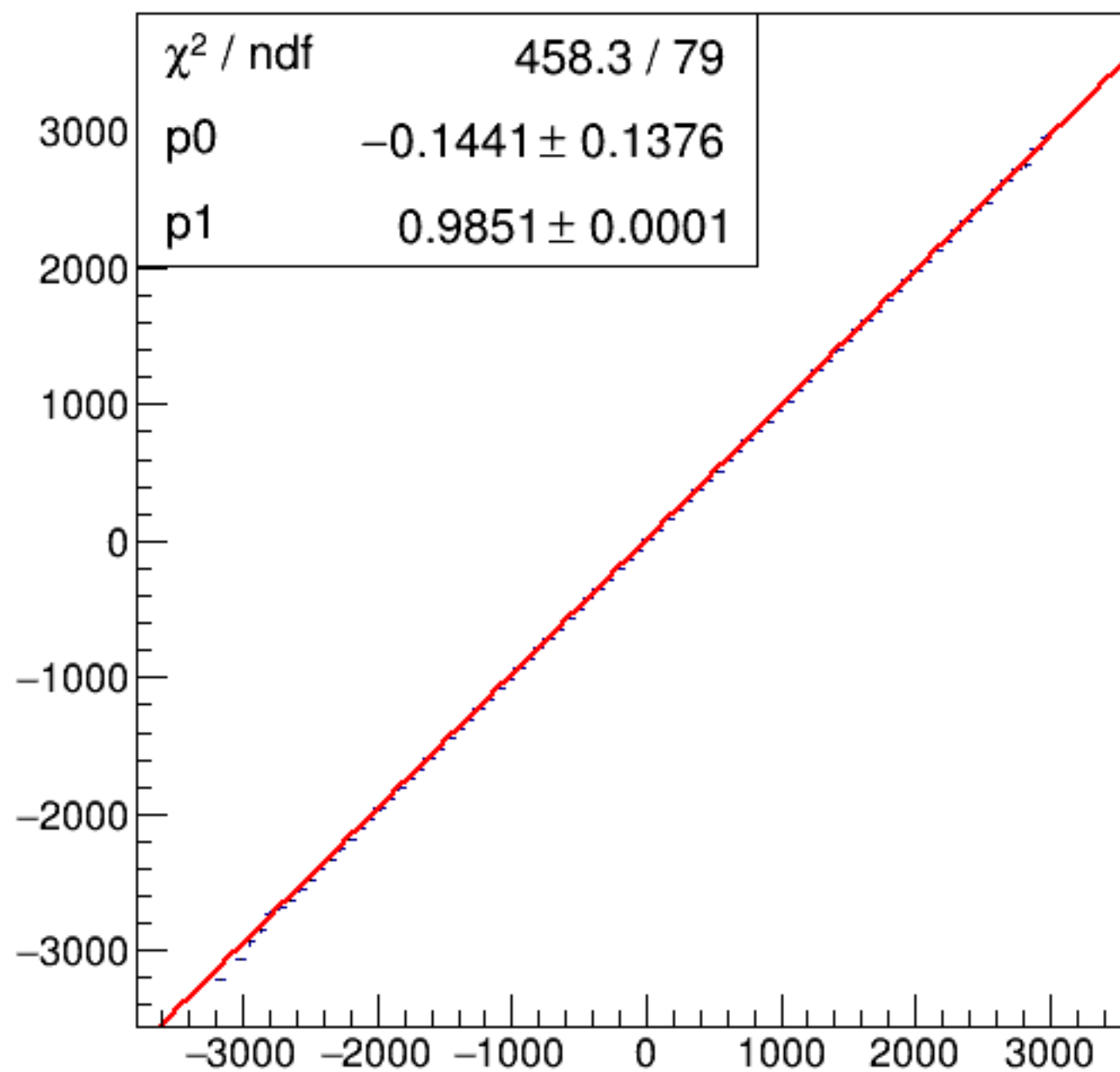
asym_bcm_dg_us/ppm-asym_bcm_an_ds/ppm {ErrorFlag==0}



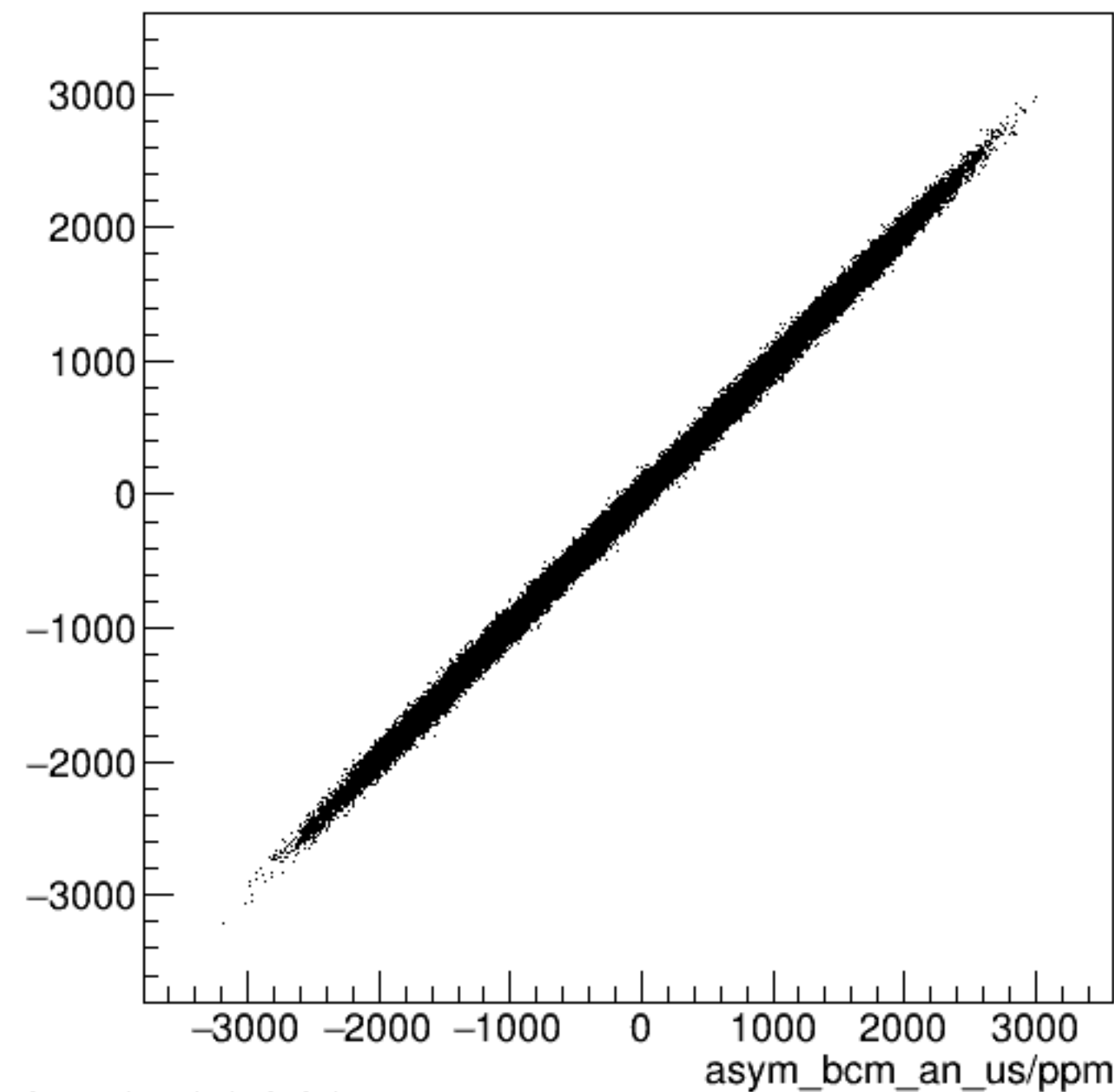
asym_bcm_dg_us/ppm-asym_bcm_an_ds/ppm:pattern_number {ErrorFlag==0}



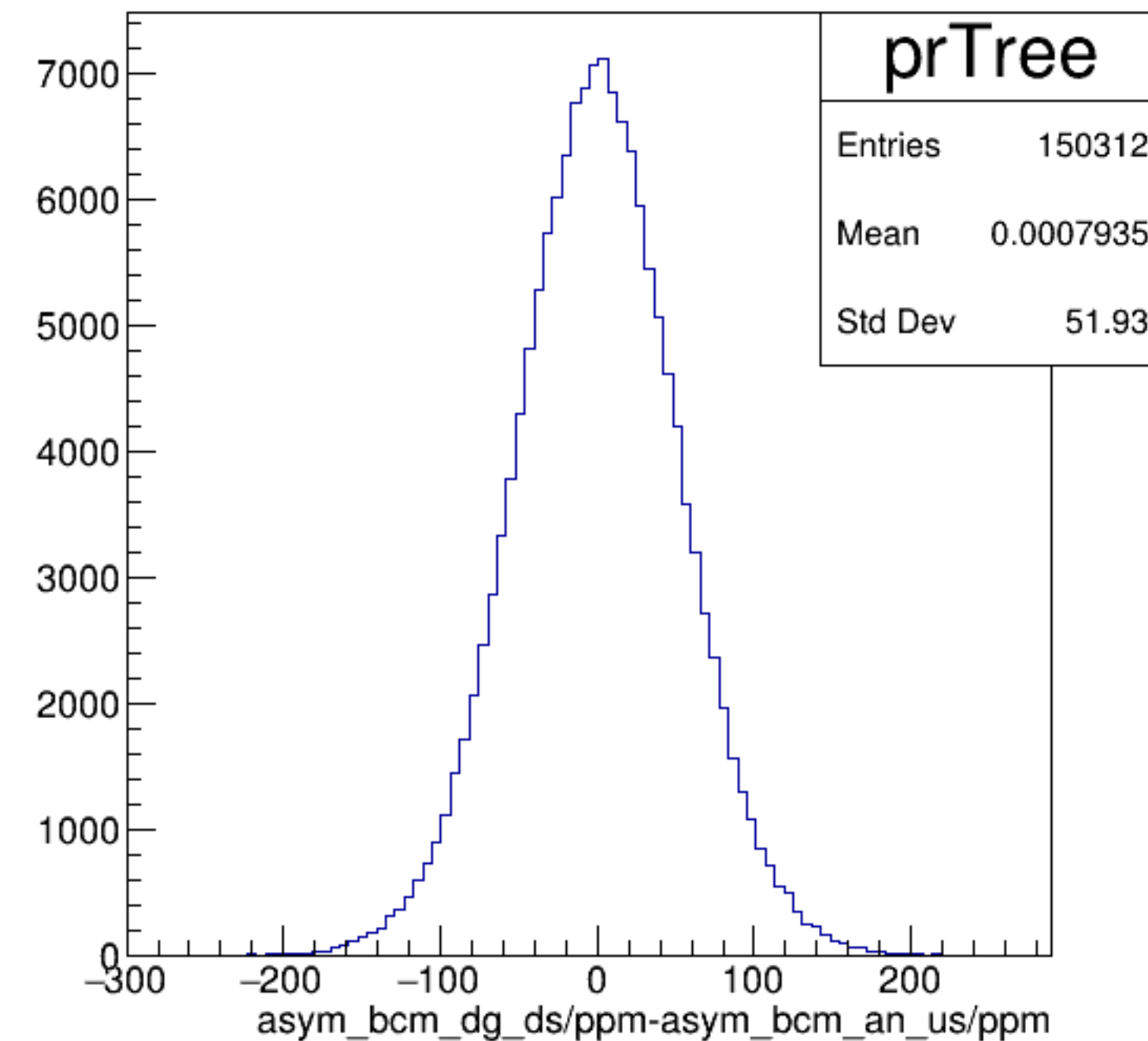
asym_bcm_dg_ds/ppm:asym_bcm_an_us/ppm {ErrorFlag==0}



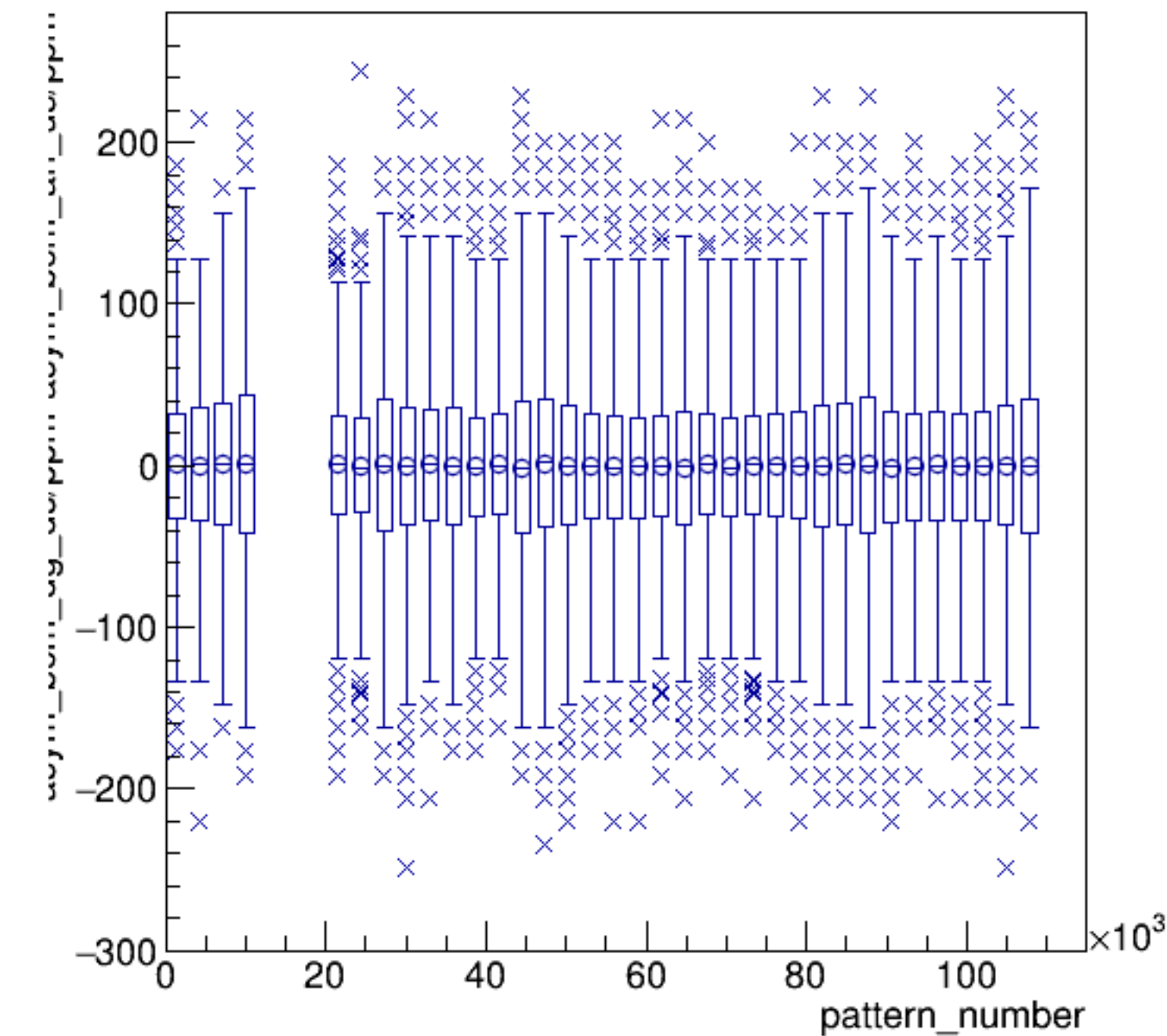
asym_bcm_dg_ds/ppm:asym_bcm_an_us/ppm {ErrorFlag==0}



asym_bcm_dg_ds/ppm-asym_bcm_an_us/ppm {ErrorFlag==0}



asym_bcm_dg_ds/ppm-asym_bcm_an_us/ppm:pattern_number {ErrorFlag==0}



run8304_000_asym_bcm_dg_ds_vs_asym_bcm_an_us_pr.png