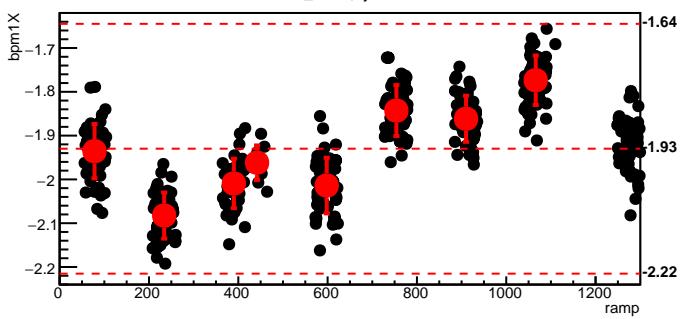
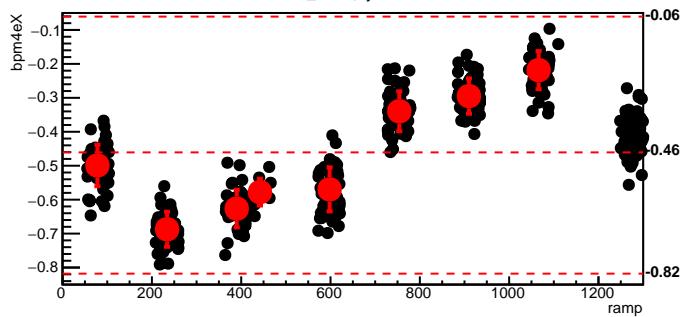


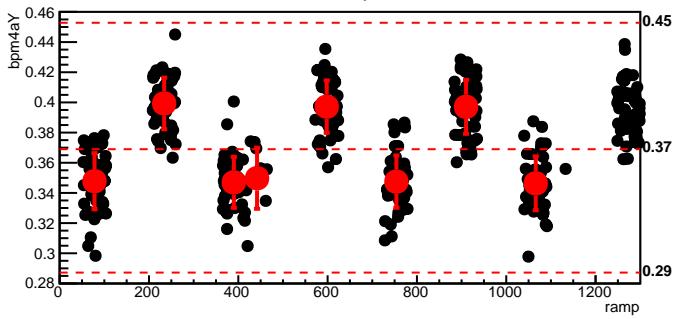
bmod Trim1,cyc=11146



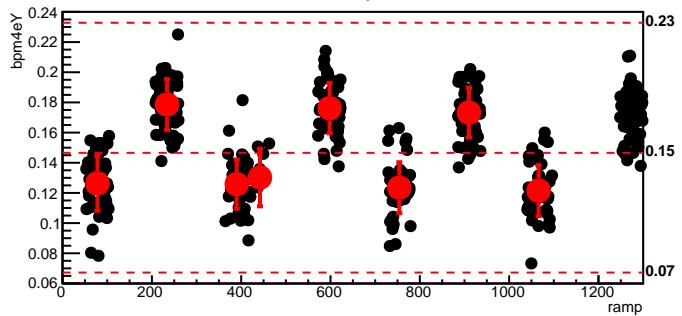
bmod Trim1,cyc=11146



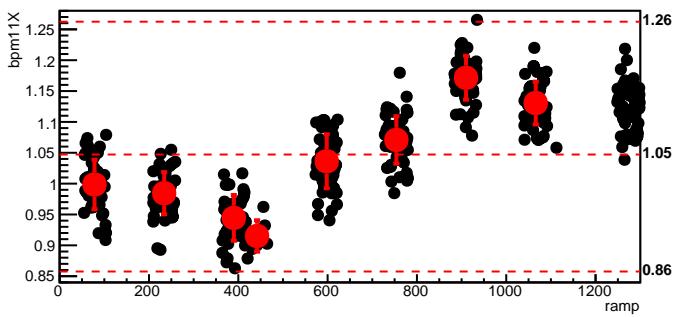
bmod Trim1,cyc=11146



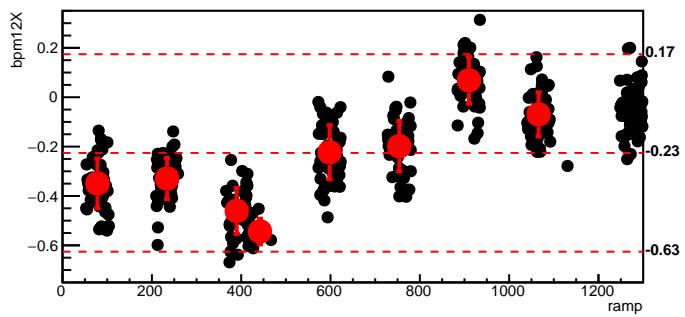
bmod Trim1,cyc=11146



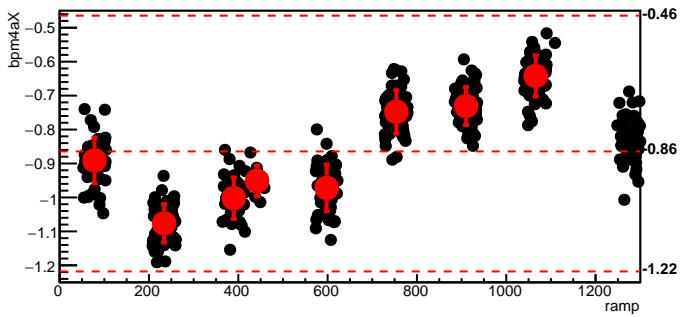
bmod Trim1,cyc=11146



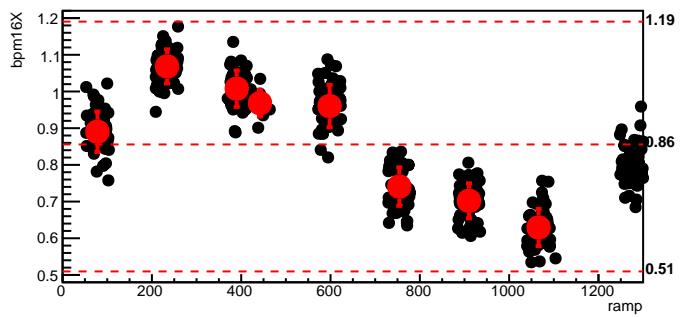
bmod Trim1,cyc=11146



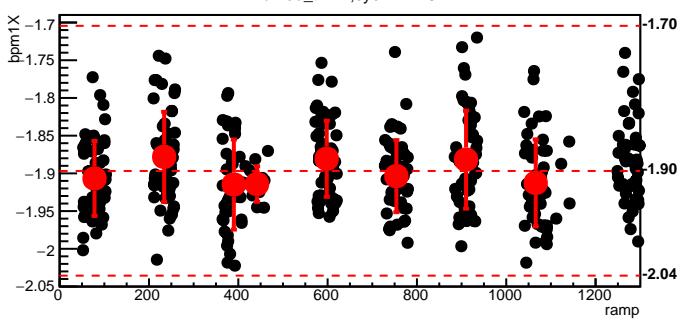
bmod Trim1,cyc=11146



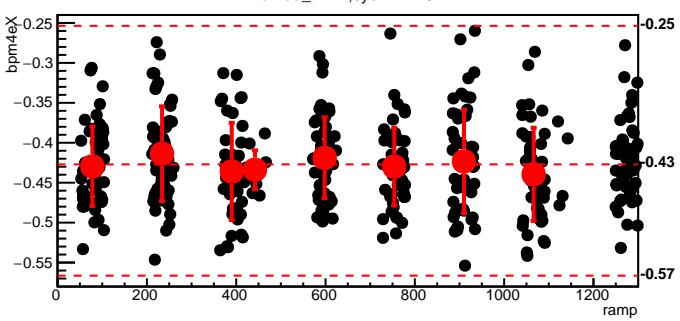
bmod Trim1,cyc=11146



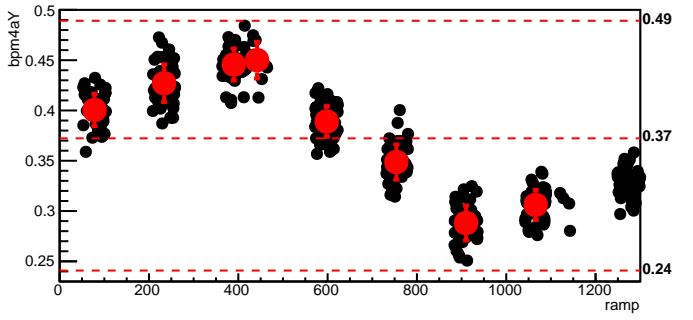
bmod Trim2,cyc=11146



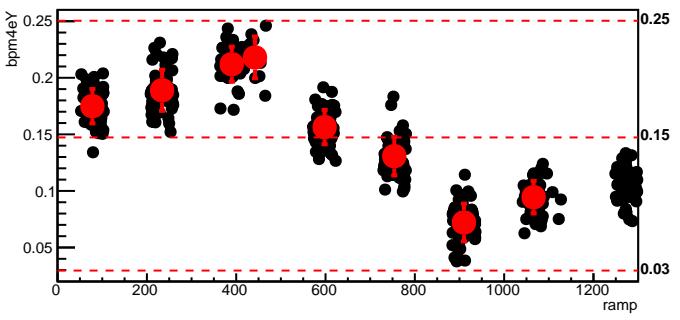
bmod Trim2,cyc=11146



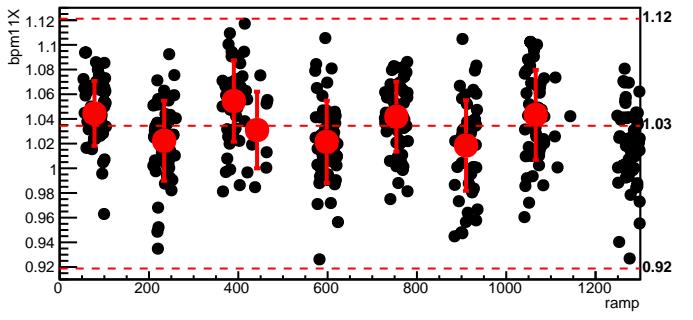
bmod Trim2,cyc=11146



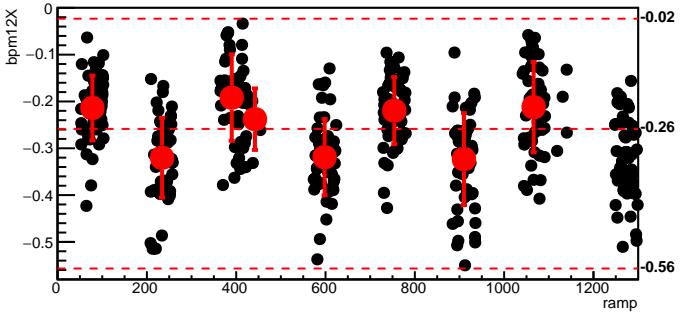
bmod Trim2,cyc=11146



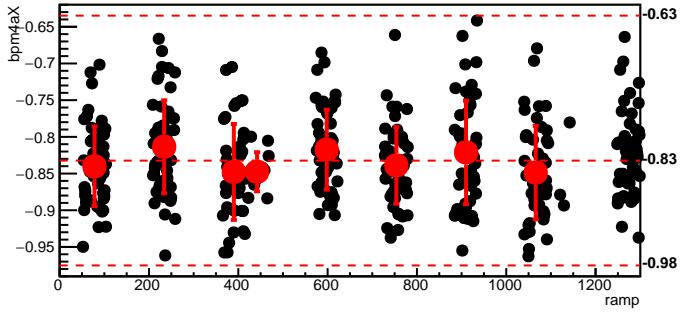
bmod Trim2,cyc=11146



bmod Trim2,cyc=11146



bmod Trim2,cyc=11146



bmod Trim2,cyc=11146

