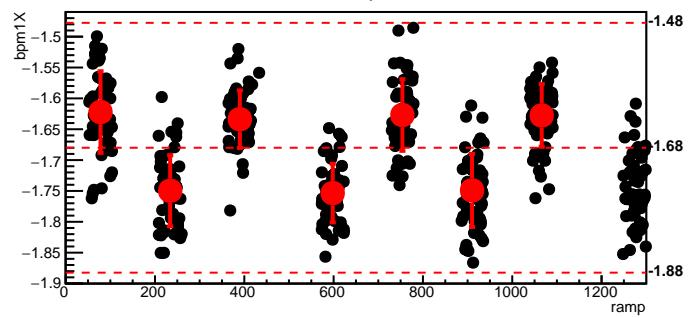
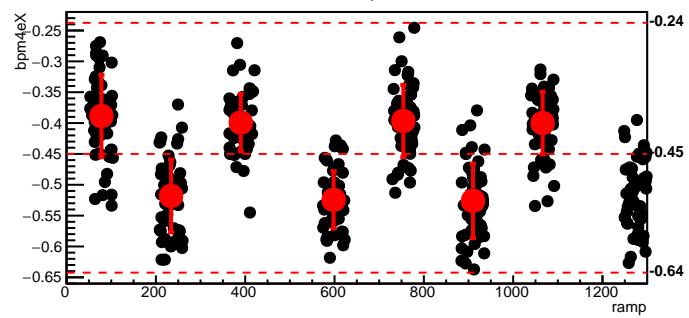


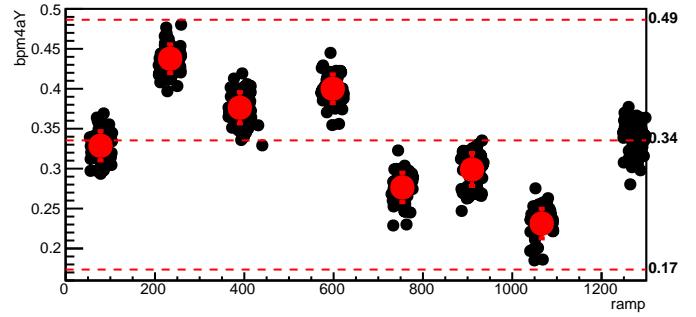
bmod Trim2,cyc=11150



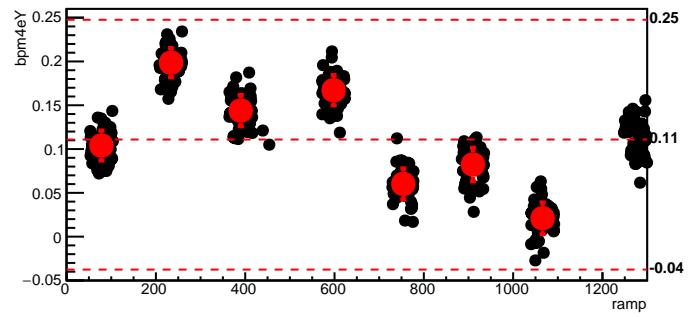
bmod Trim2,cyc=11150



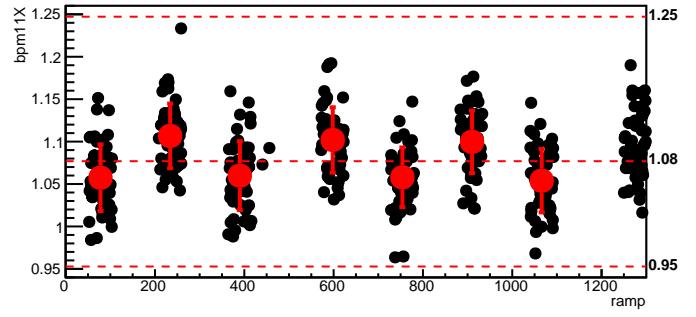
bmod Trim2,cyc=11150



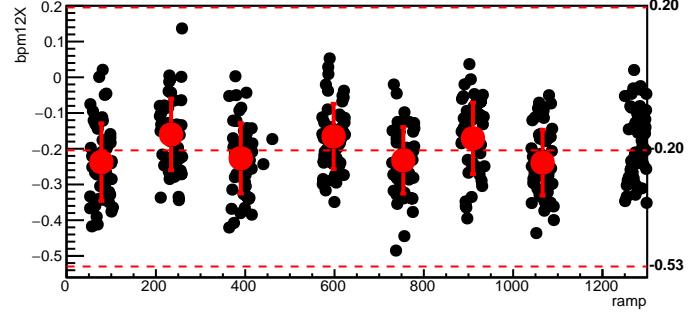
bmod Trim2,cyc=11150



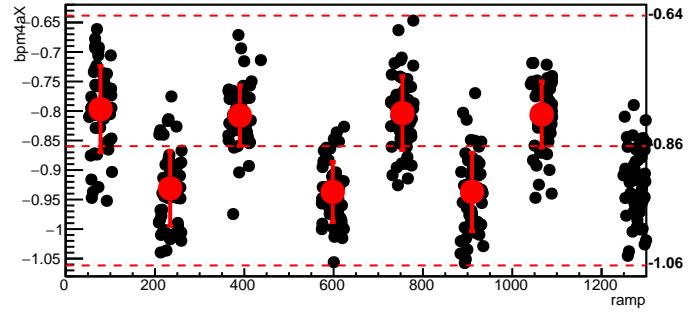
bmod Trim2,cyc=11150



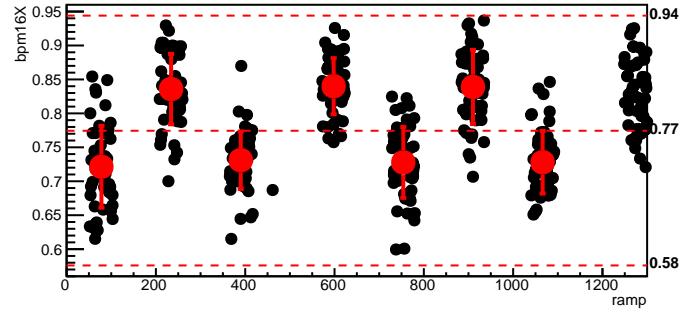
bmod Trim2,cyc=11150



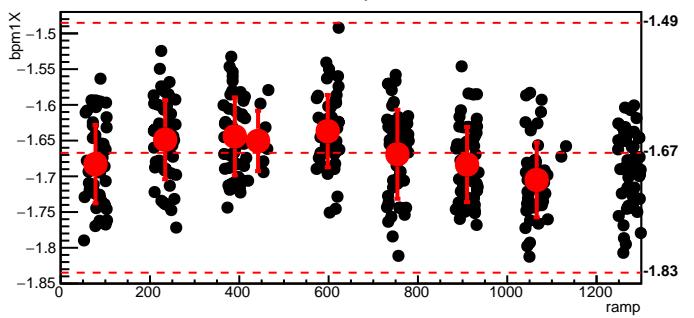
bmod Trim2,cyc=11150



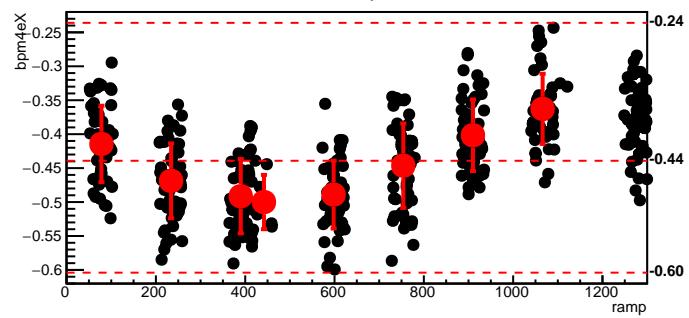
bmod Trim2,cyc=11150



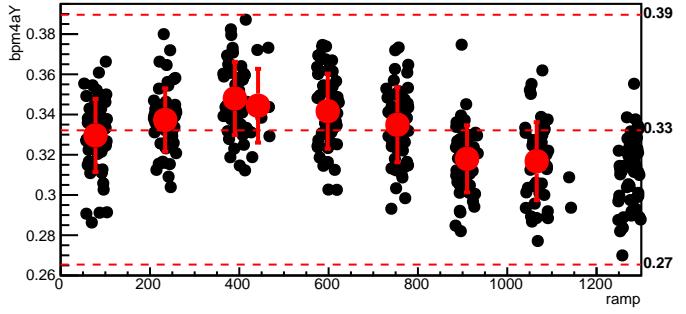
bmod Trim3,cyc=11150



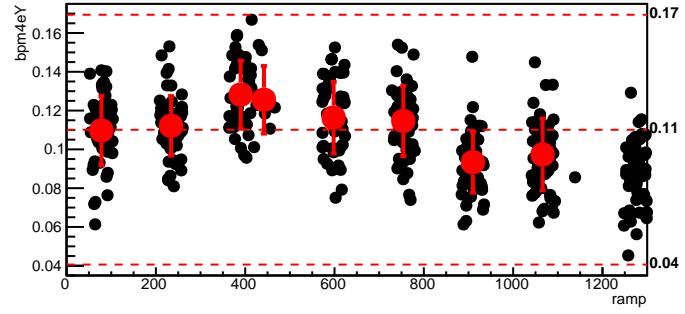
bmod Trim3,cyc=11150



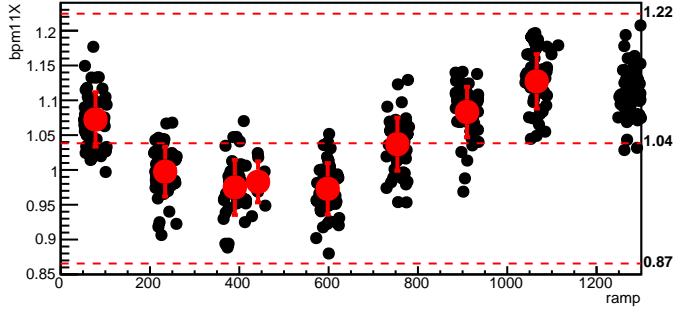
bmod Trim3,cyc=11150



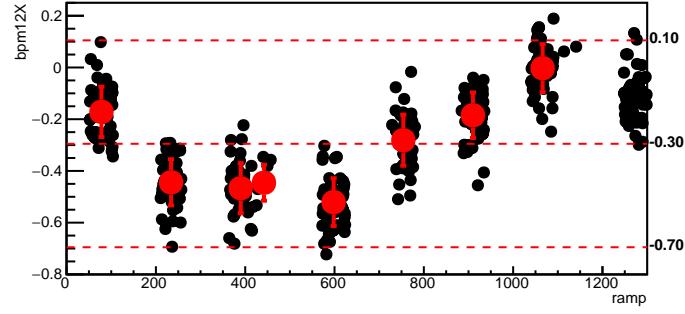
bmod Trim3,cyc=11150



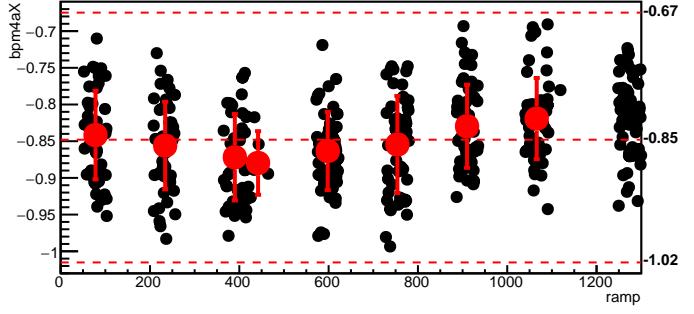
bmod Trim3,cyc=11150



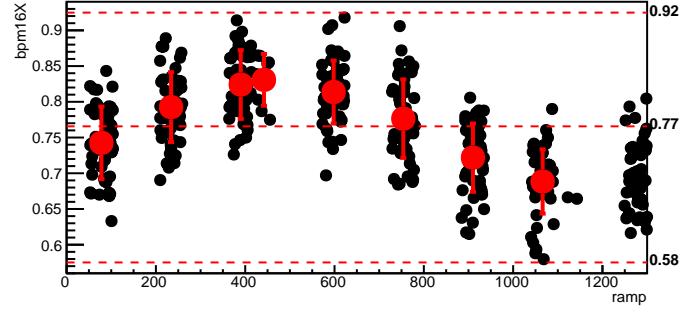
bmod Trim3,cyc=11150



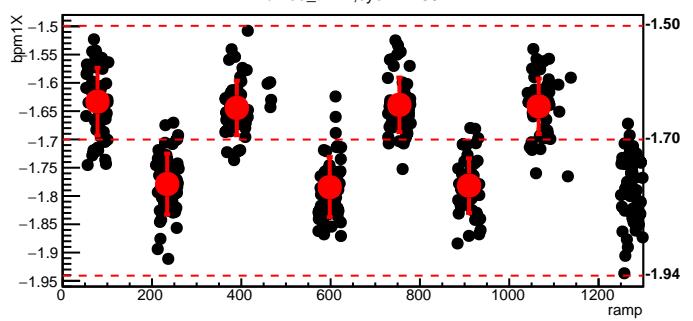
bmod Trim3,cyc=11150



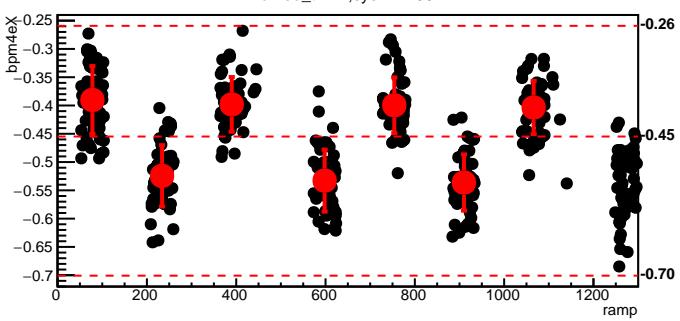
bmod Trim3,cyc=11150



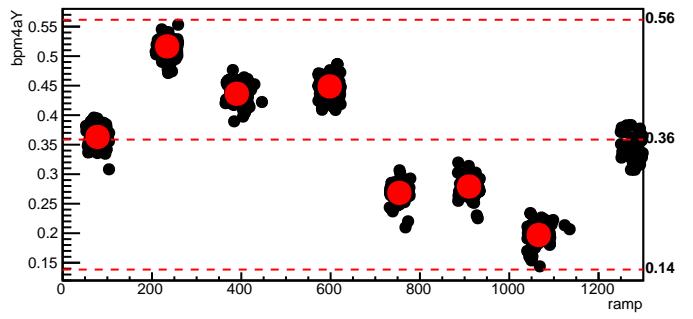
bmod Trim4,cyc=11150



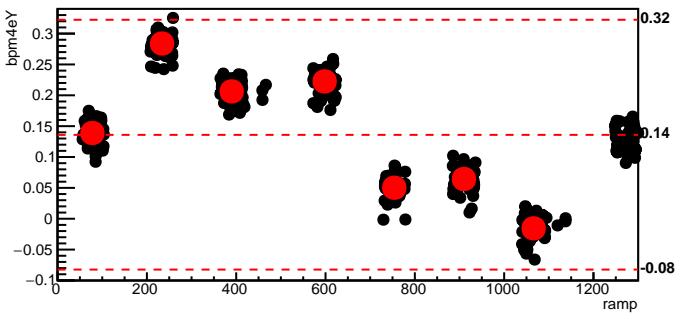
bmod Trim4,cyc=11150



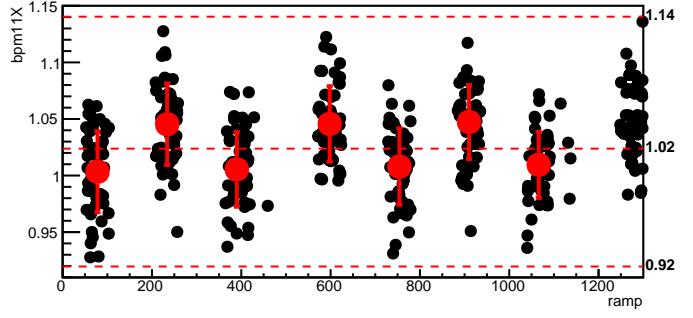
bmod Trim4,cyc=11150



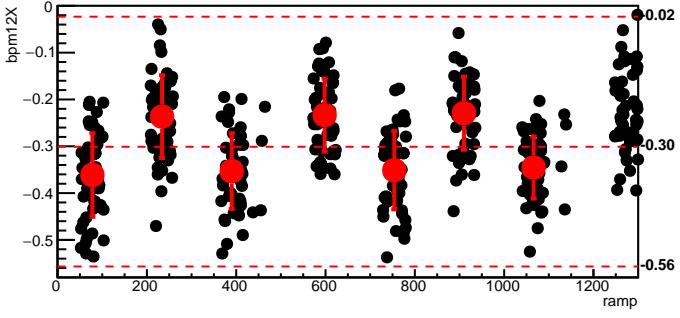
bmod Trim4,cyc=11150



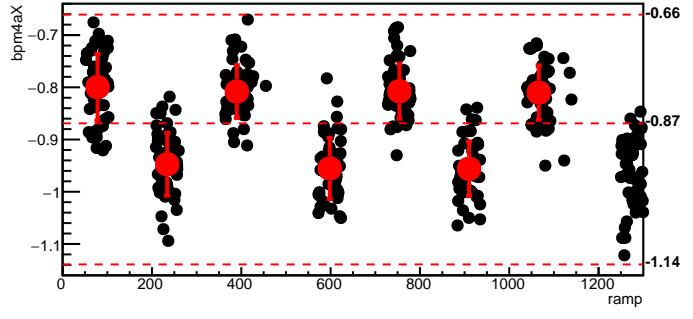
bmod Trim4,cyc=11150



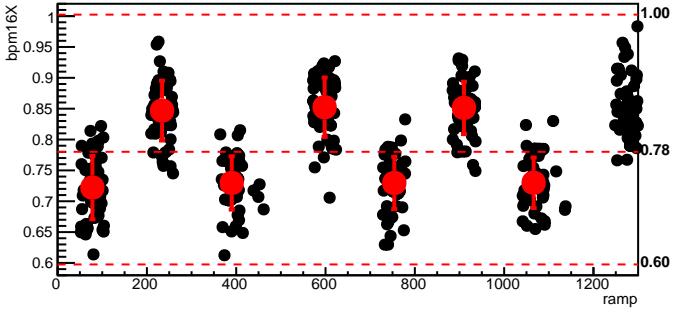
bmod Trim4,cyc=11150

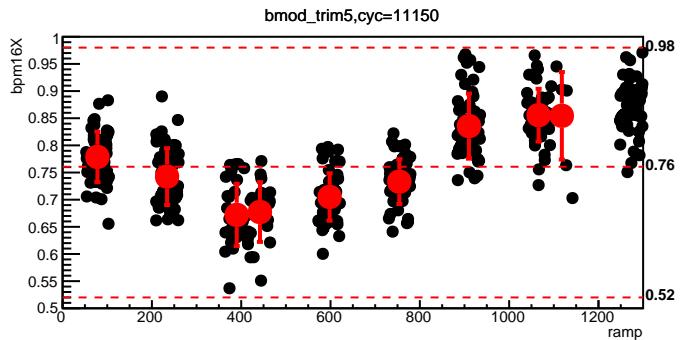
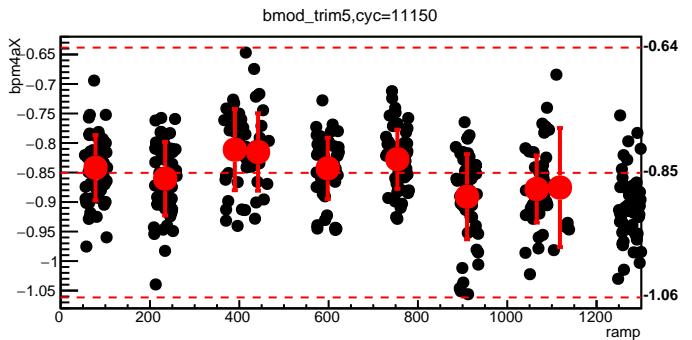
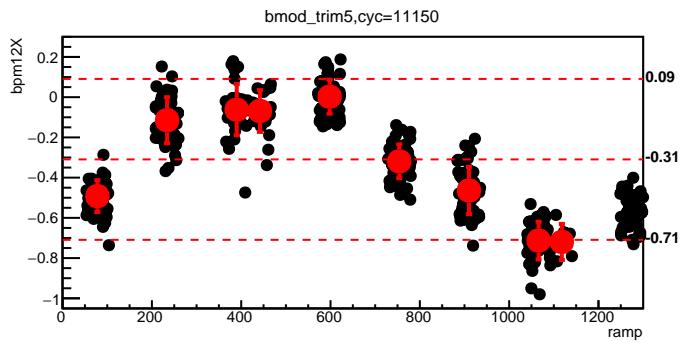
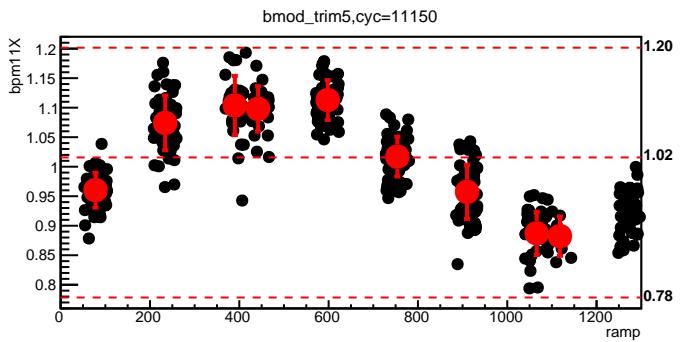
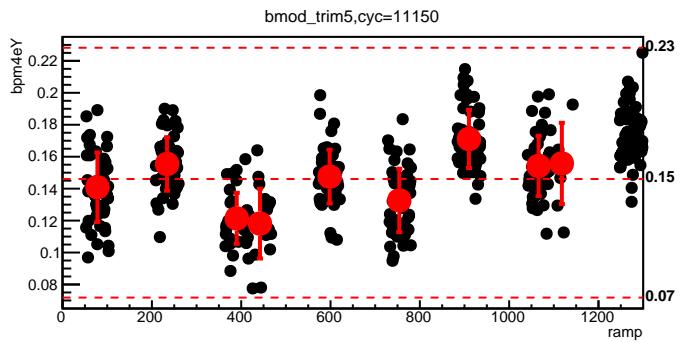
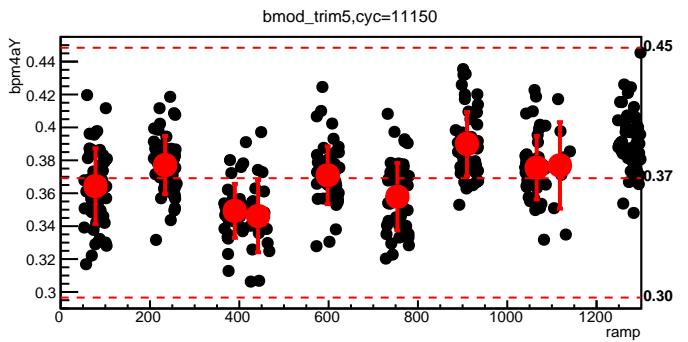
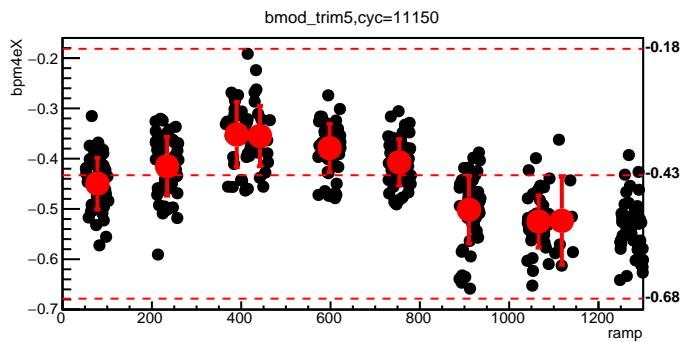
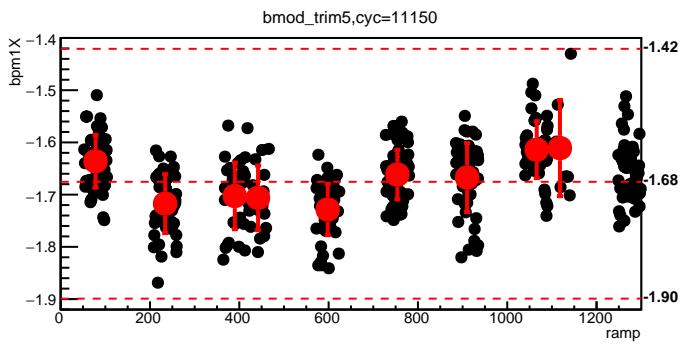


bmod Trim4,cyc=11150

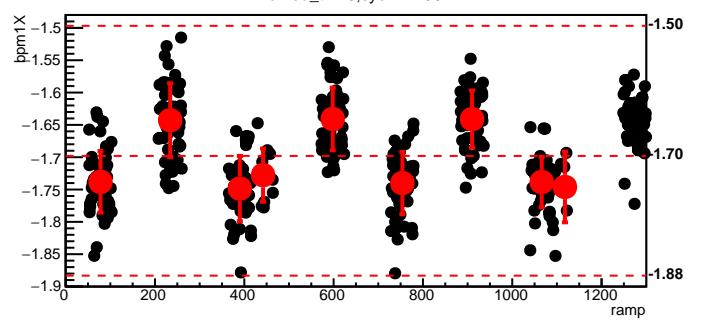


bmod Trim4,cyc=11150

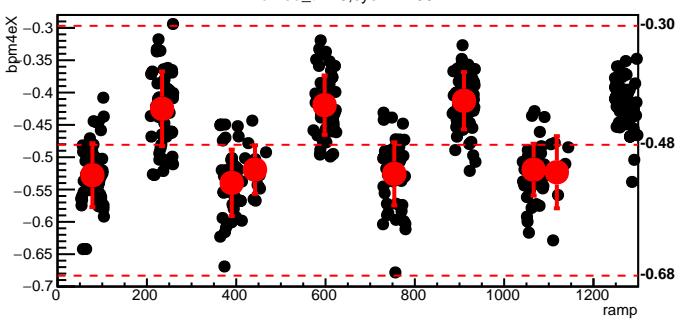




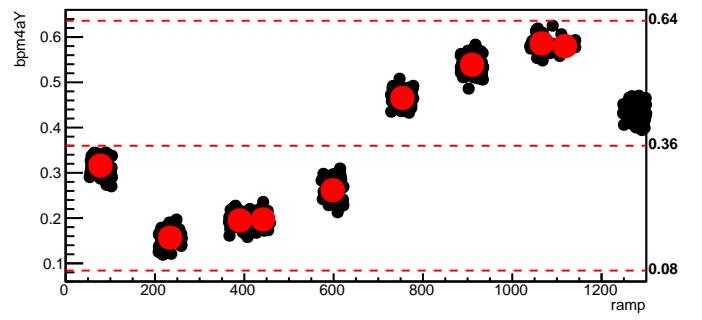
bmod Trim6,cyc=11150



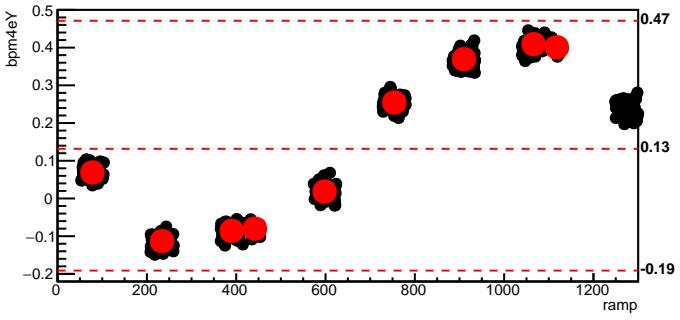
bmod Trim6,cyc=11150



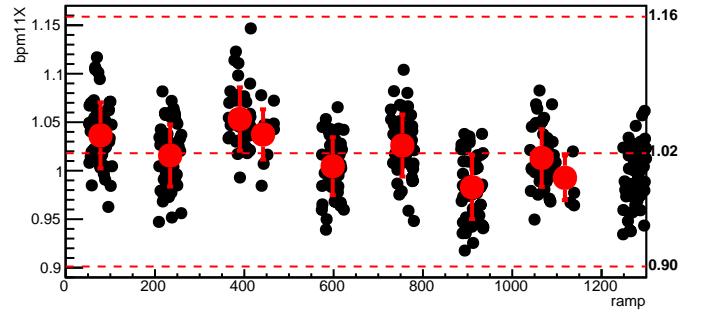
bmod Trim6,cyc=11150



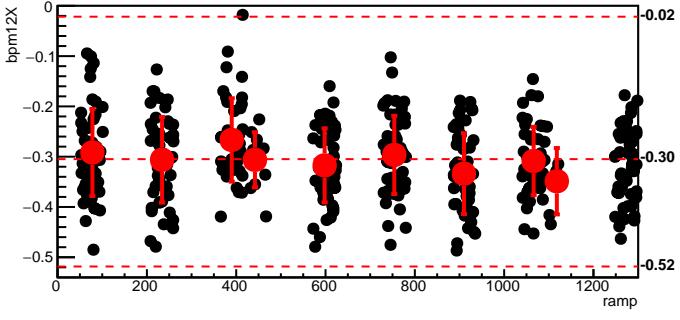
bmod Trim6,cyc=11150



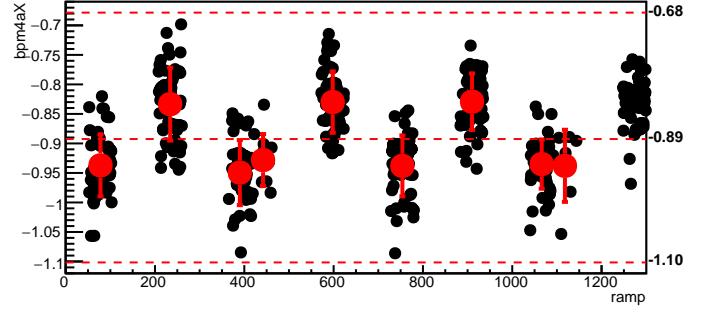
bmod Trim6,cyc=11150



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