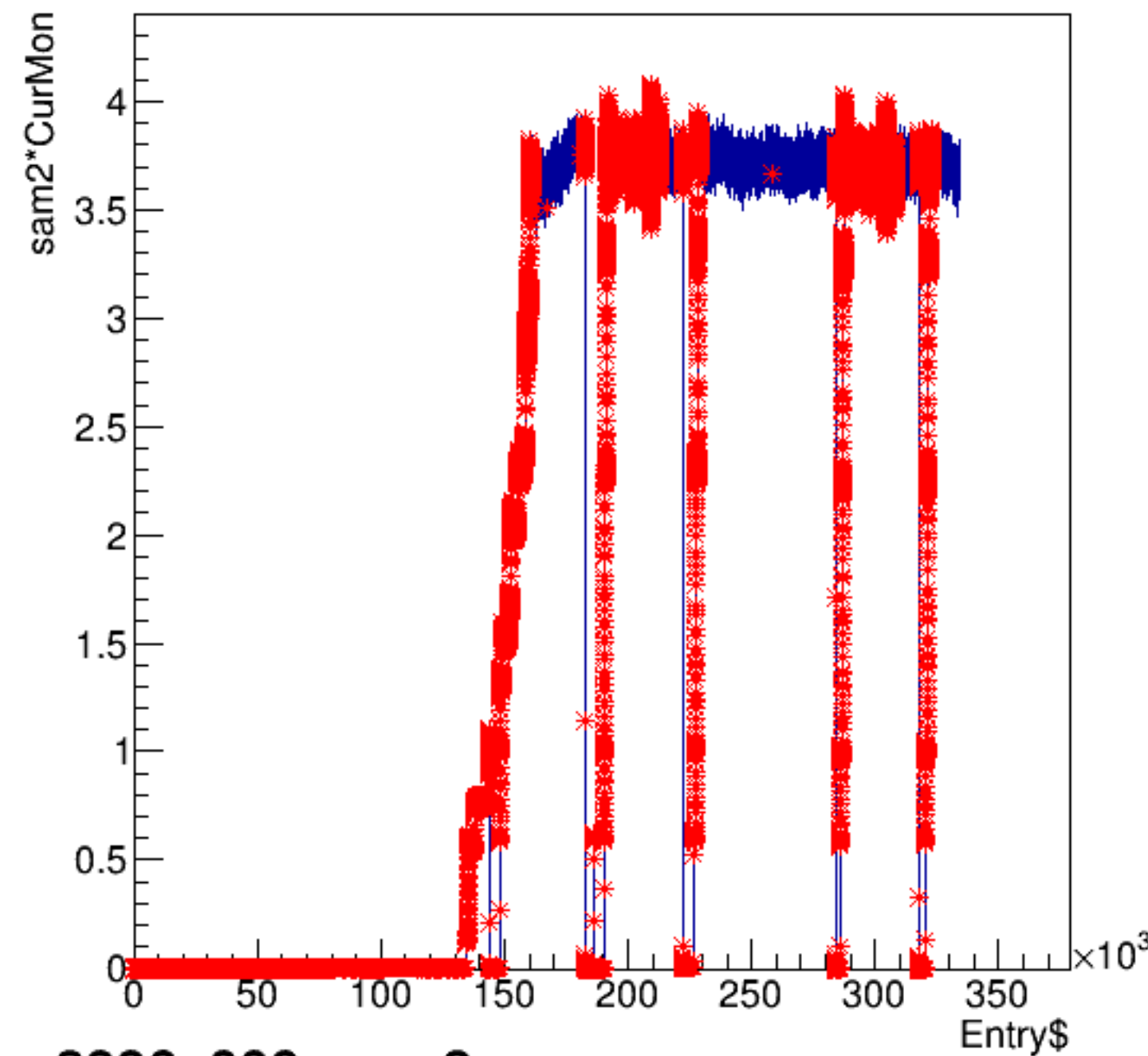
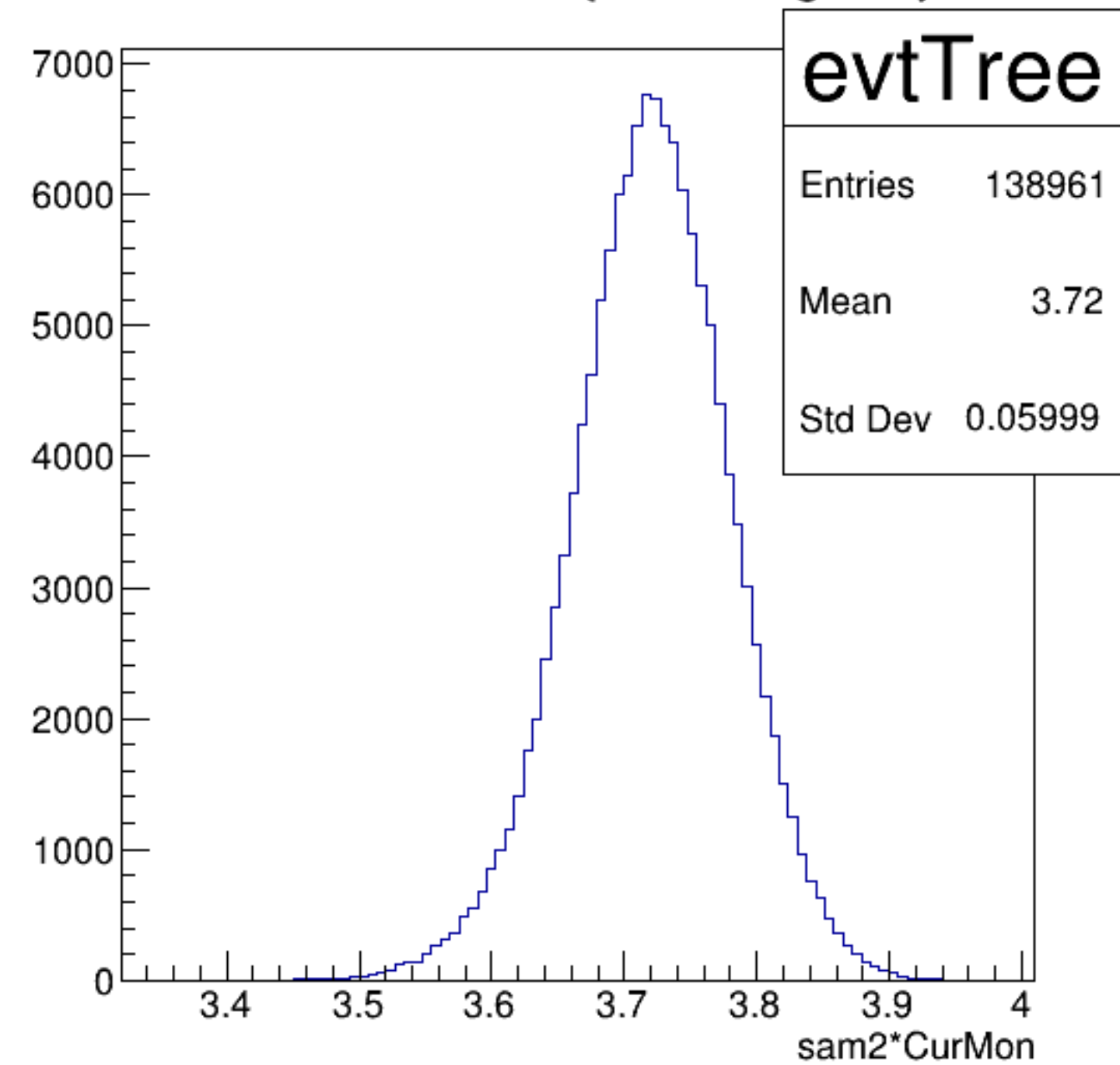


sam2*CurMon:Entry\$

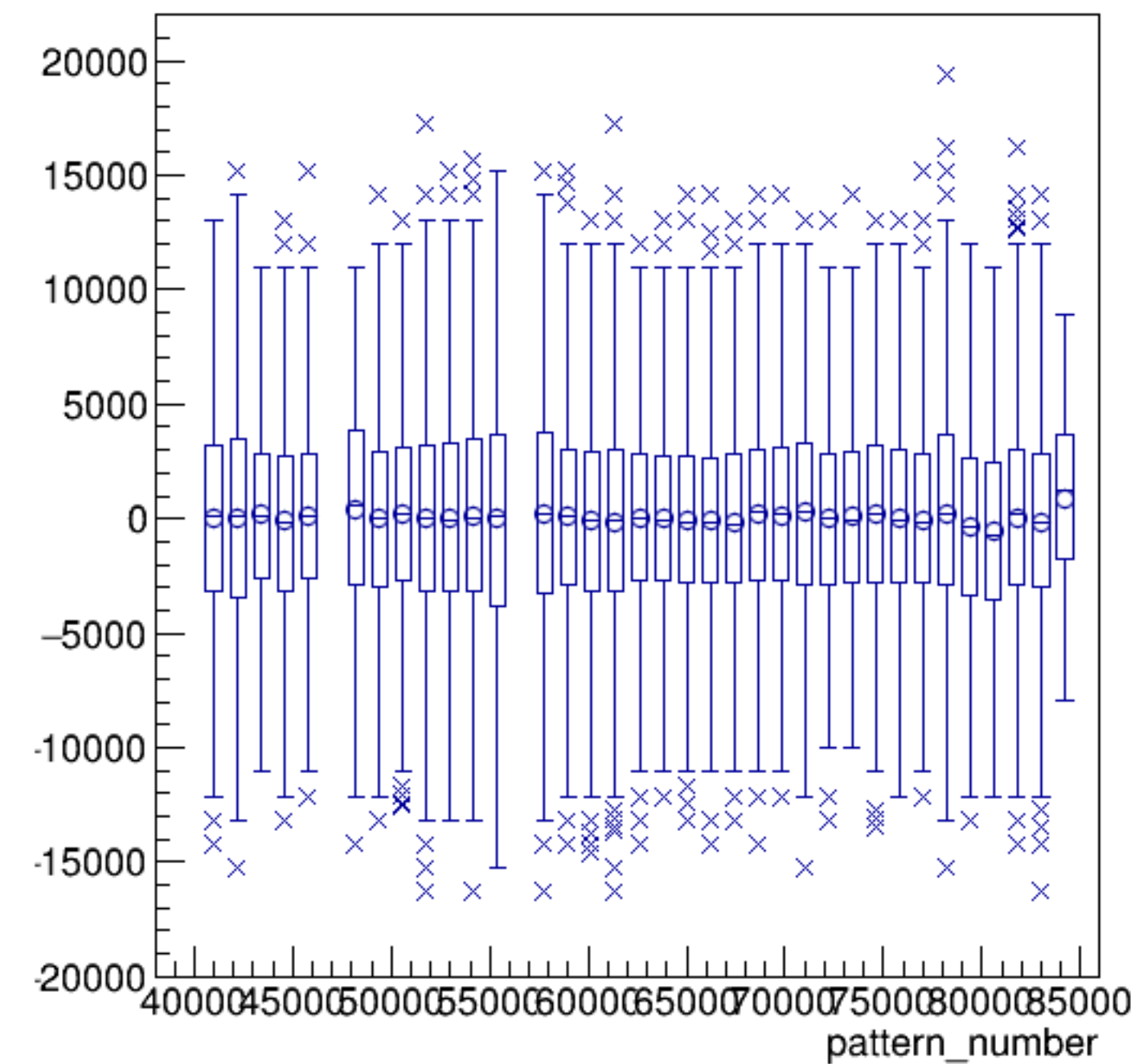


run8336_000_sam2.png

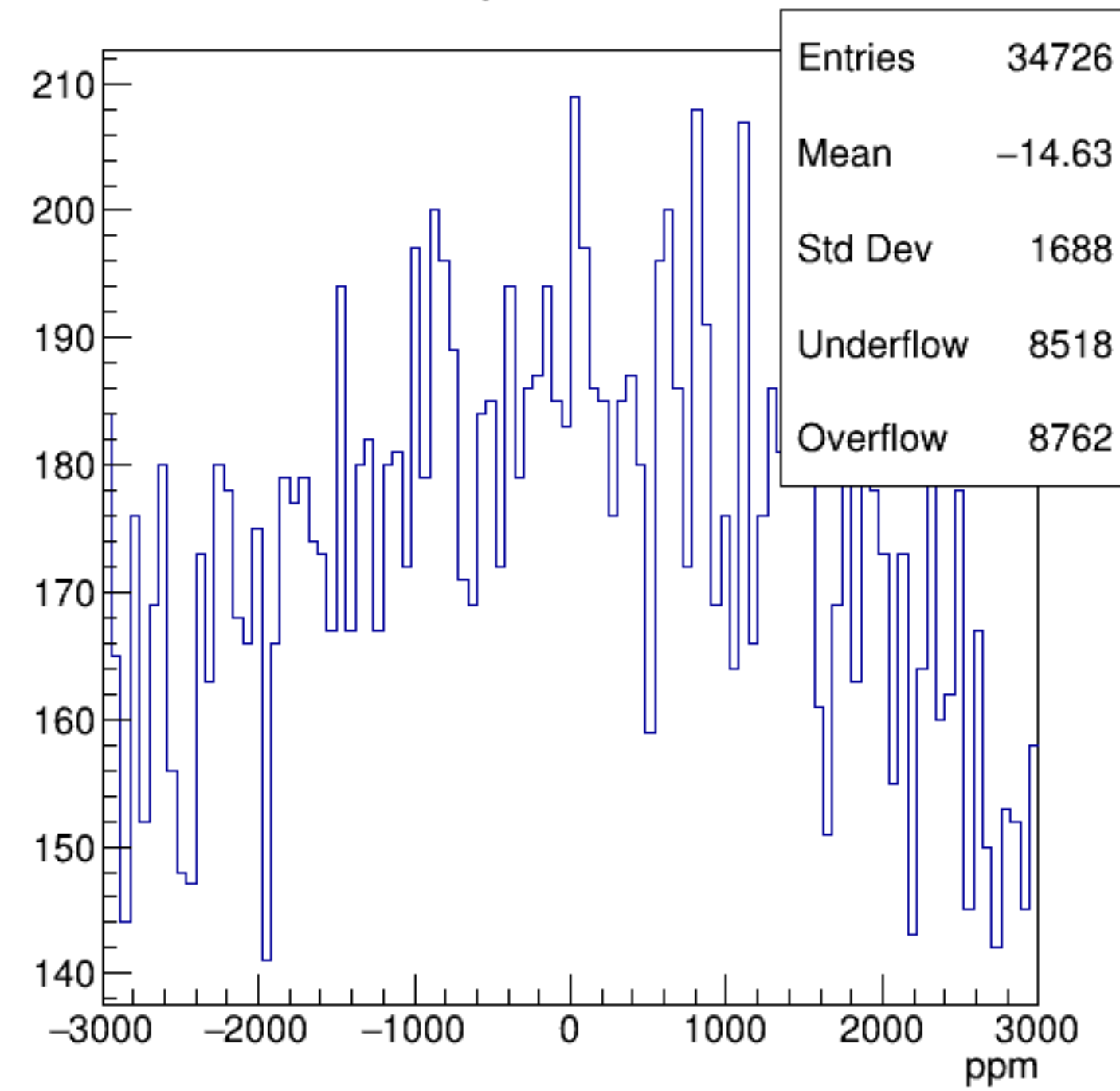
sam2*CurMon {ErrorFlag==0}



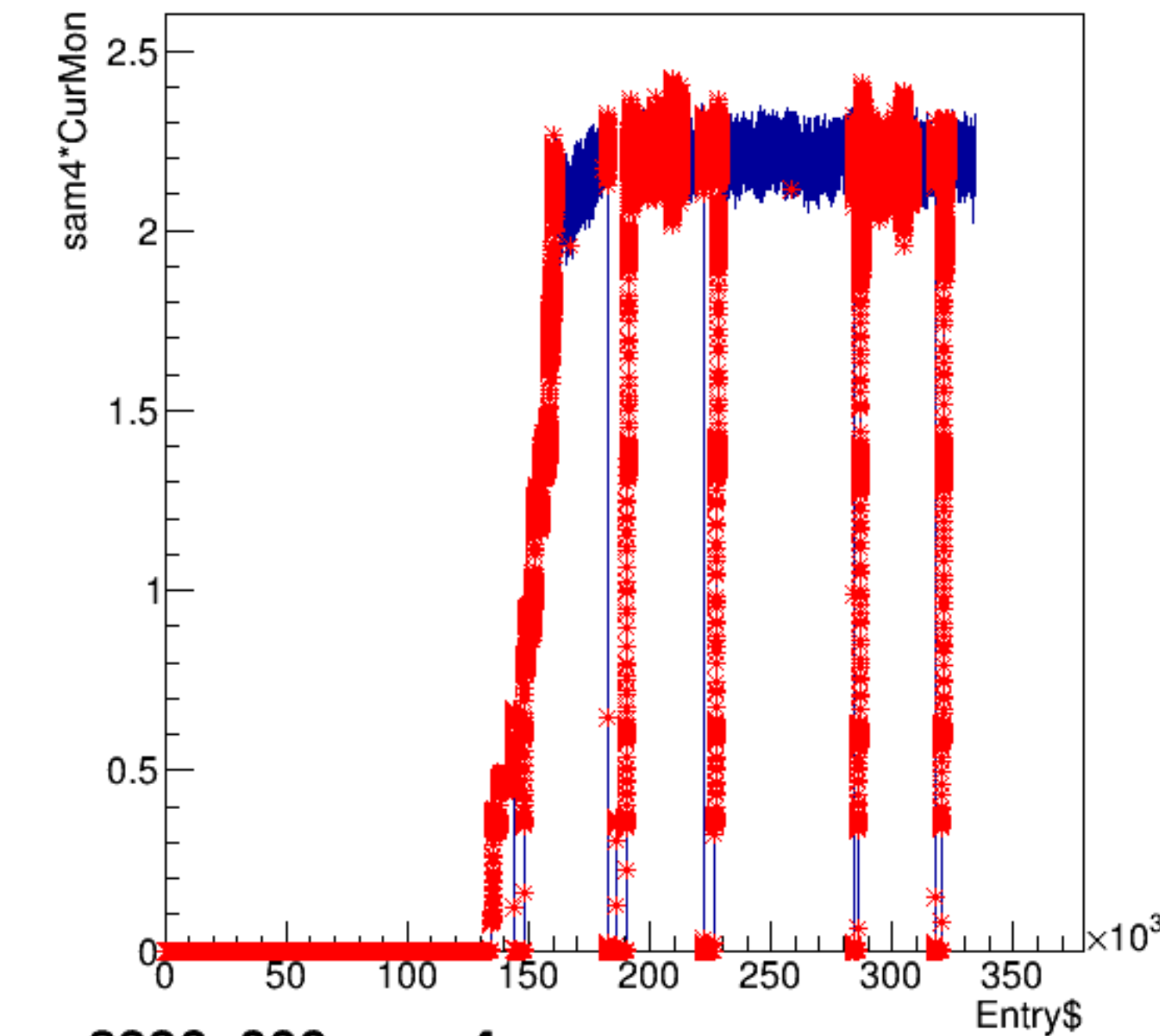
asym_sam2/ppm:pattern_number {ErrorFlag==0}



asym_sam2

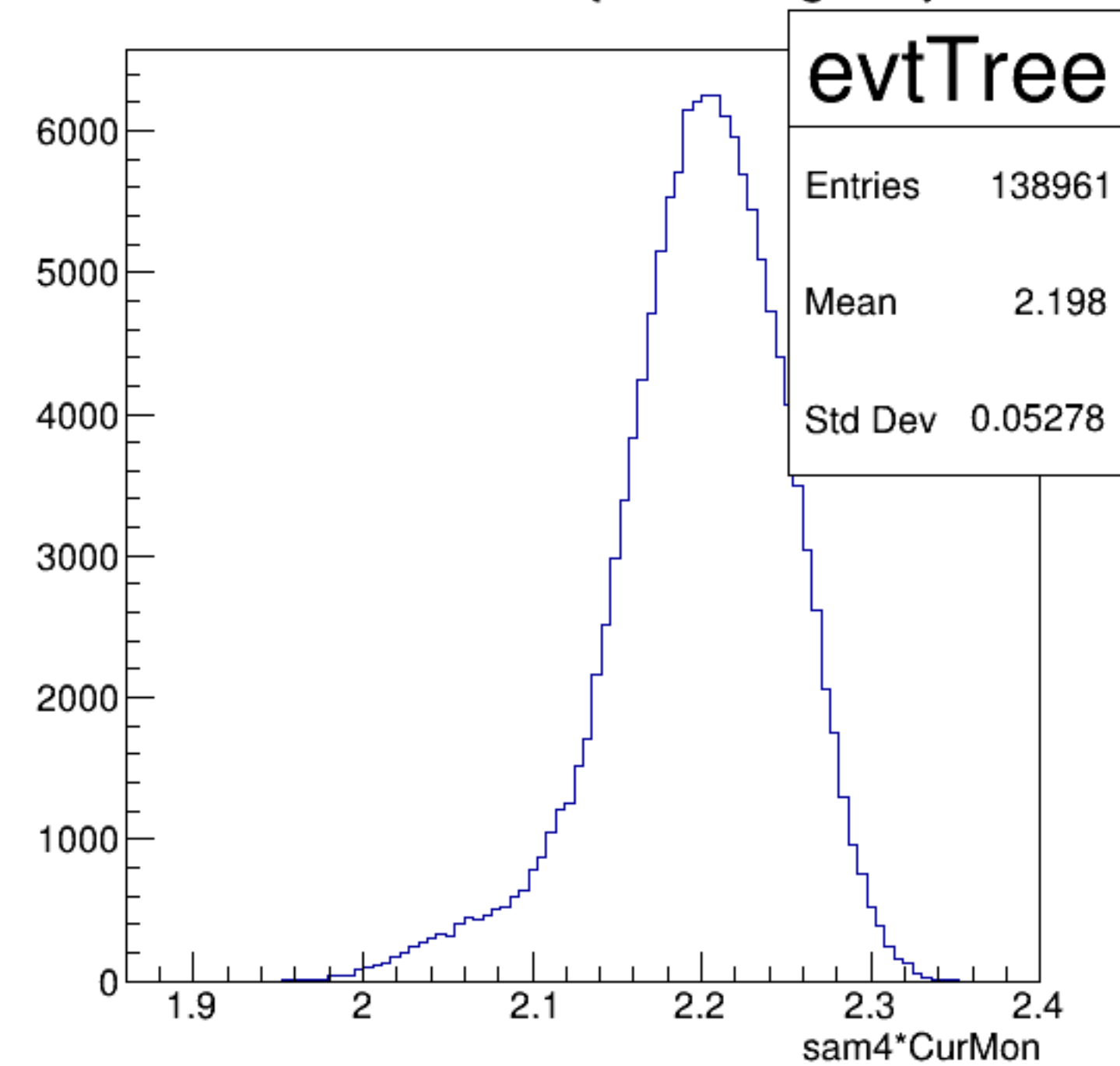


sam4*CurMon:Entry\$

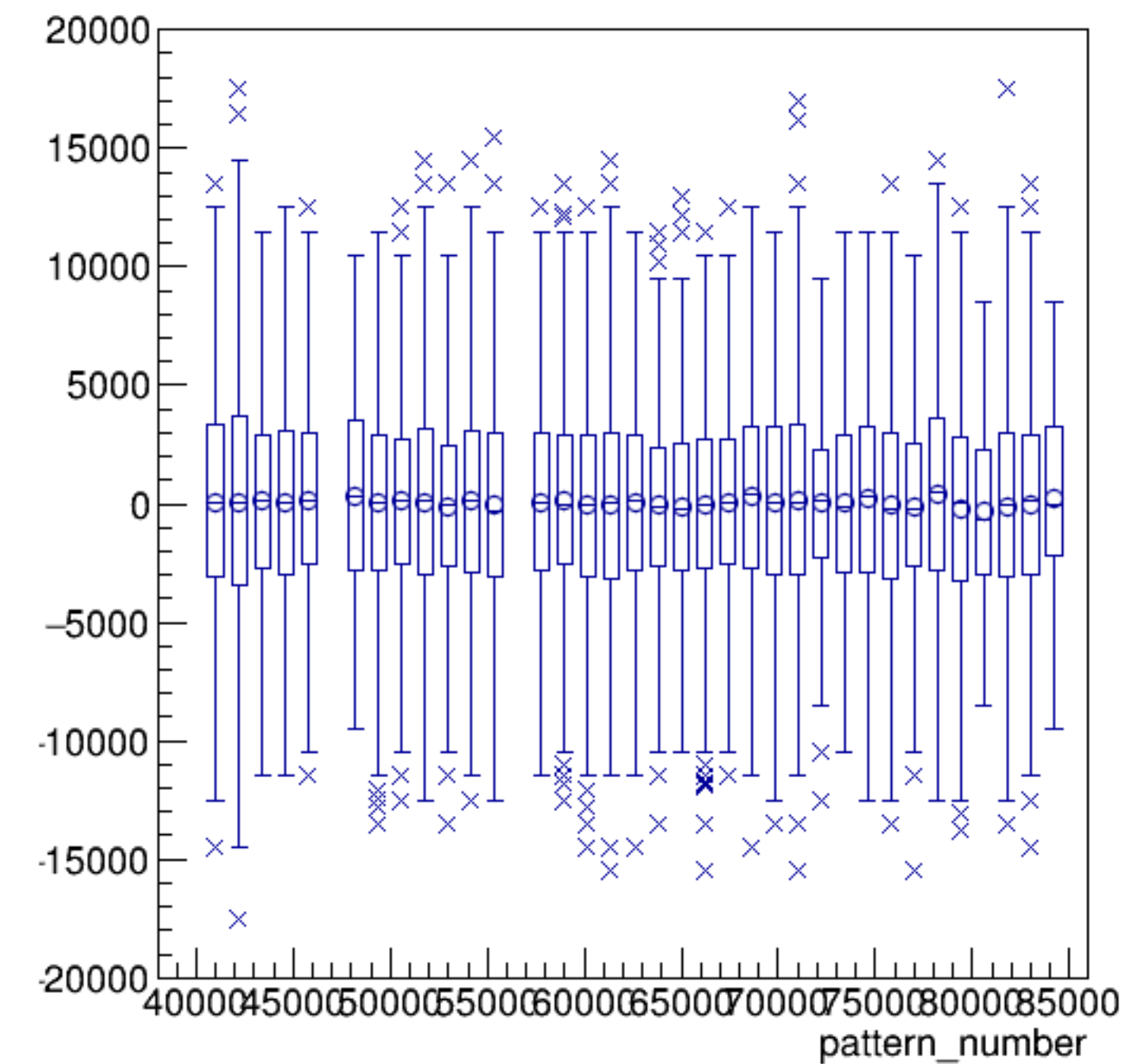


run8336_000_sam4.png

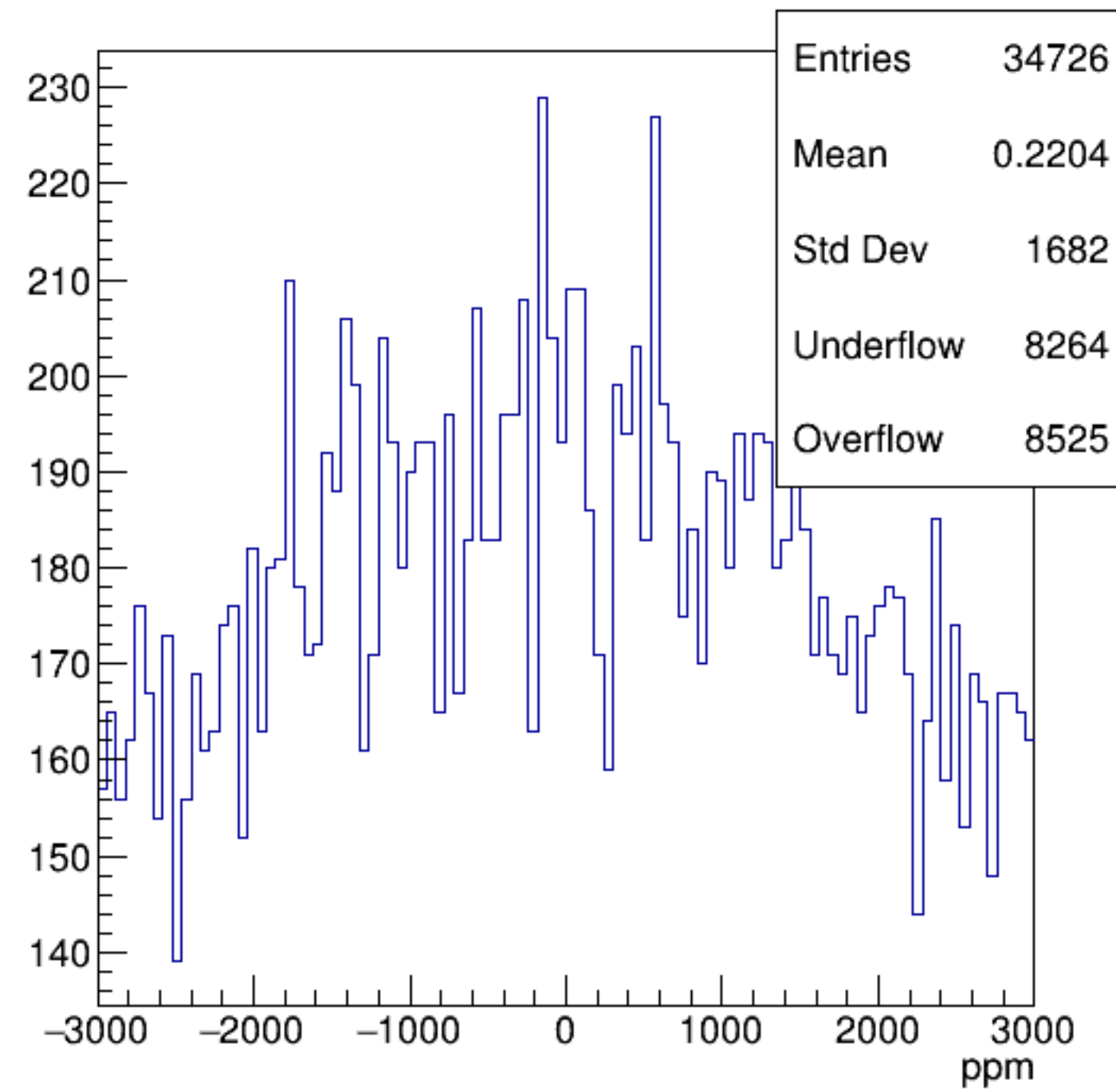
sam4*CurMon {ErrorFlag==0}



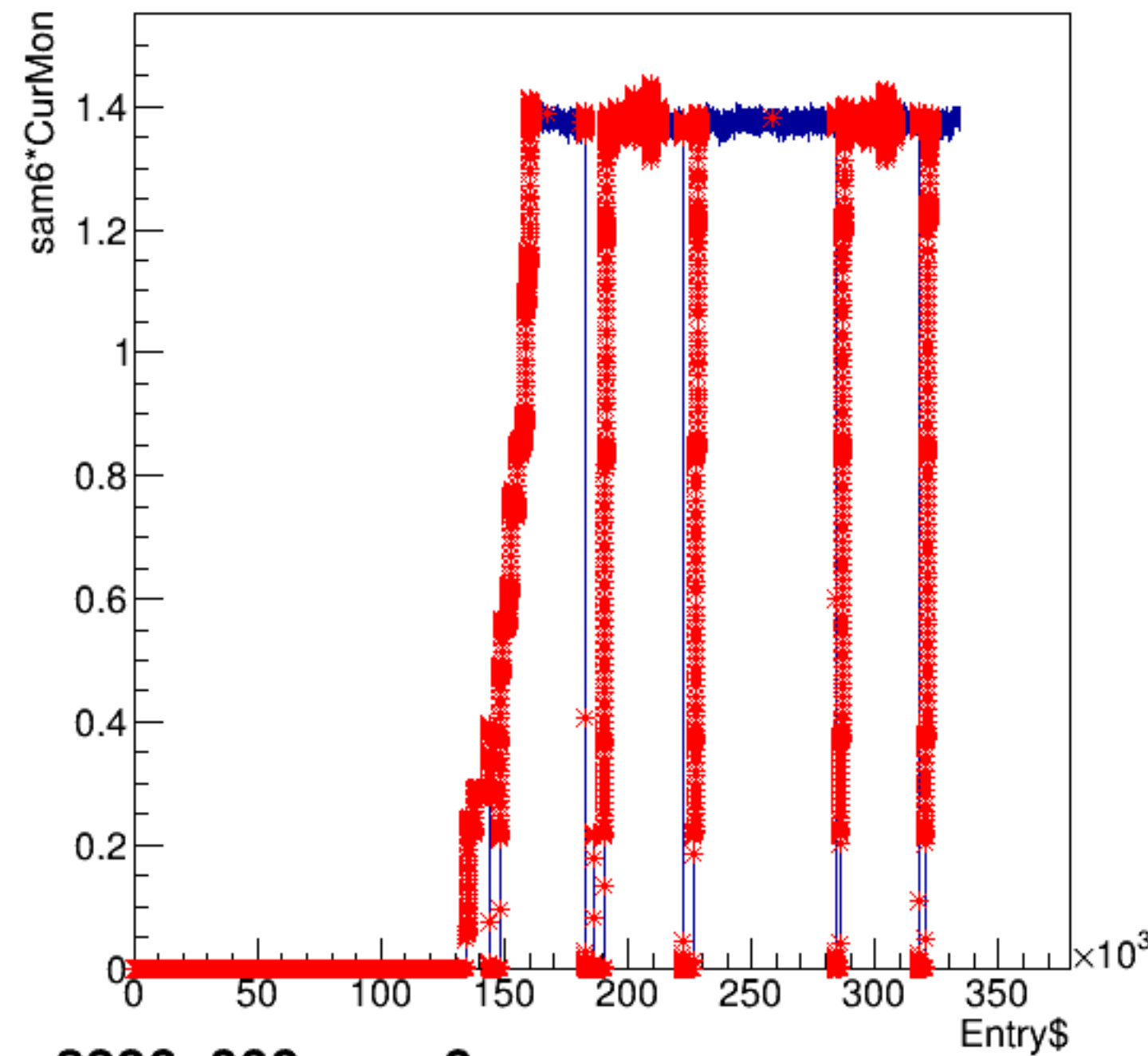
asym_sam4/ppm:pattern_number {ErrorFlag==0}



asym_sam4

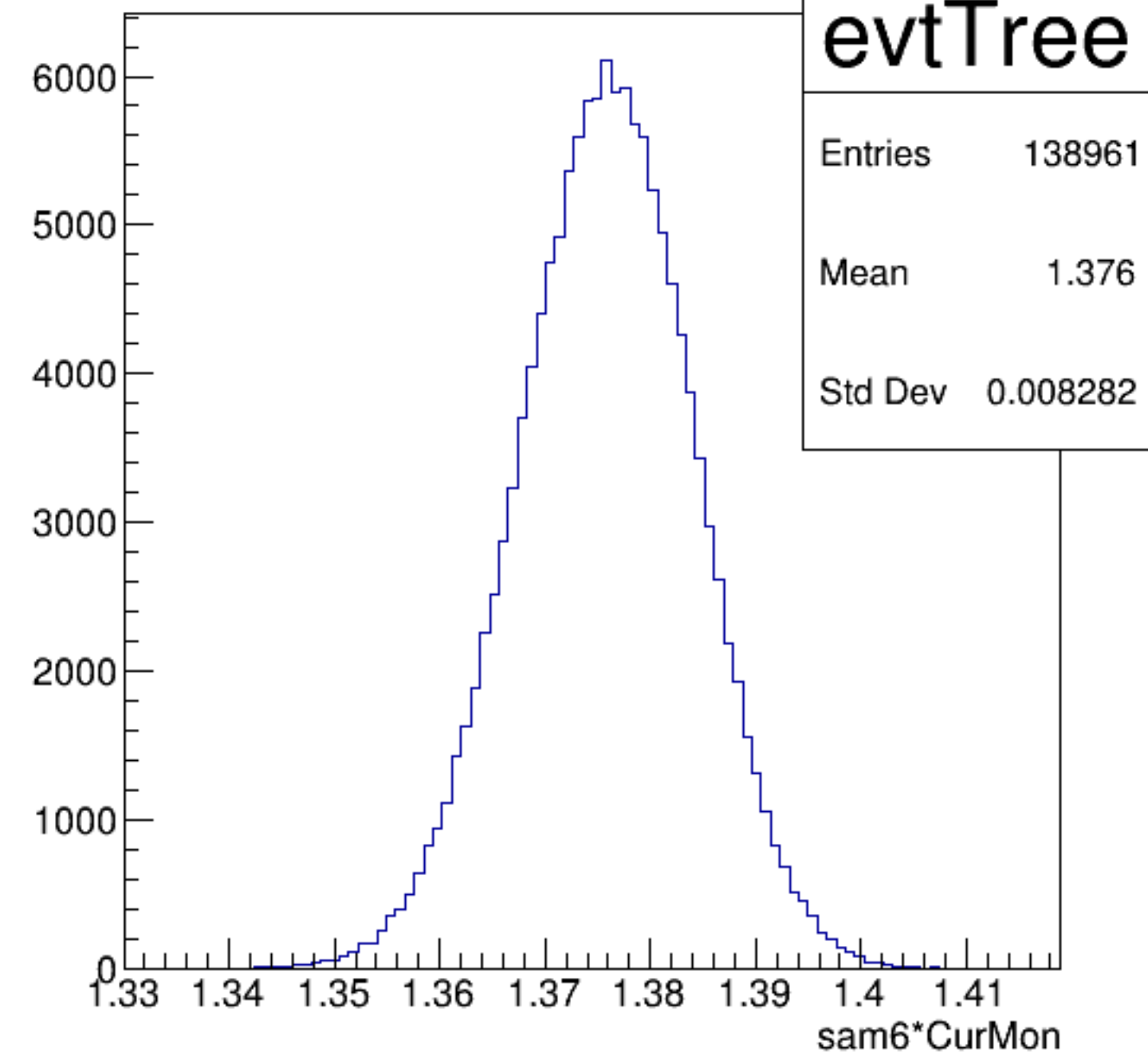


sam6*CurMon:Entry\$



run8336_000_sam6.png

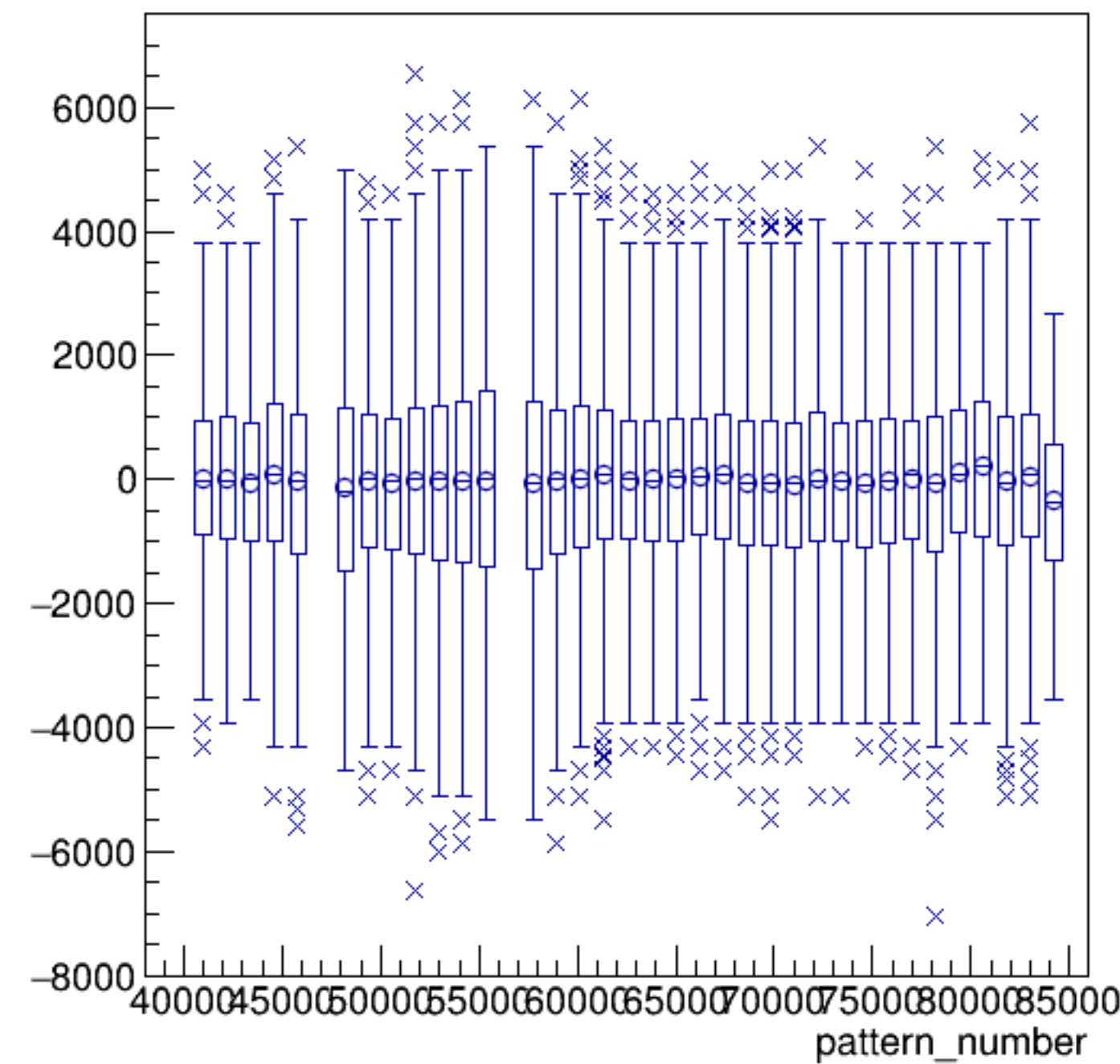
sam6*CurMon {ErrorFlag==0}



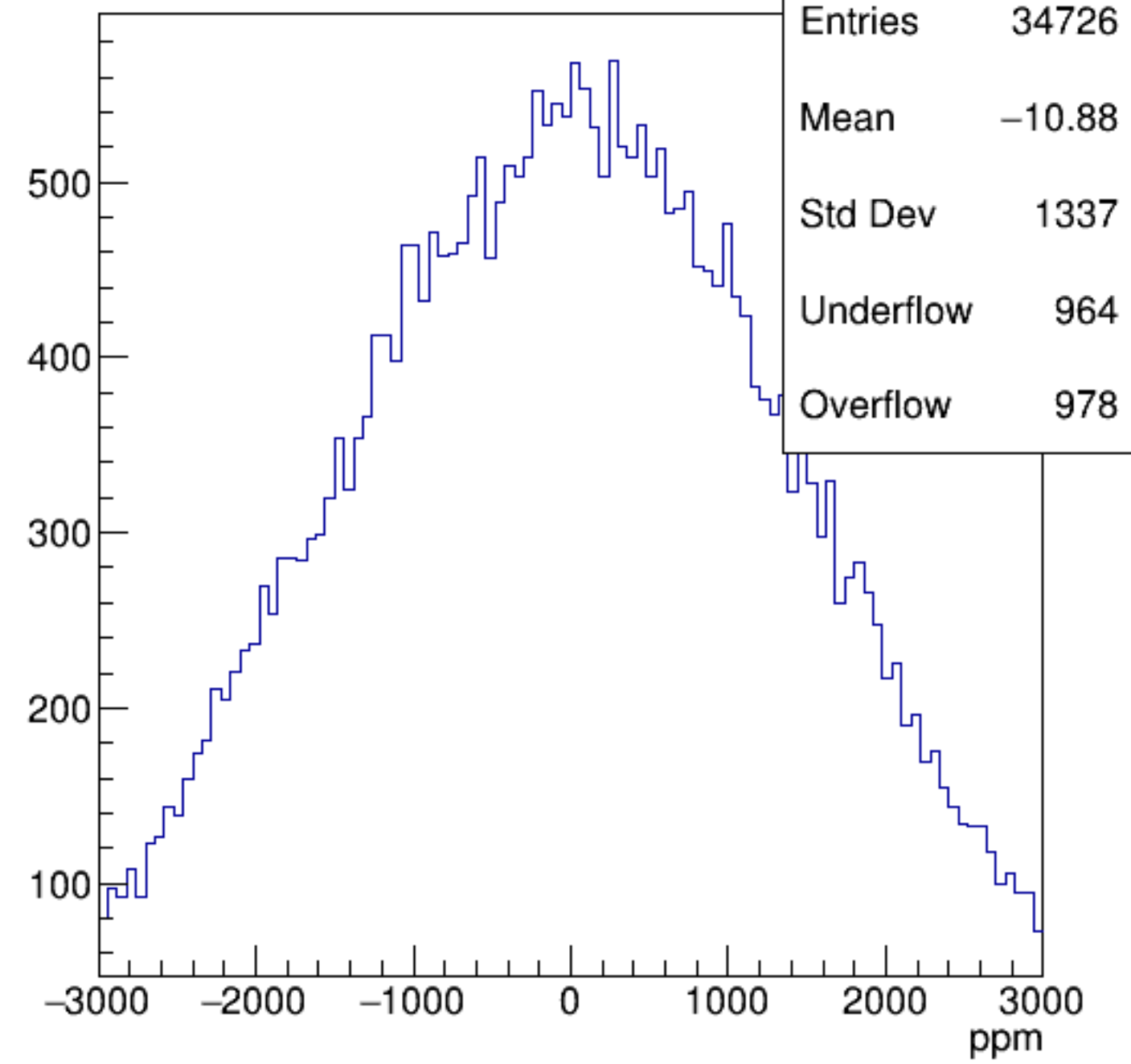
evtTree

Entries	138961
Mean	1.376
Std Dev	0.008282

asym_sam6/ppm:pattern_number {ErrorFlag==0}

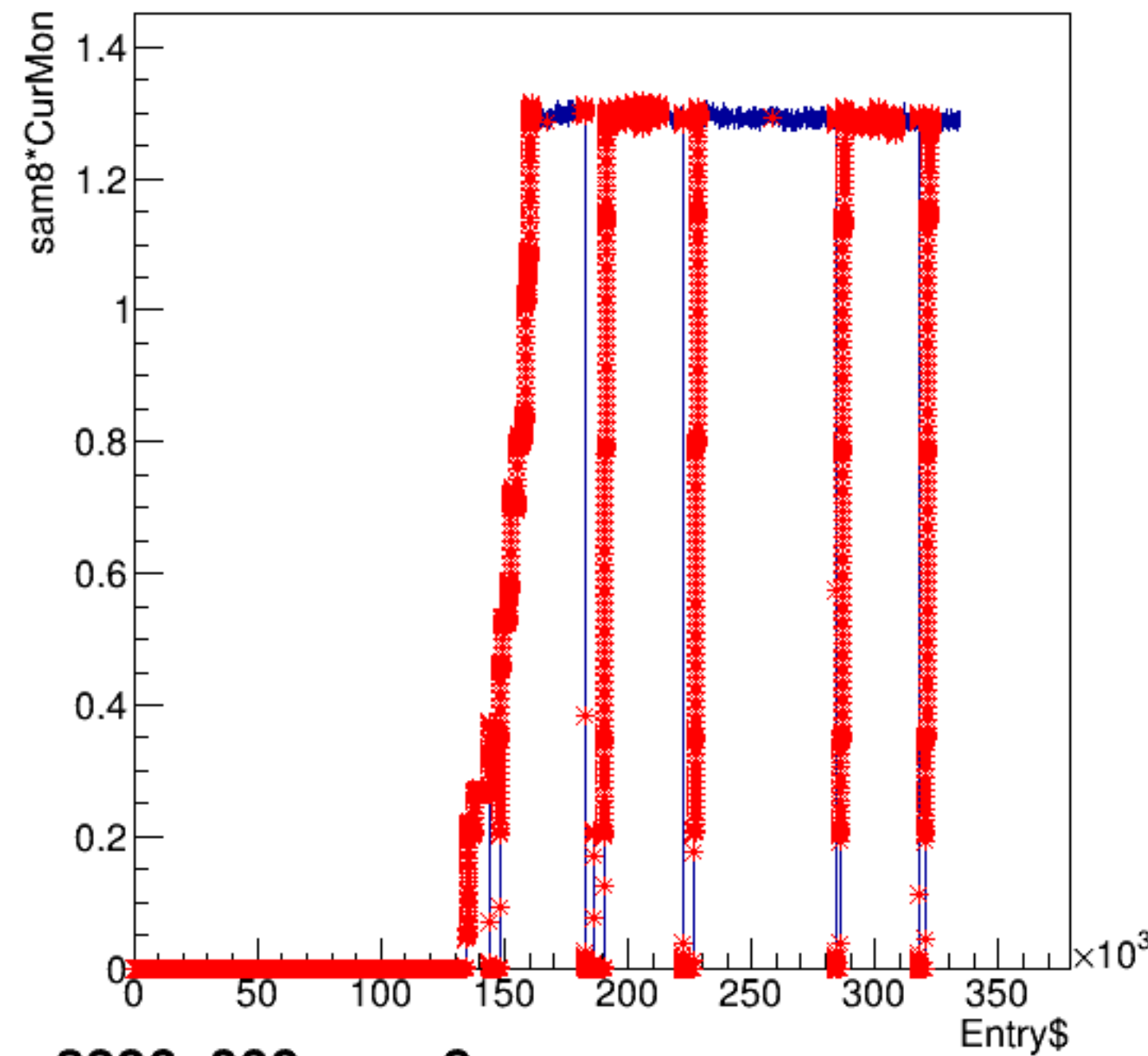


asym_sam6



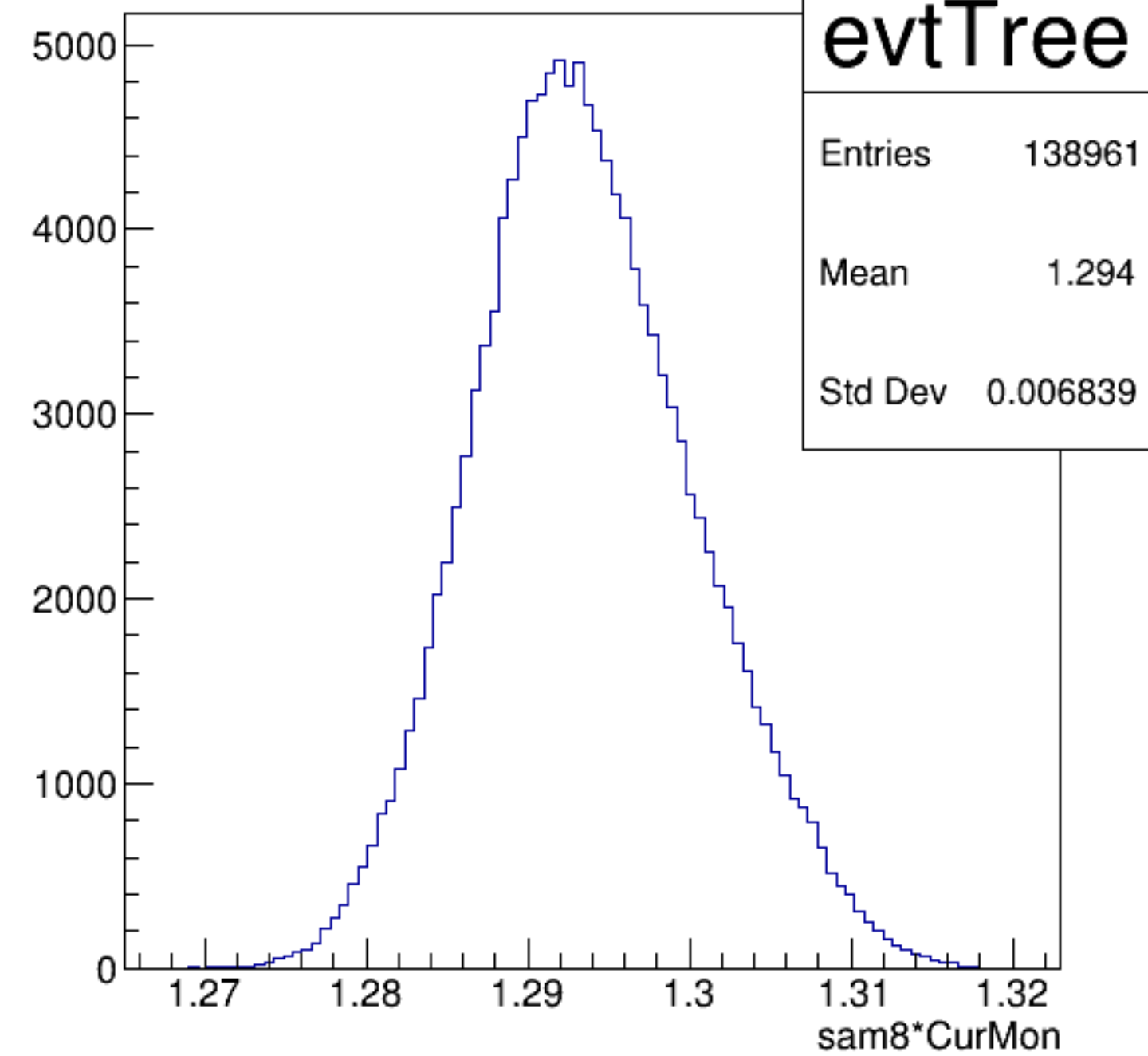
Entries	34726
Mean	-10.88
Std Dev	1337
Underflow	964
Overflow	978

sam8*CurMon:Entry\$



run8336_000_sam8.png

sam8*CurMon {ErrorFlag==0}



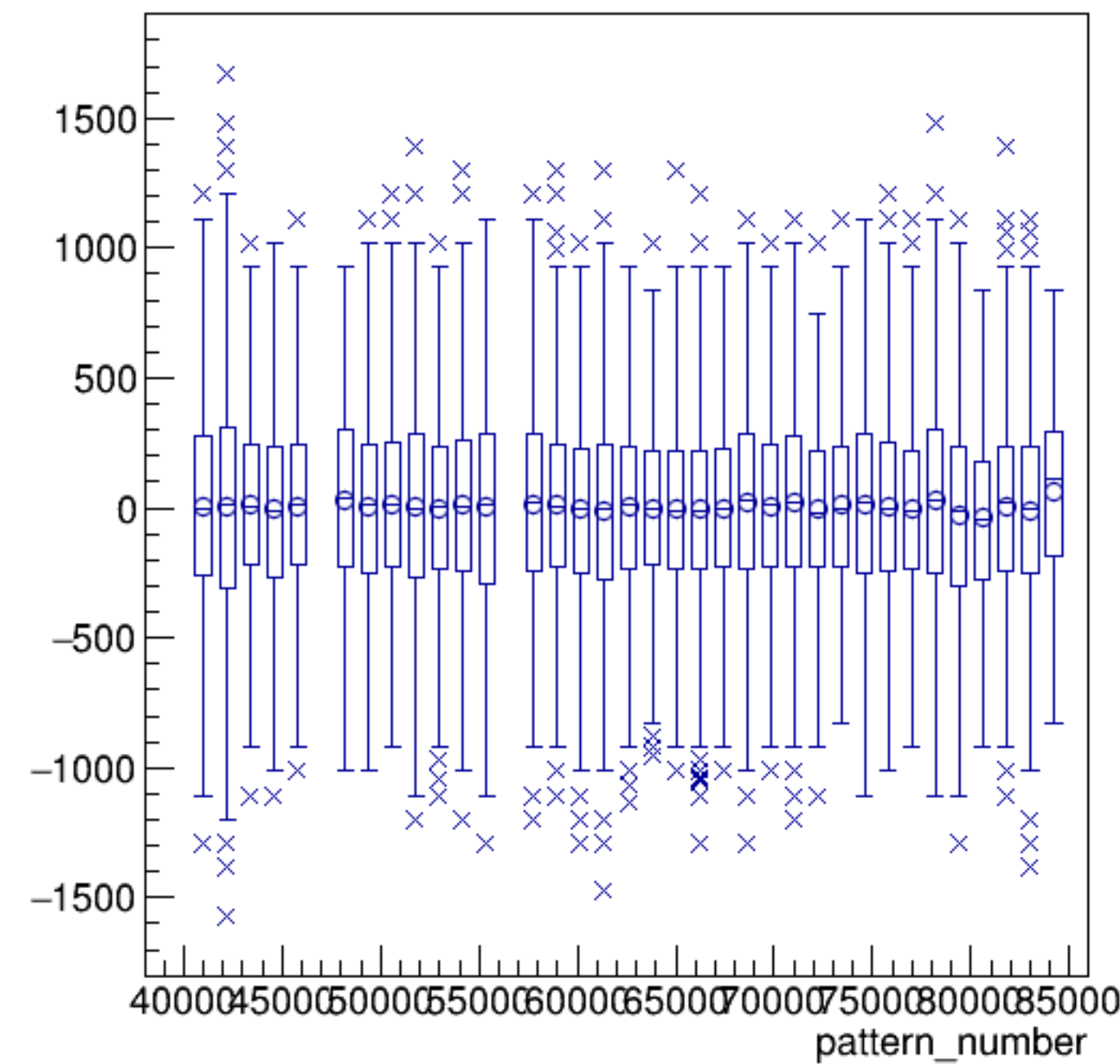
evtTree

Entries 138961

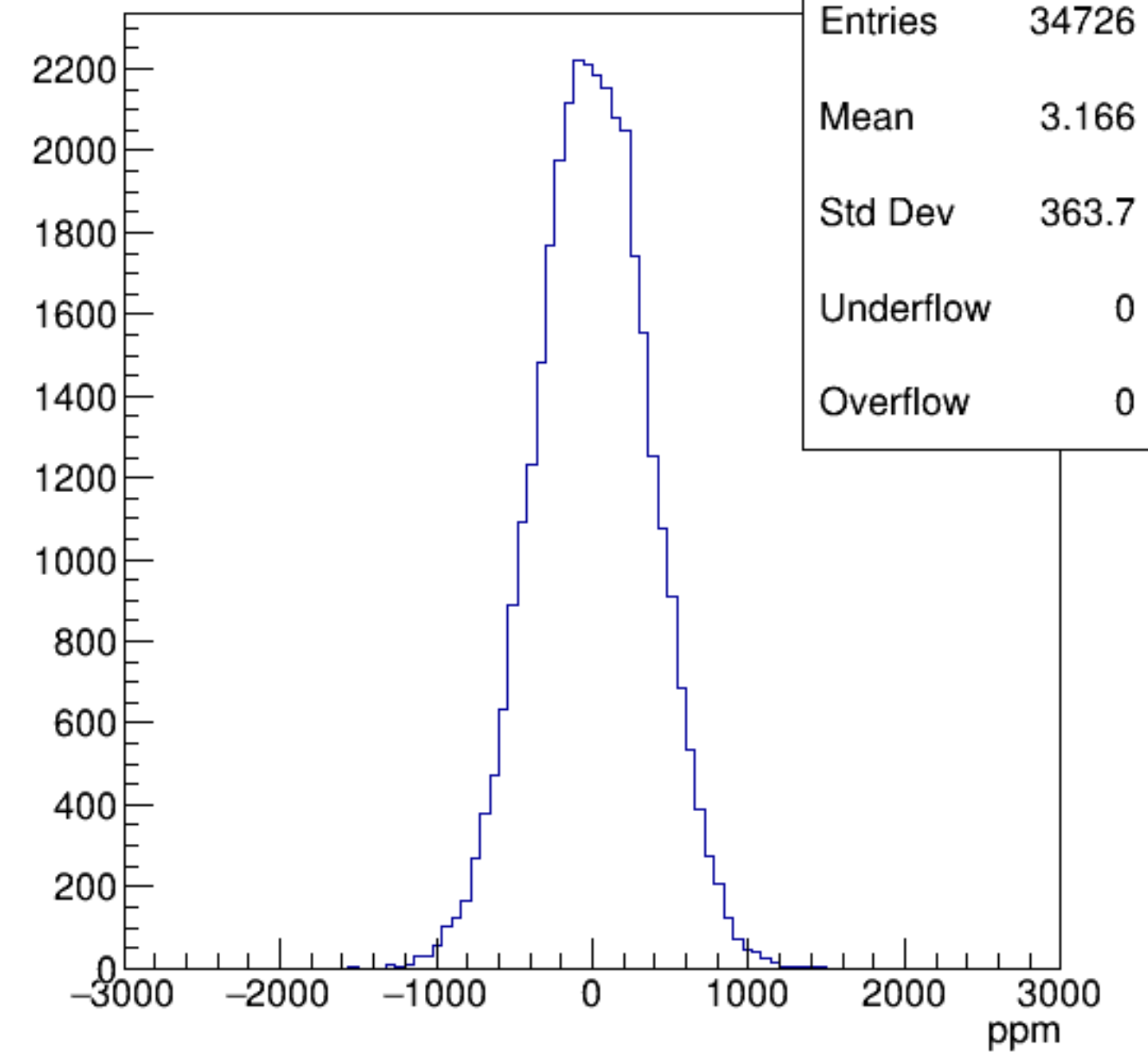
Mean 1.294

Std Dev 0.006839

asym_sam8/ppm:pattern_number {ErrorFlag==0}

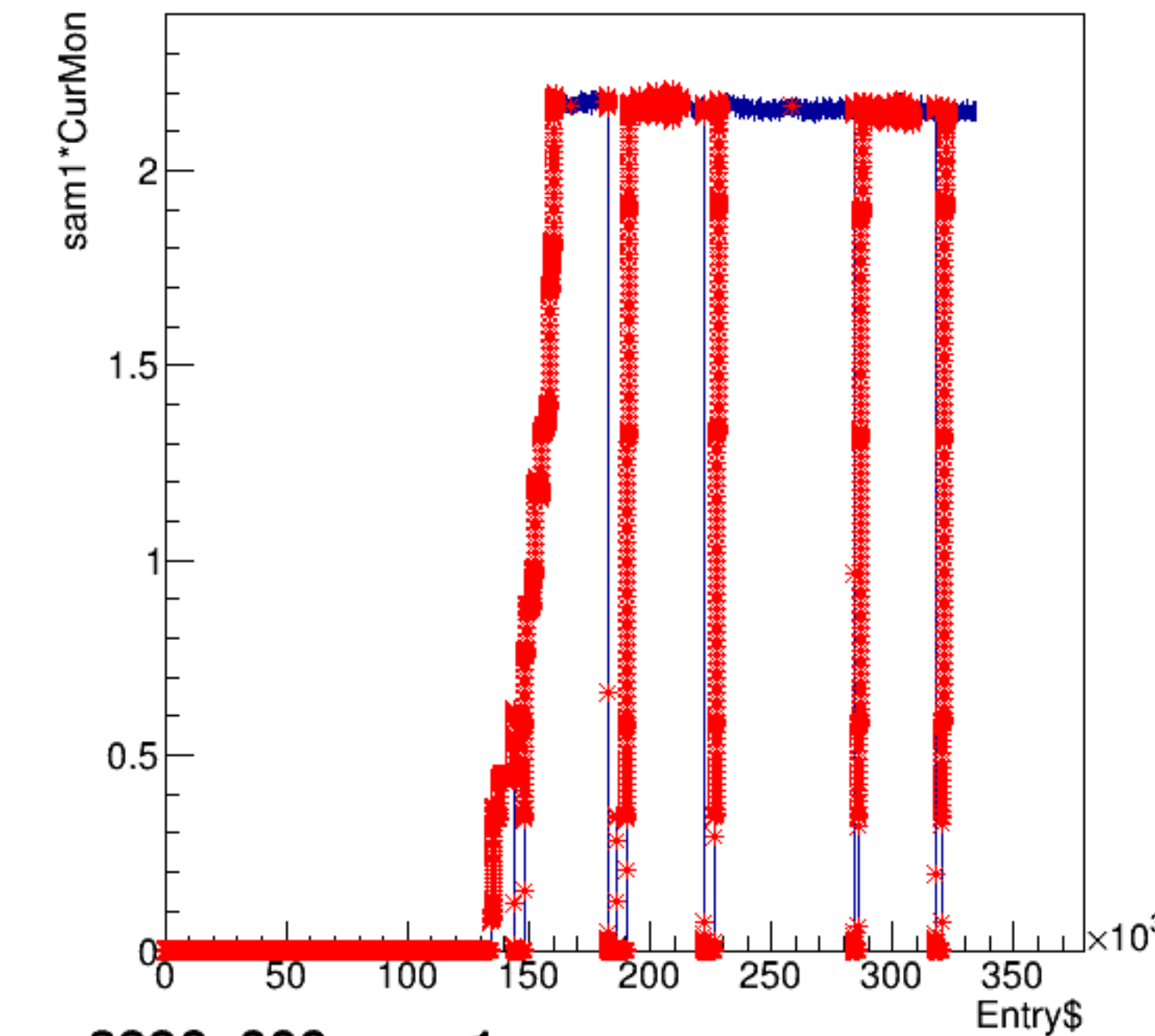


asym_sam8



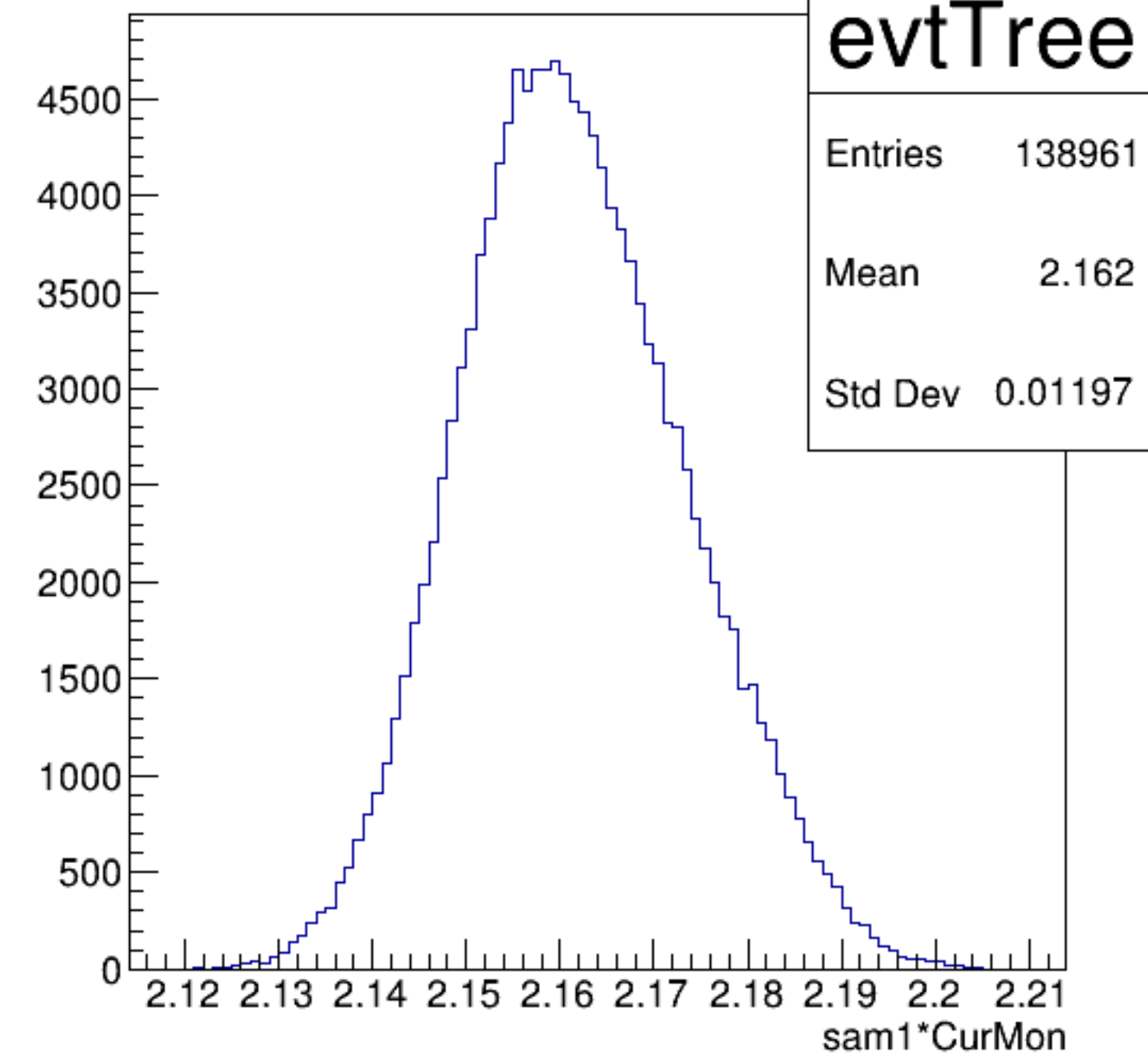
Entries	34726
Mean	3.166
Std Dev	363.7
Underflow	0
Overflow	0

sam1*CurMon:Entry\$



run8336_000_sam1.png

sam1*CurMon {ErrorFlag==0}



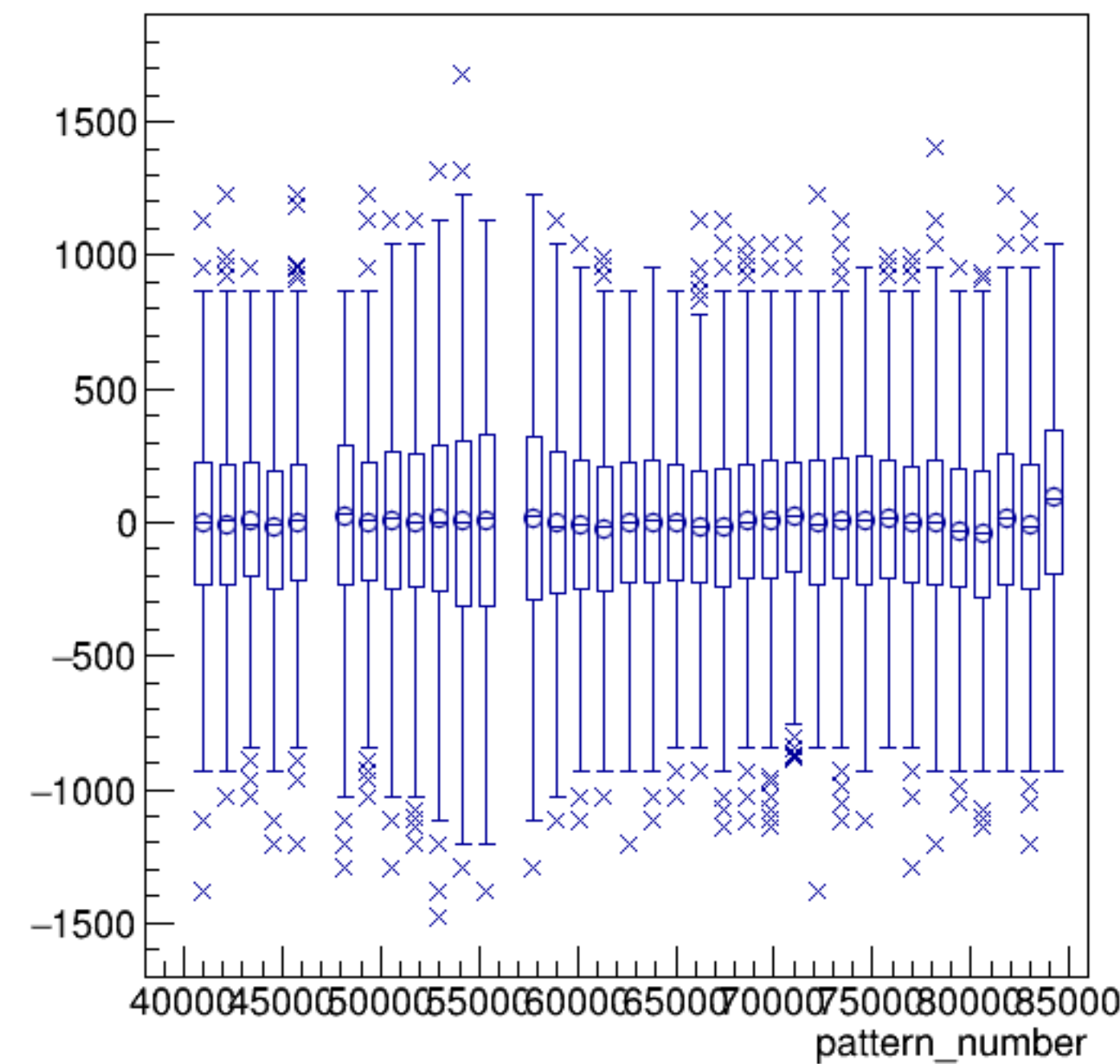
evtTree

Entries 138961

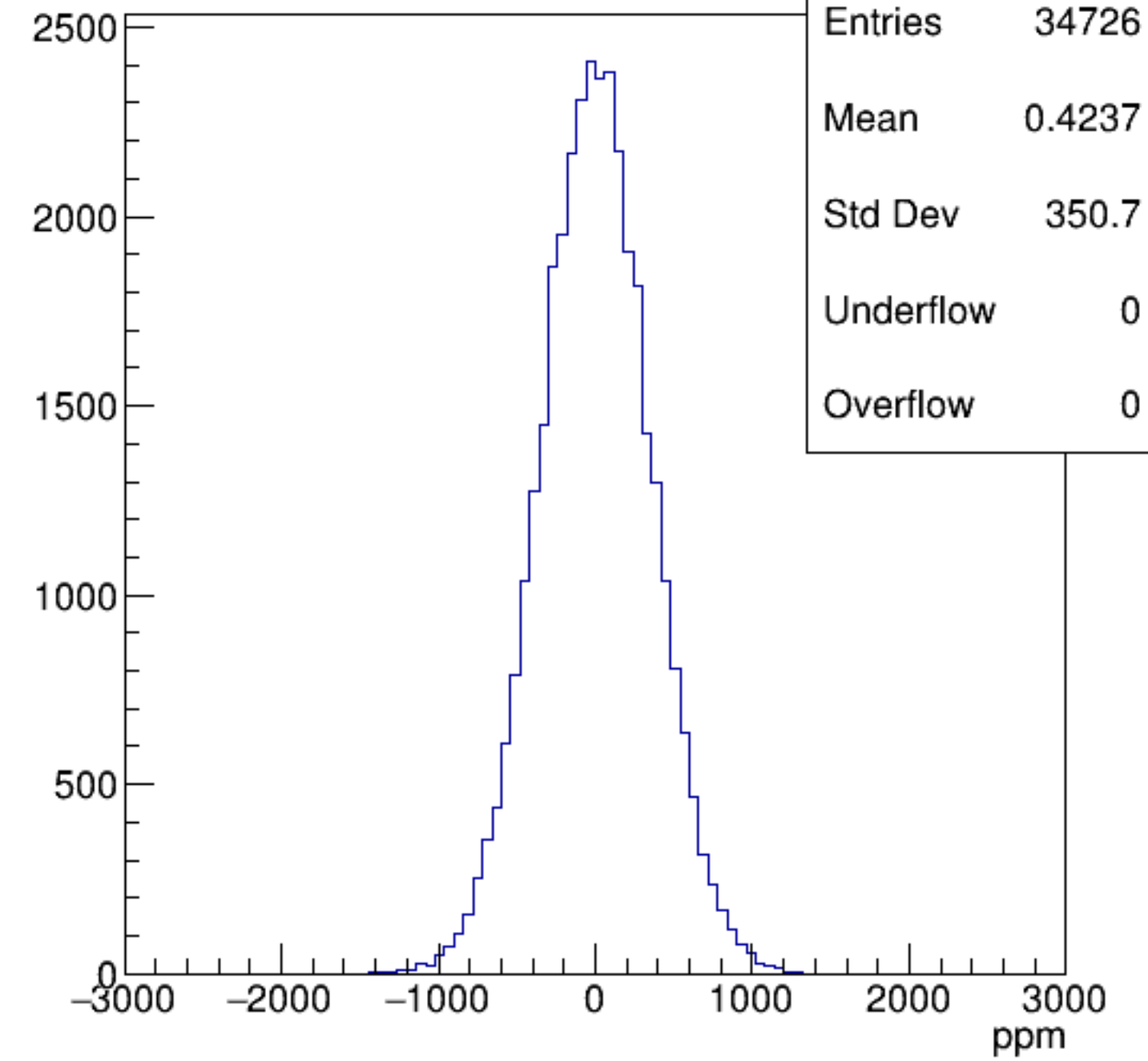
Mean 2.162

Std Dev 0.01197

asym_sam1/ppm:pattern_number {ErrorFlag==0}



asym_sam1



Entries 34726

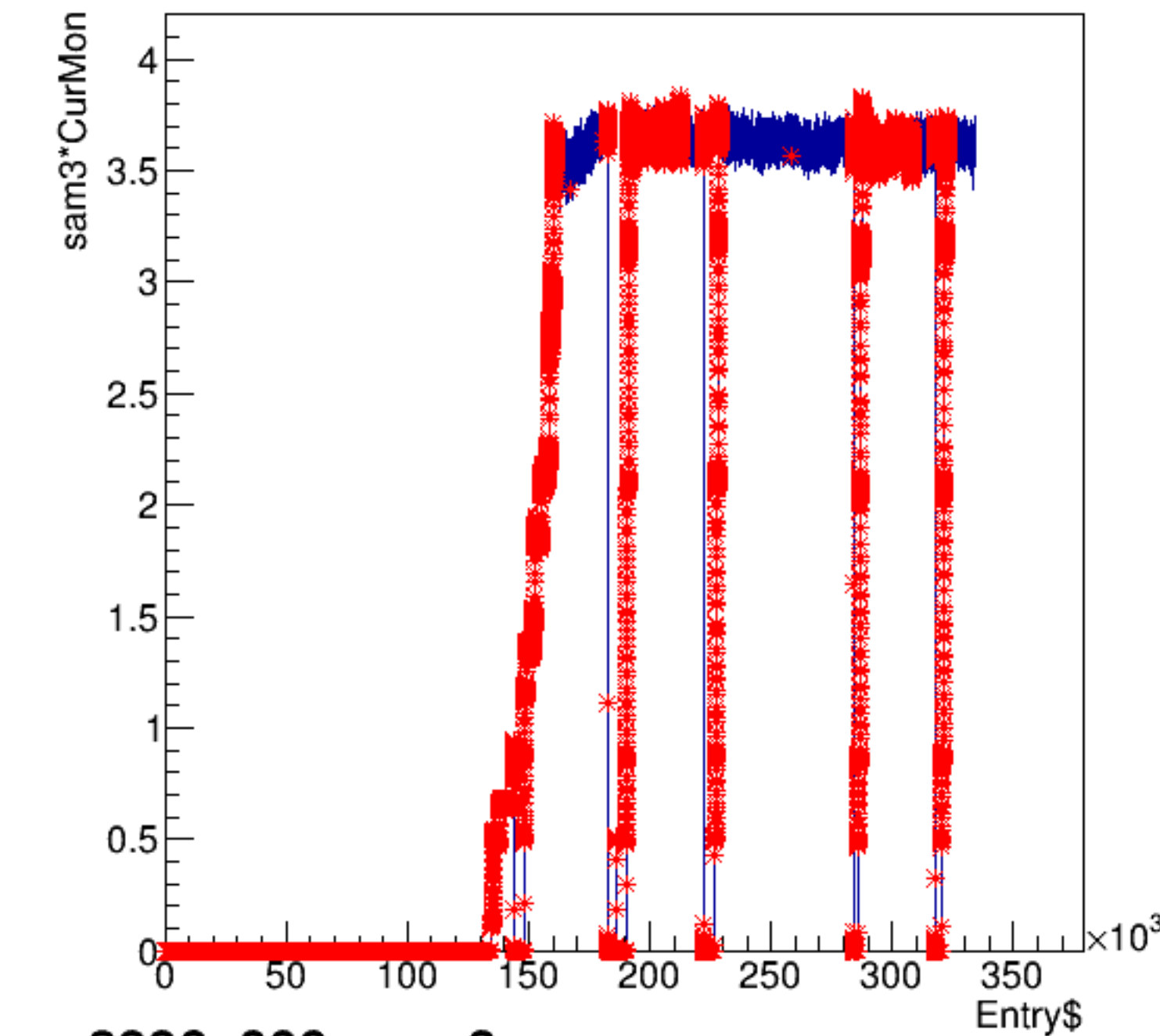
Mean 0.4237

Std Dev 350.7

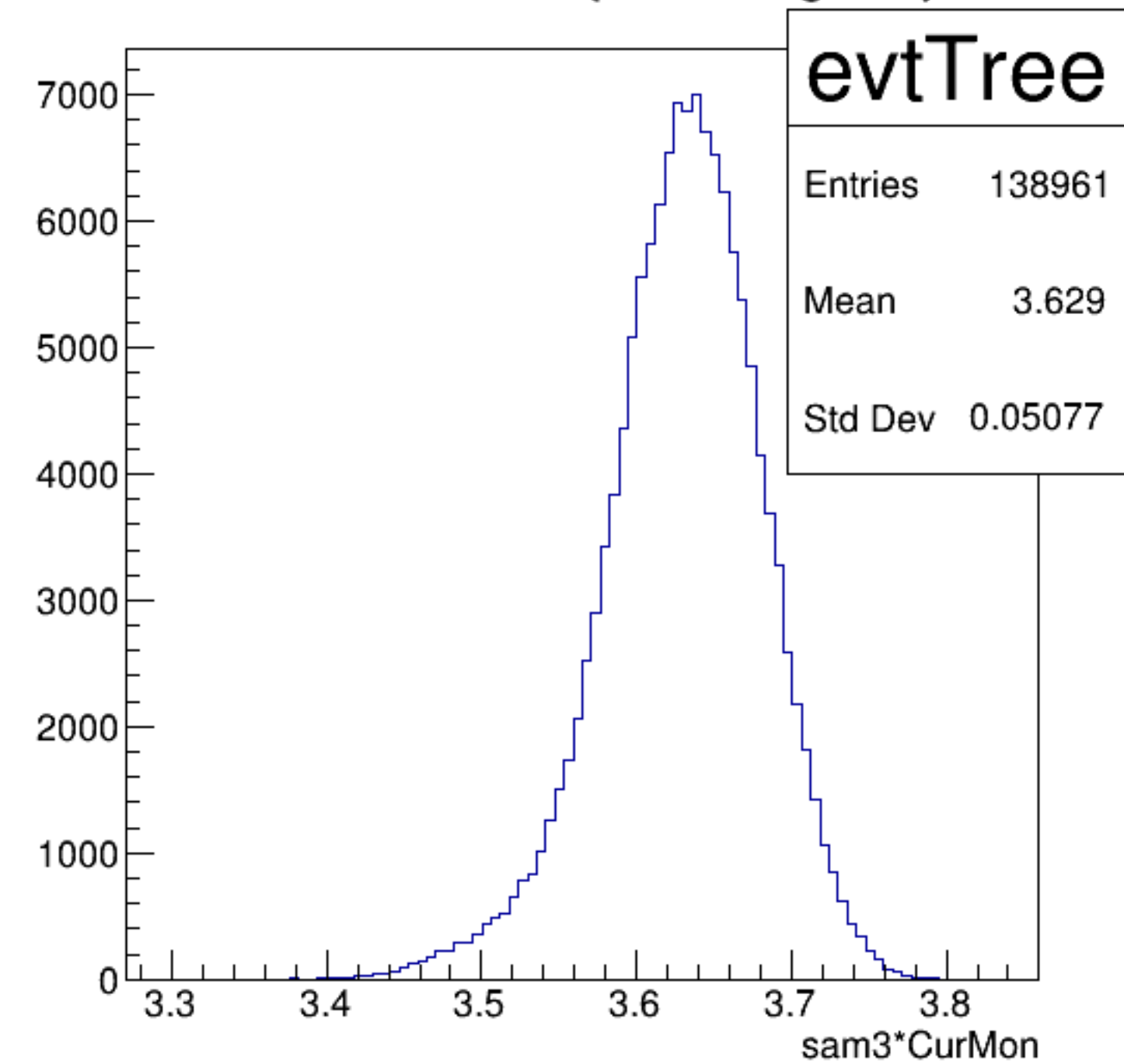
Underflow 0

Overflow 0

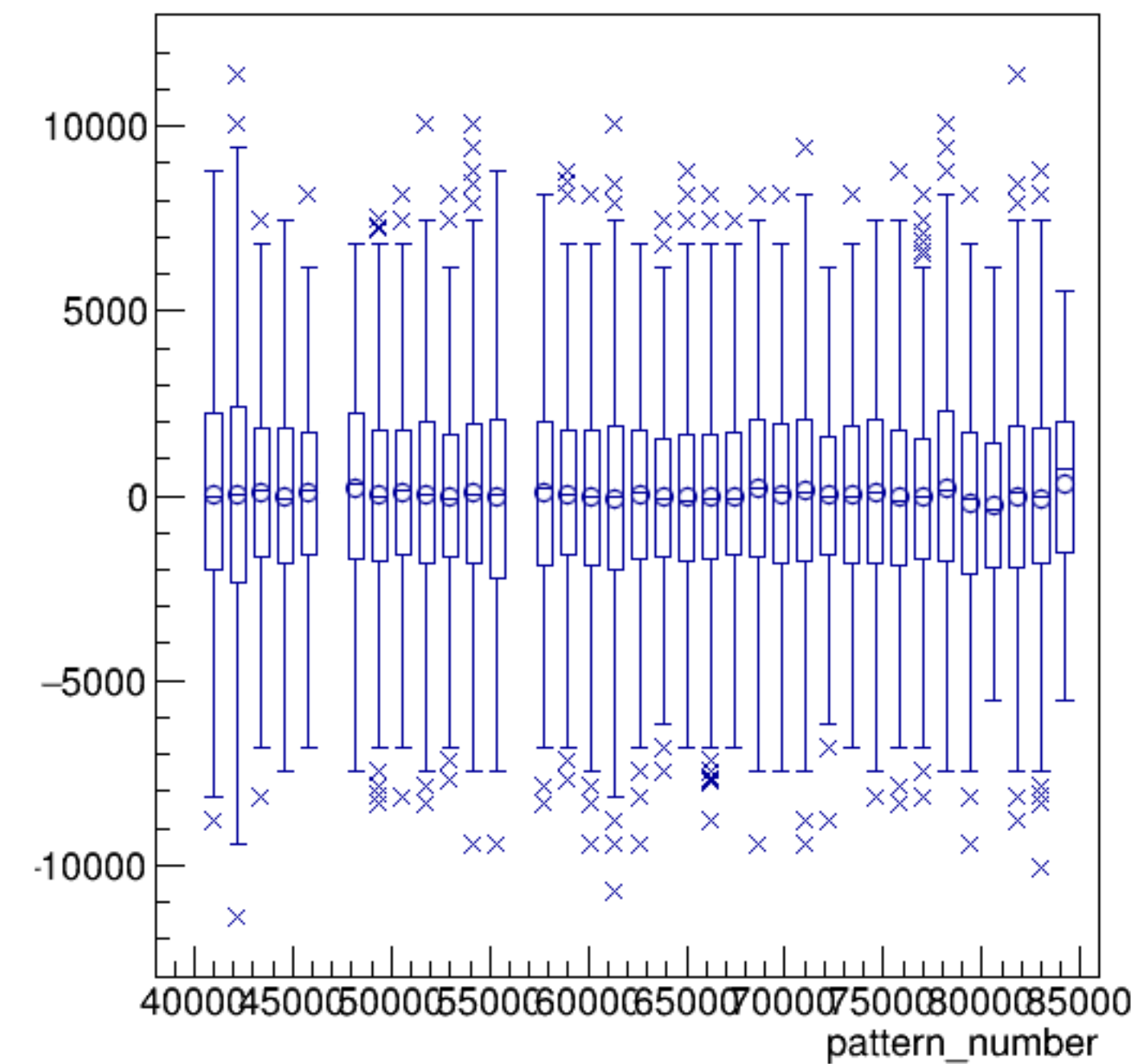
sam3*CurMon:Entry\$



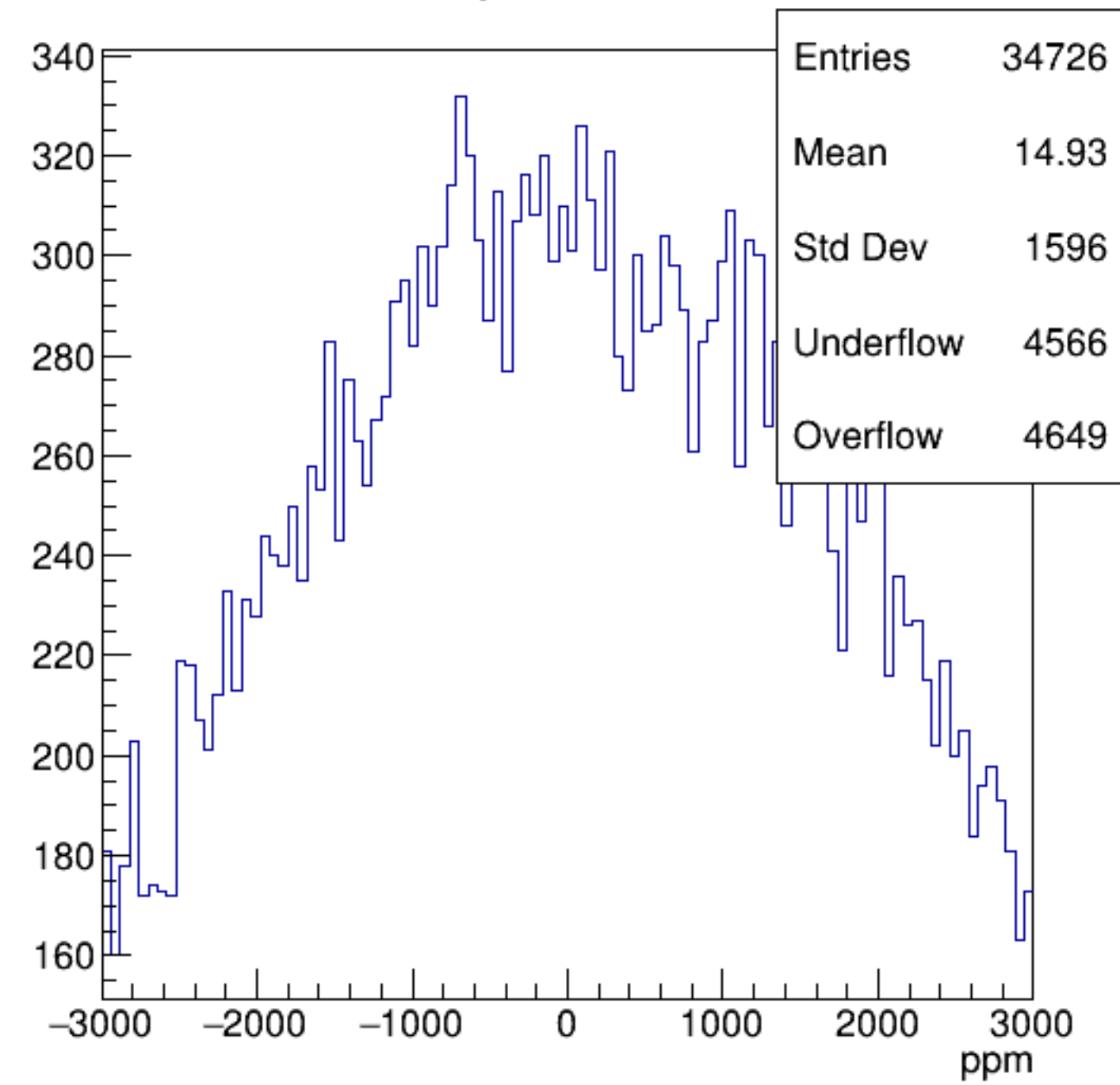
sam3*CurMon {ErrorFlag==0}



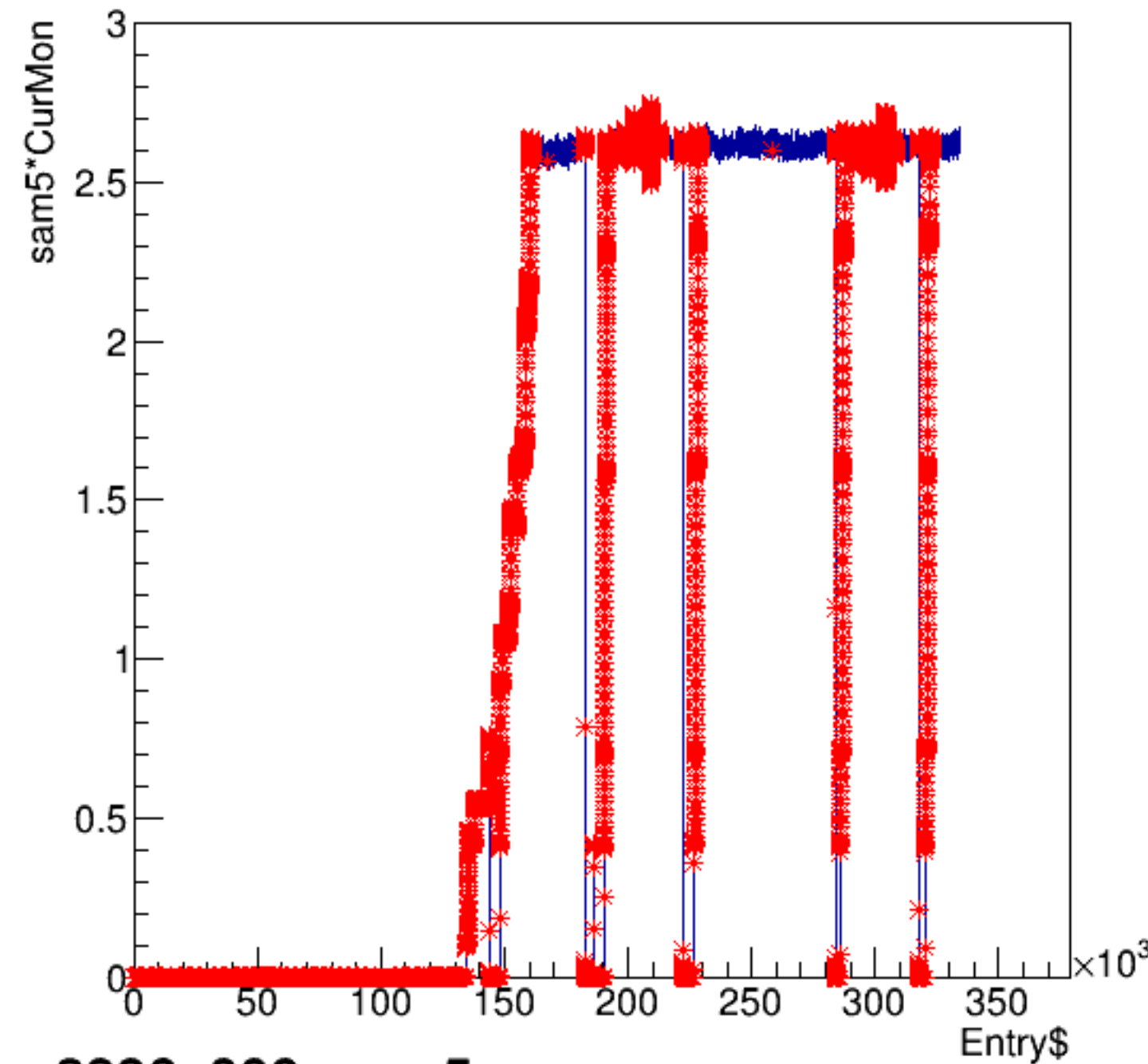
asym_sam3/ppm:pattern_number {ErrorFlag==0}



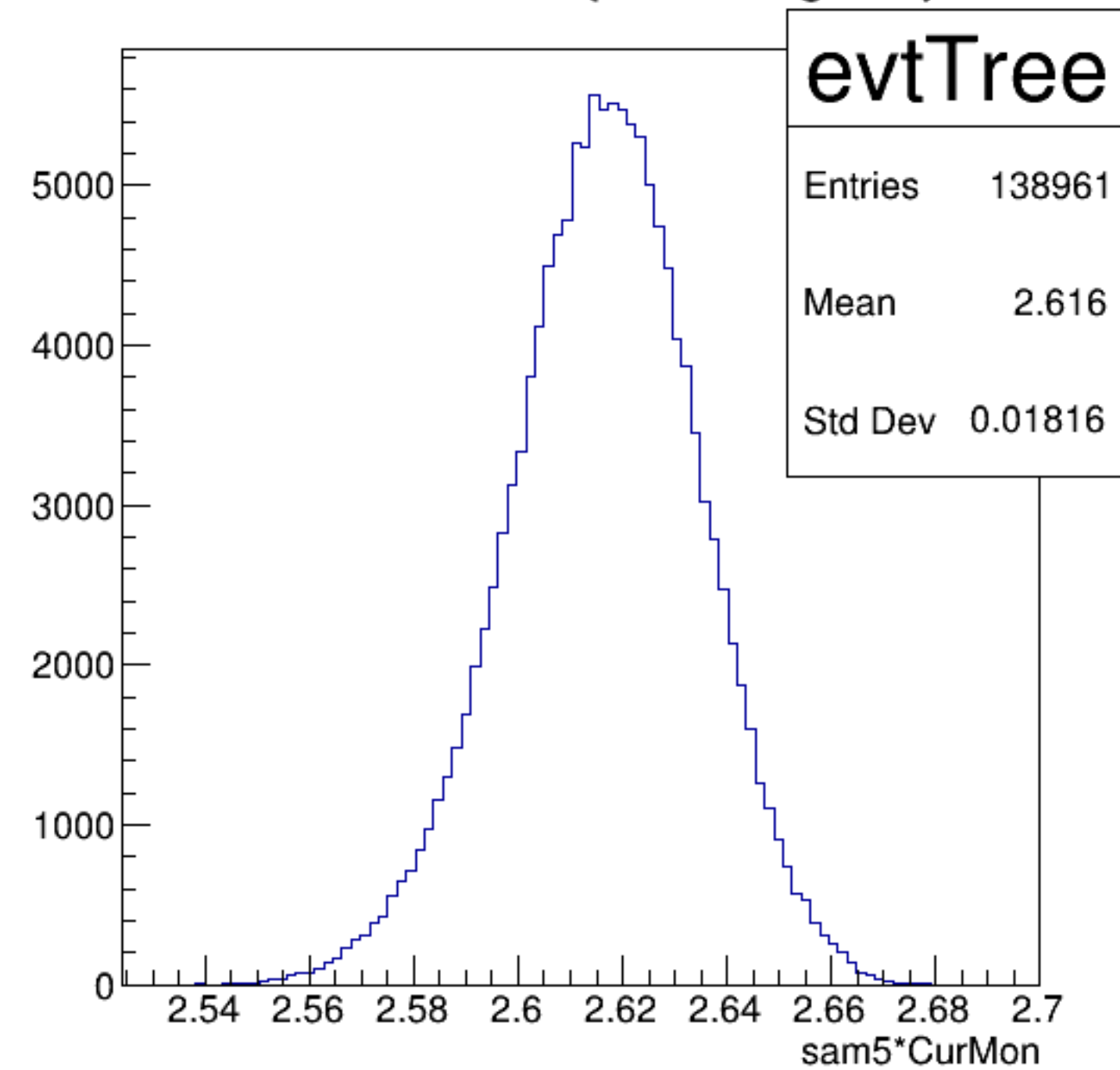
asym_sam3



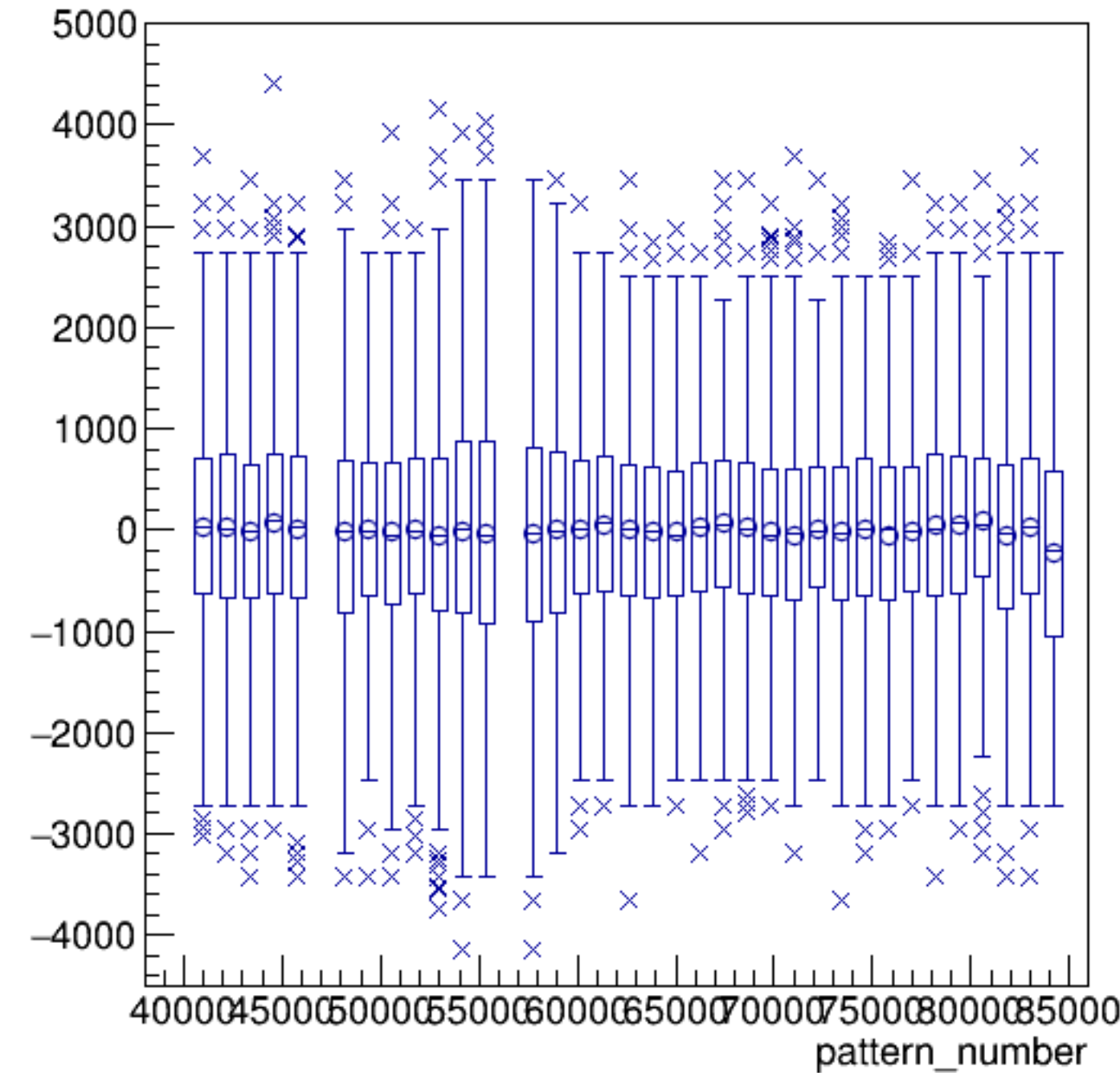
sam5*CurMon:Entry\$



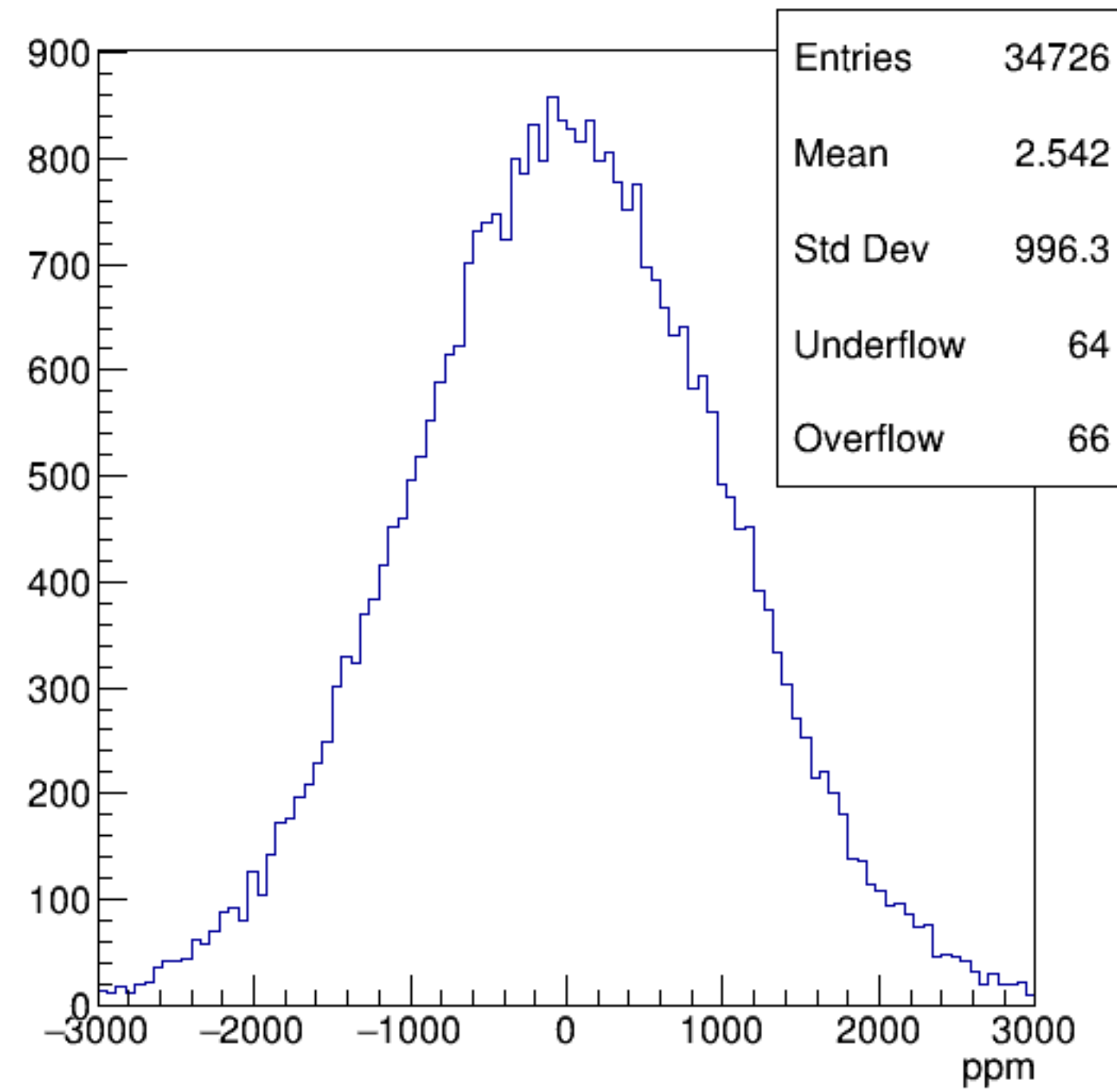
sam5*CurMon {ErrorFlag==0}



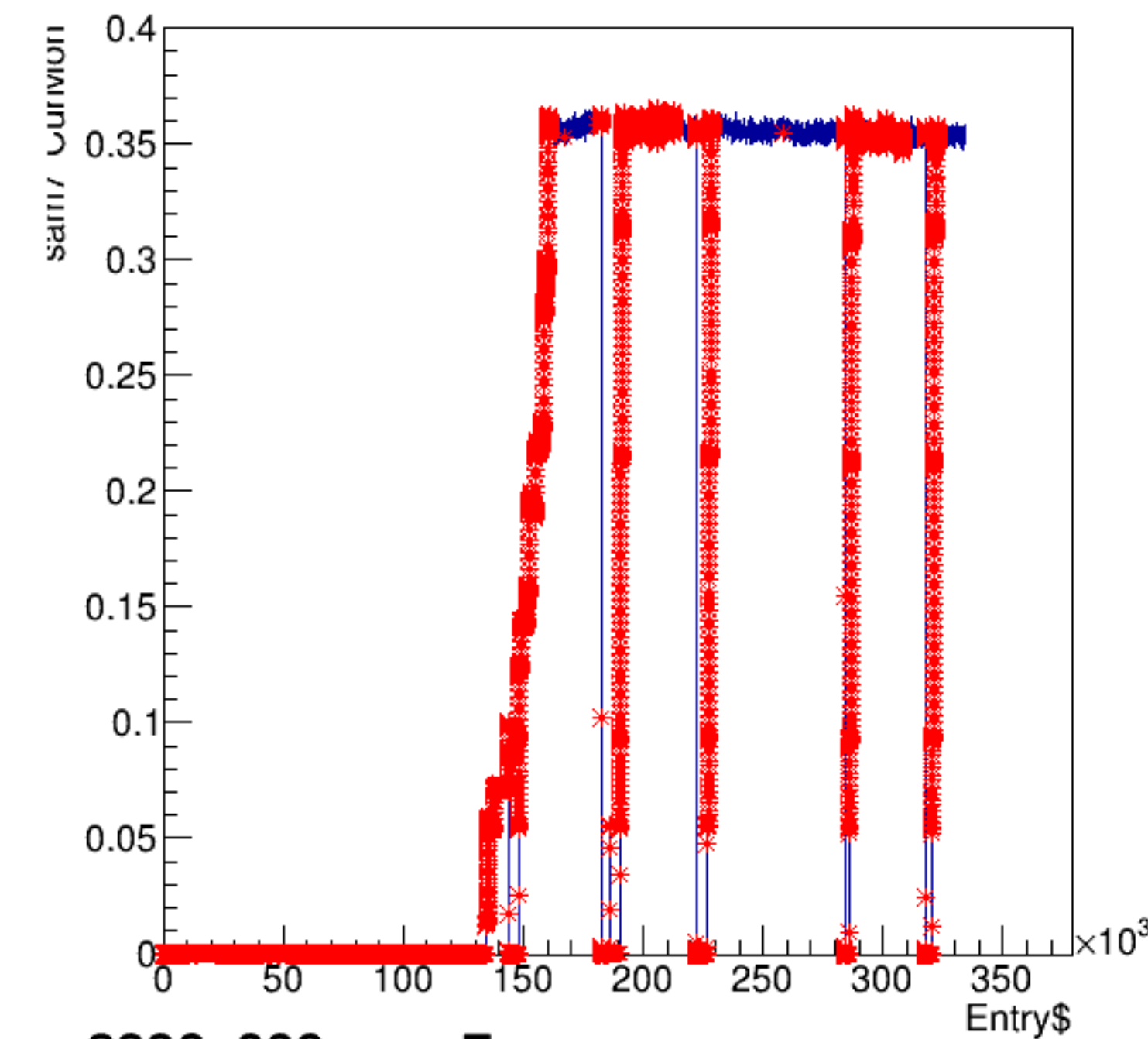
asym_sam5/ppm:pattern_number {ErrorFlag==0}



asym_sam5

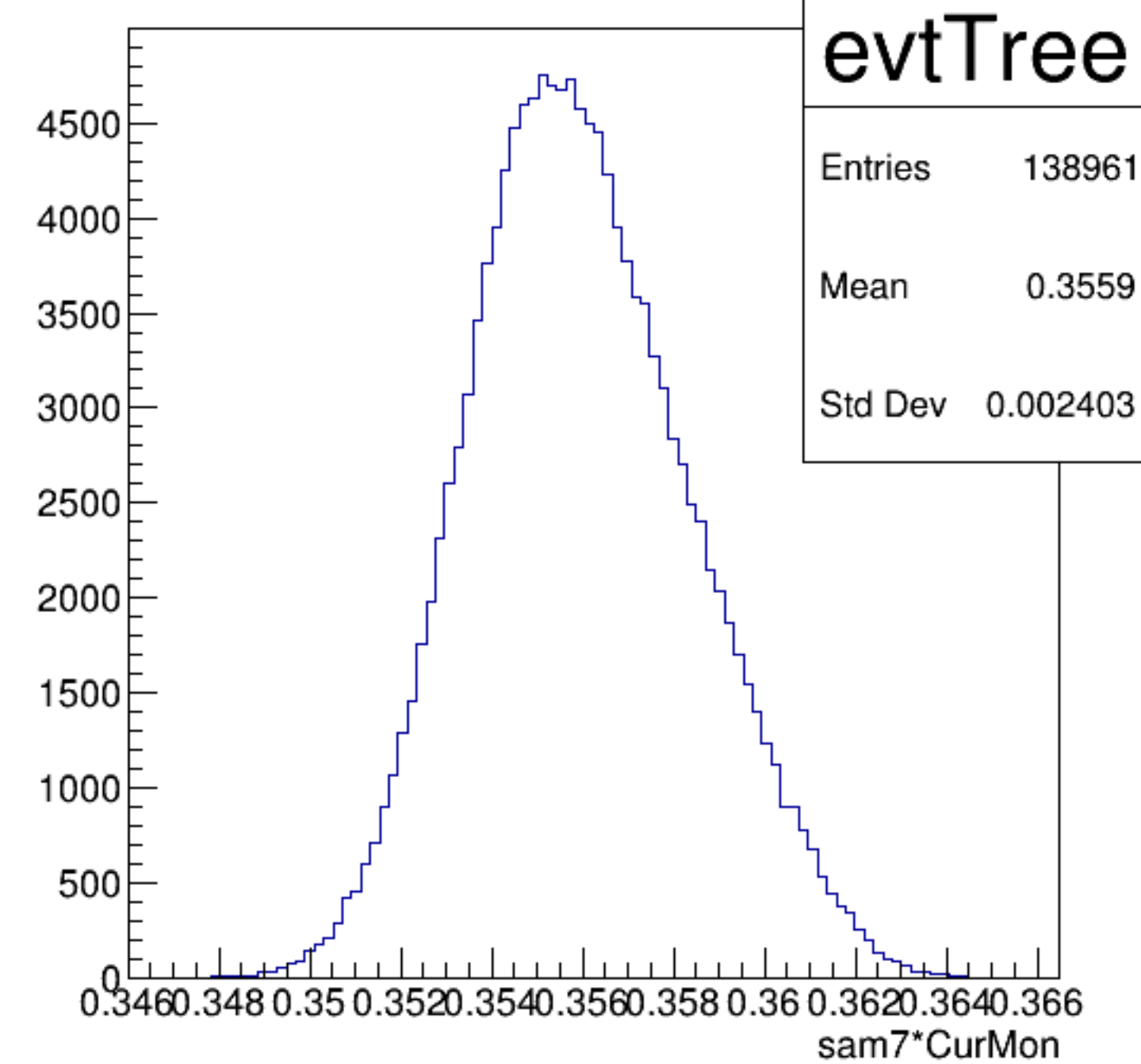


sam7*CurMon:Entry\$

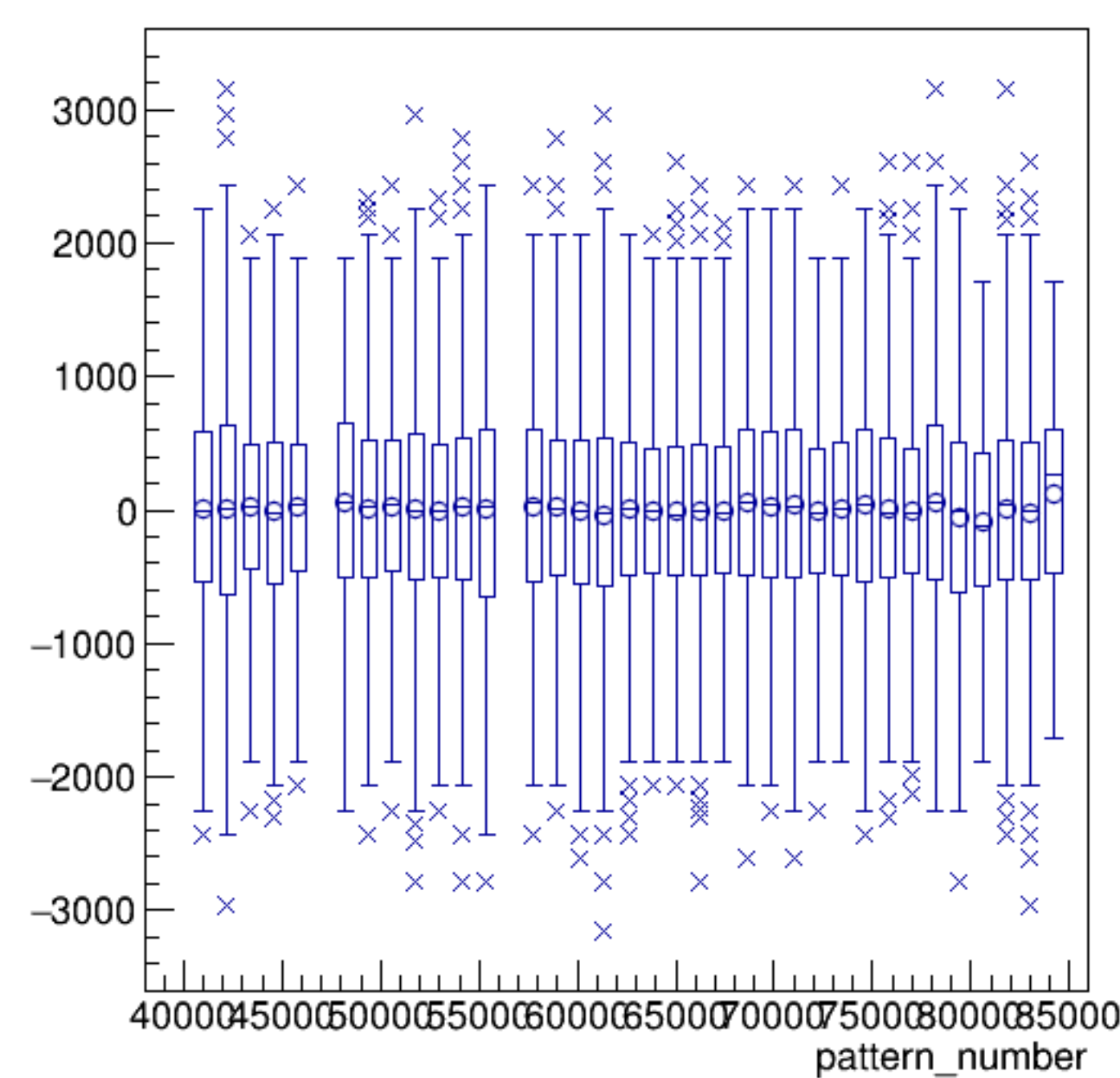


run8336_000_sam7.png

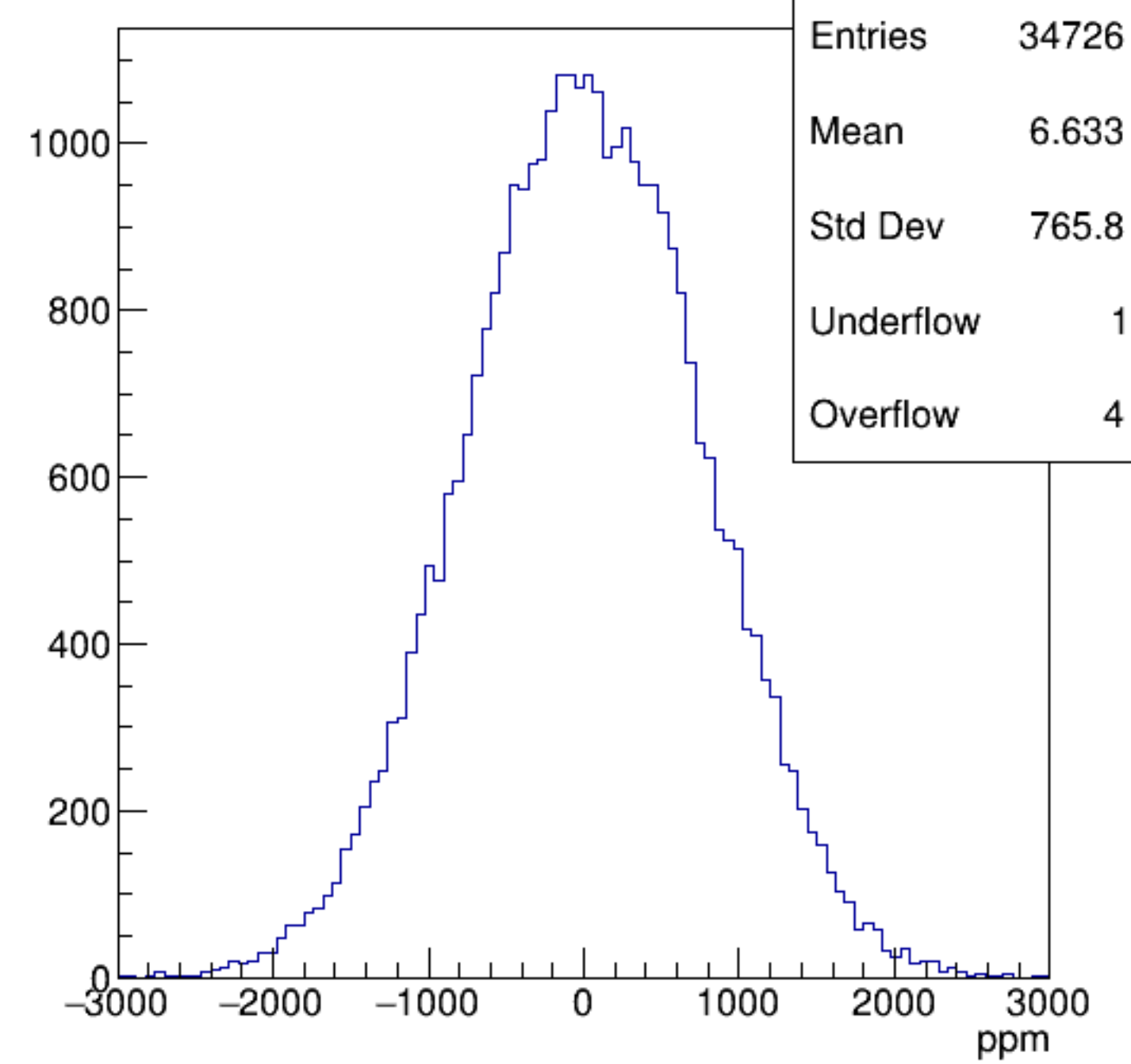
sam7*CurMon {ErrorFlag==0}



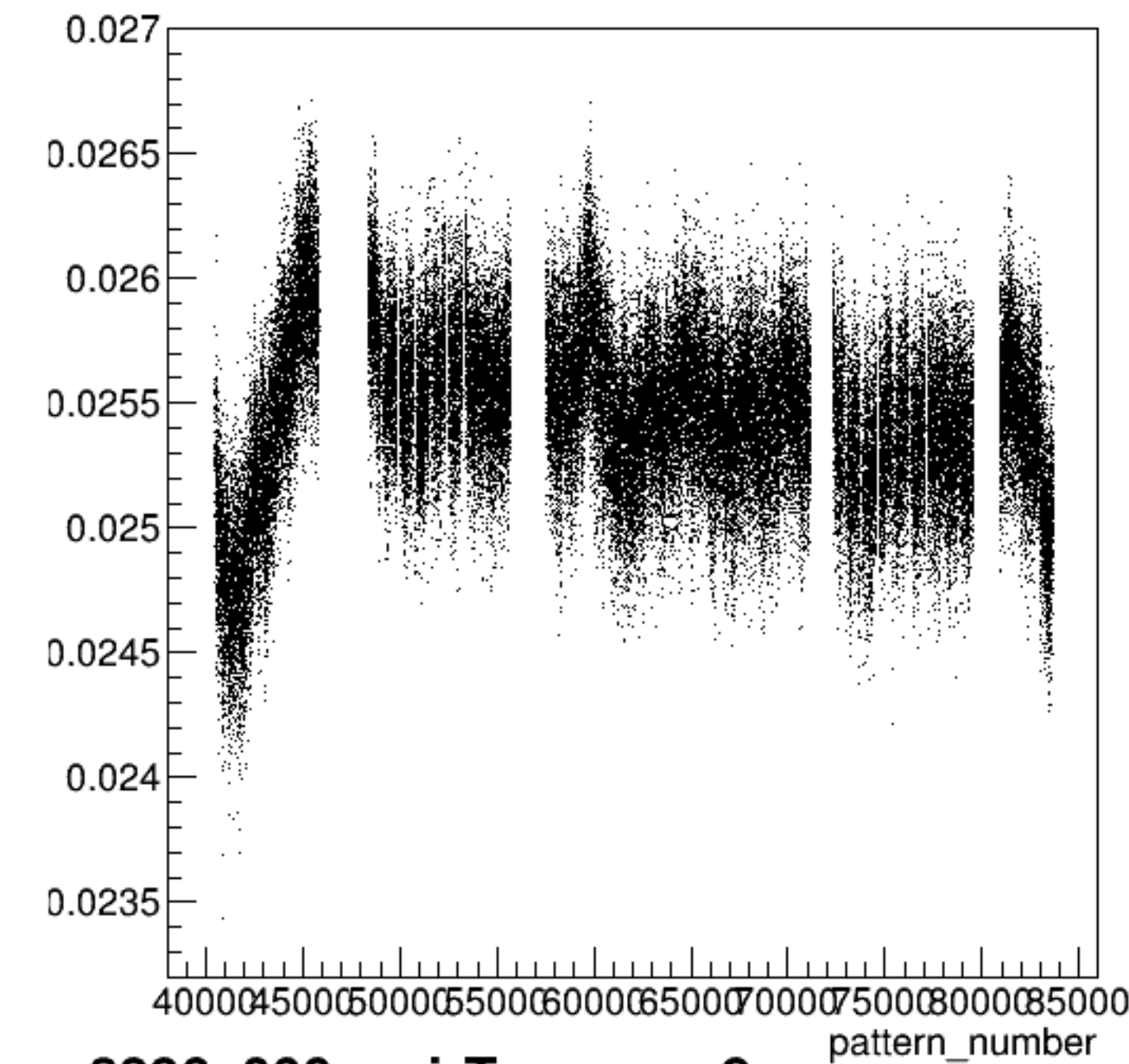
asym_sam7/ppm:pattern_number {ErrorFlag==0}



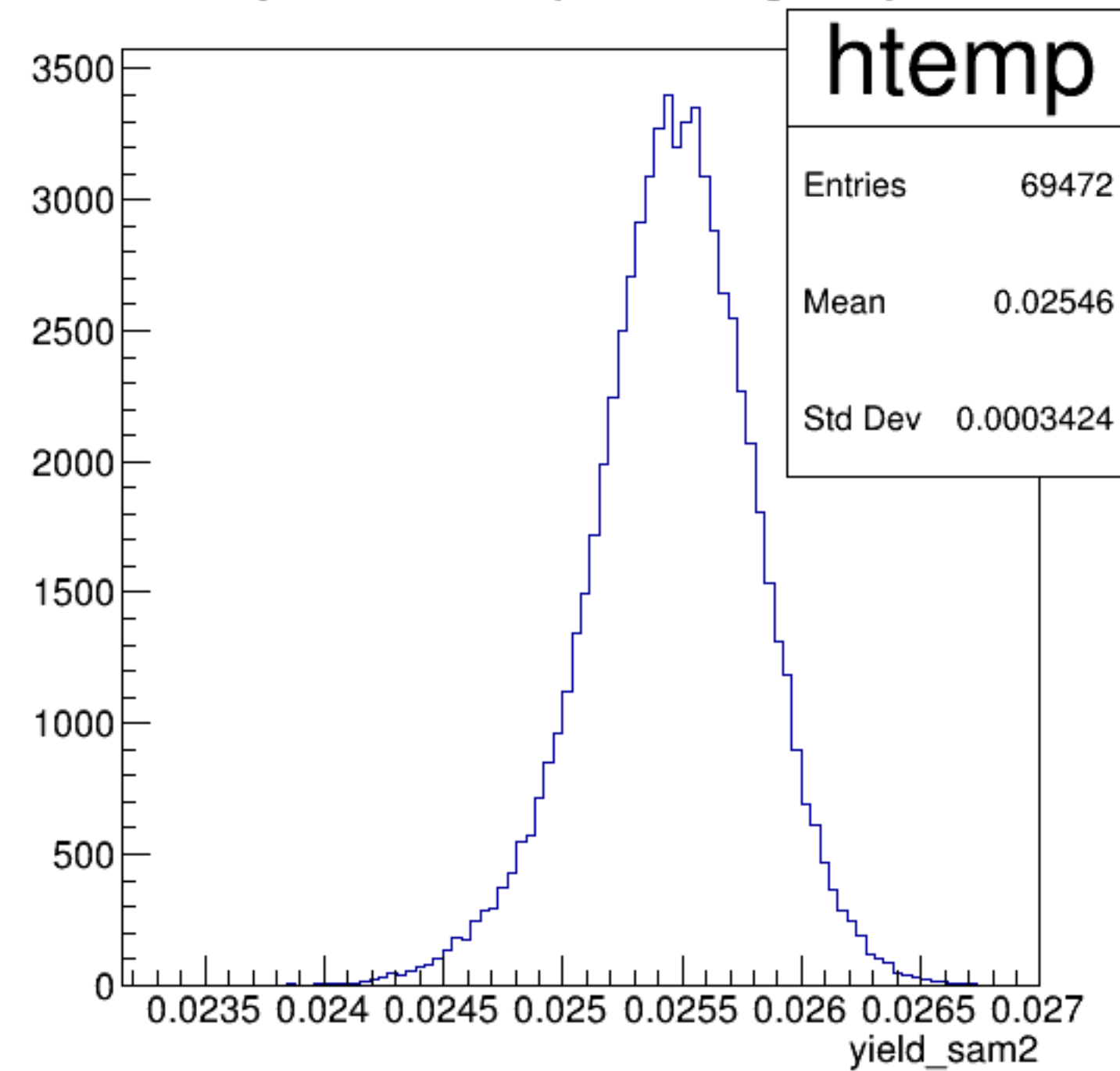
asym_sam7



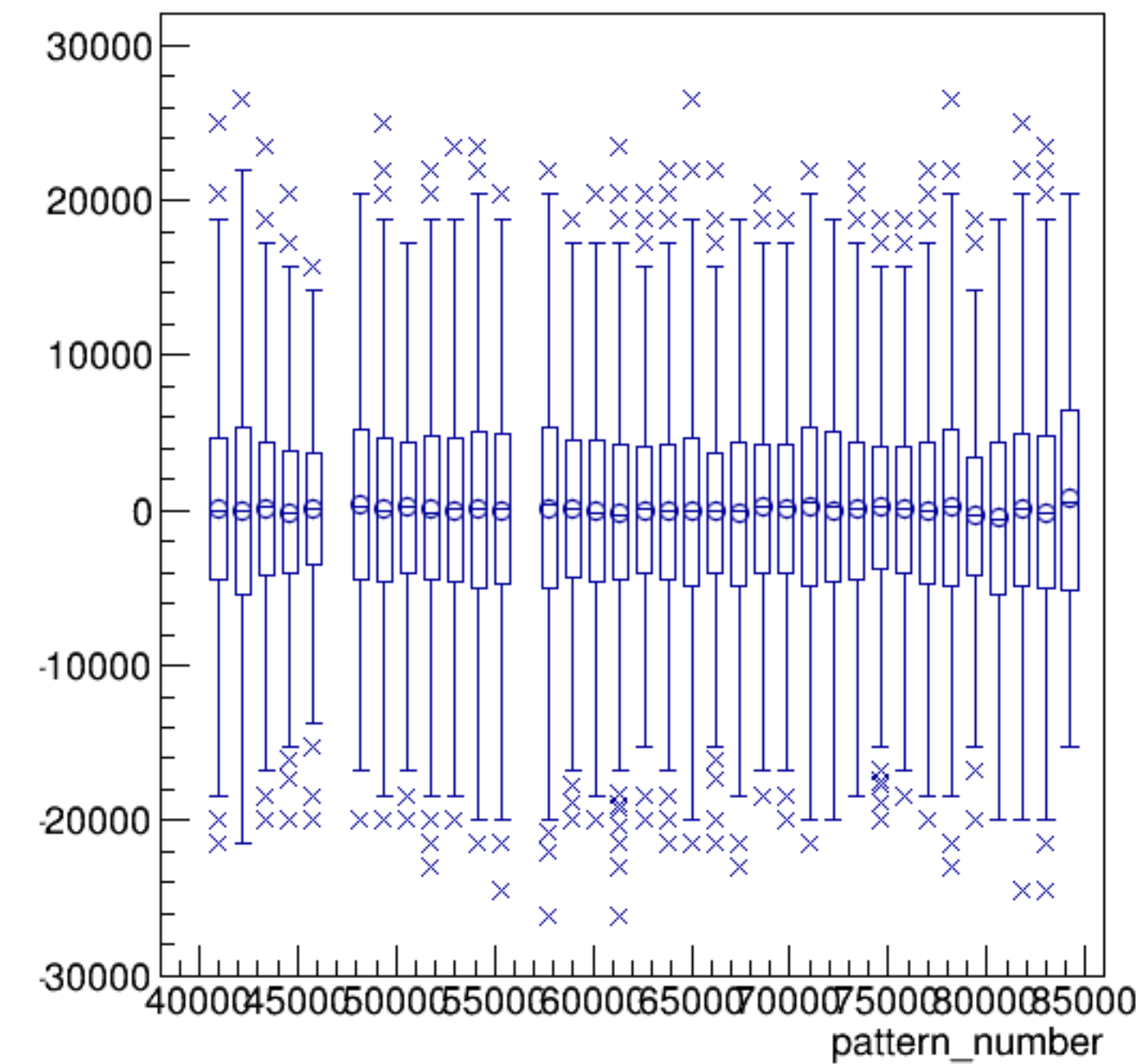
yield_sam2:pattern_number {ErrorFlag==0}



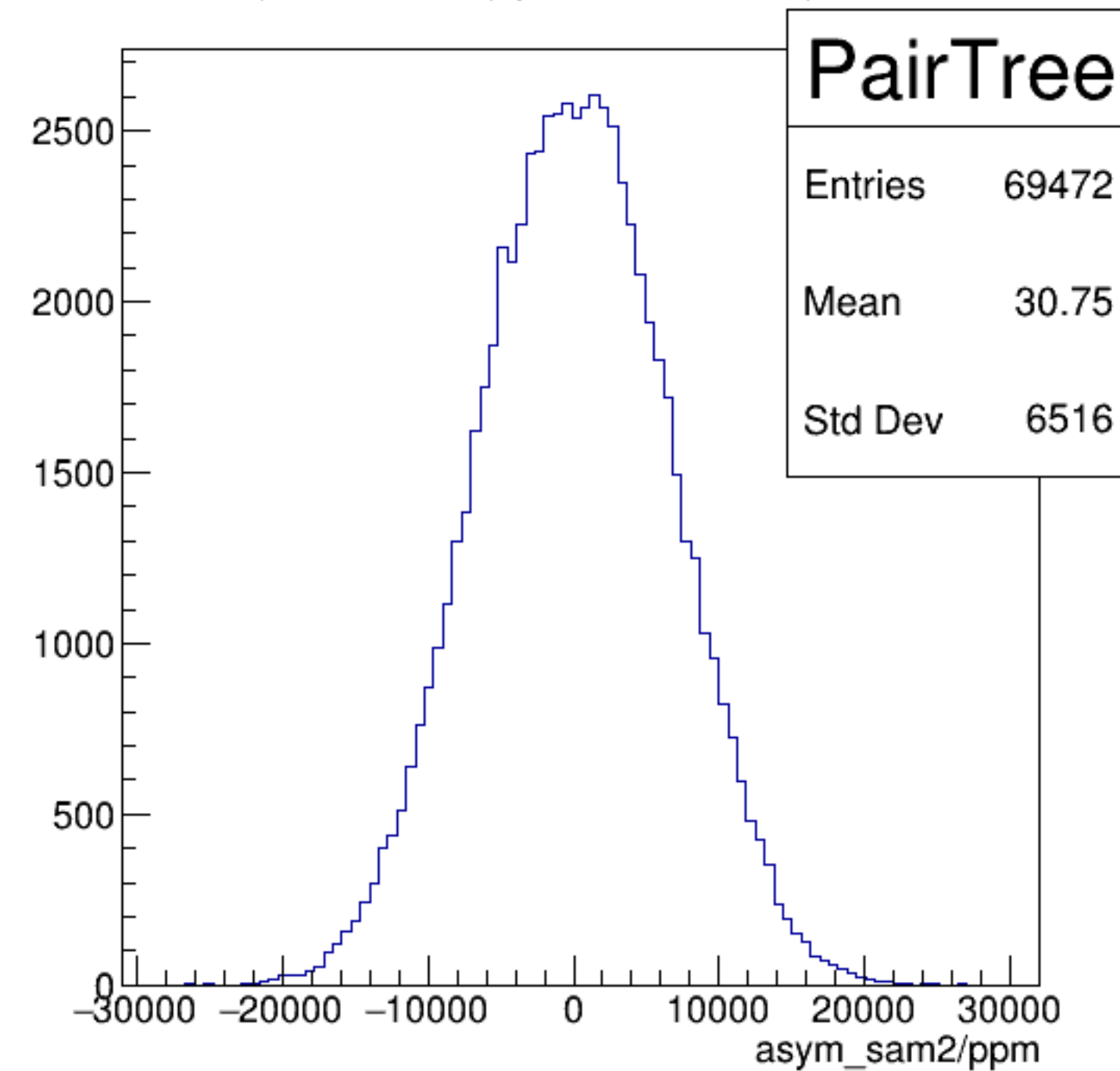
yield_sam2 {ErrorFlag==0}



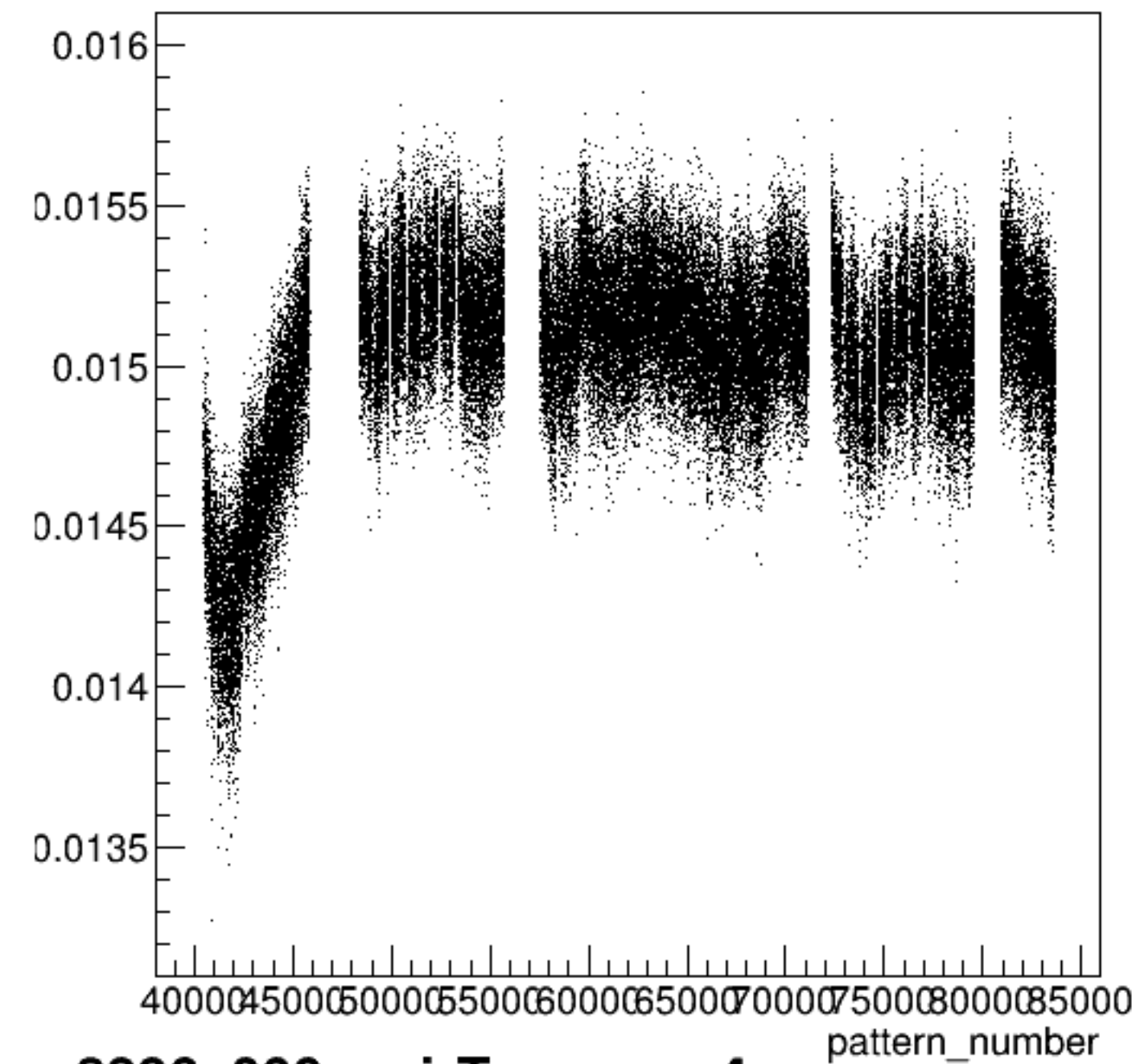
asym_sam2/ppm:pattern_number {ErrorFlag==0}



asym_sam2/ppm {ErrorFlag==0}

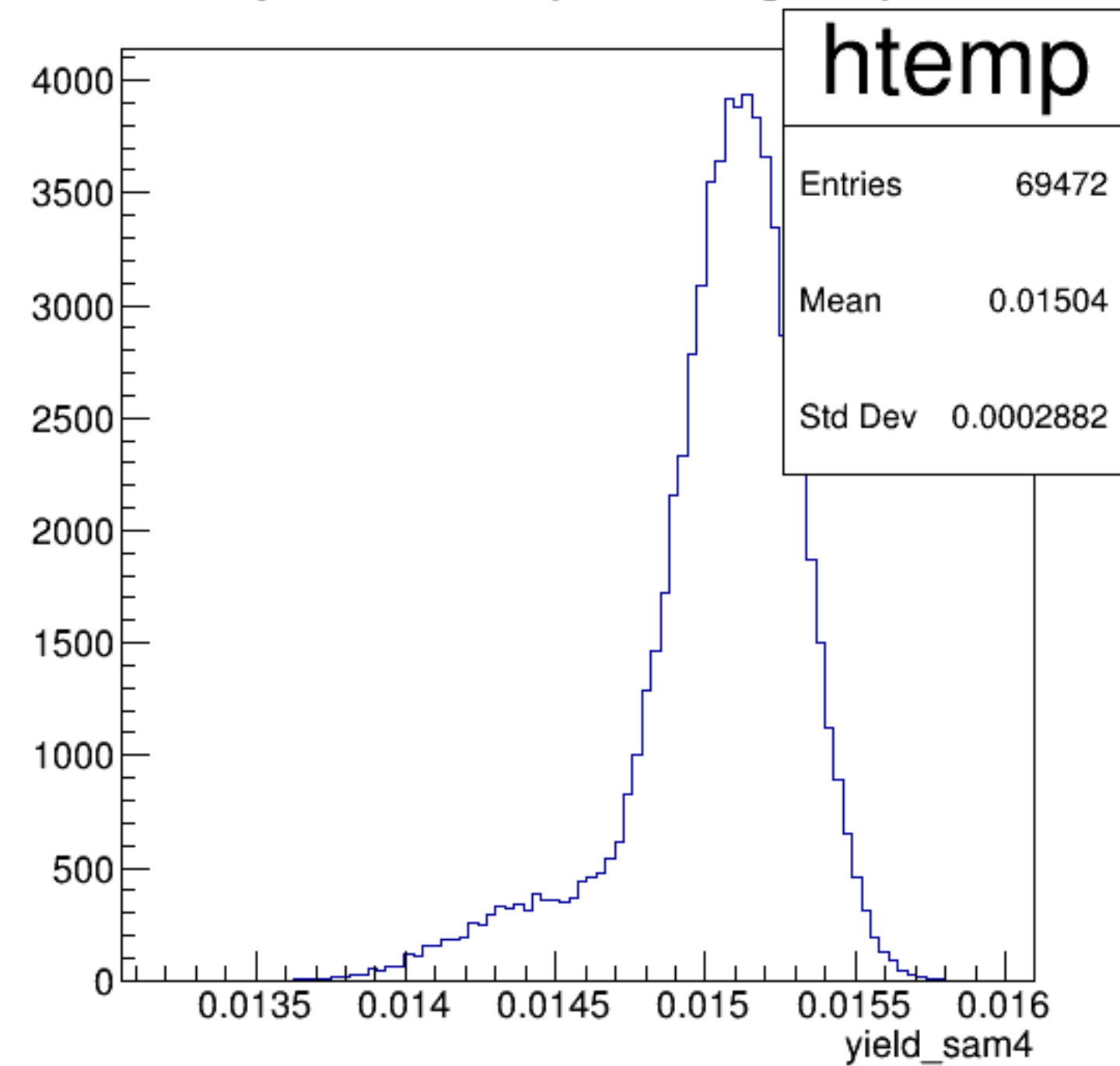


yield_sam4:pattern_number {ErrorFlag==0}

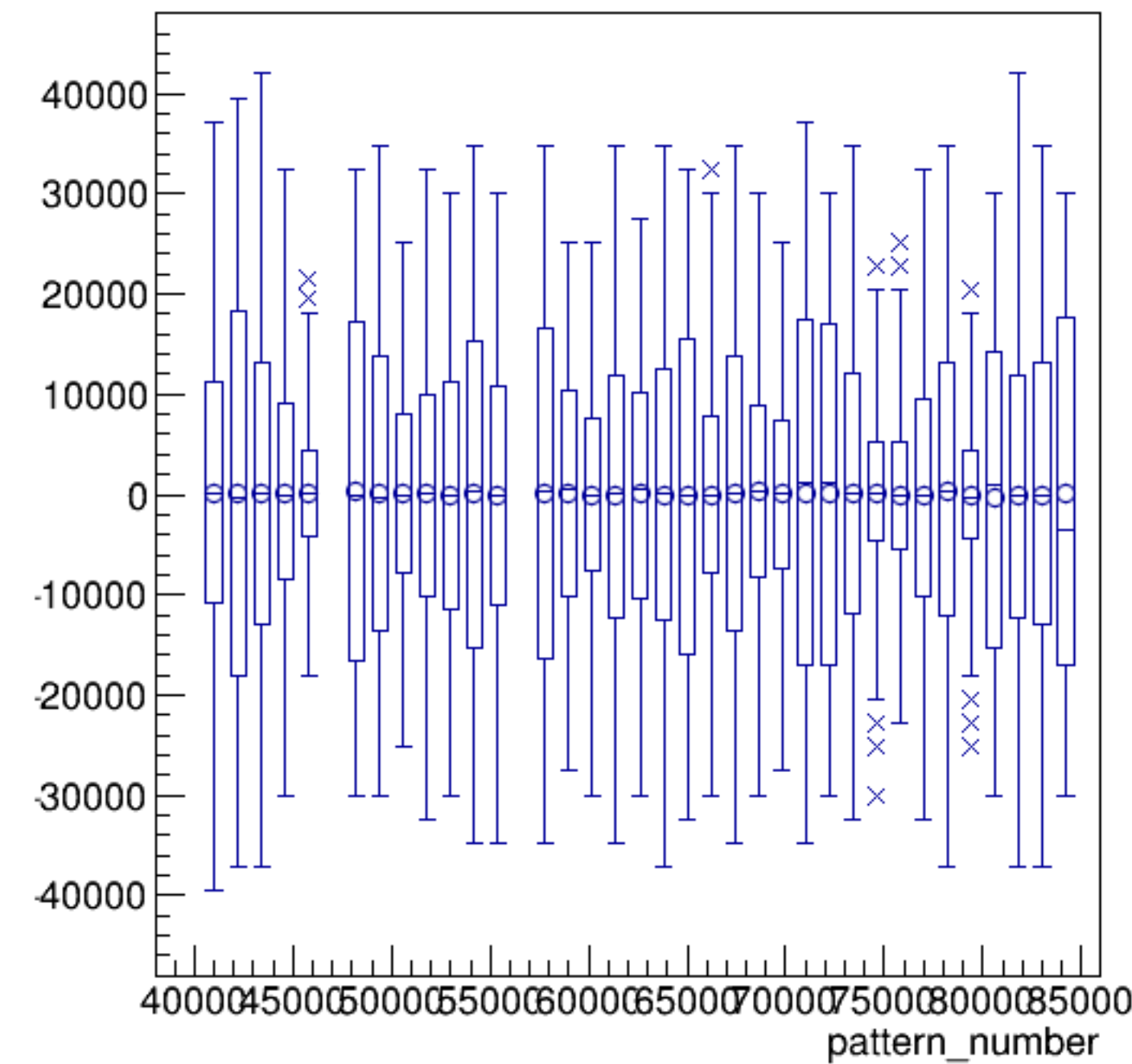


run8336_000_pairTree_sam4.png

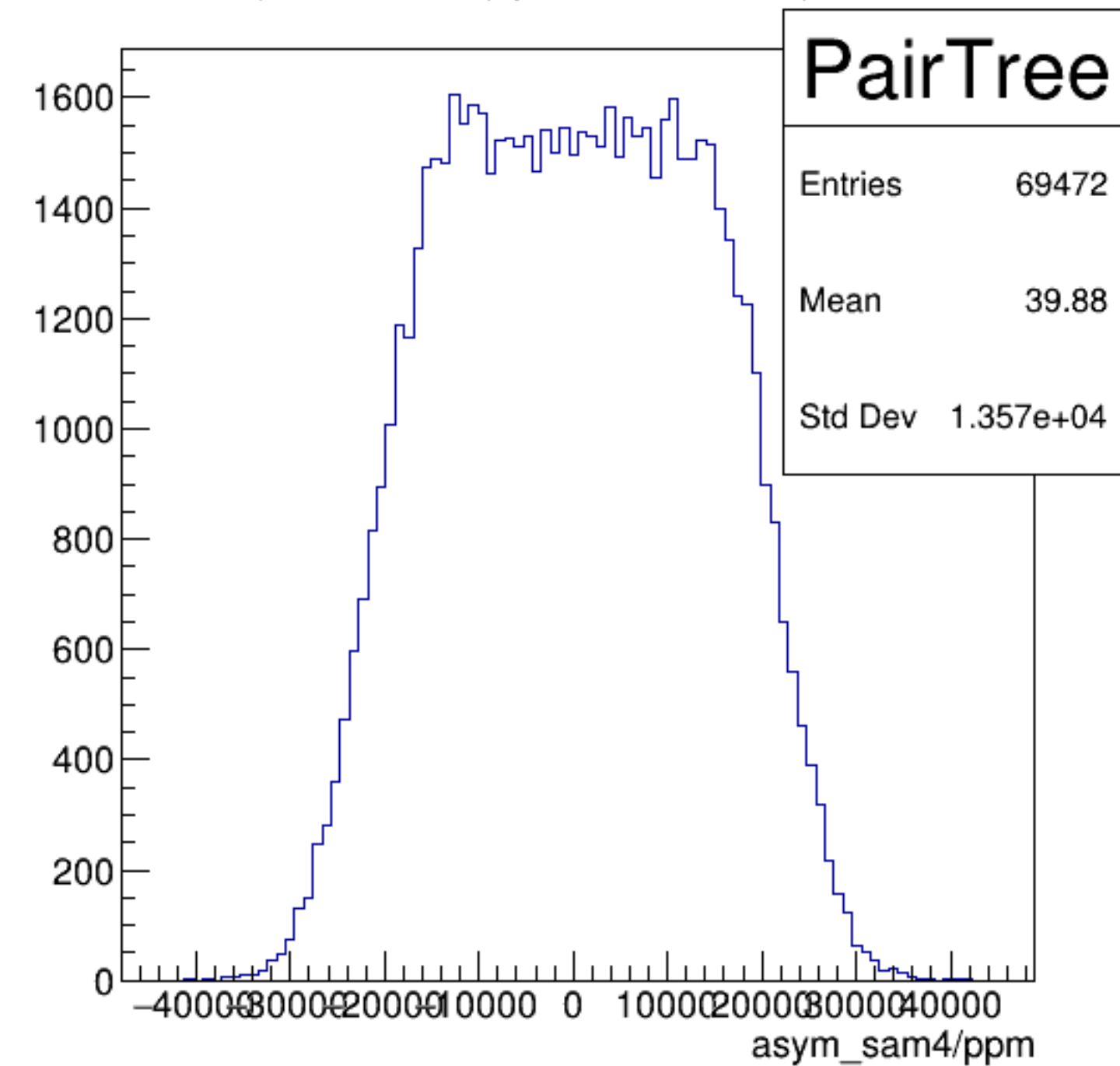
yield_sam4 {ErrorFlag==0}



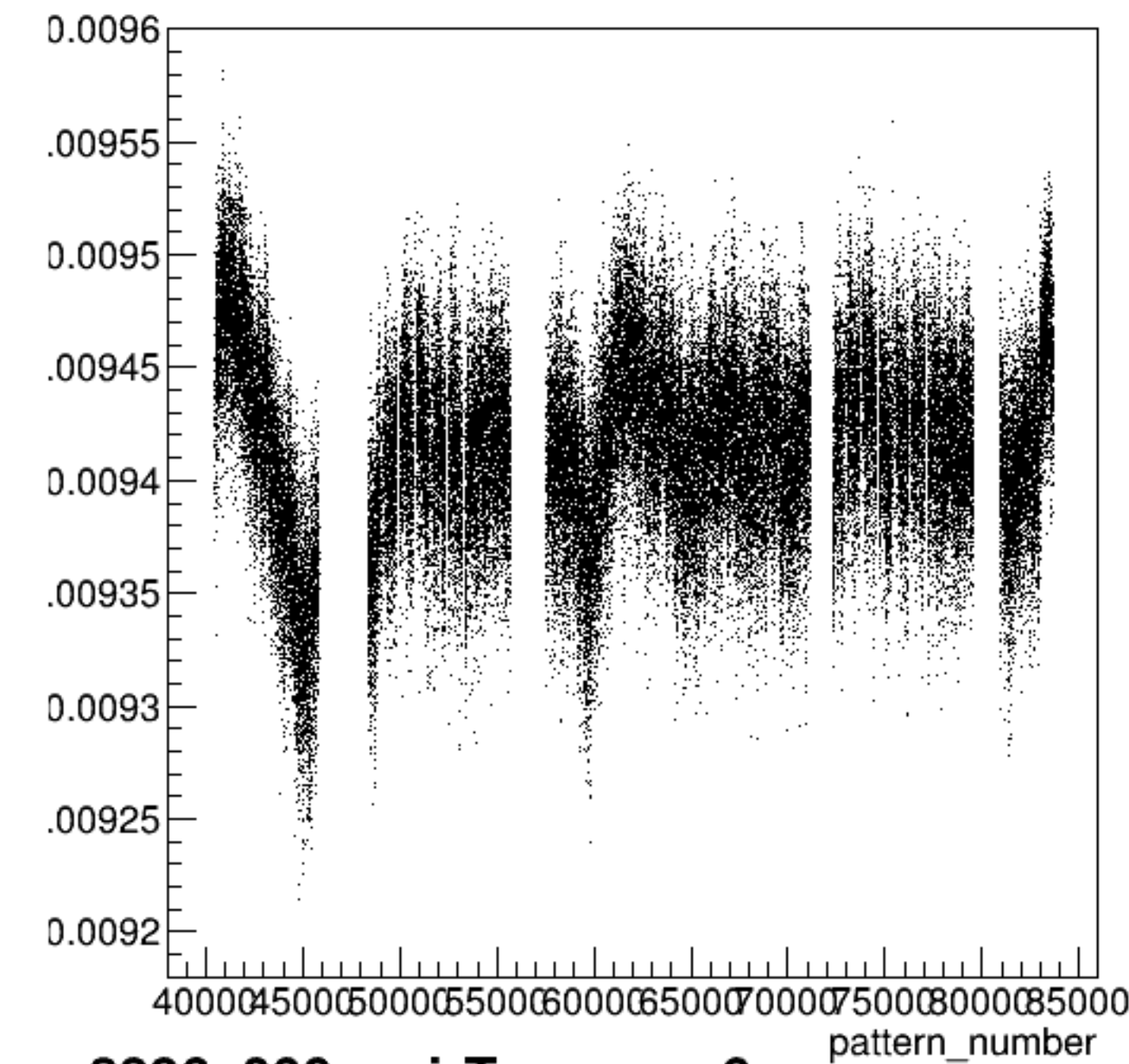
asym_sam4/ppm:pattern_number {ErrorFlag==0}



asym_sam4/ppm {ErrorFlag==0}

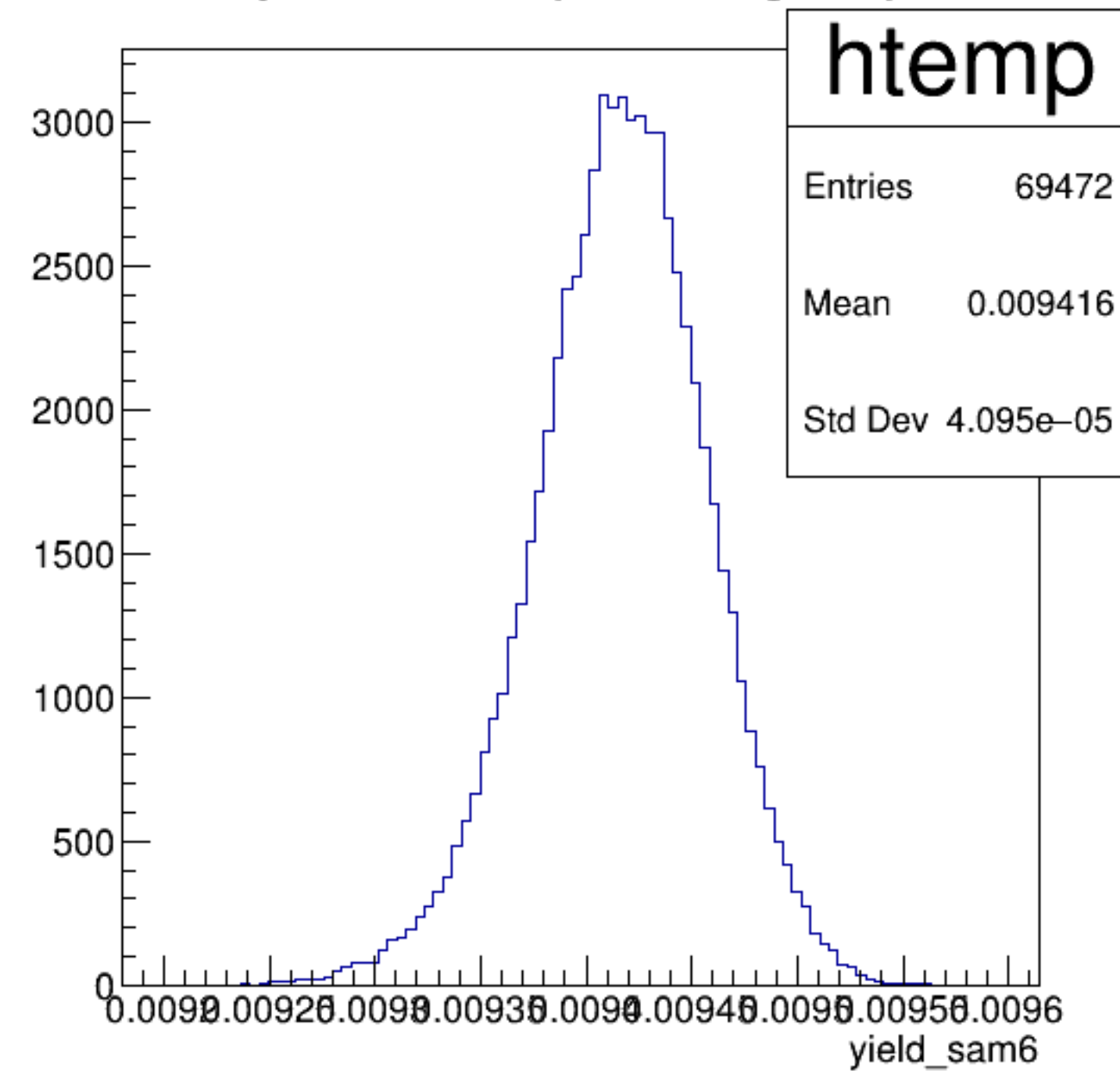


yield_sam6:pattern_number {ErrorFlag==0}

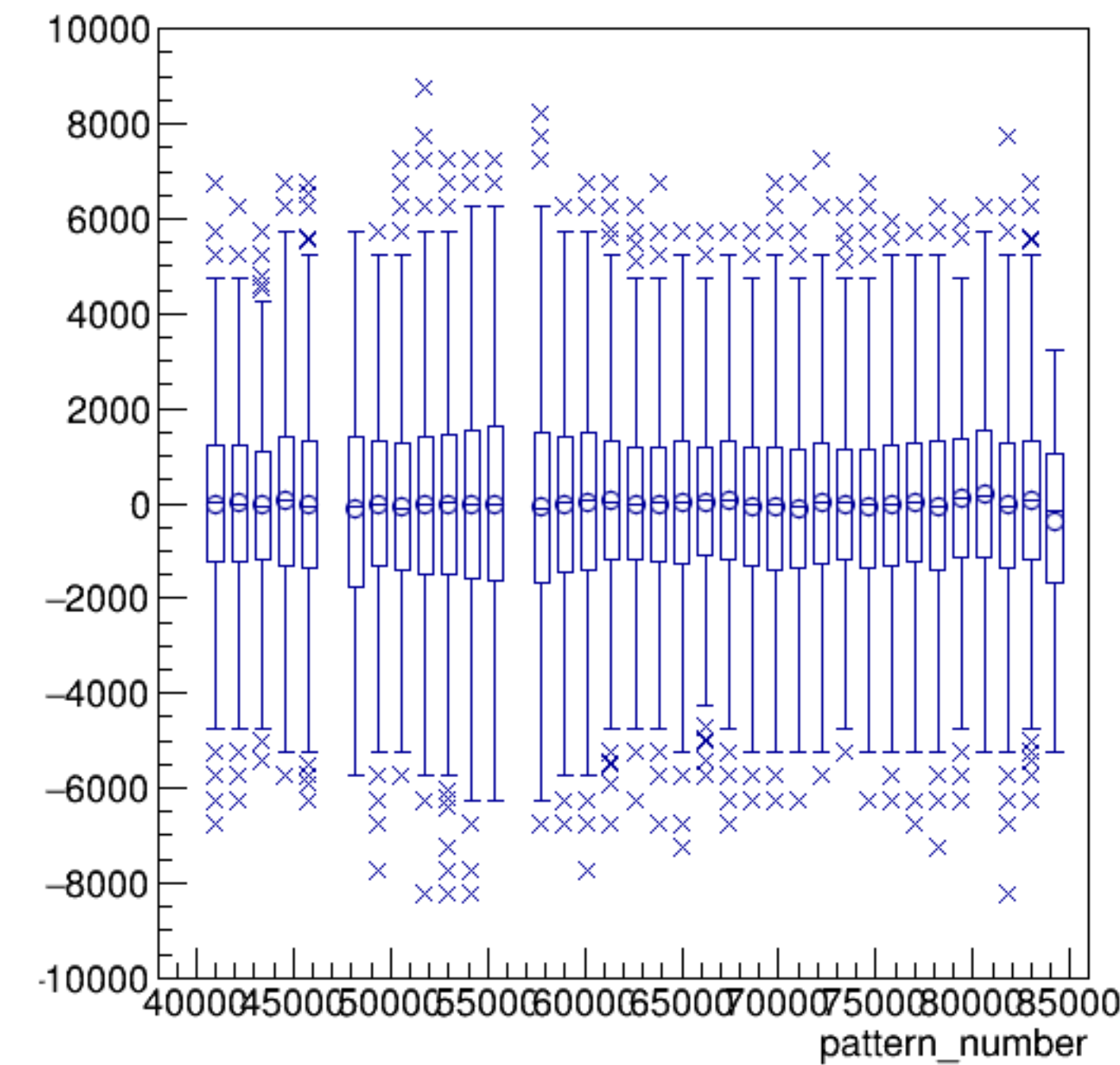


run8336_000_pairTree_sam6.png

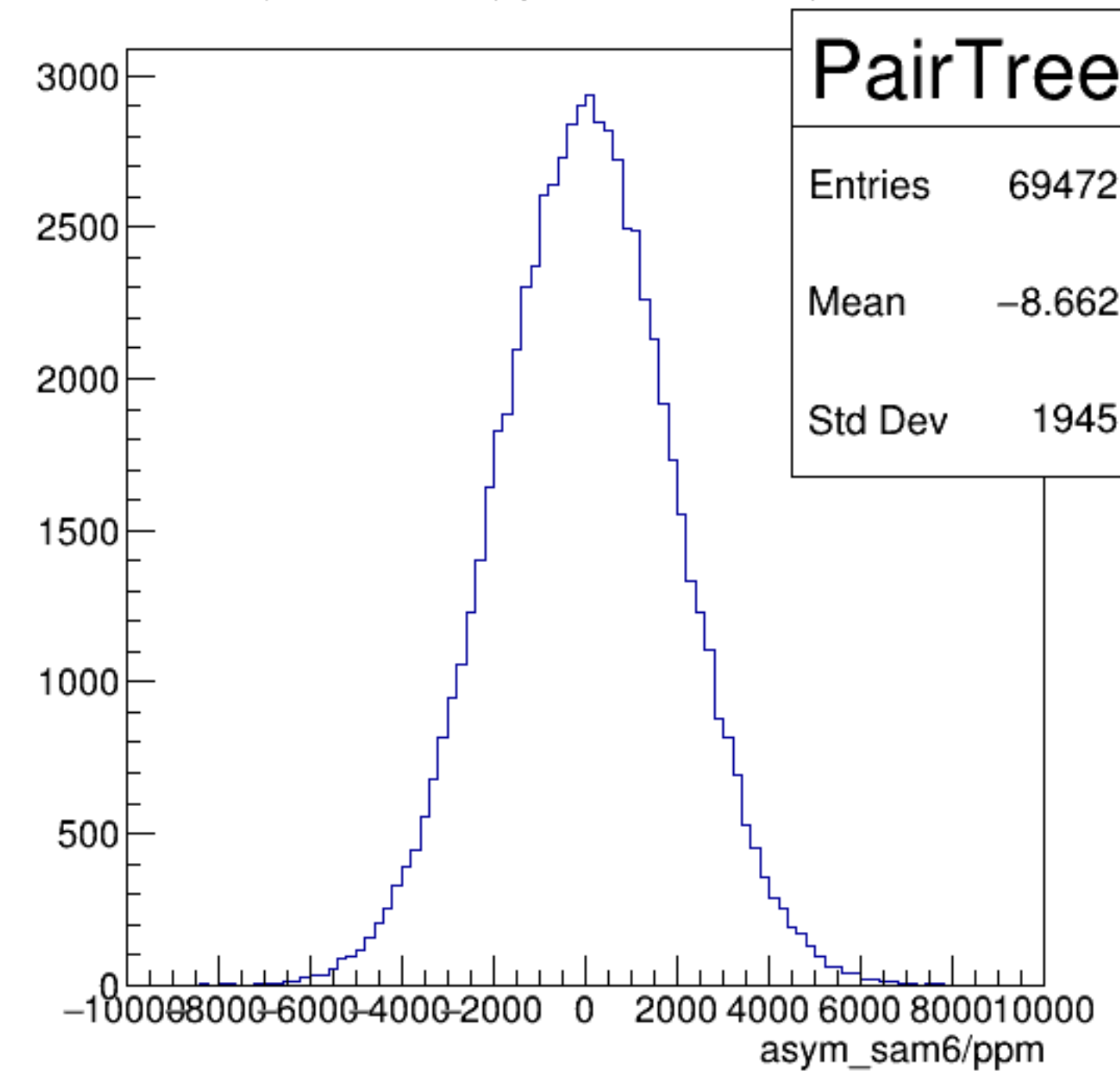
yield_sam6 {ErrorFlag==0}



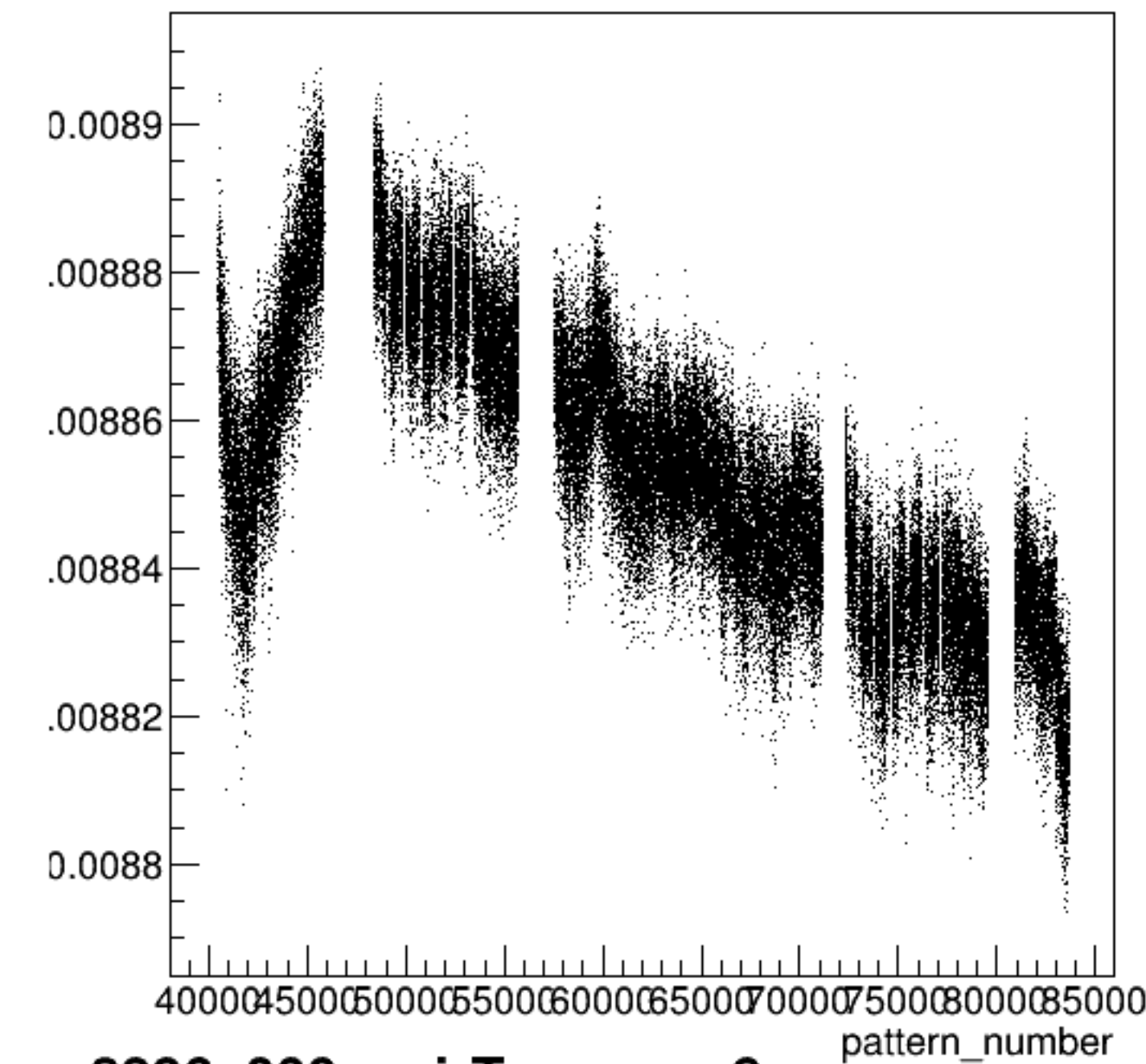
asym_sam6/ppm:pattern_number {ErrorFlag==0}



asym_sam6/ppm {ErrorFlag==0}

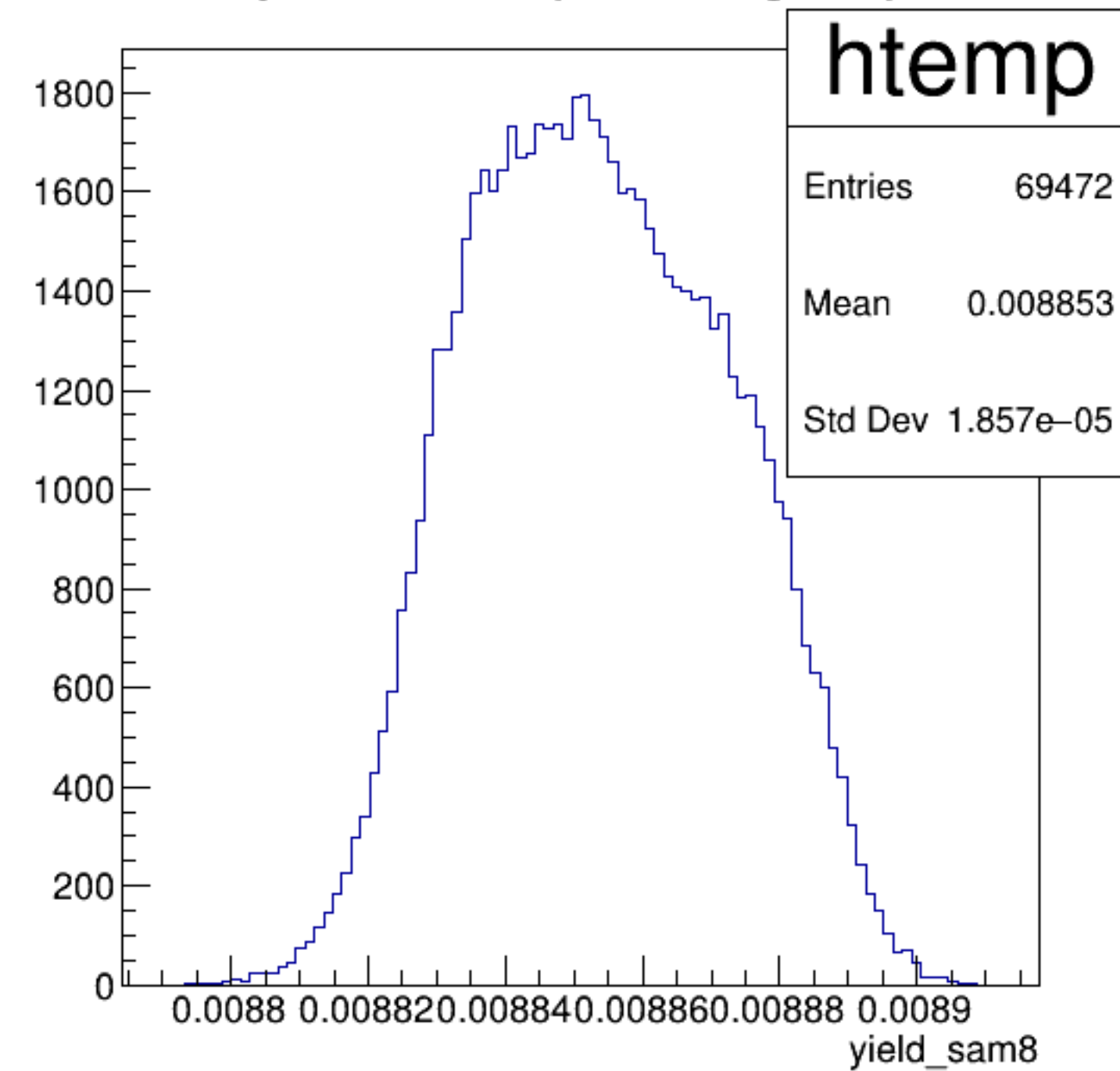


yield_sam8:pattern_number {ErrorFlag==0}

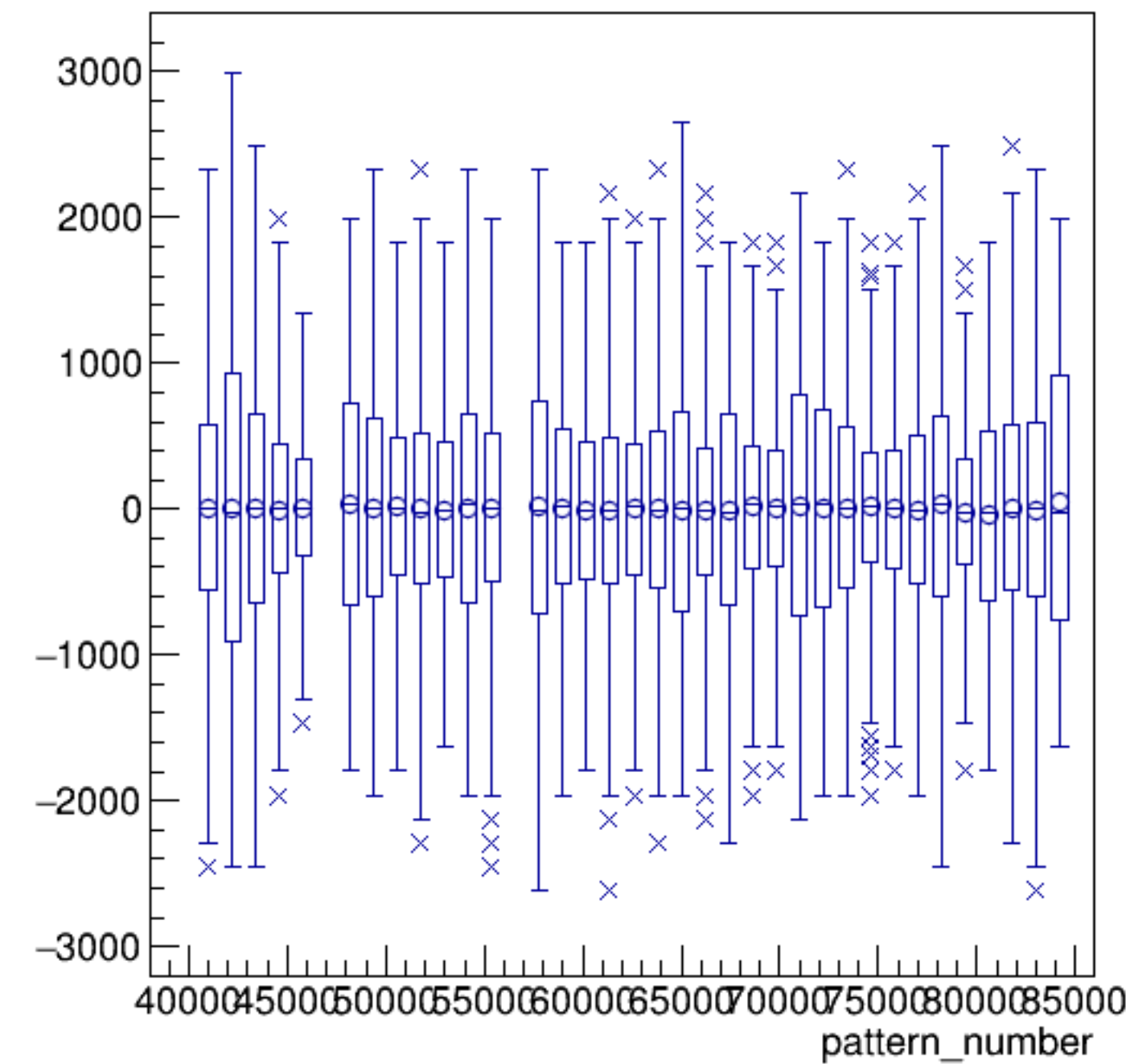


run8336_000_pairTree_sam8.png

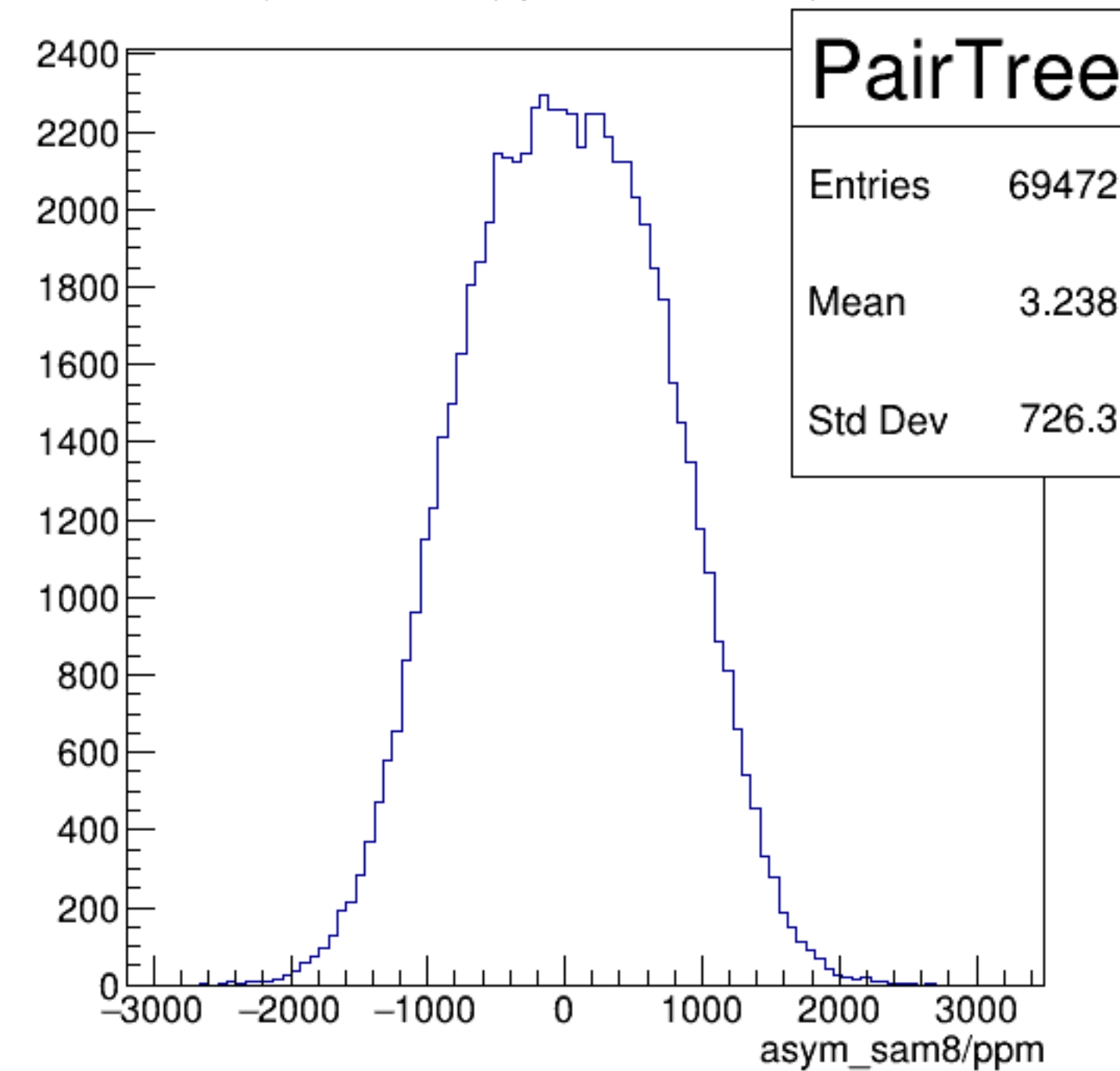
yield_sam8 {ErrorFlag==0}



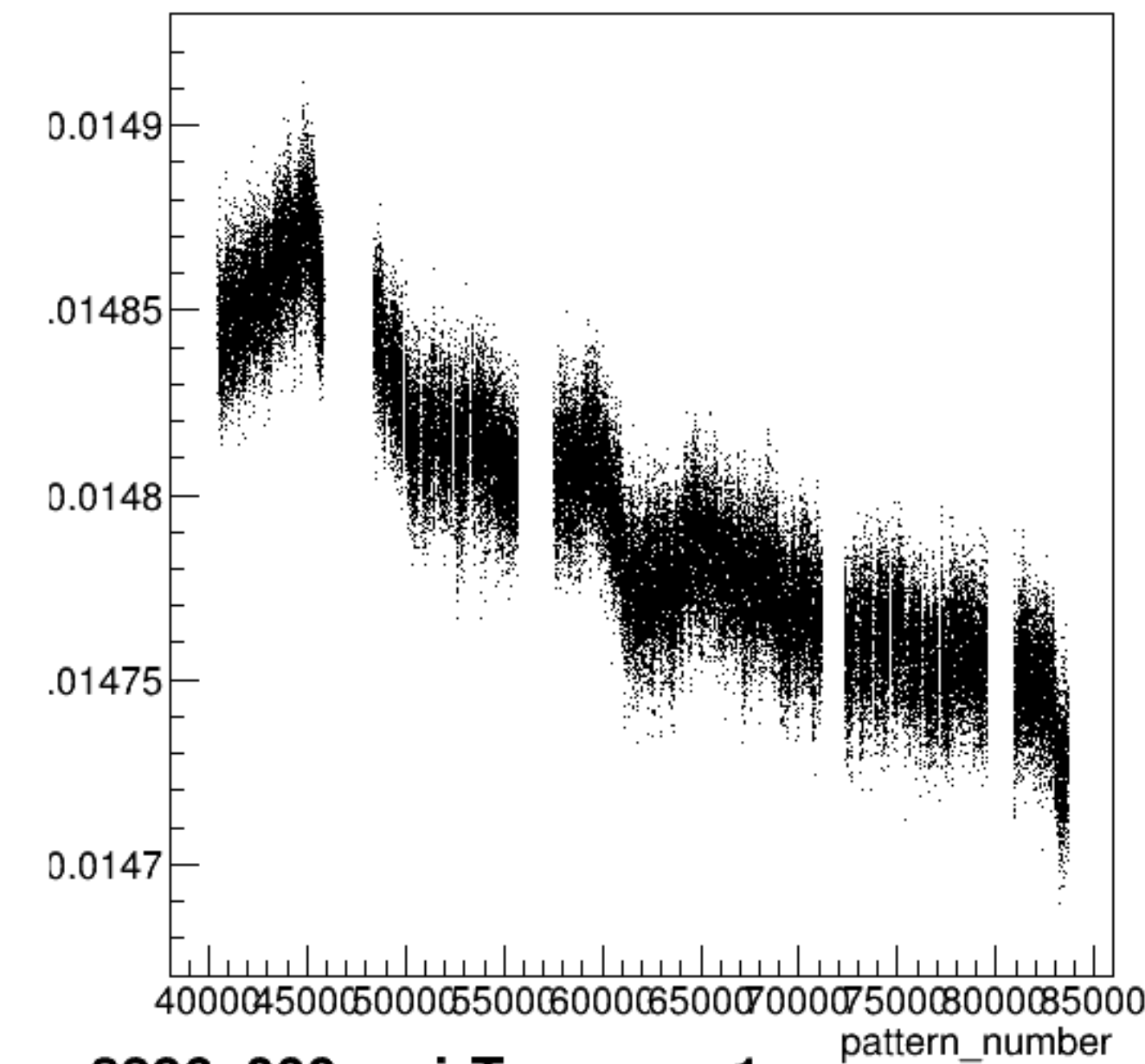
asym_sam8/ppm:pattern_number {ErrorFlag==0}



asym_sam8/ppm {ErrorFlag==0}

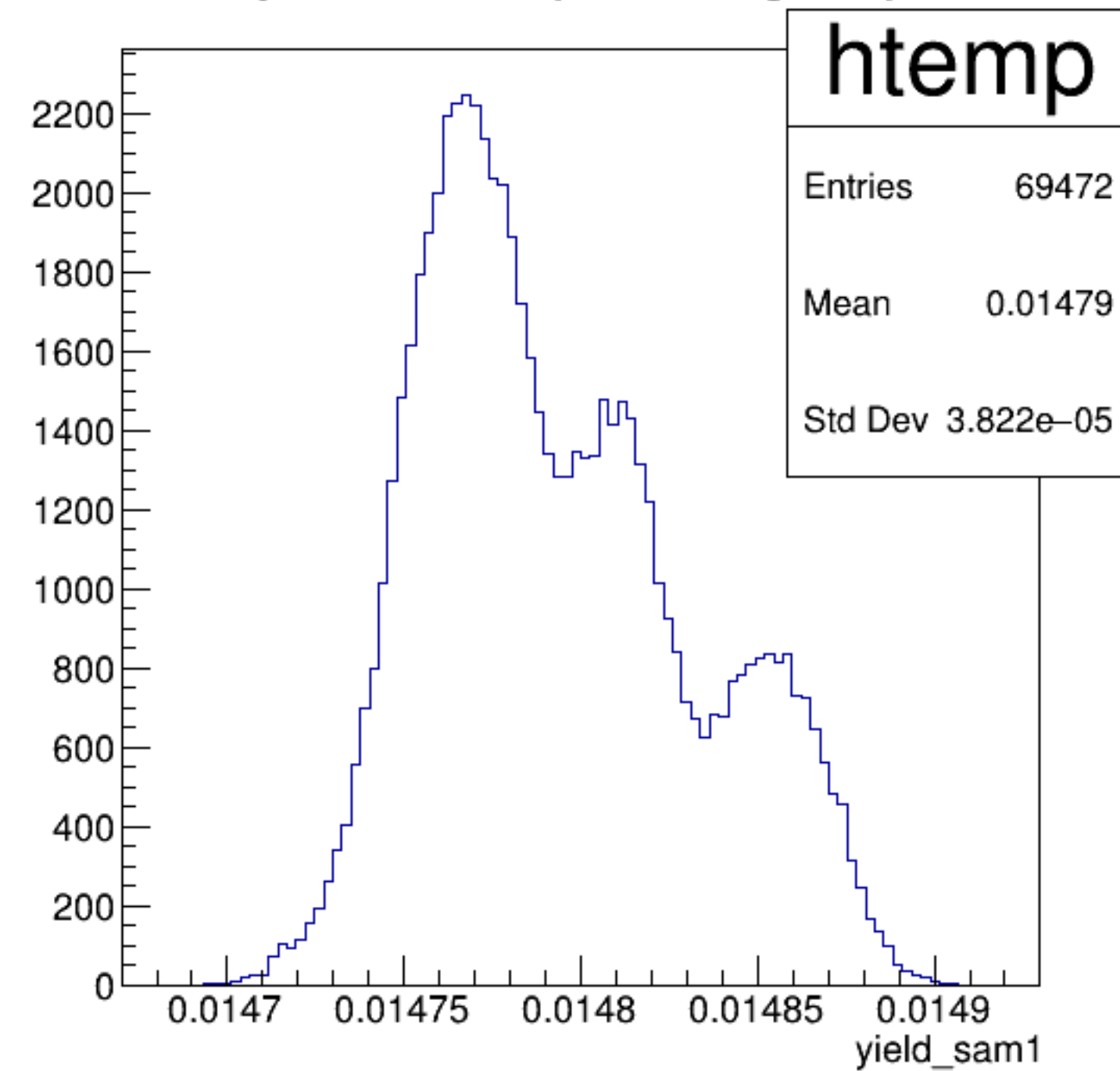


yield_sam1:pattern_number {ErrorFlag==0}

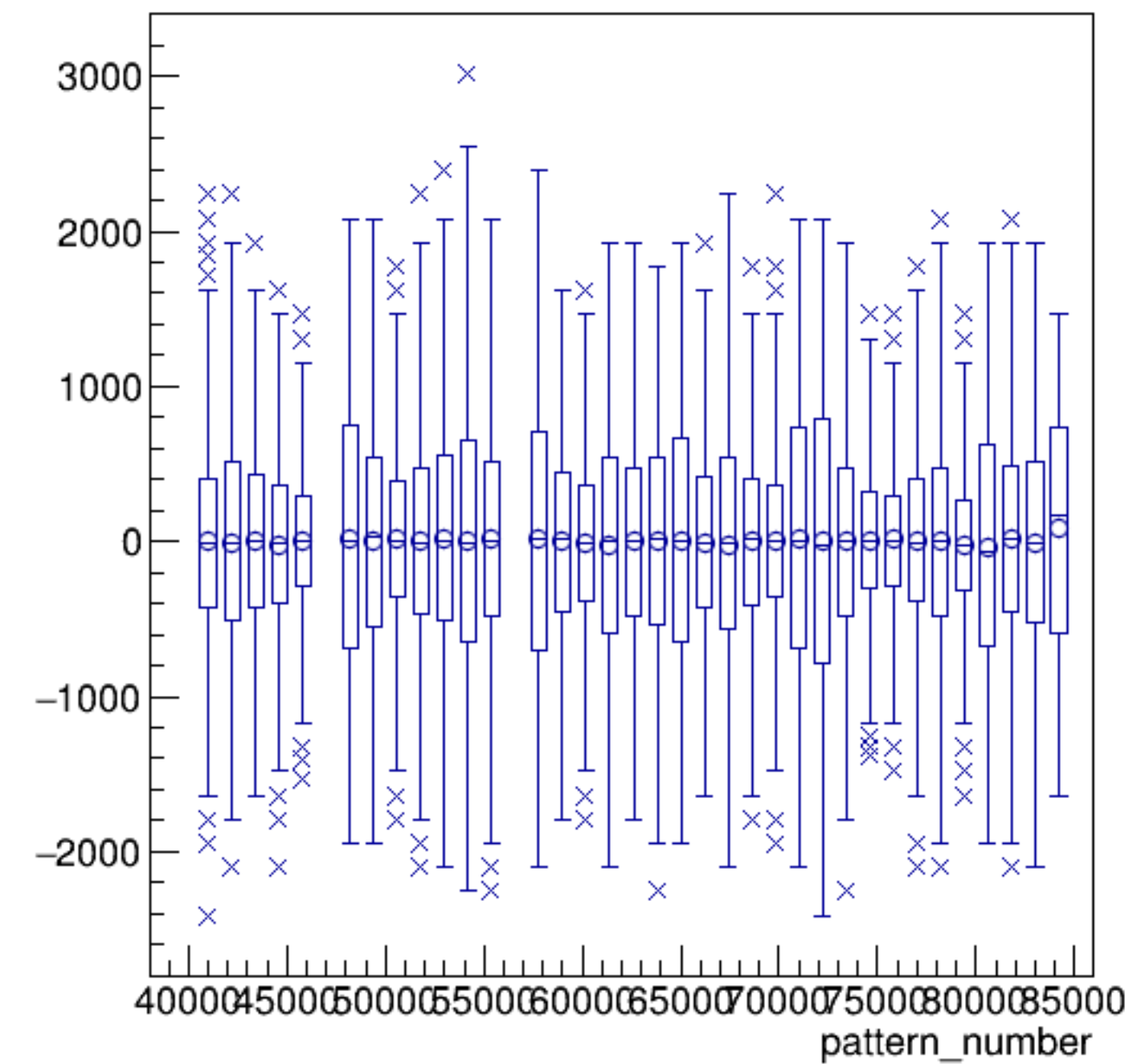


run8336_000_pairTree_sam1.png

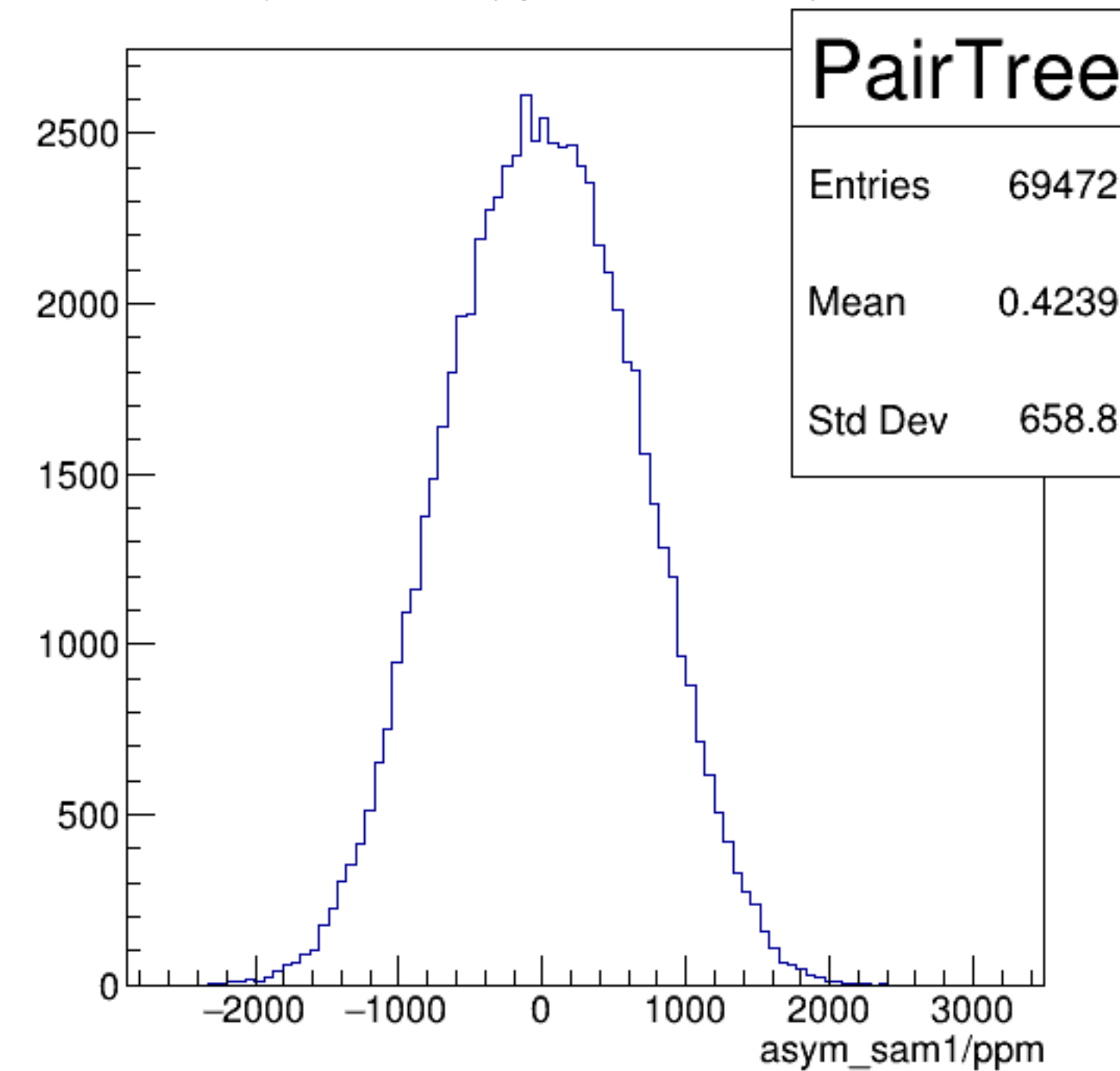
yield_sam1 {ErrorFlag==0}



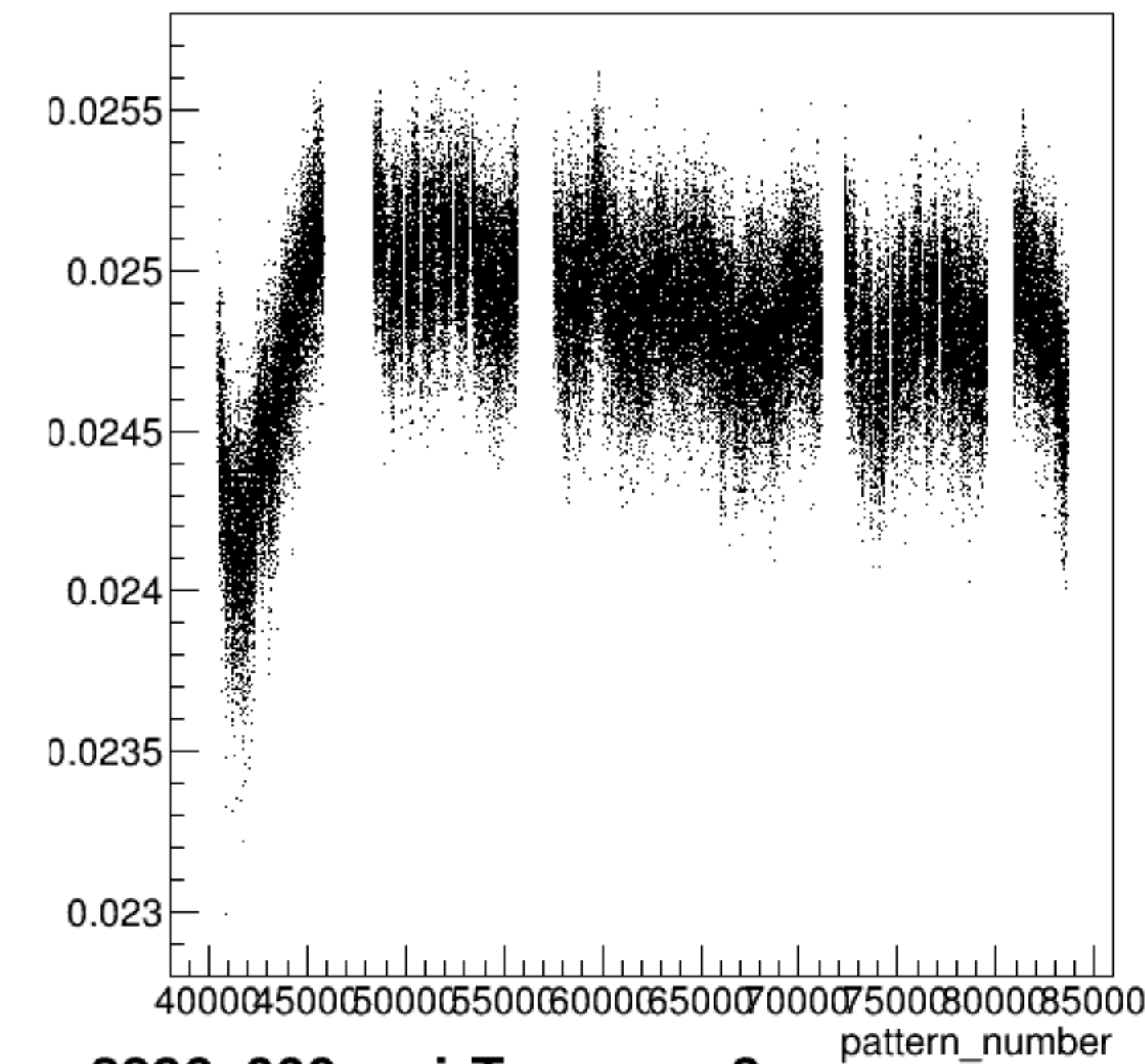
asym_sam1/ppm:pattern_number {ErrorFlag==0}



asym_sam1/ppm {ErrorFlag==0}

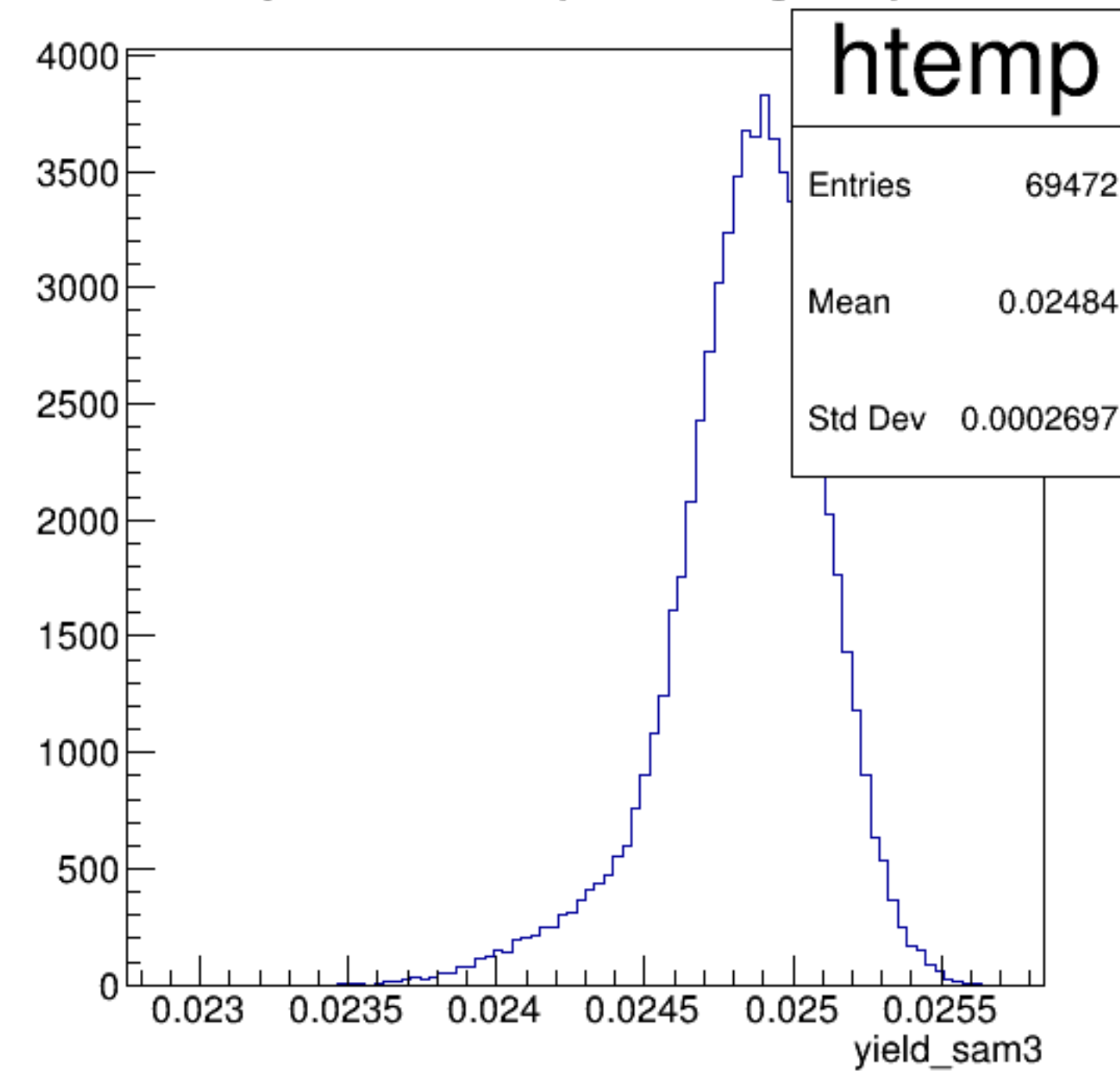


yield_sam3:pattern_number {ErrorFlag==0}

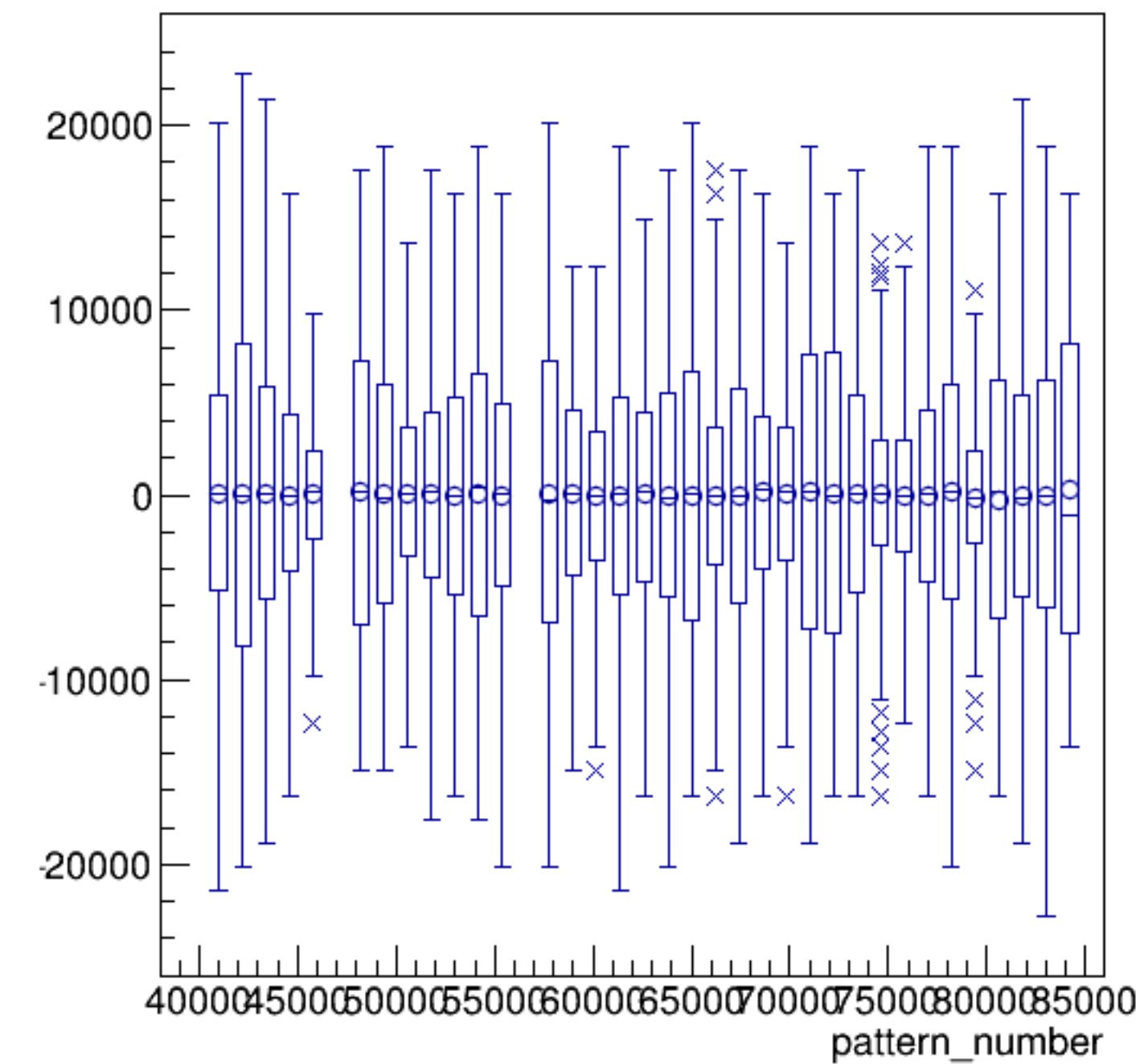


run8336_000_pairTree_sam3.png

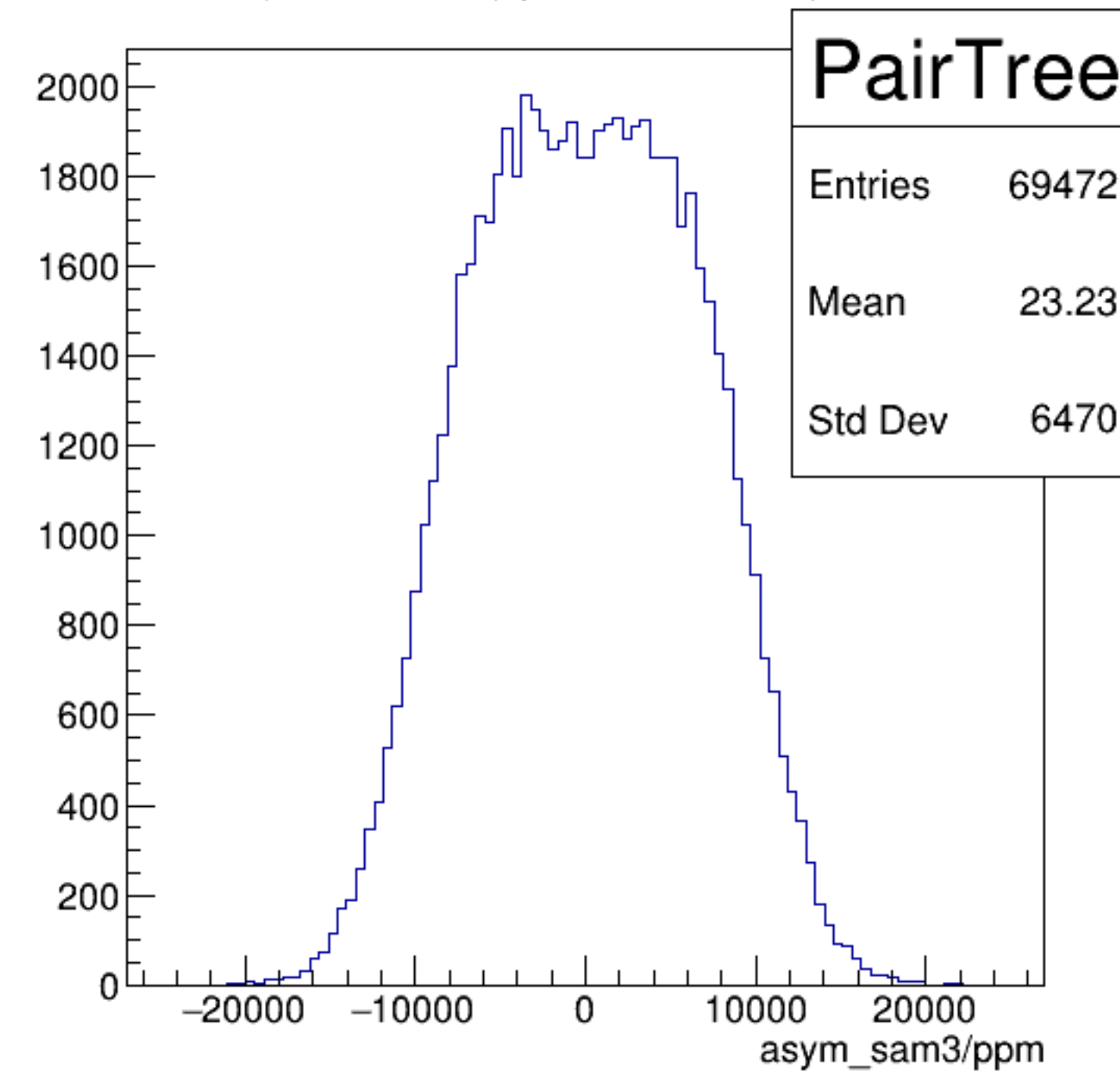
yield_sam3 {ErrorFlag==0}



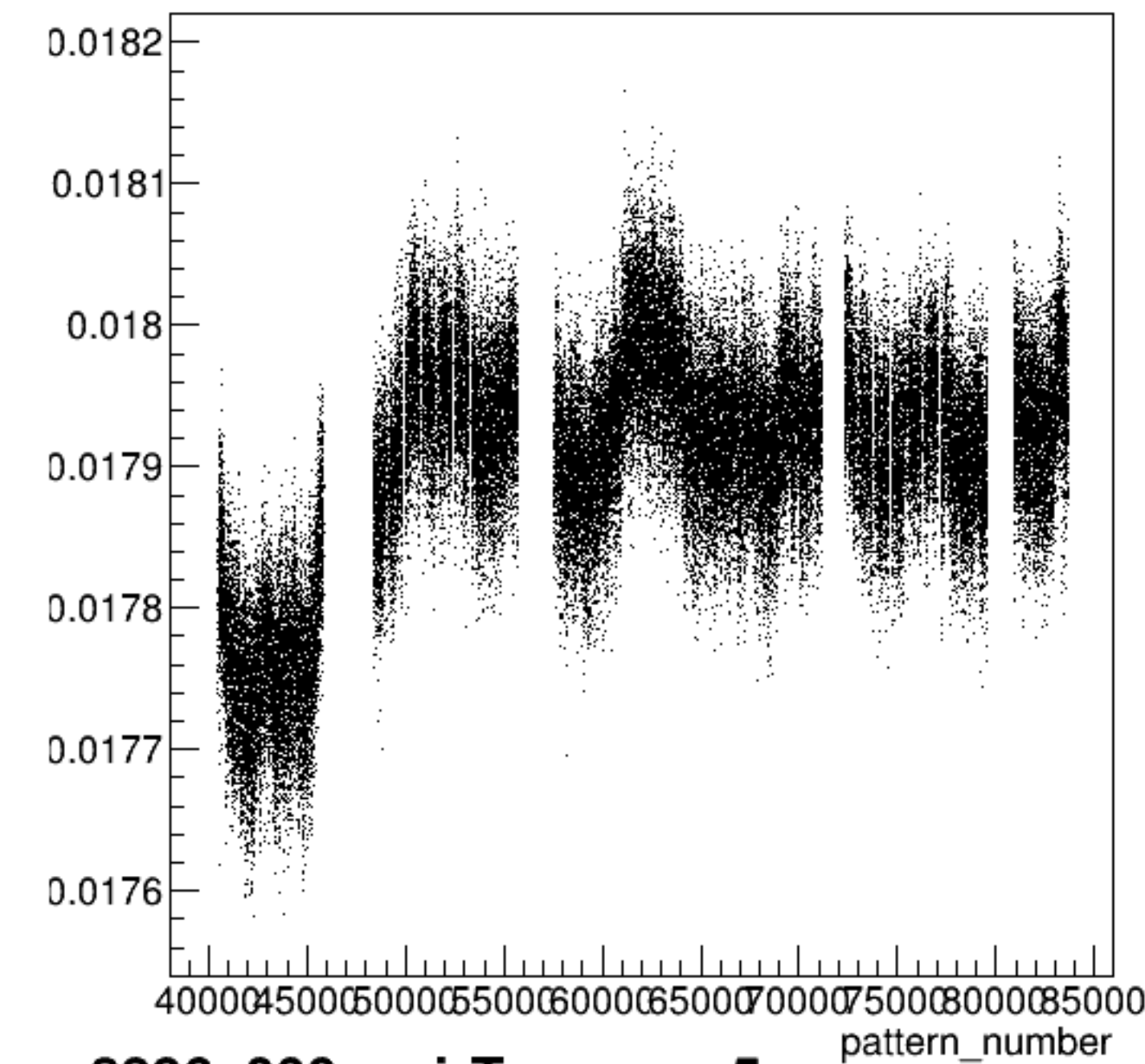
asym_sam3/ppm:pattern_number {ErrorFlag==0}



asym_sam3/ppm {ErrorFlag==0}

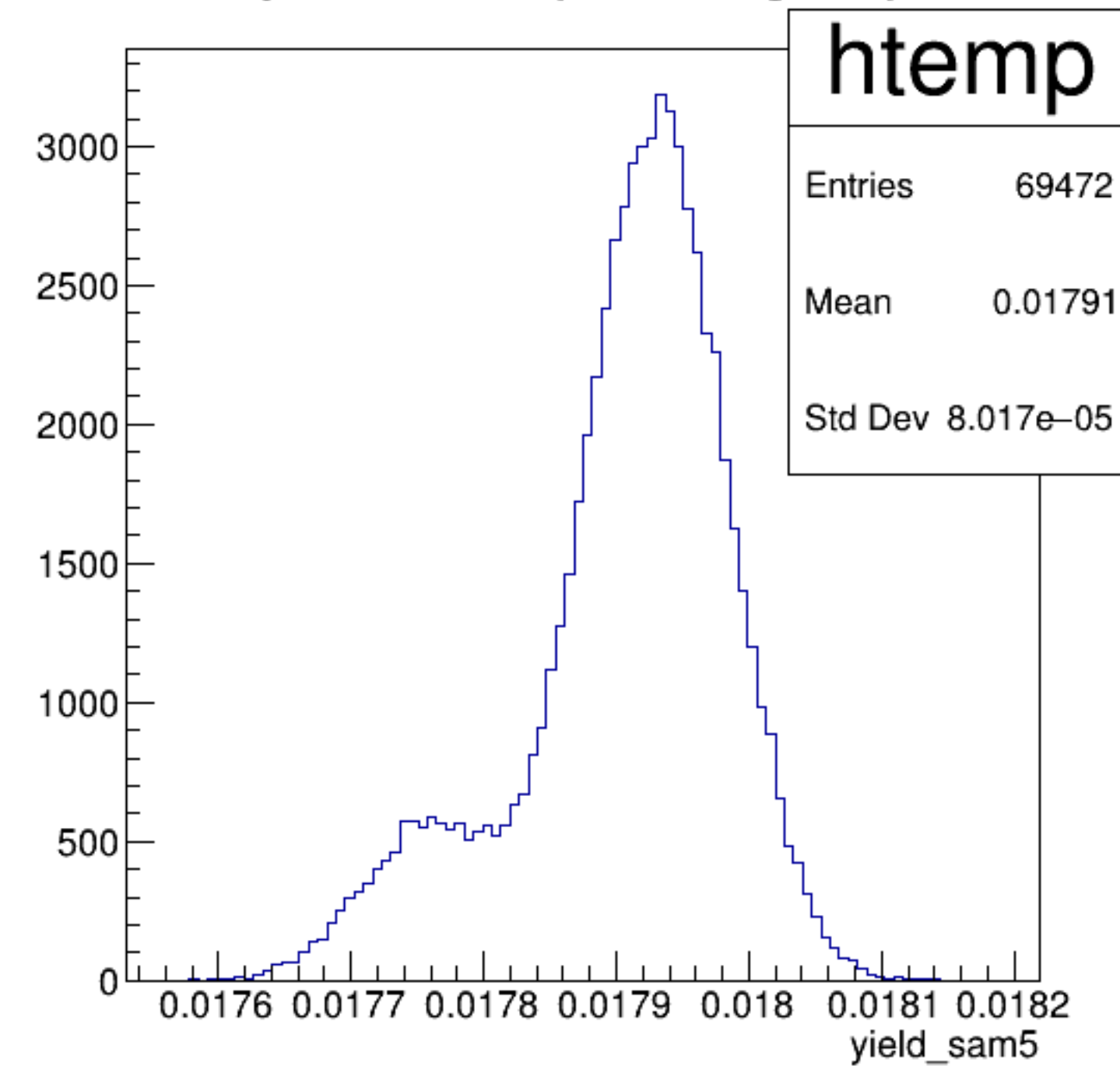


yield_sam5:pattern_number {ErrorFlag==0}

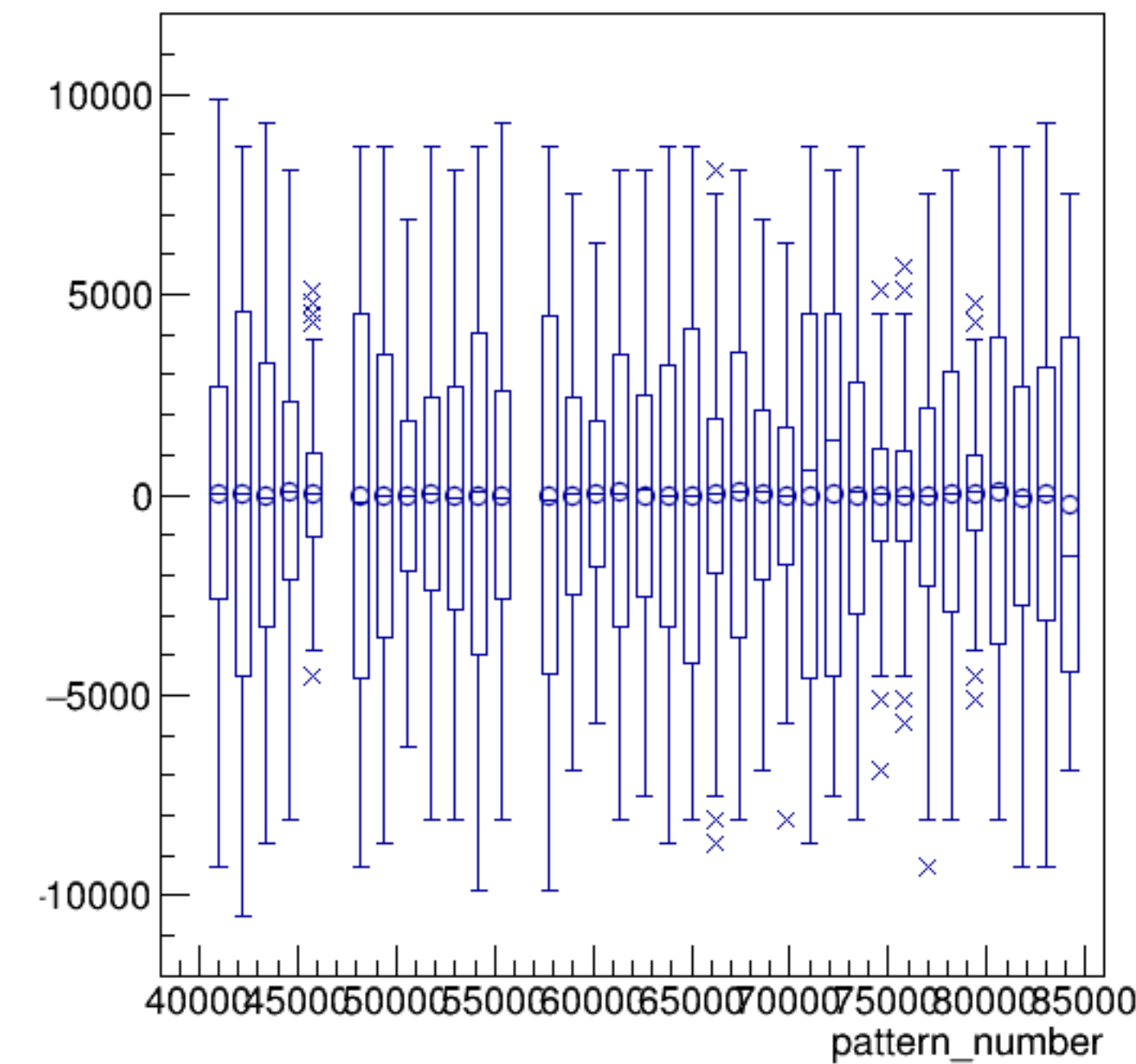


run8336_000_pairTree_sam5.png

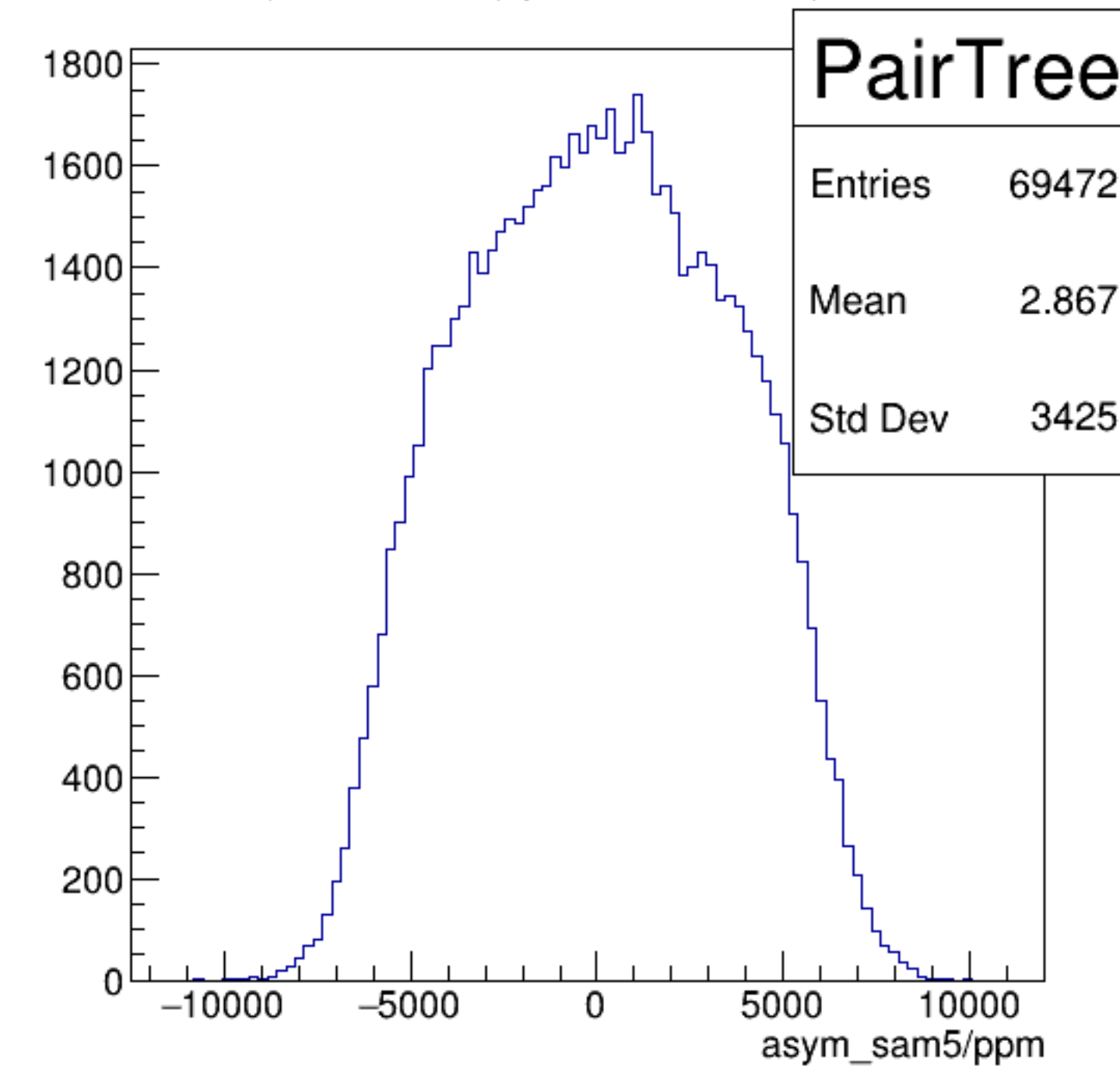
yield_sam5 {ErrorFlag==0}

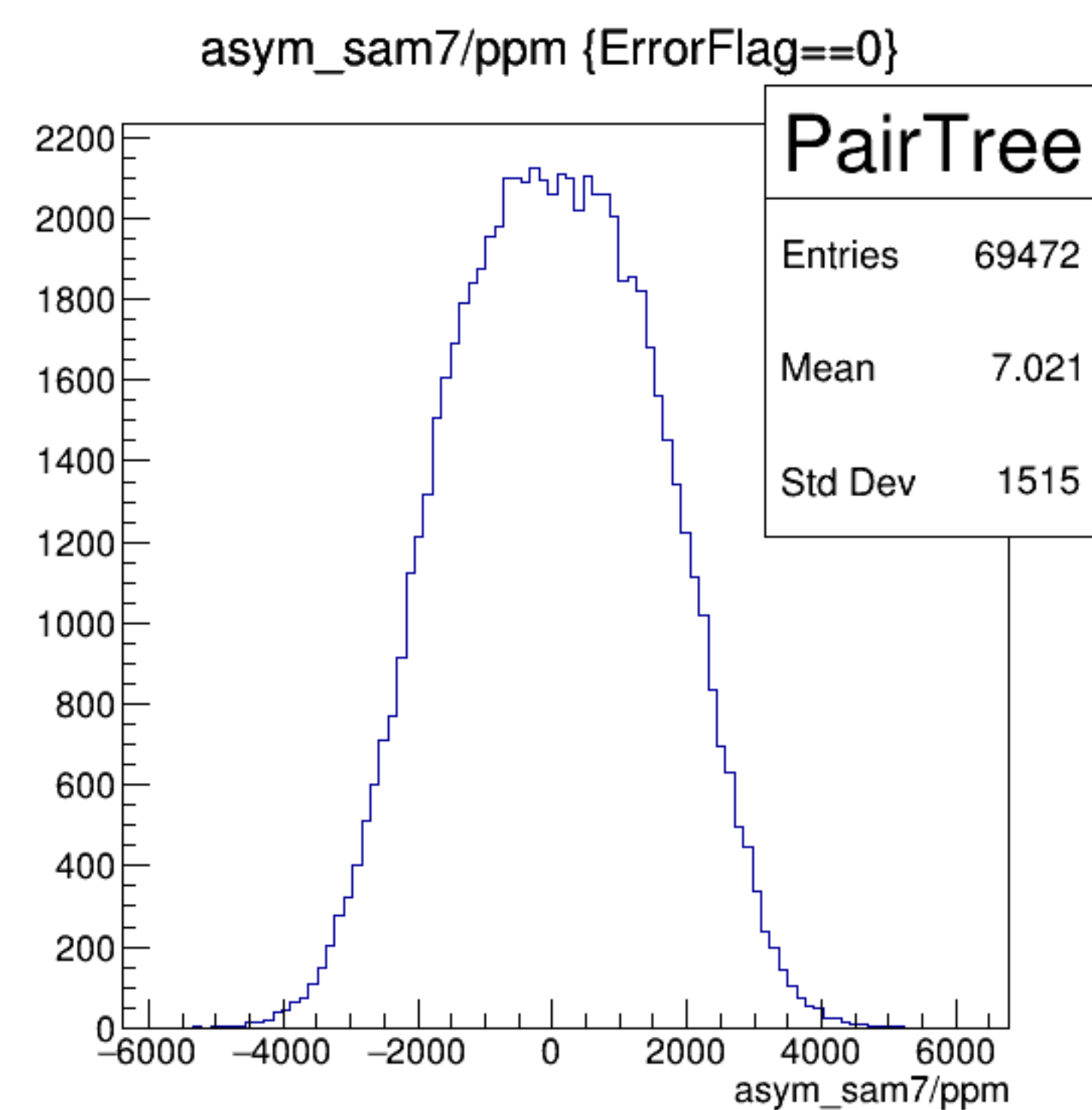
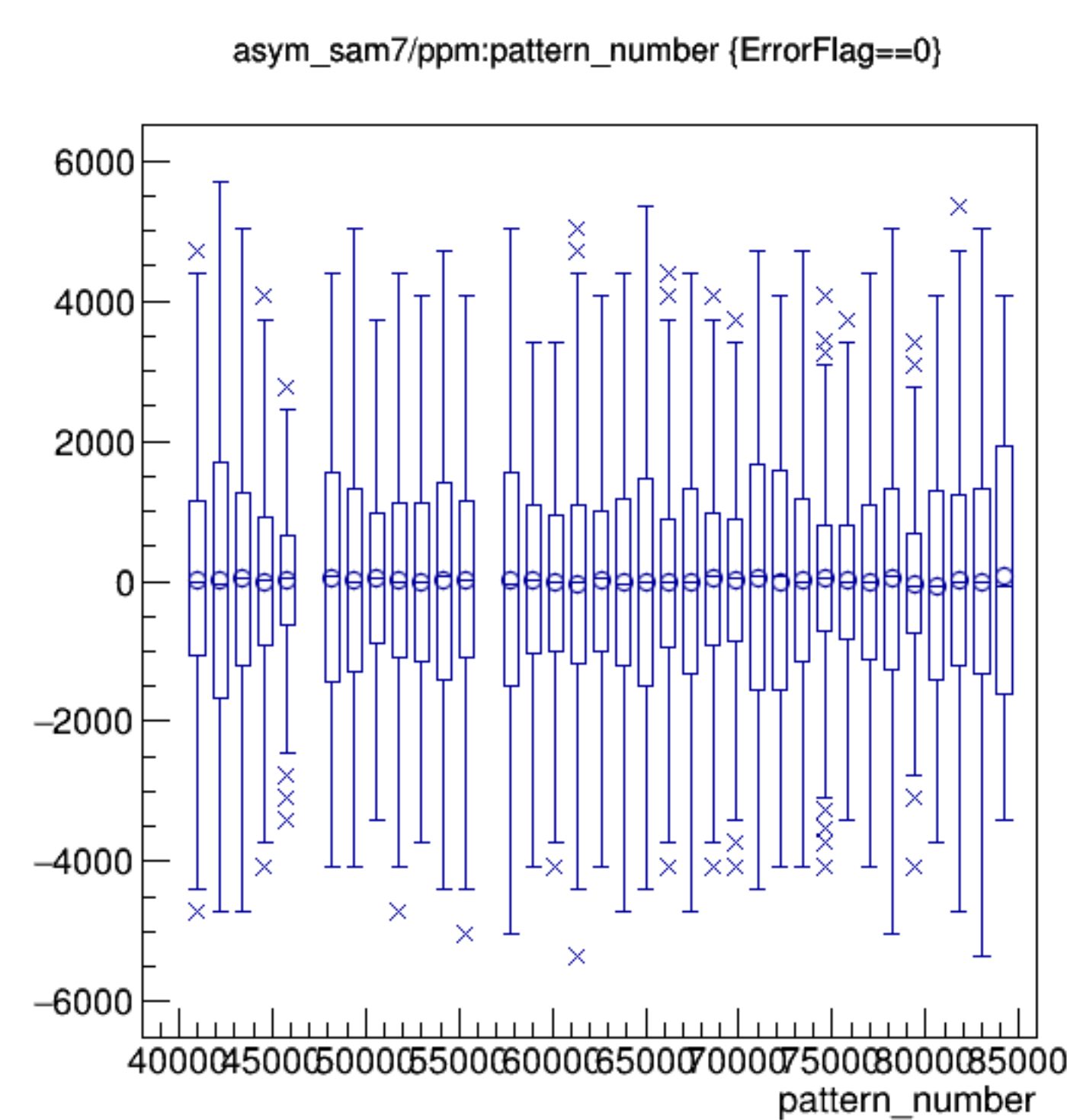
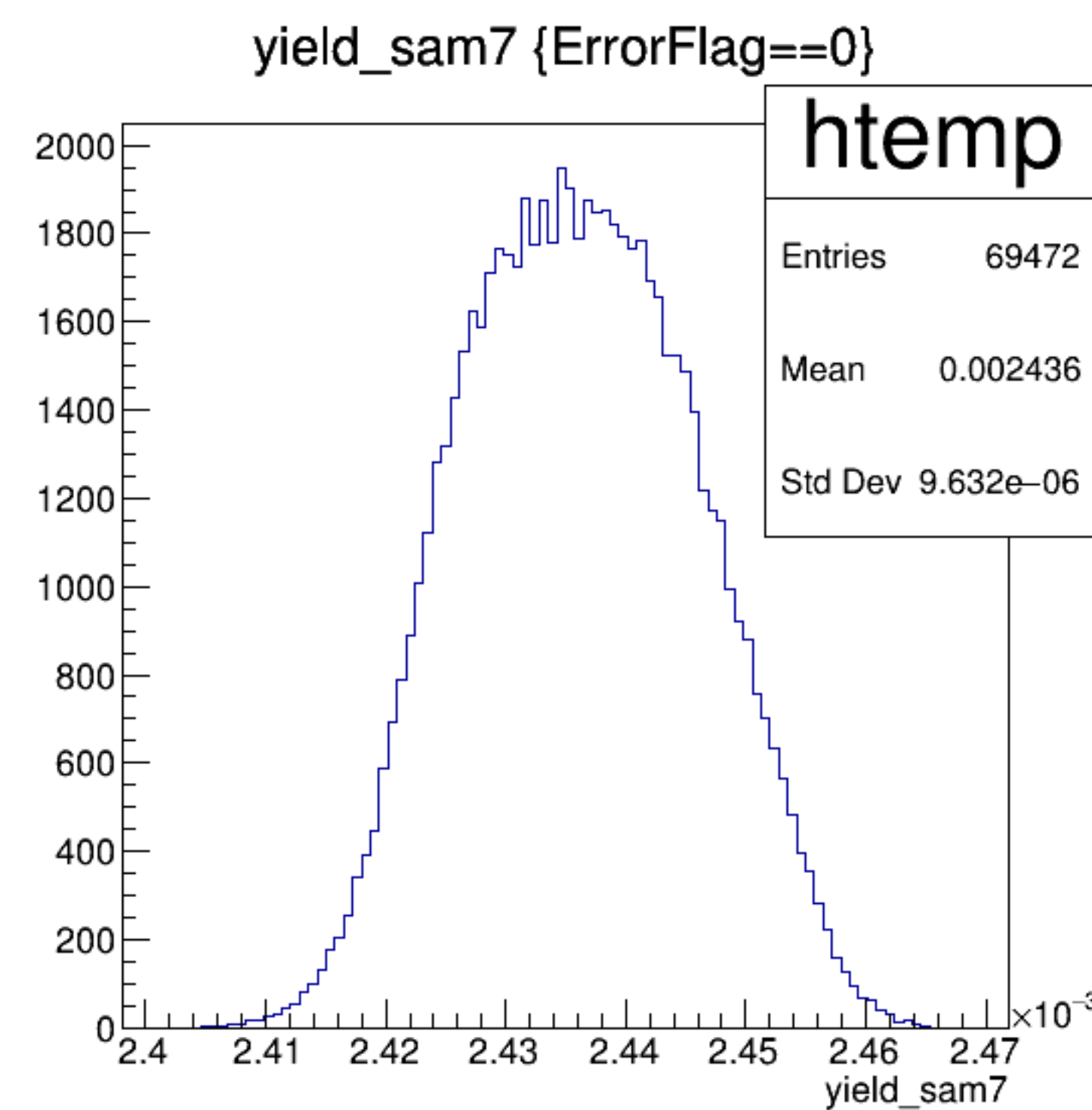
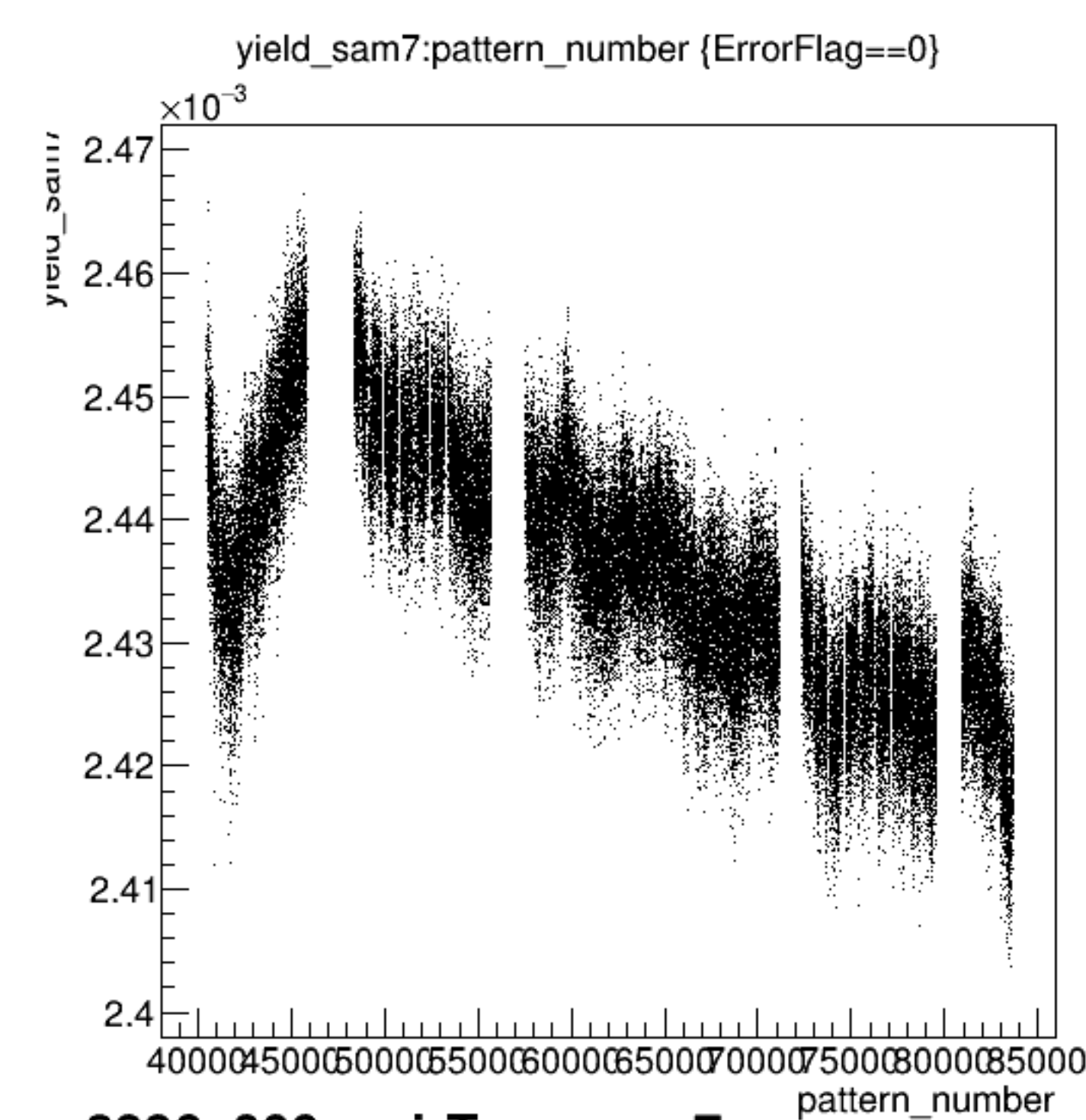


asym_sam5/ppm:pattern_number {ErrorFlag==0}

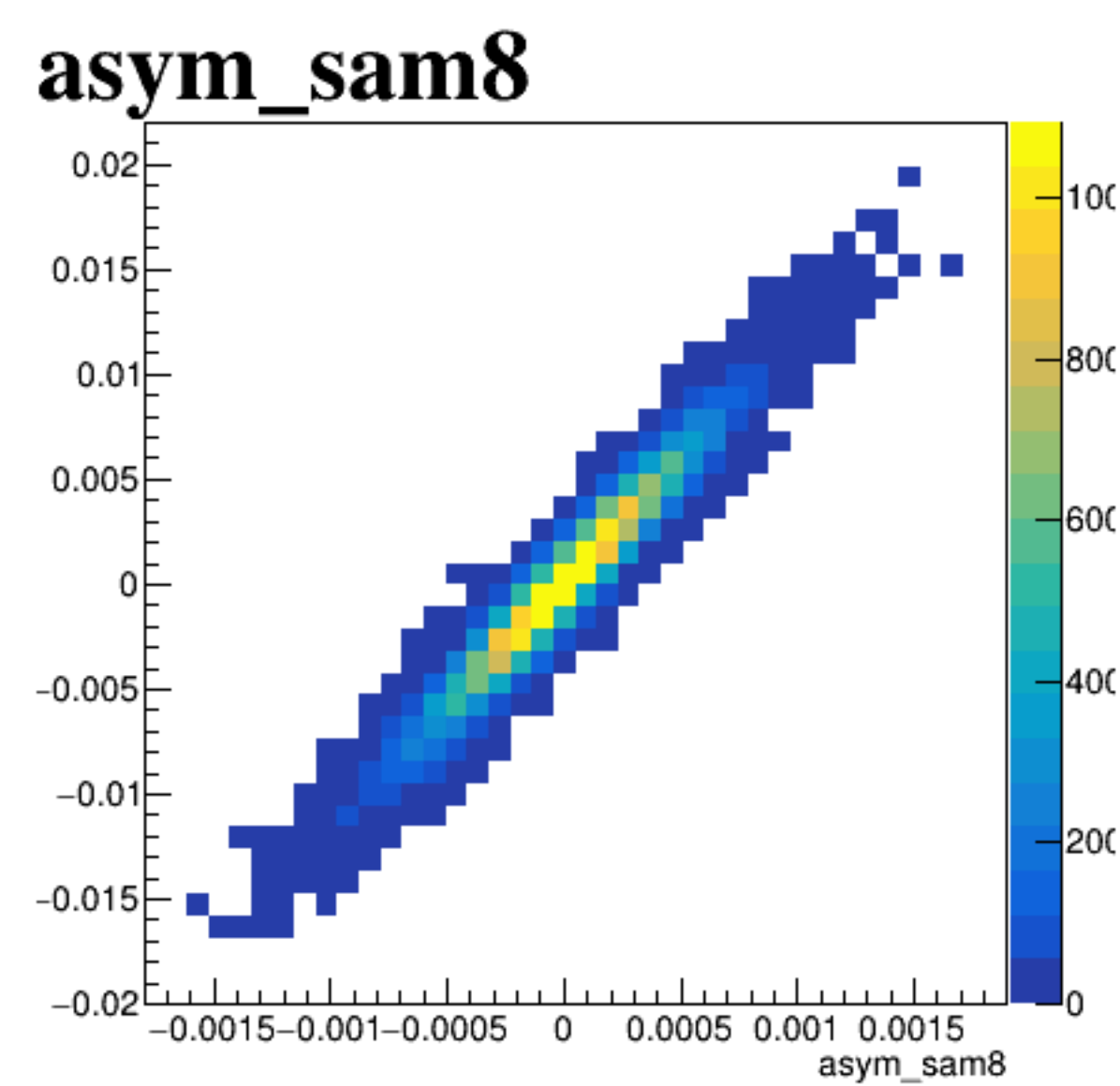
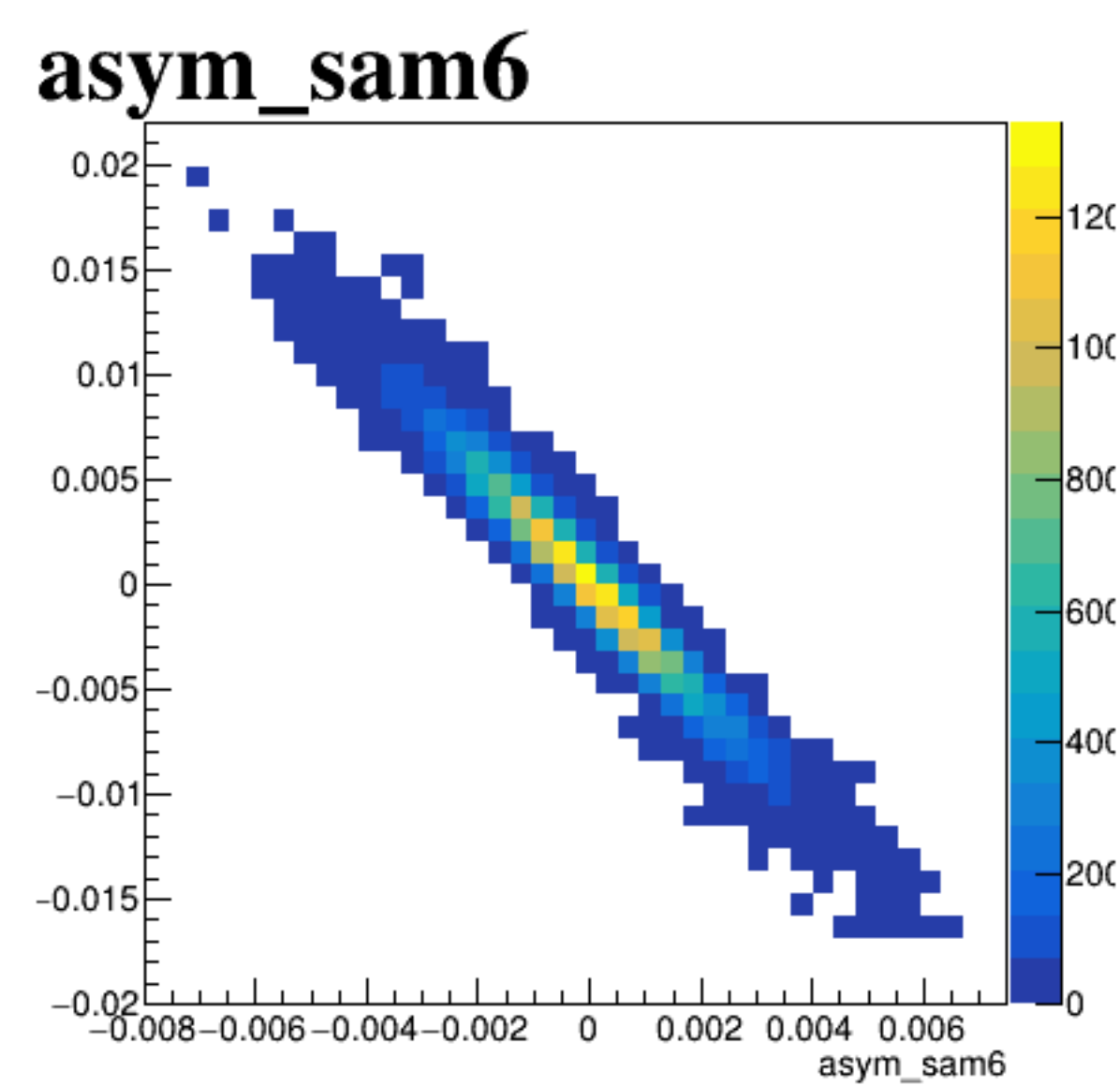
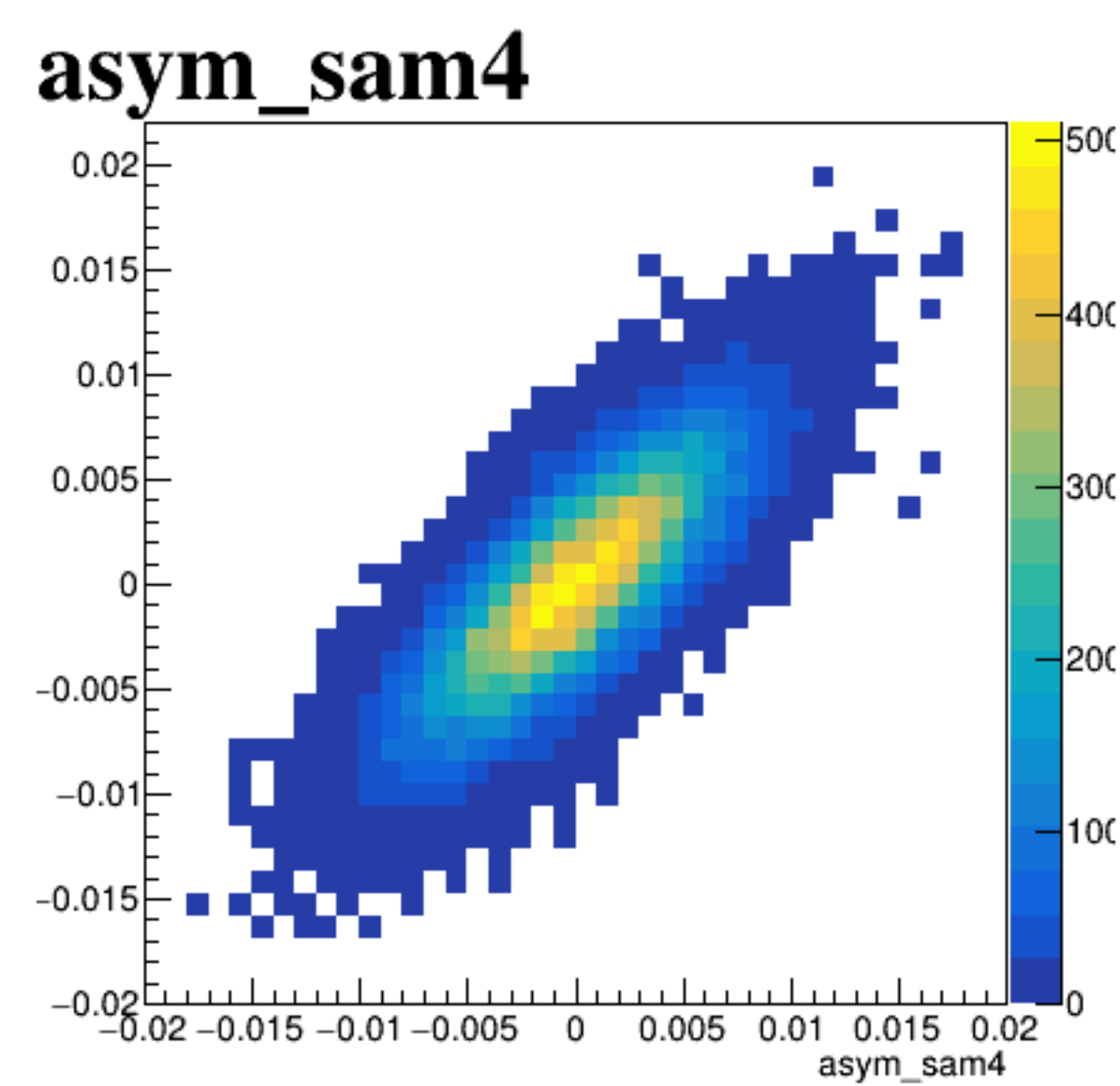
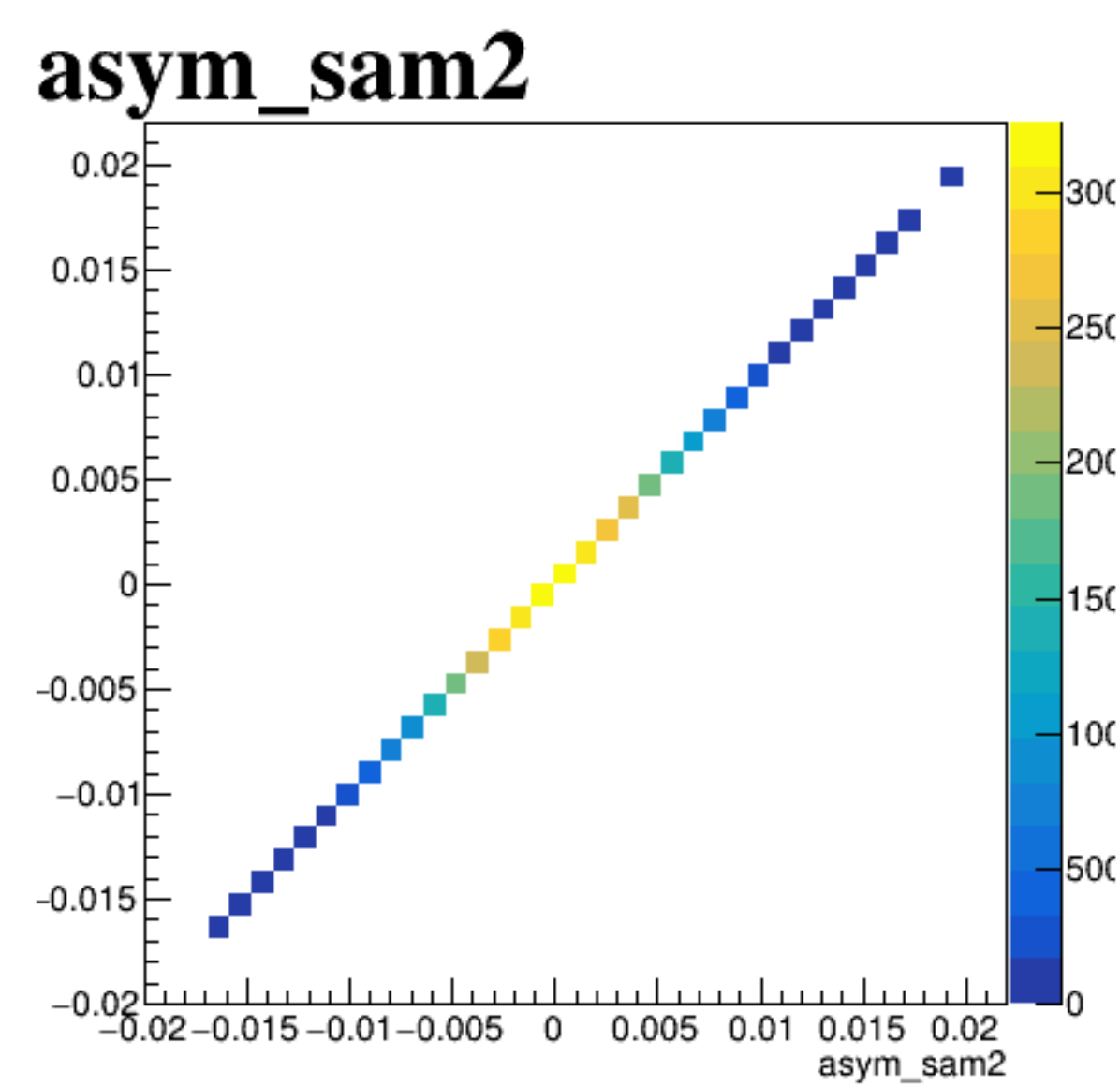


asym_sam5/ppm {ErrorFlag==0}

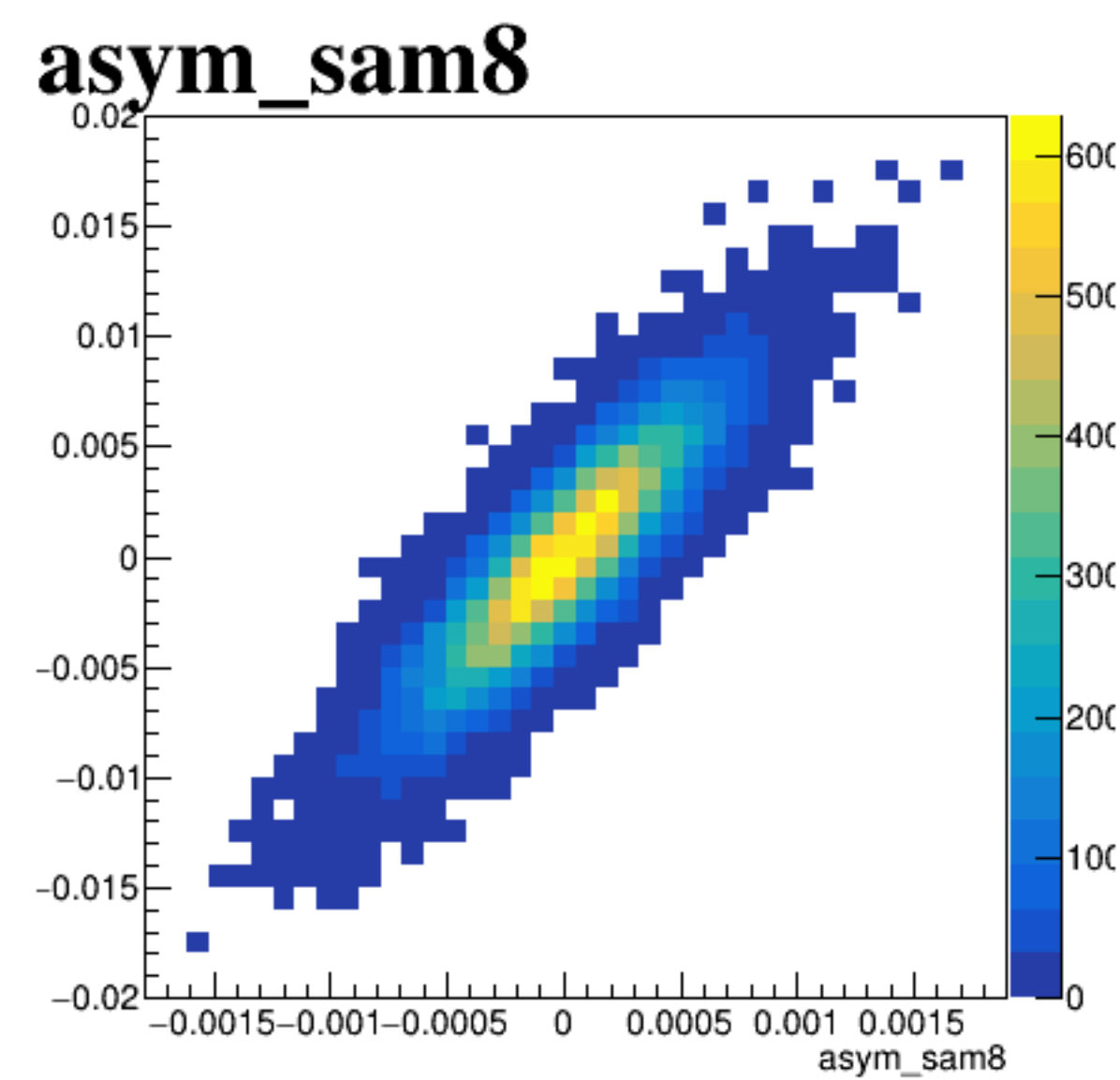
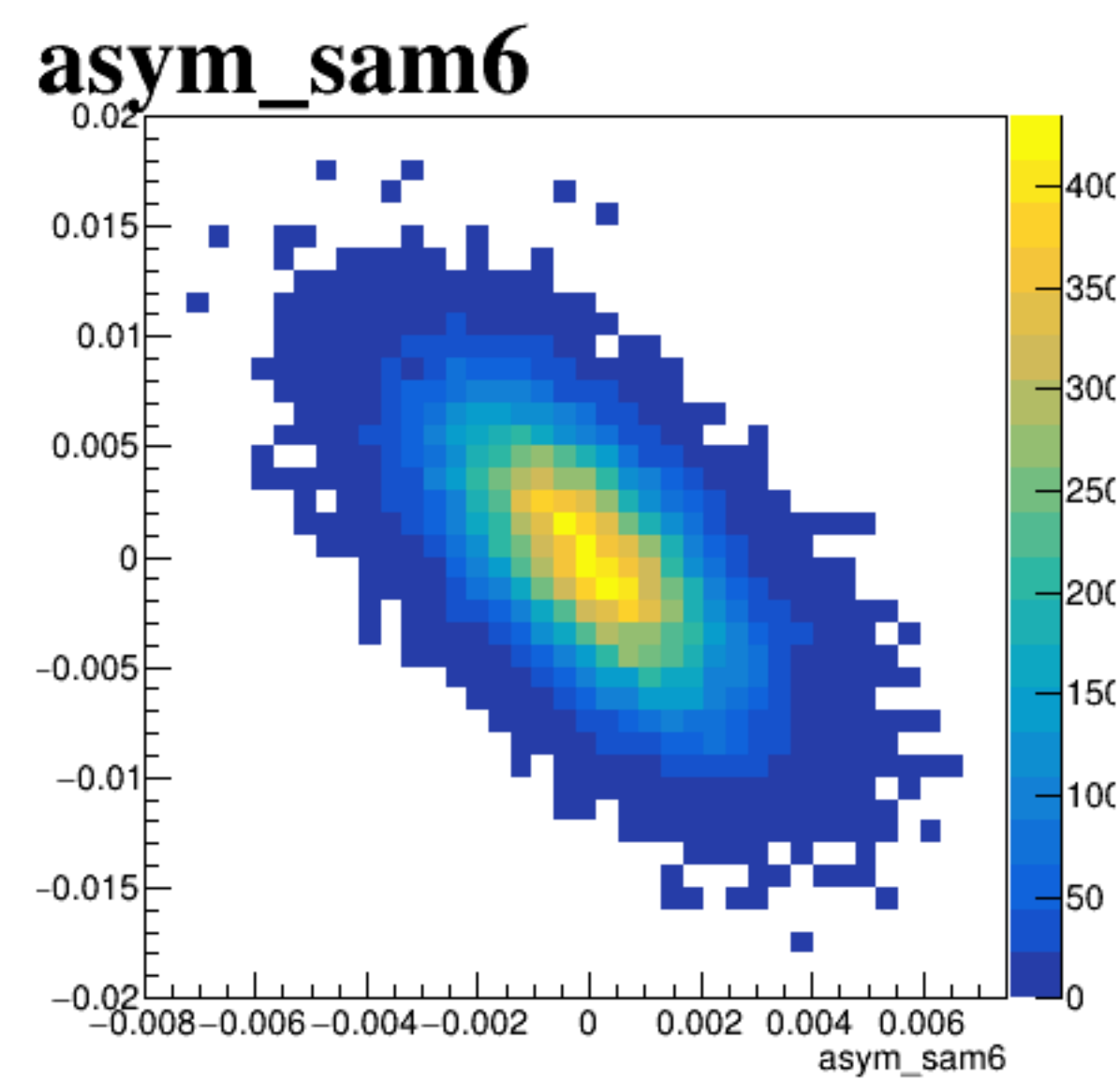
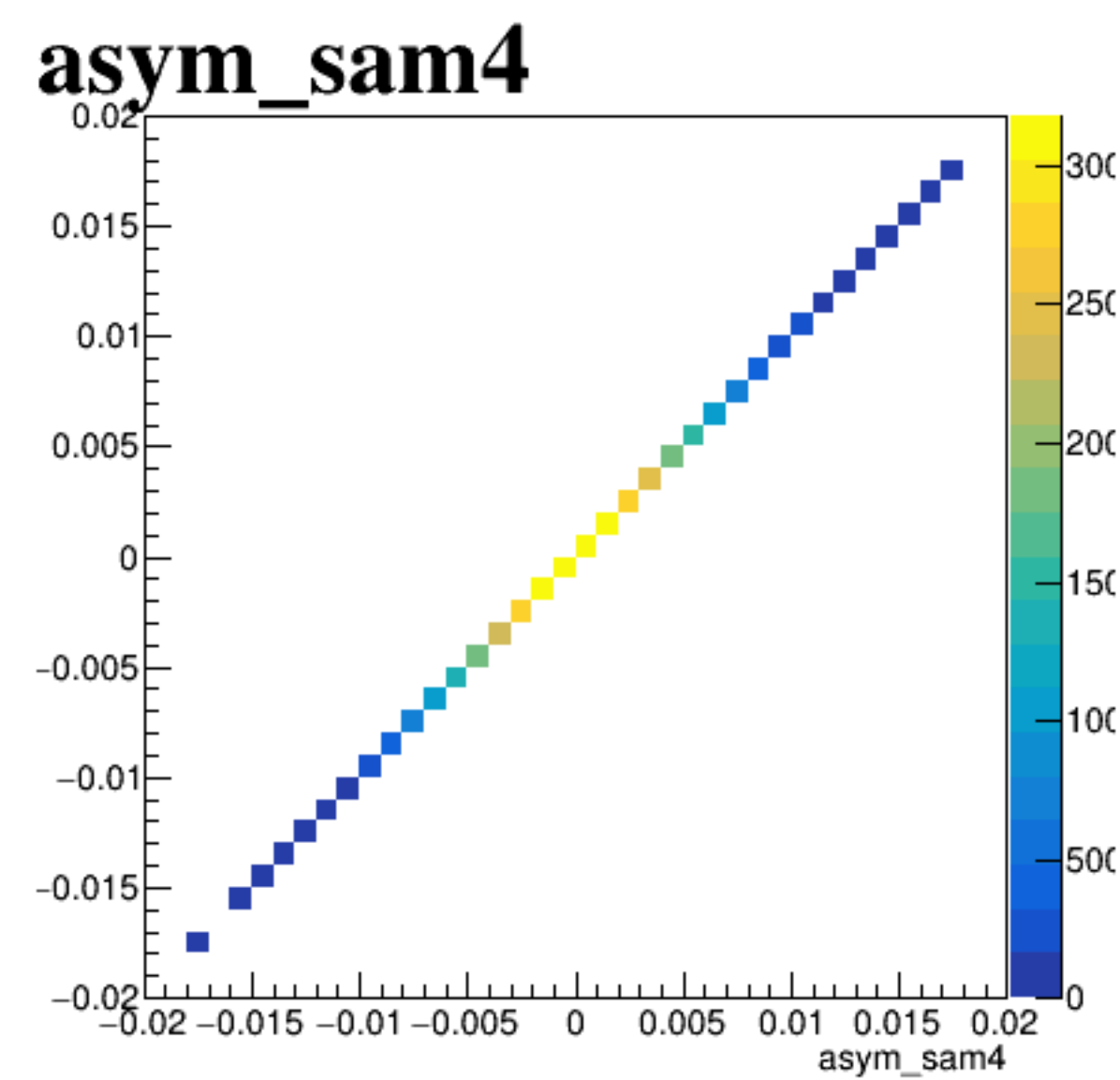
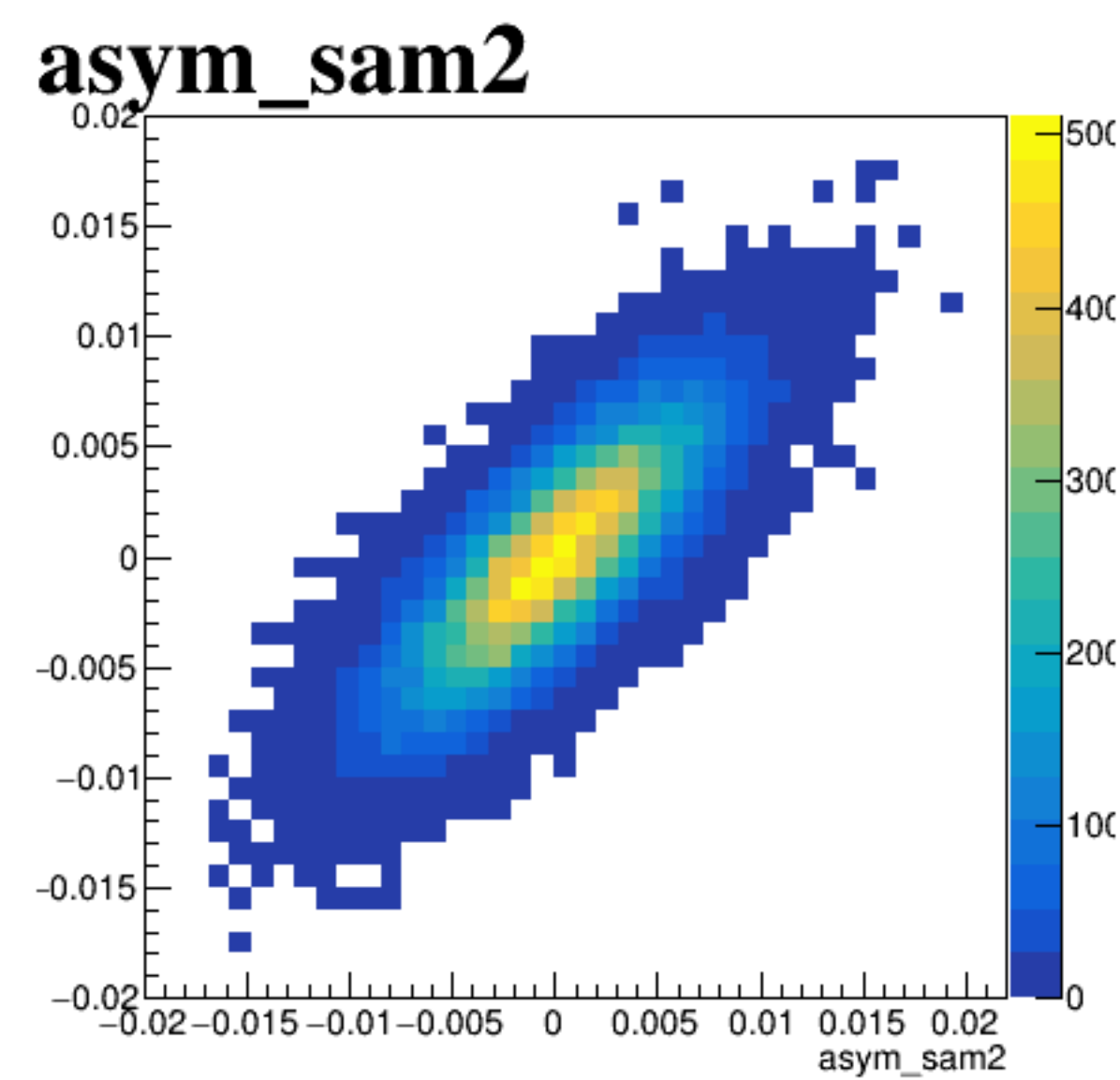




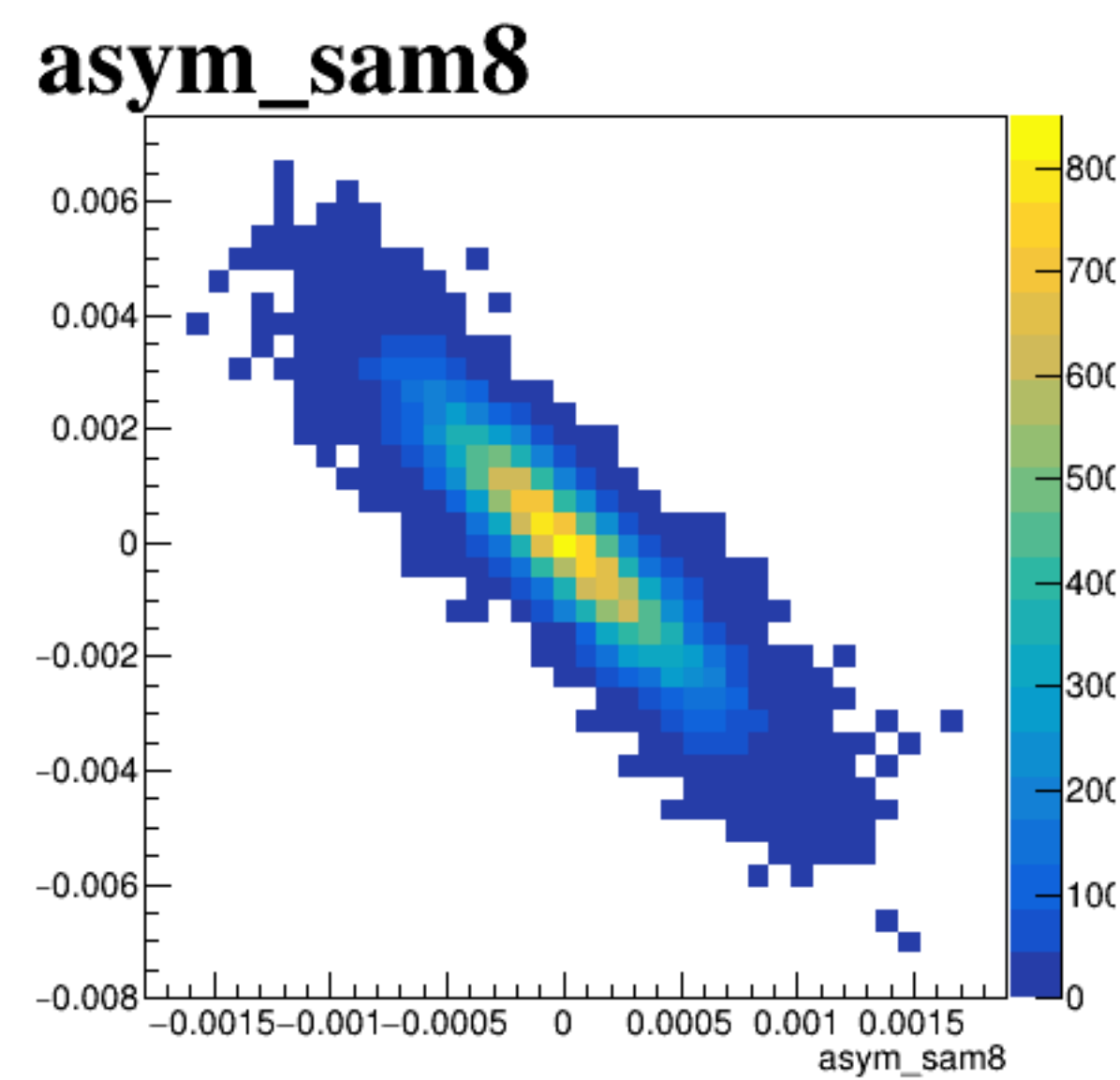
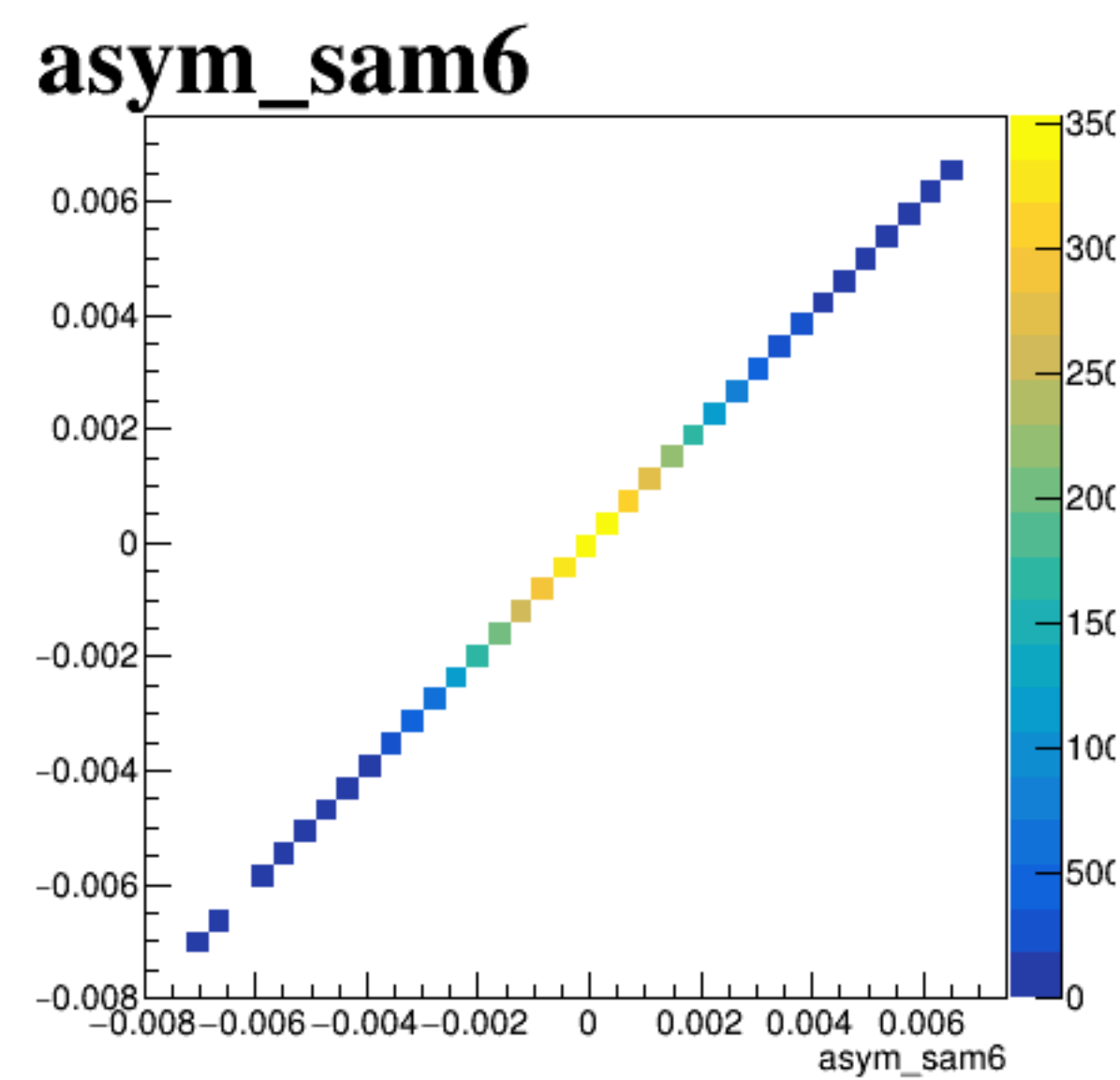
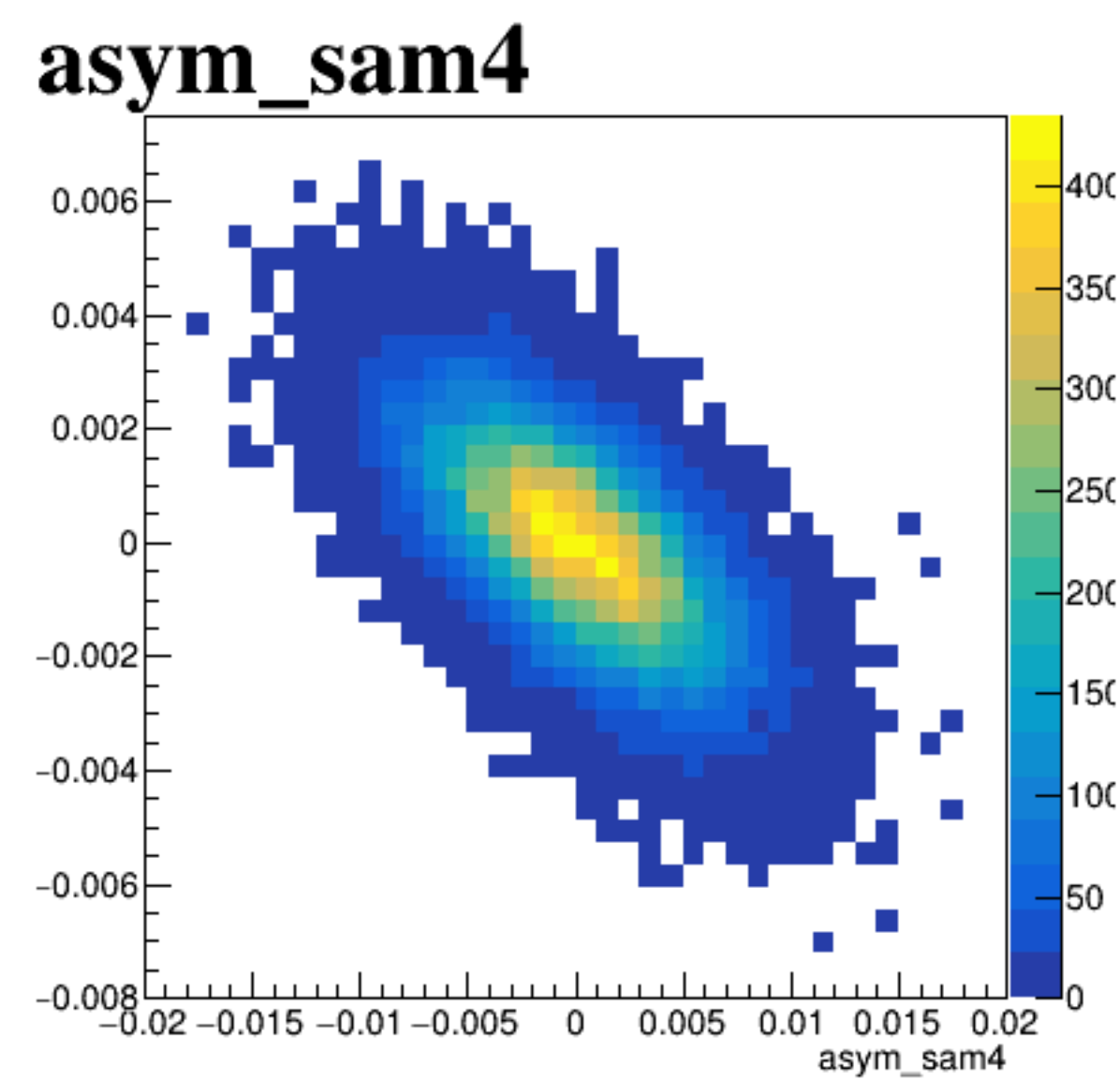
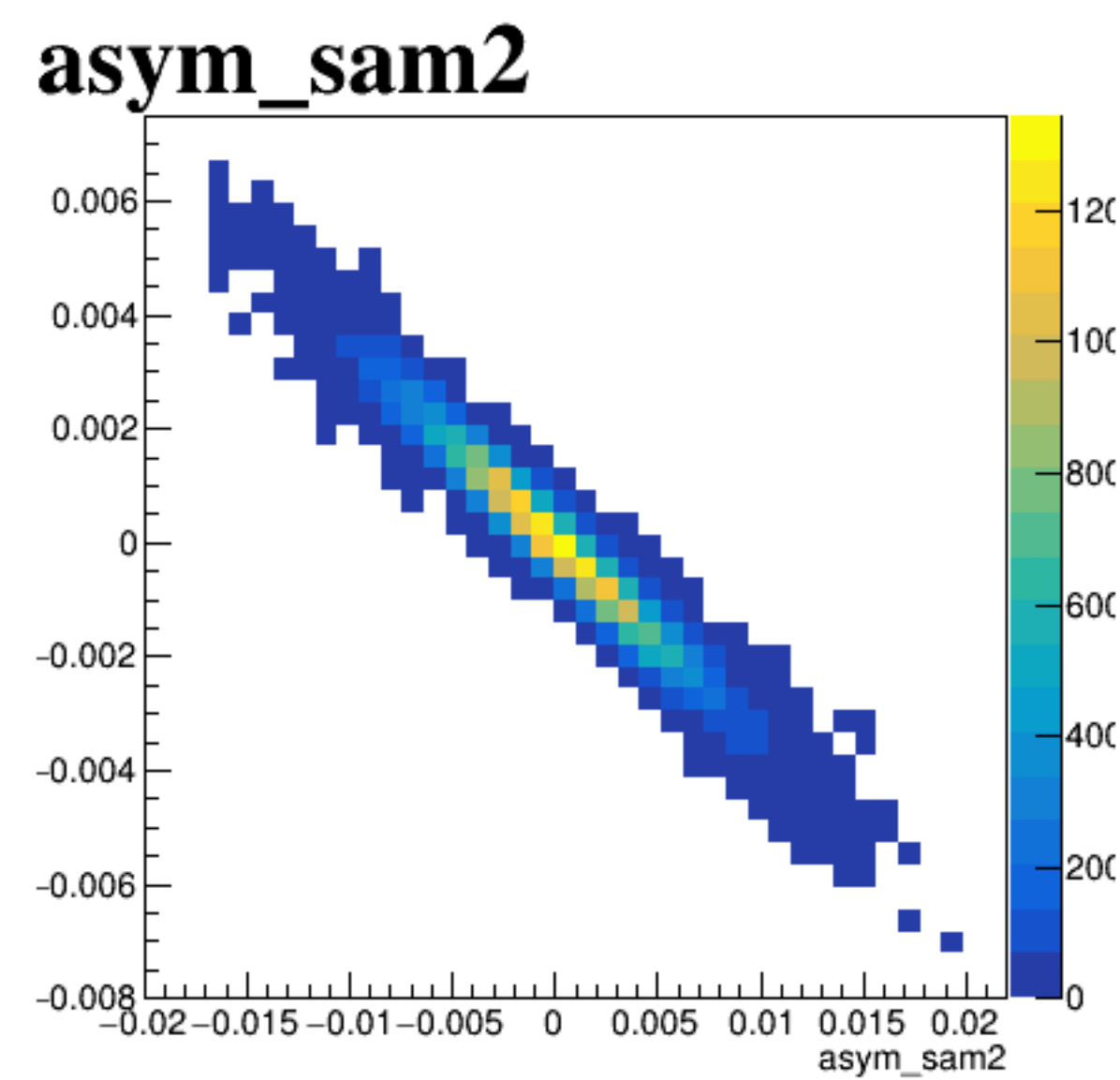
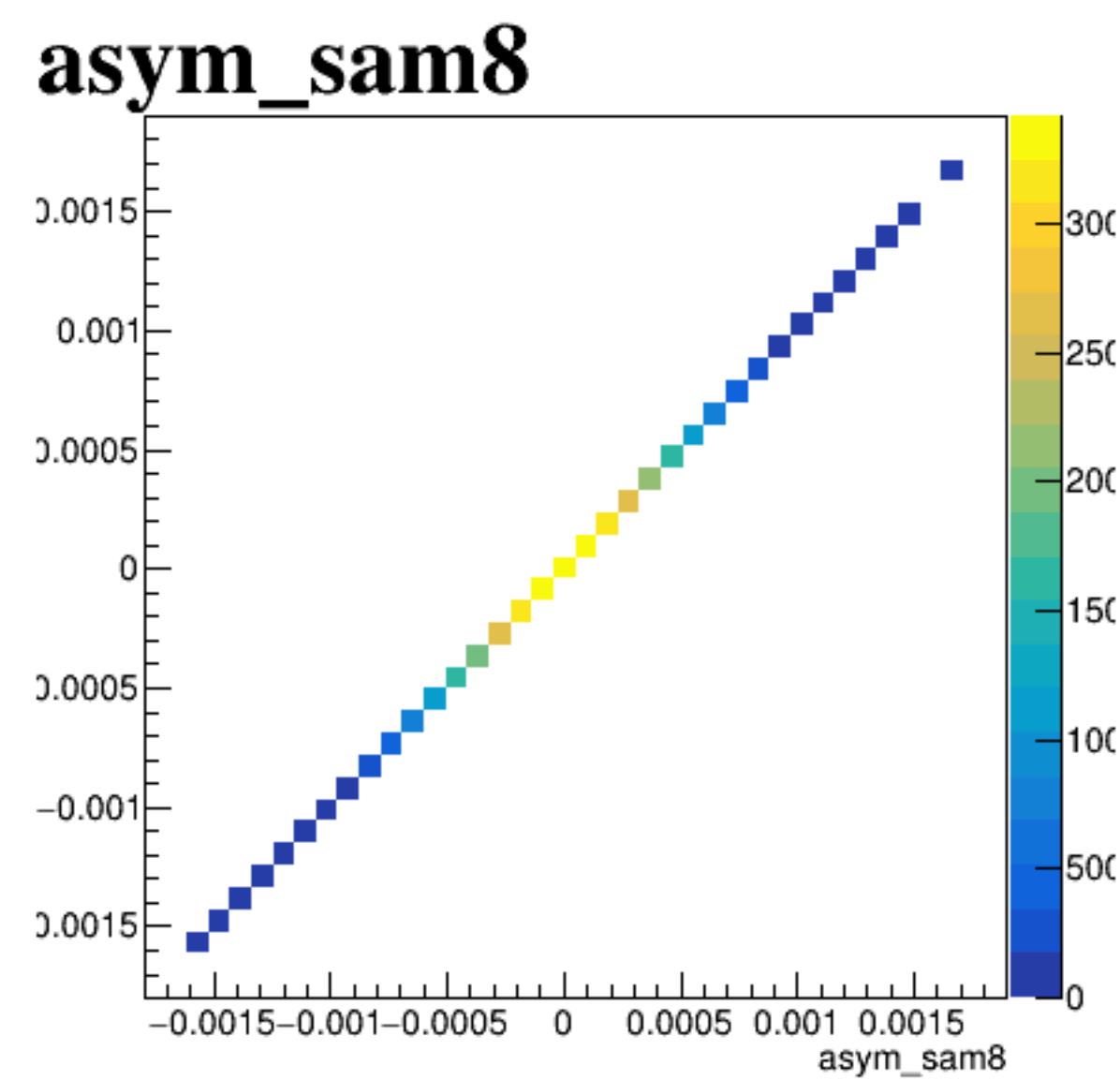
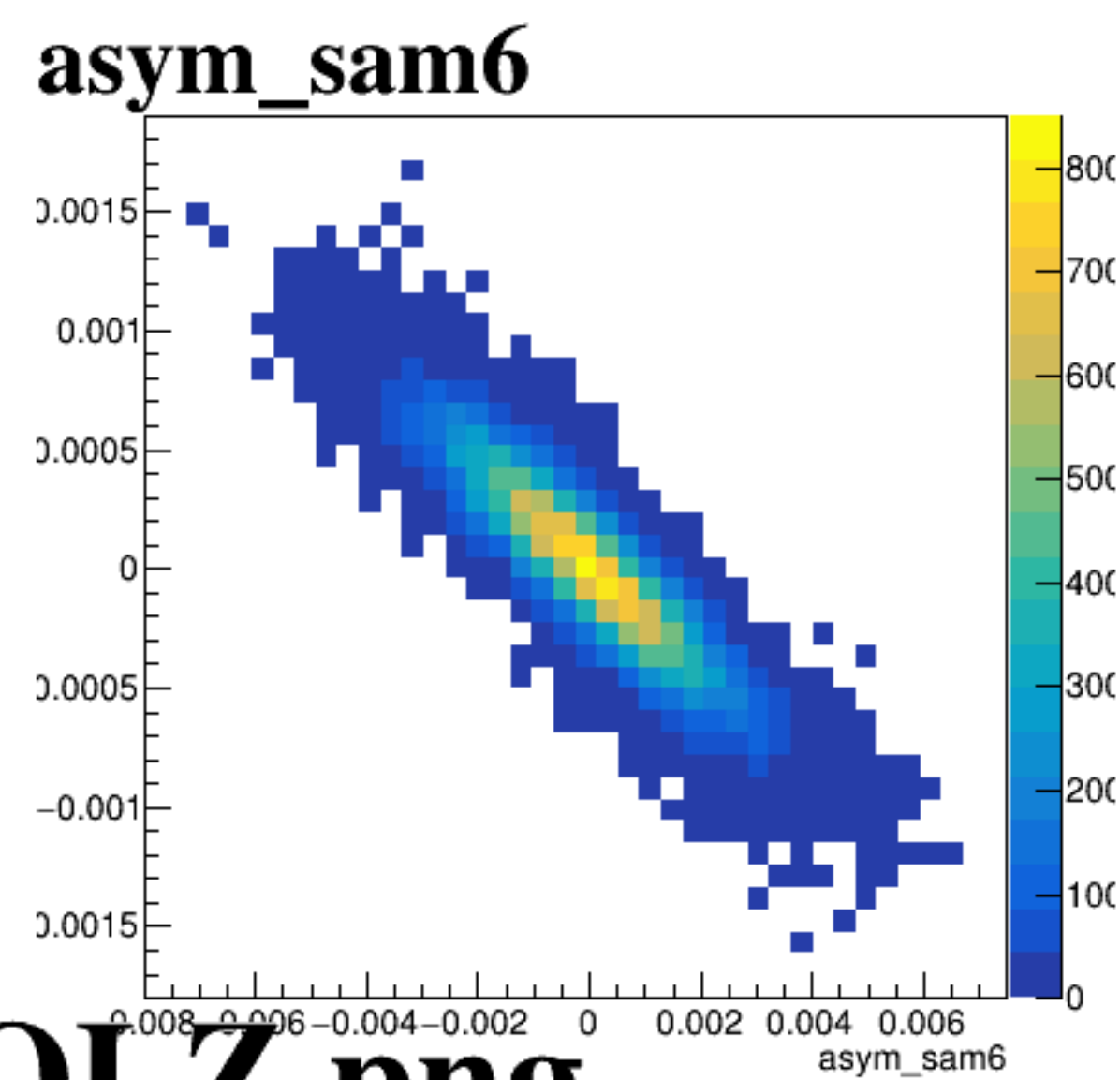
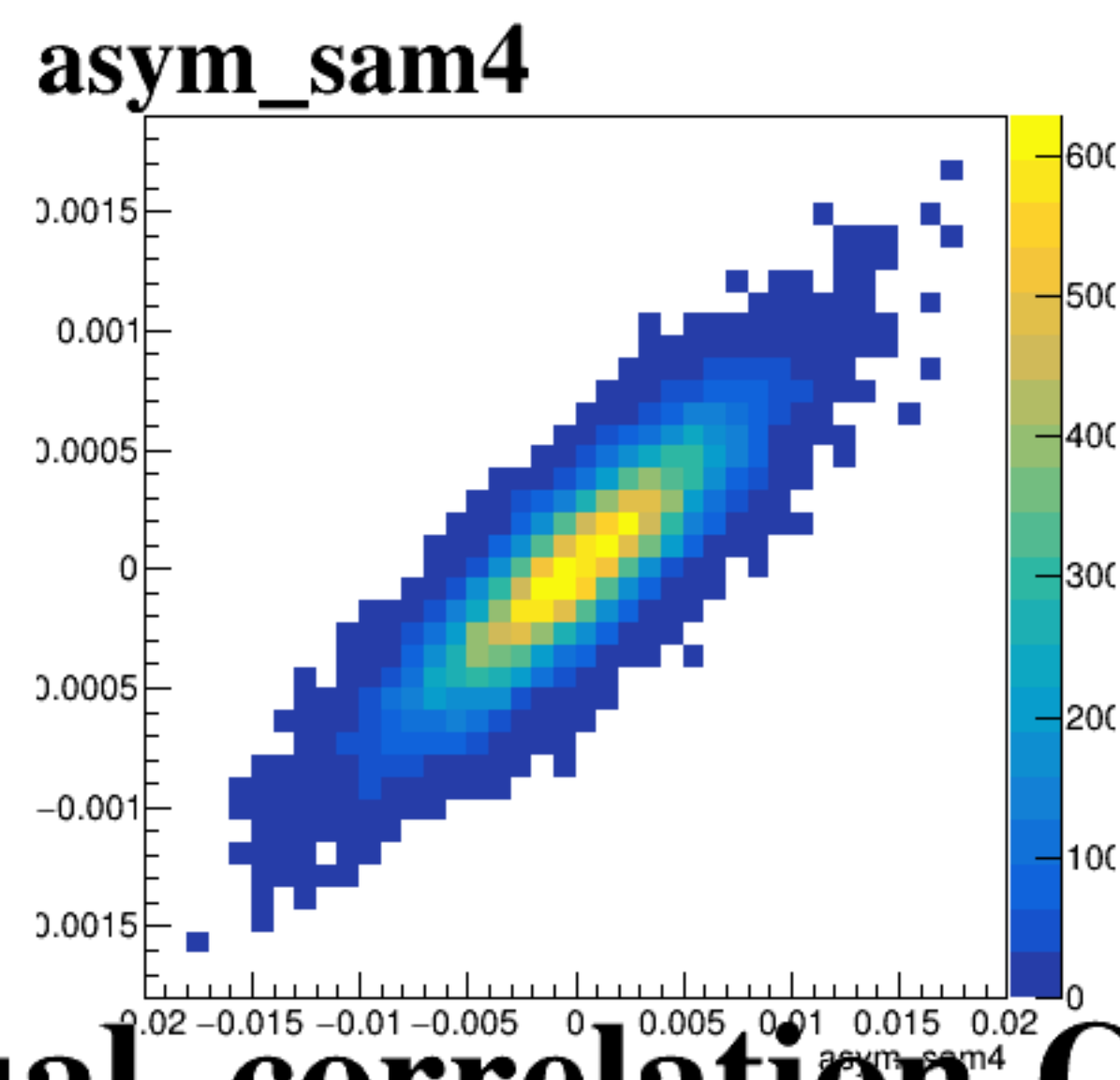
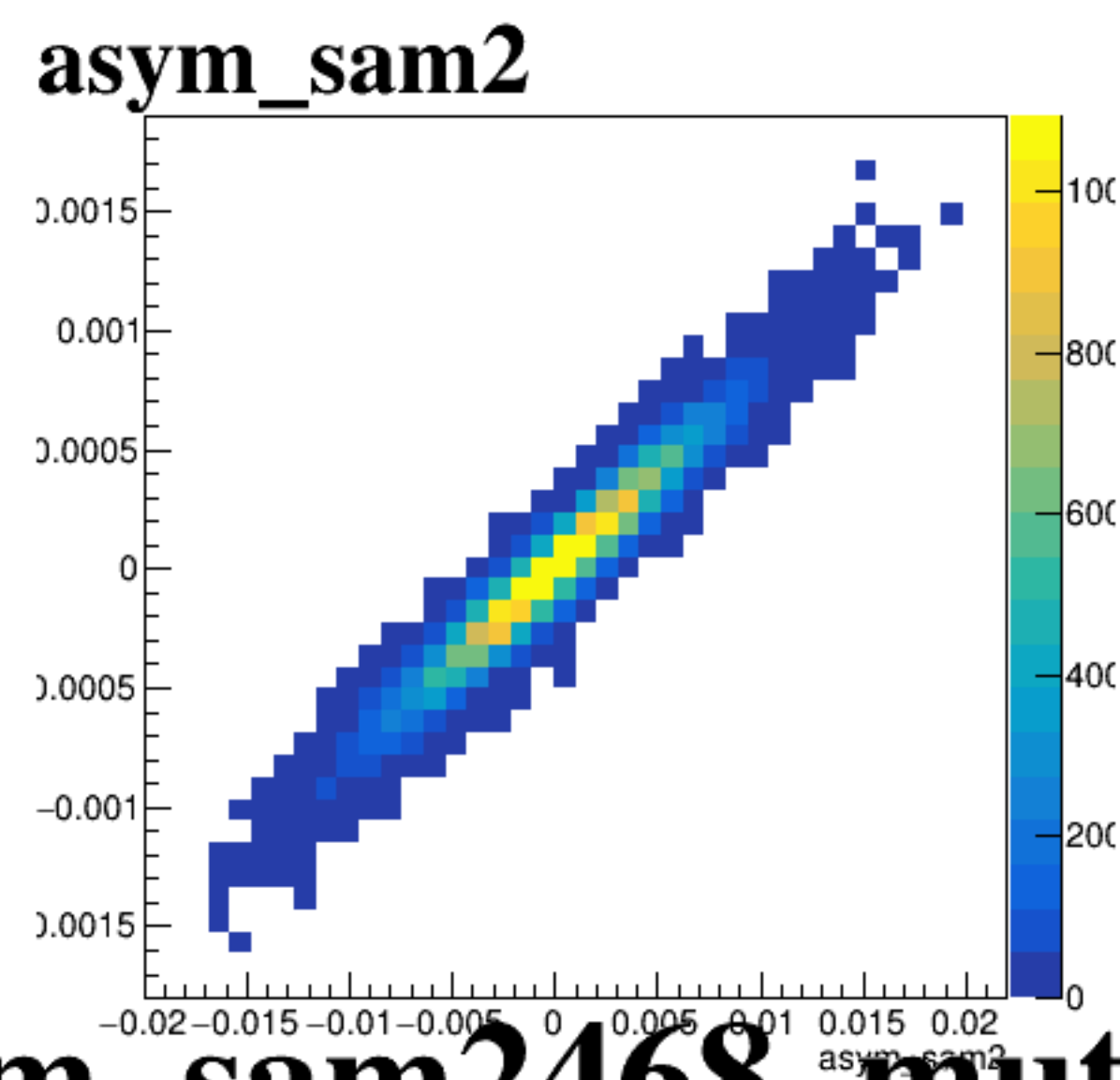
asym_sam2



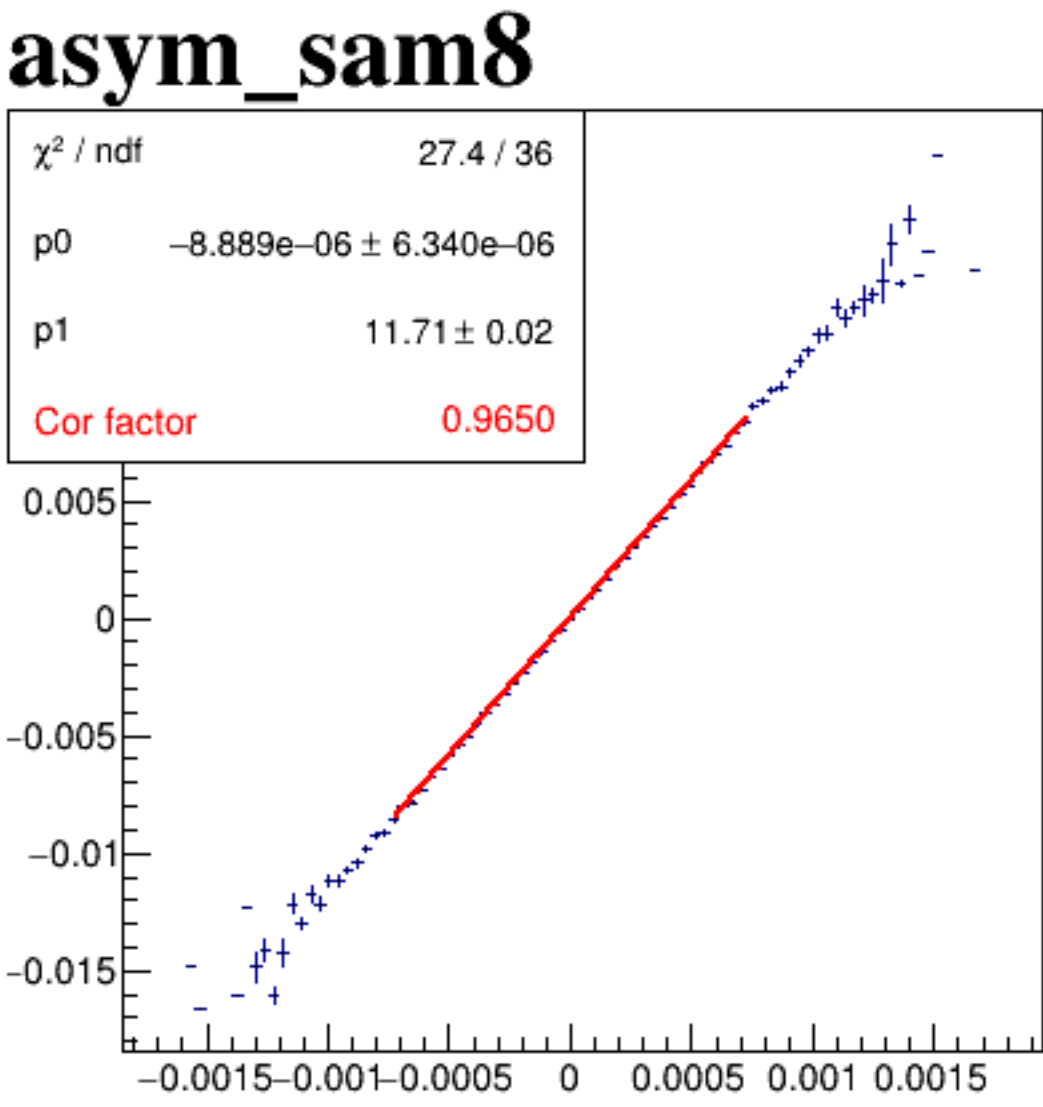
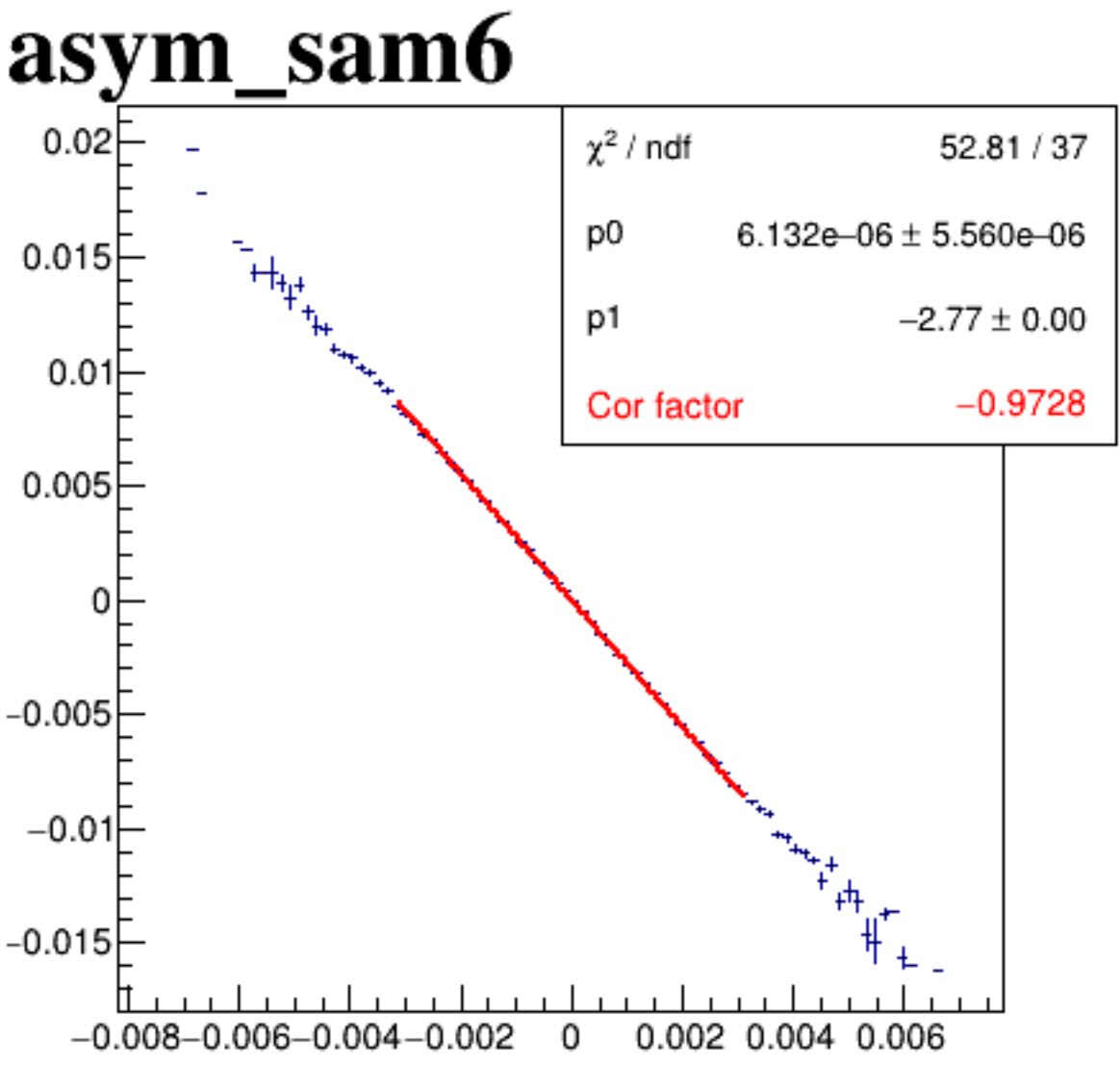
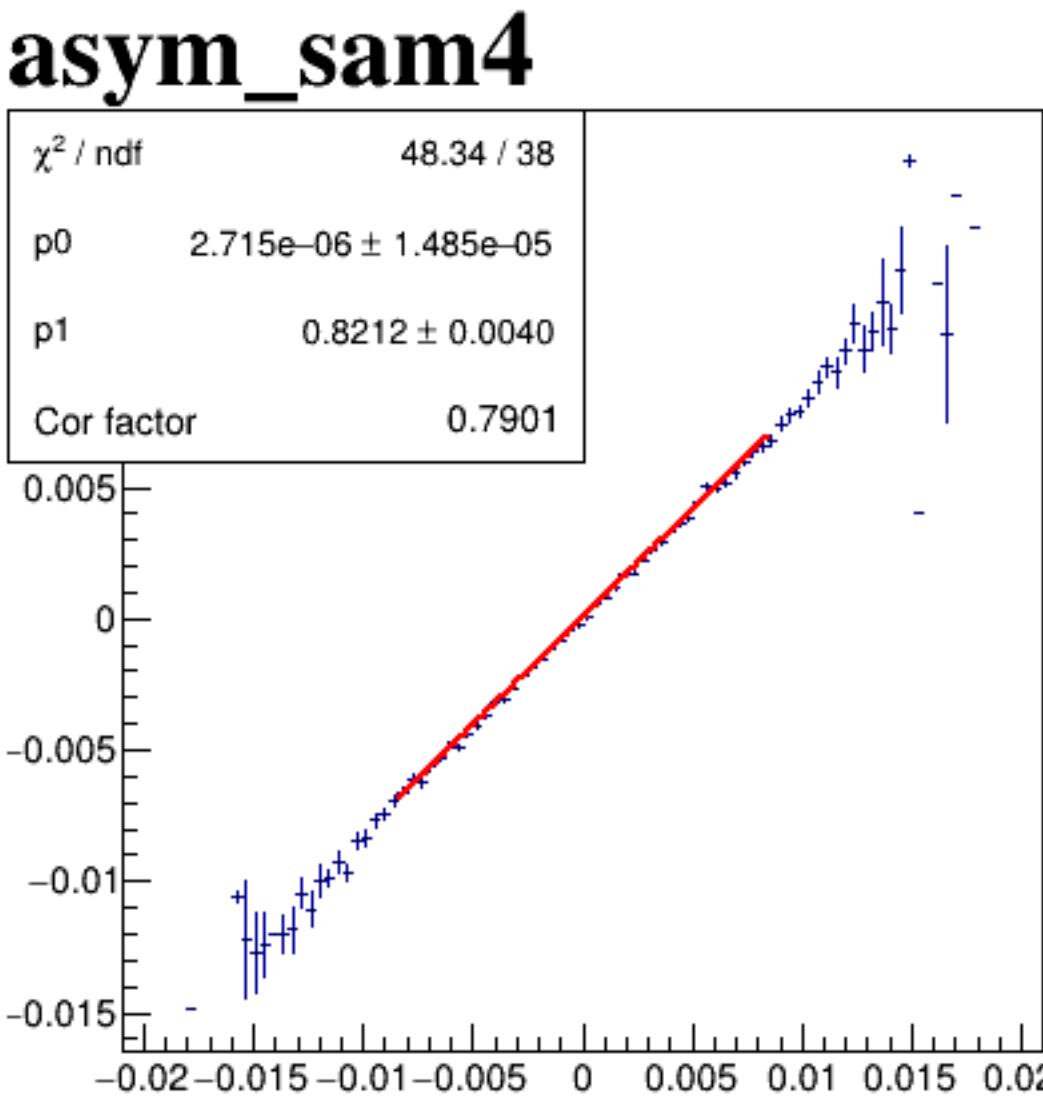
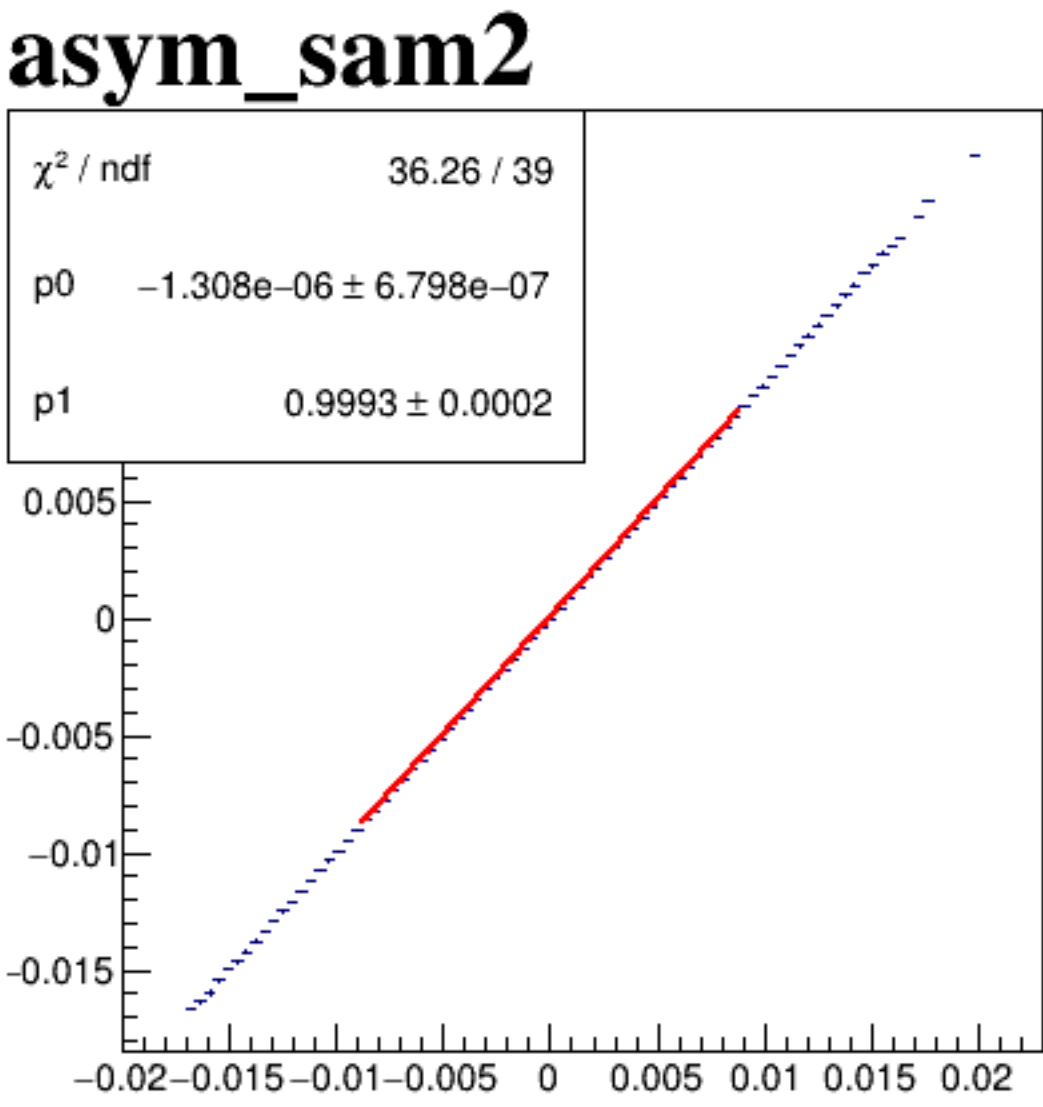
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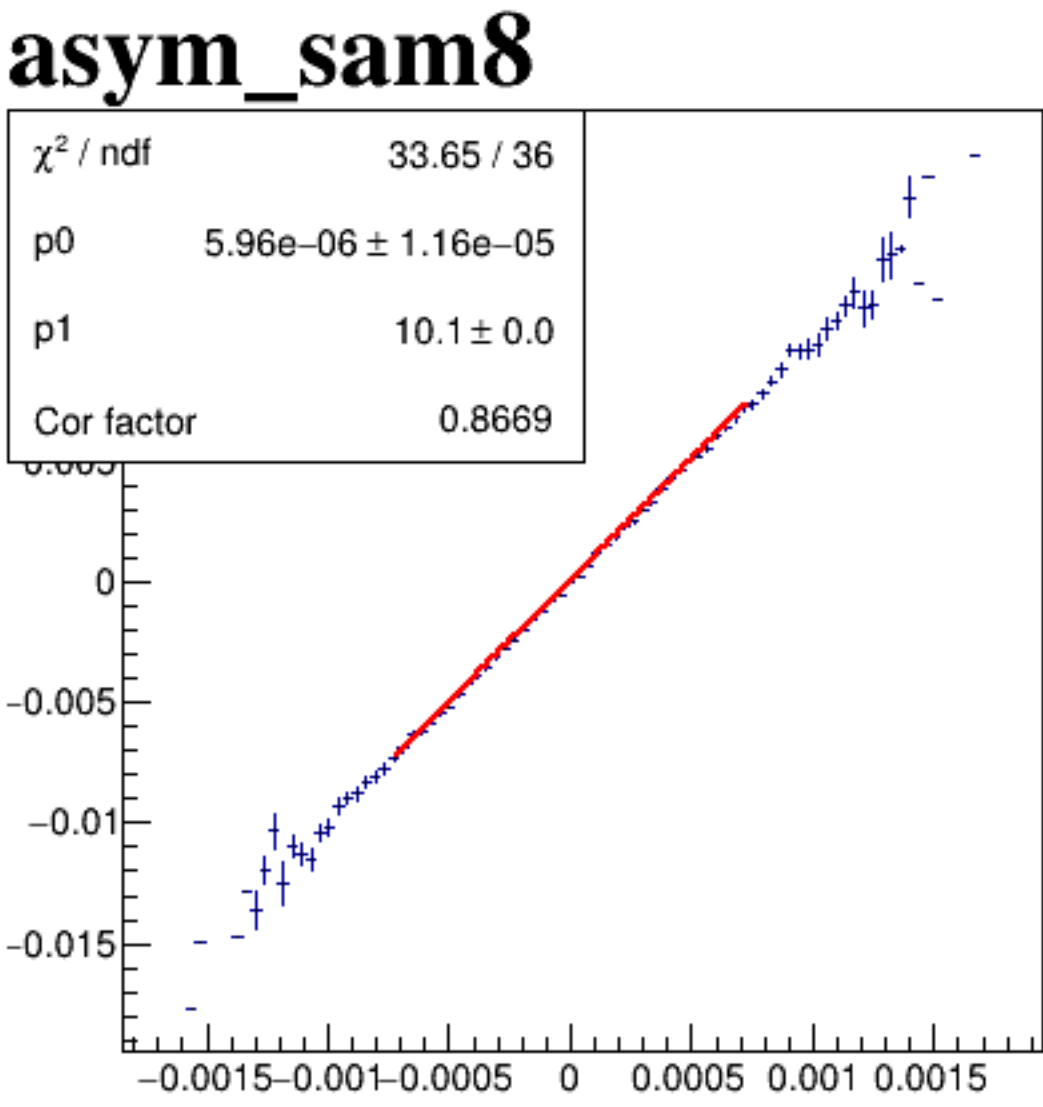
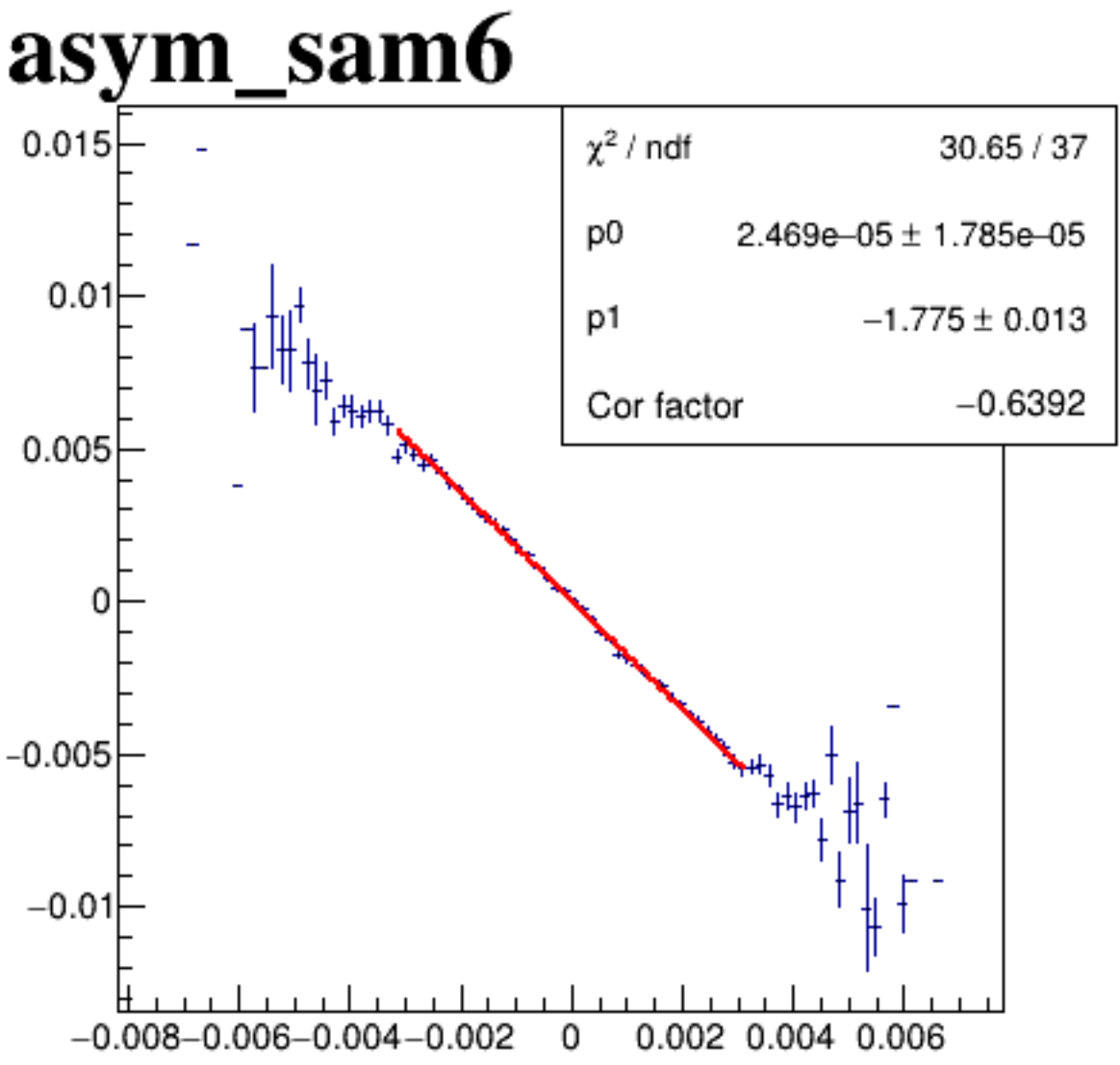
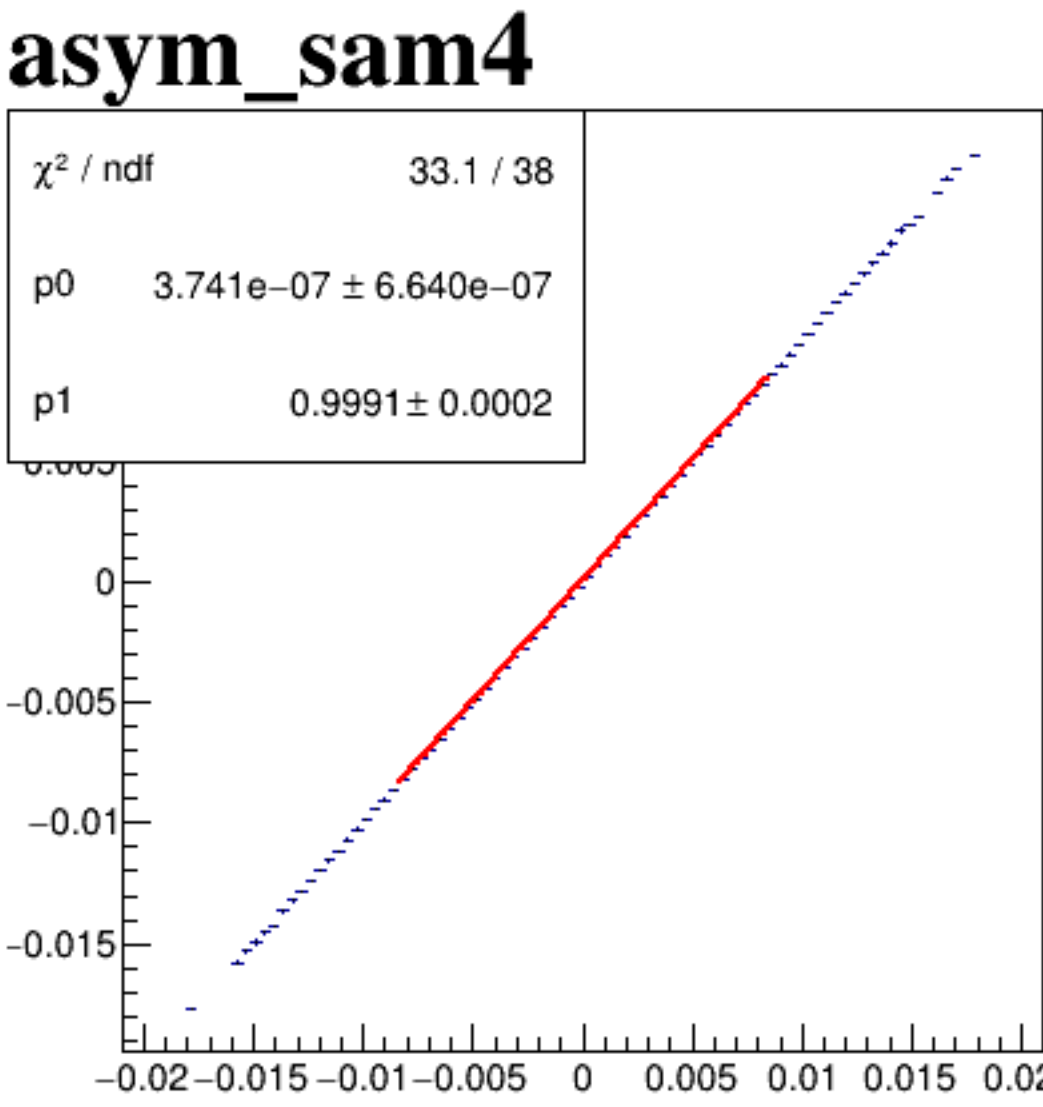
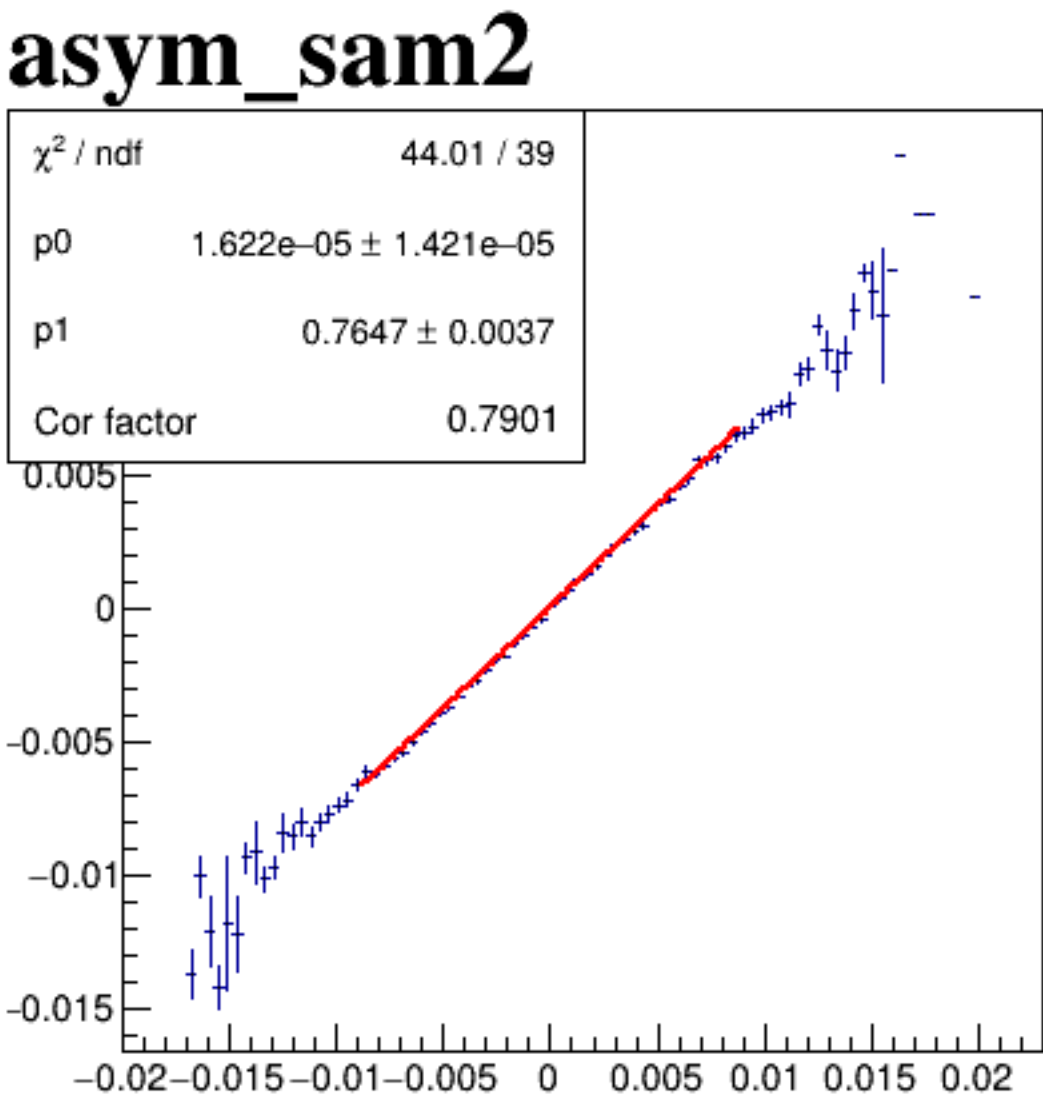
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**asym_sam8**

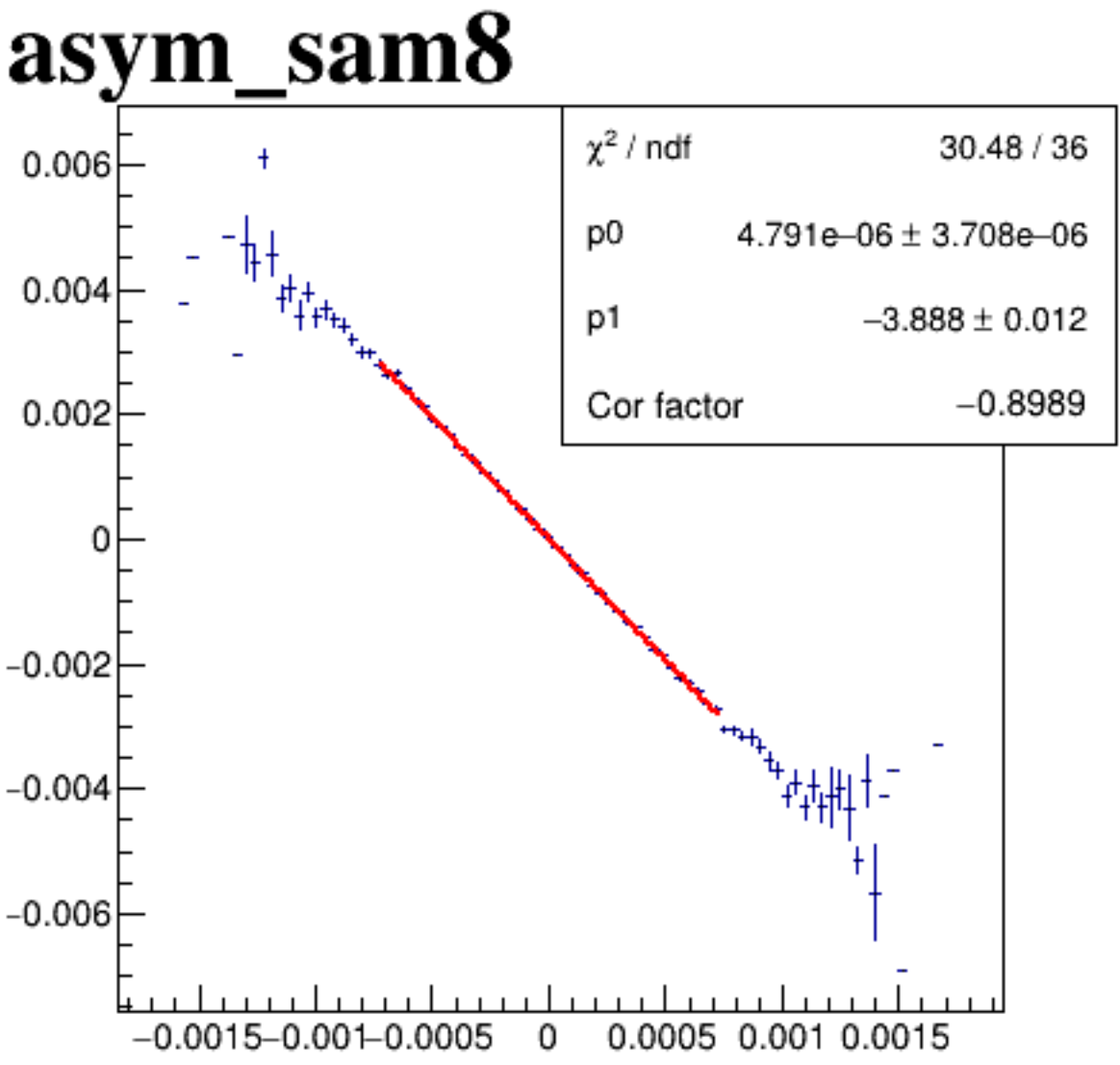
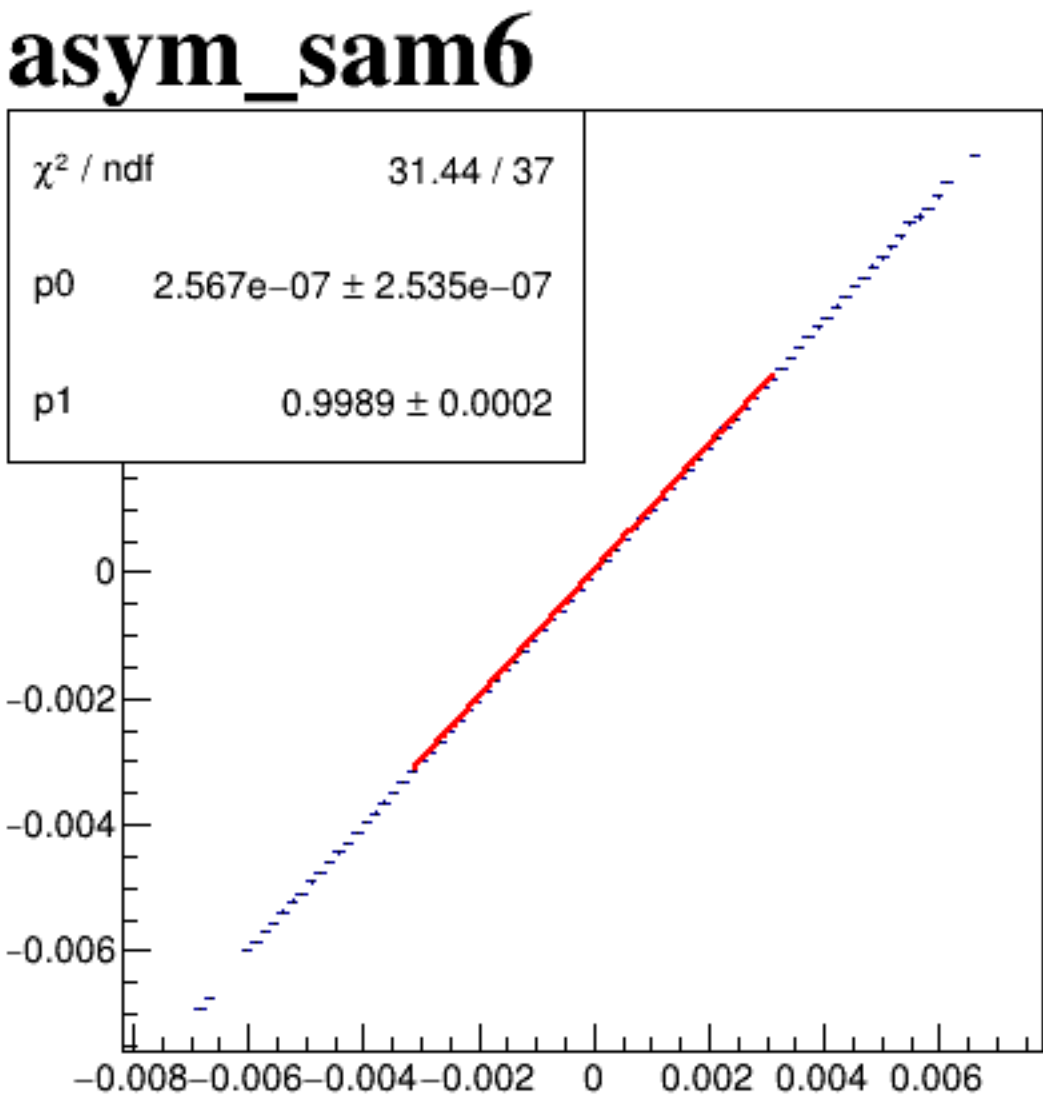
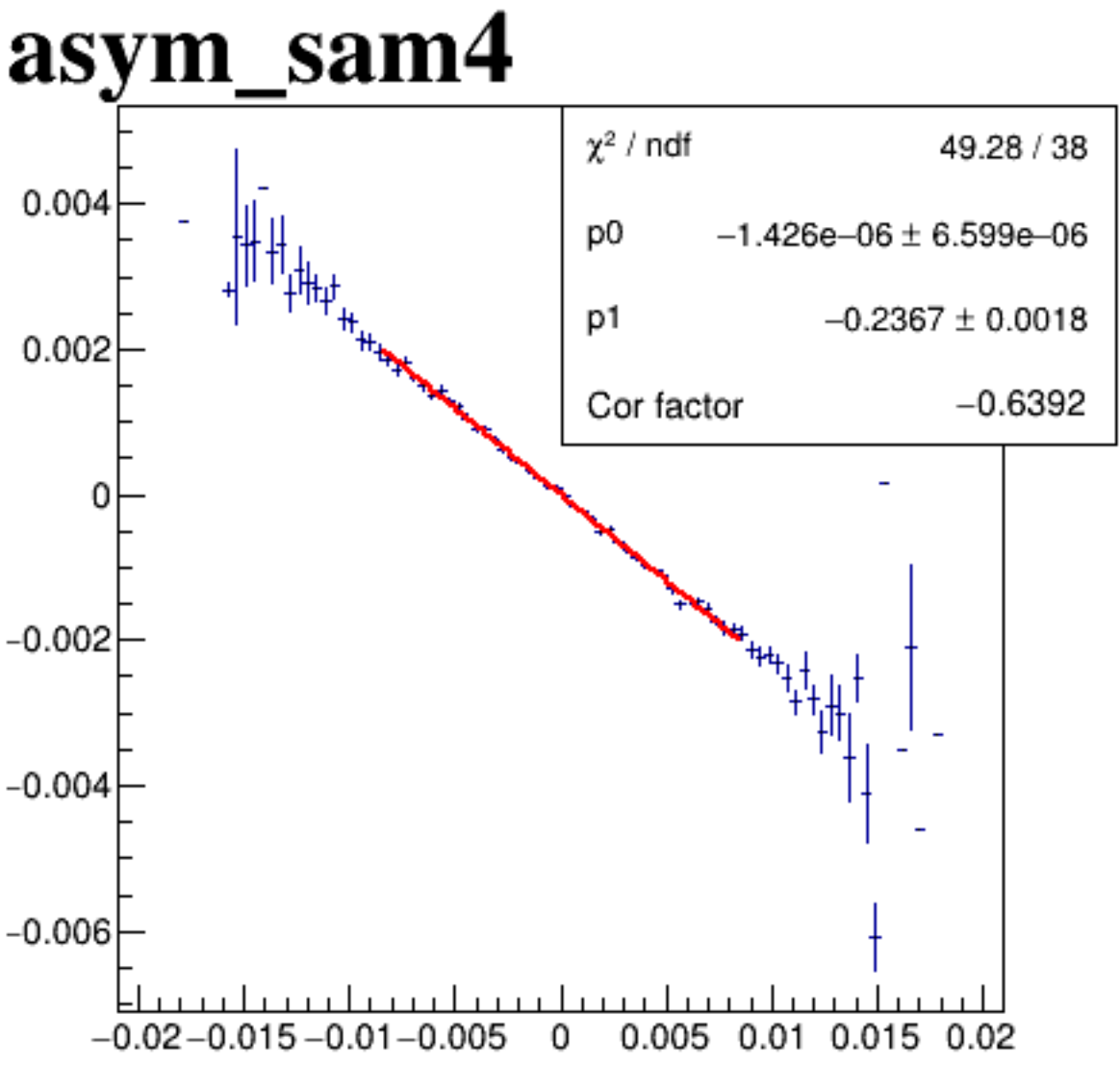
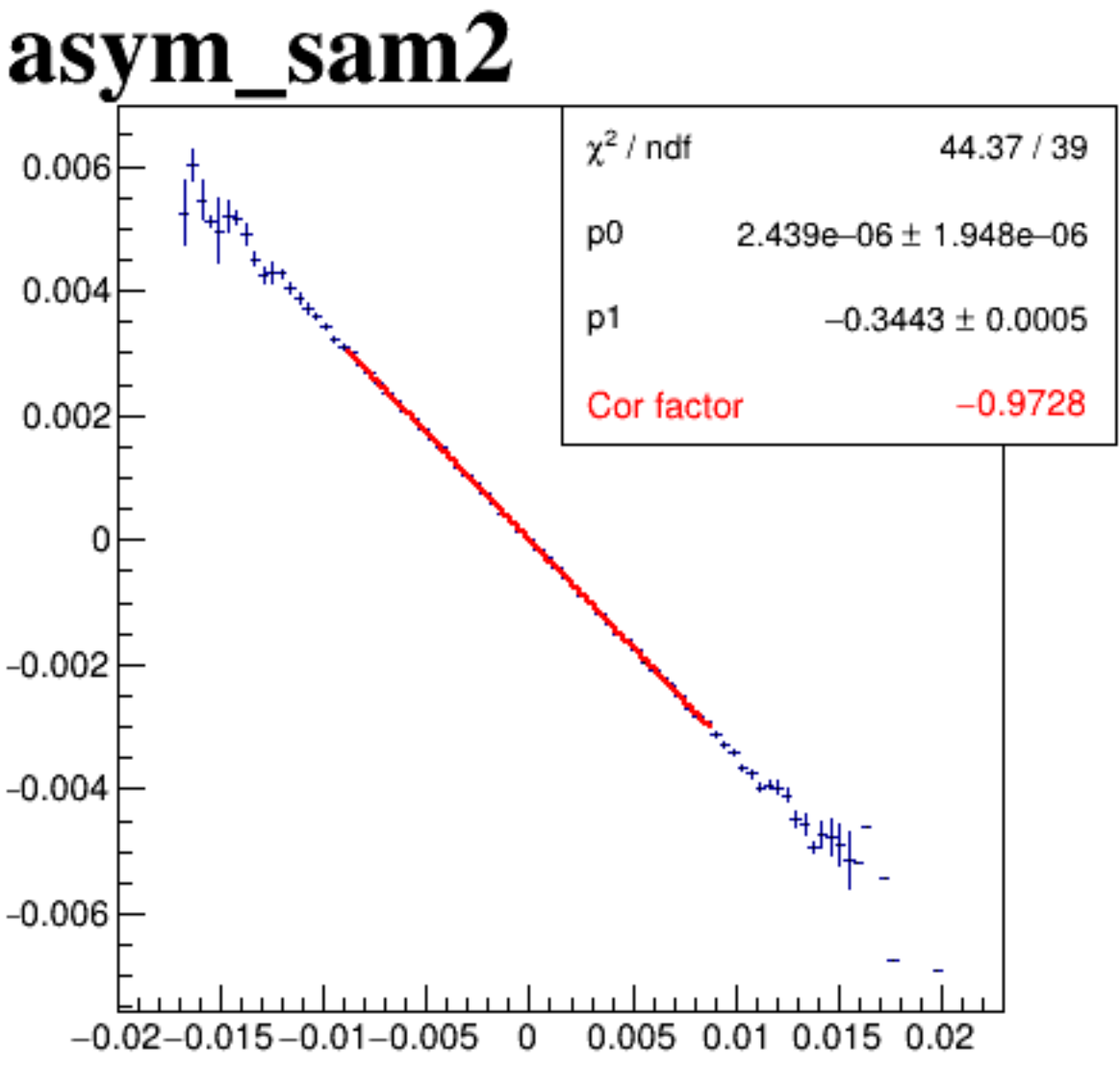
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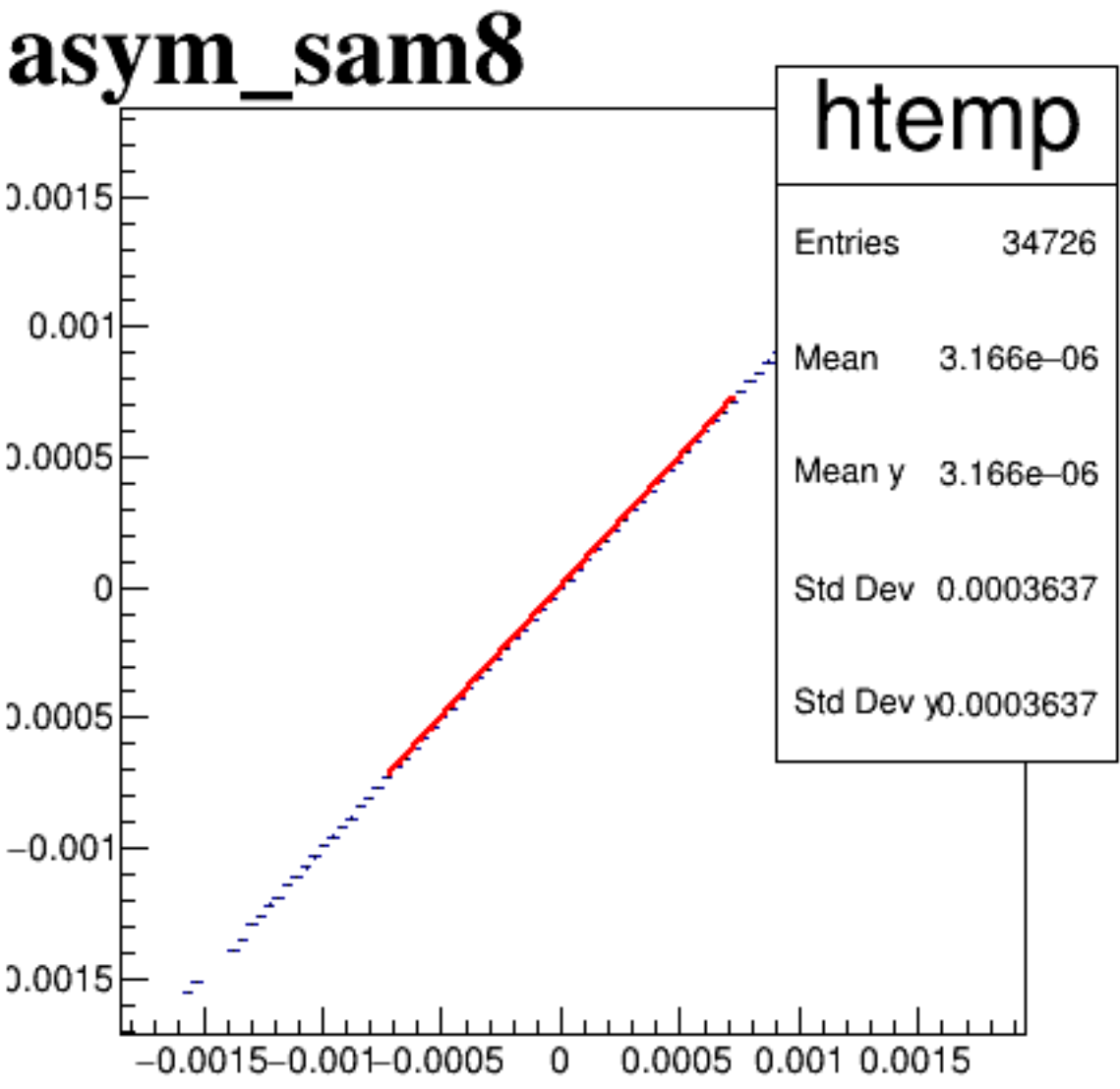
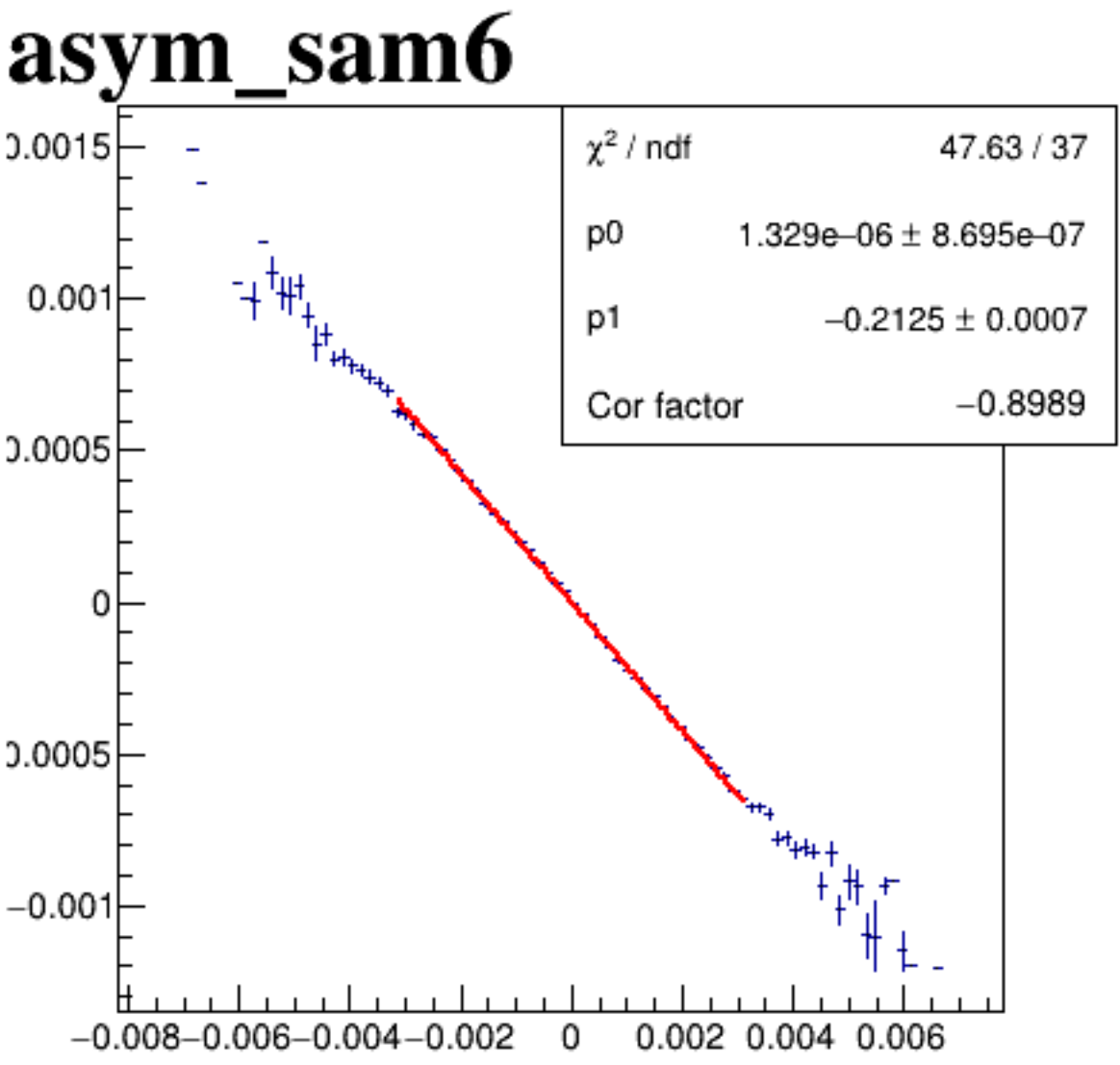
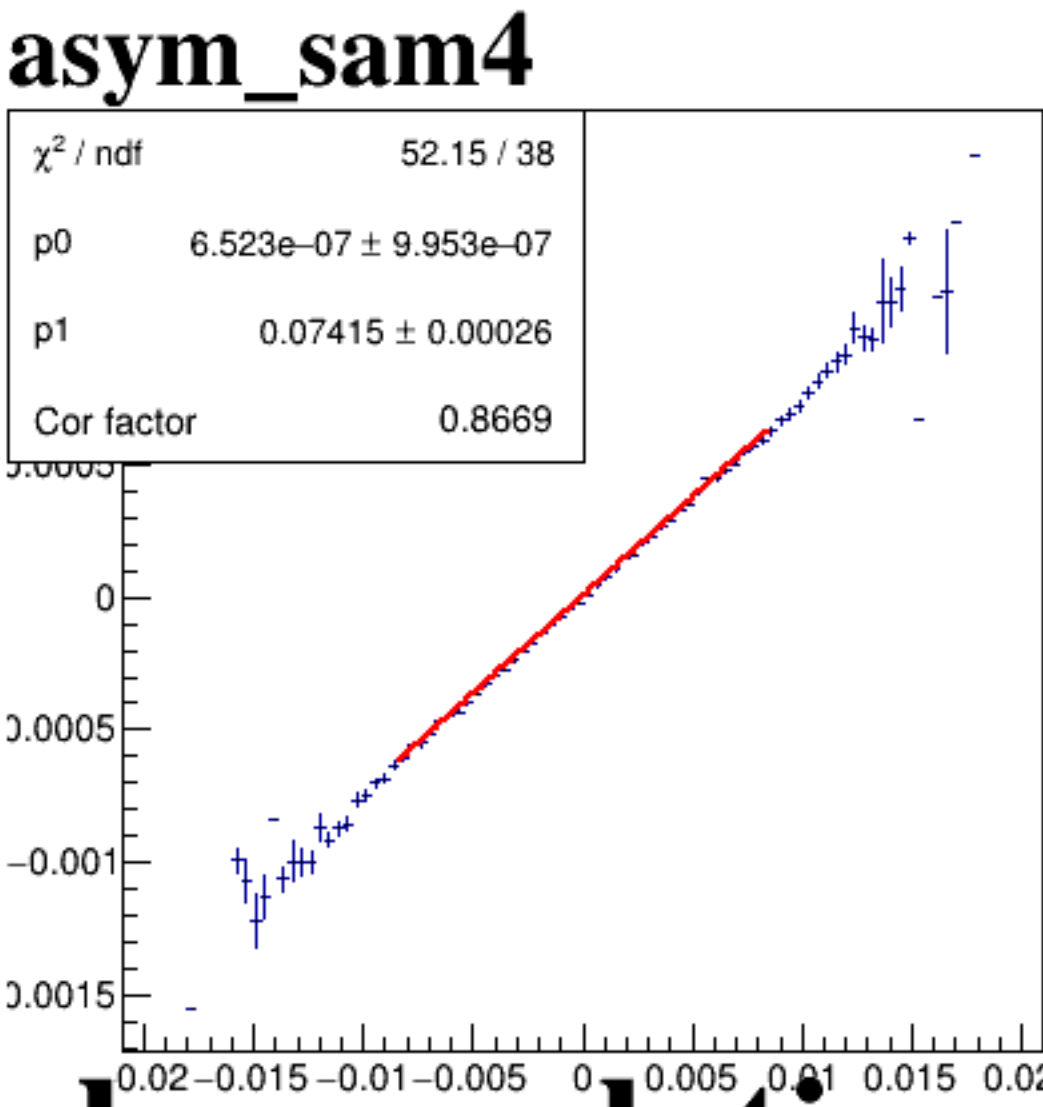
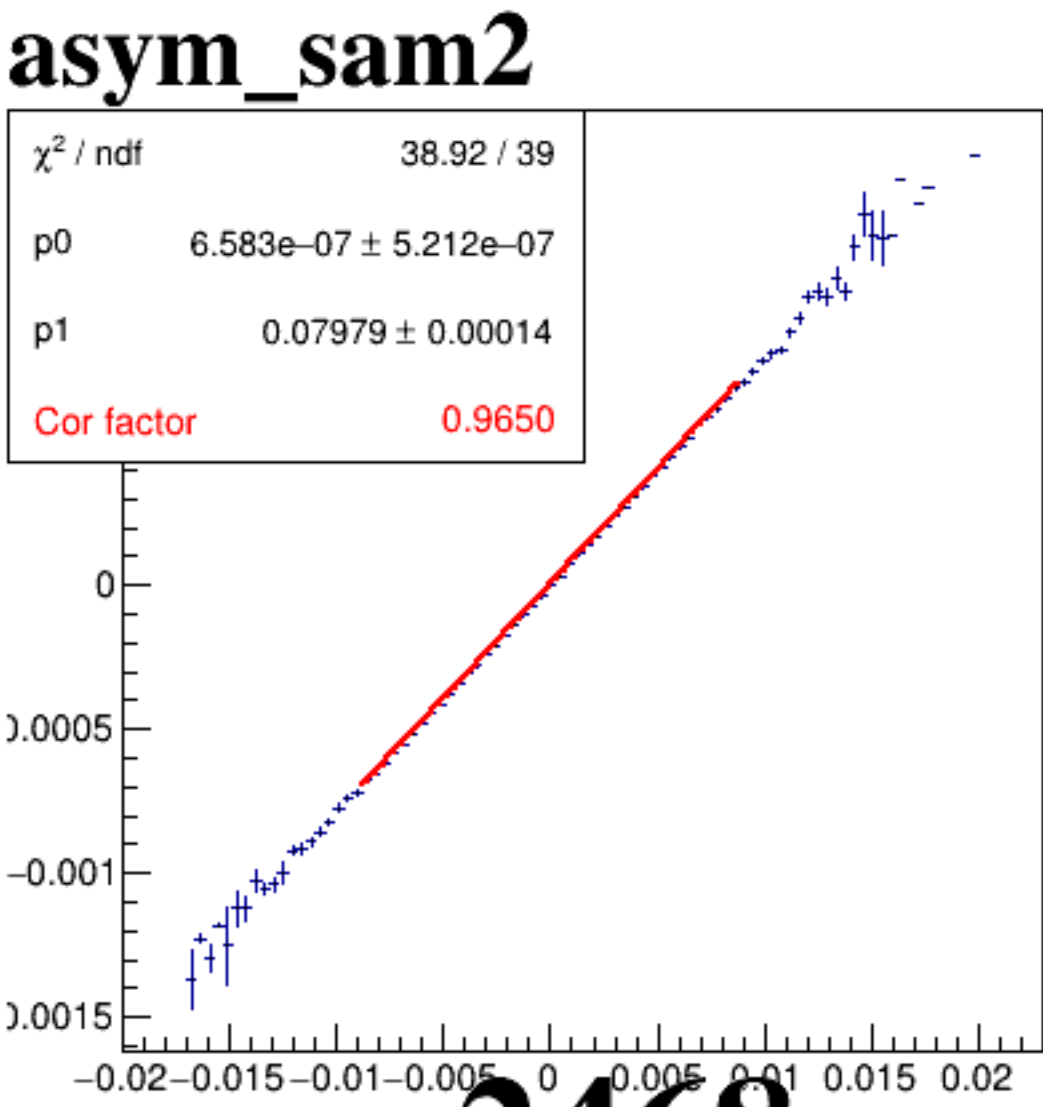
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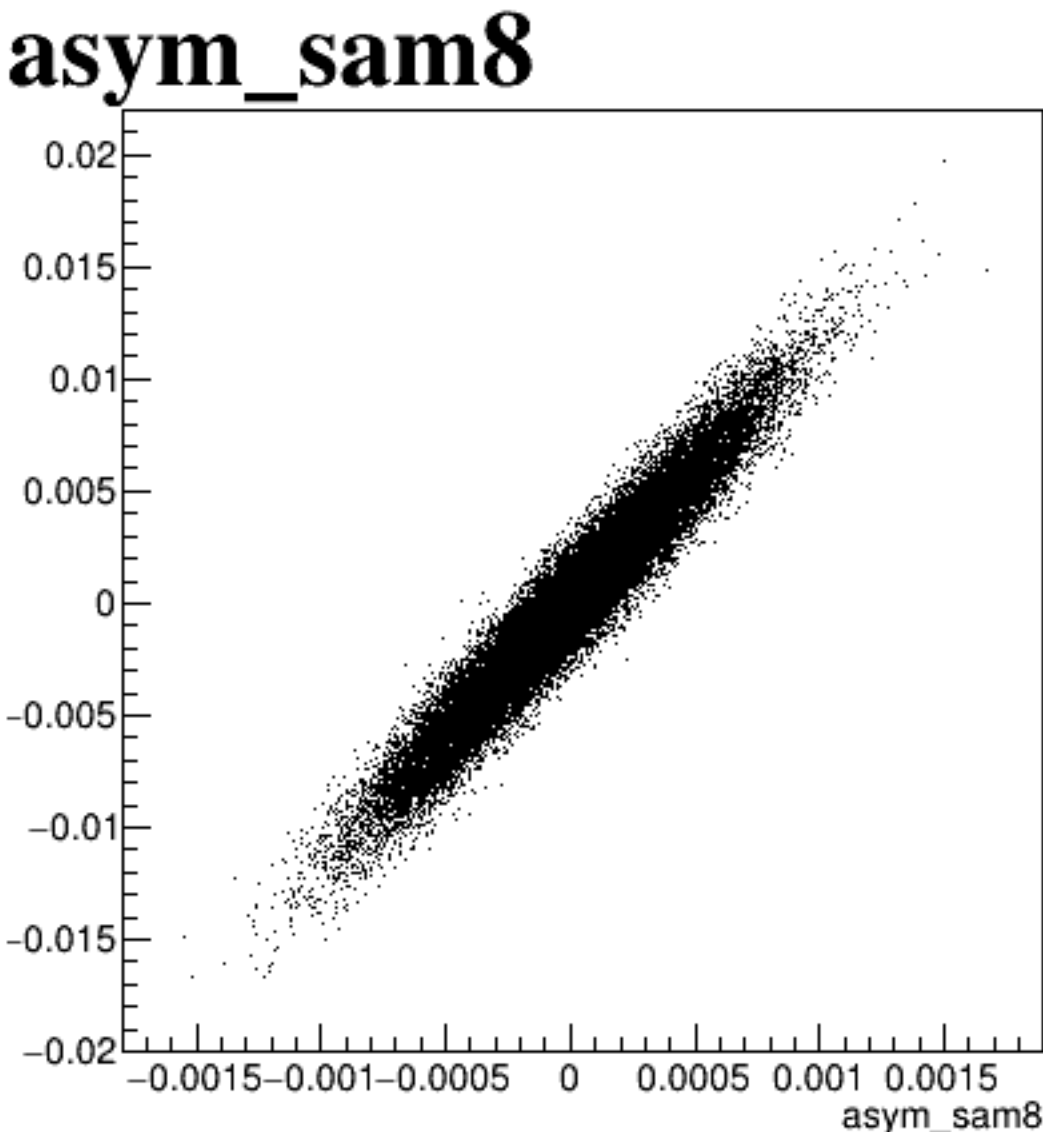
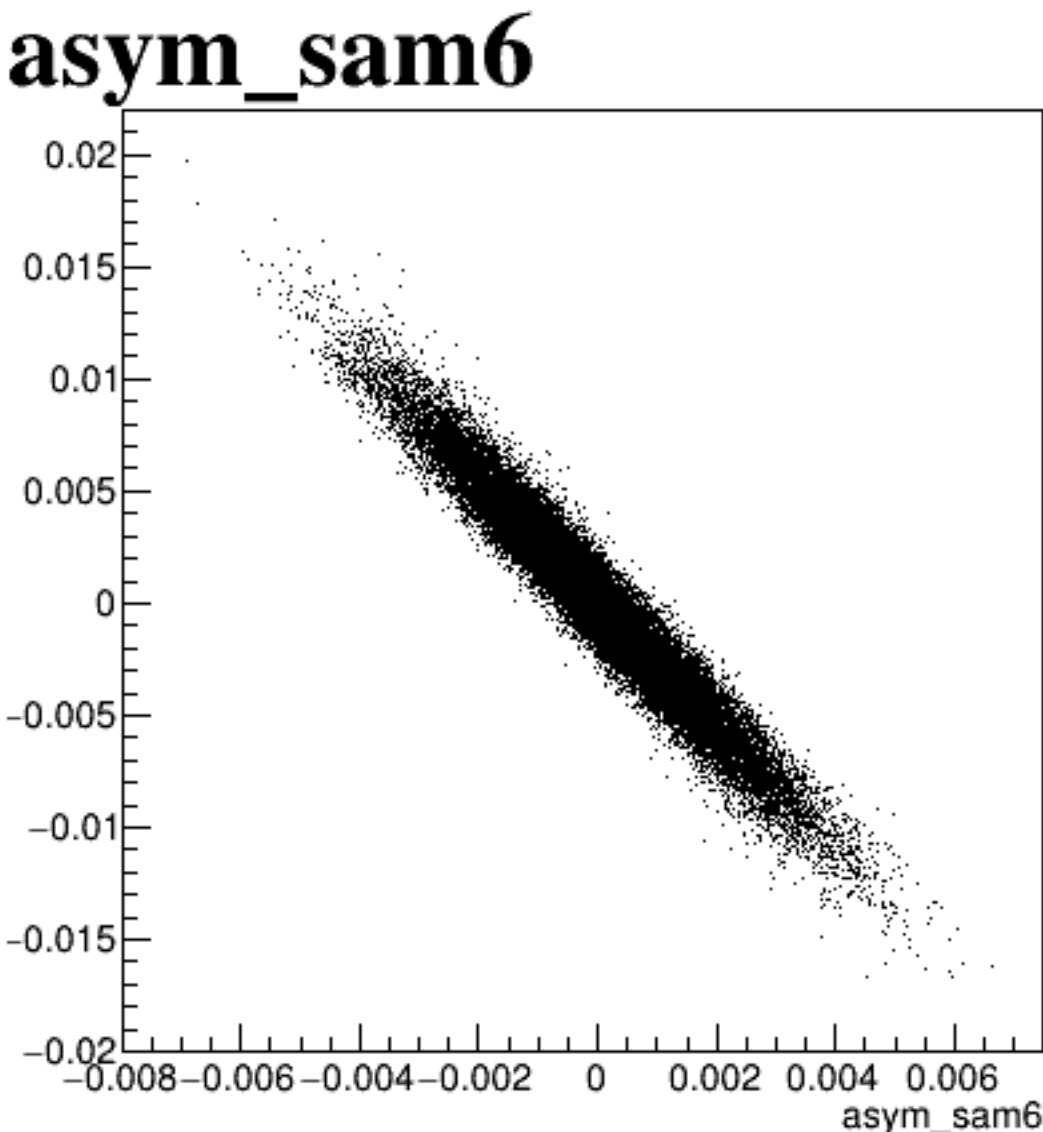
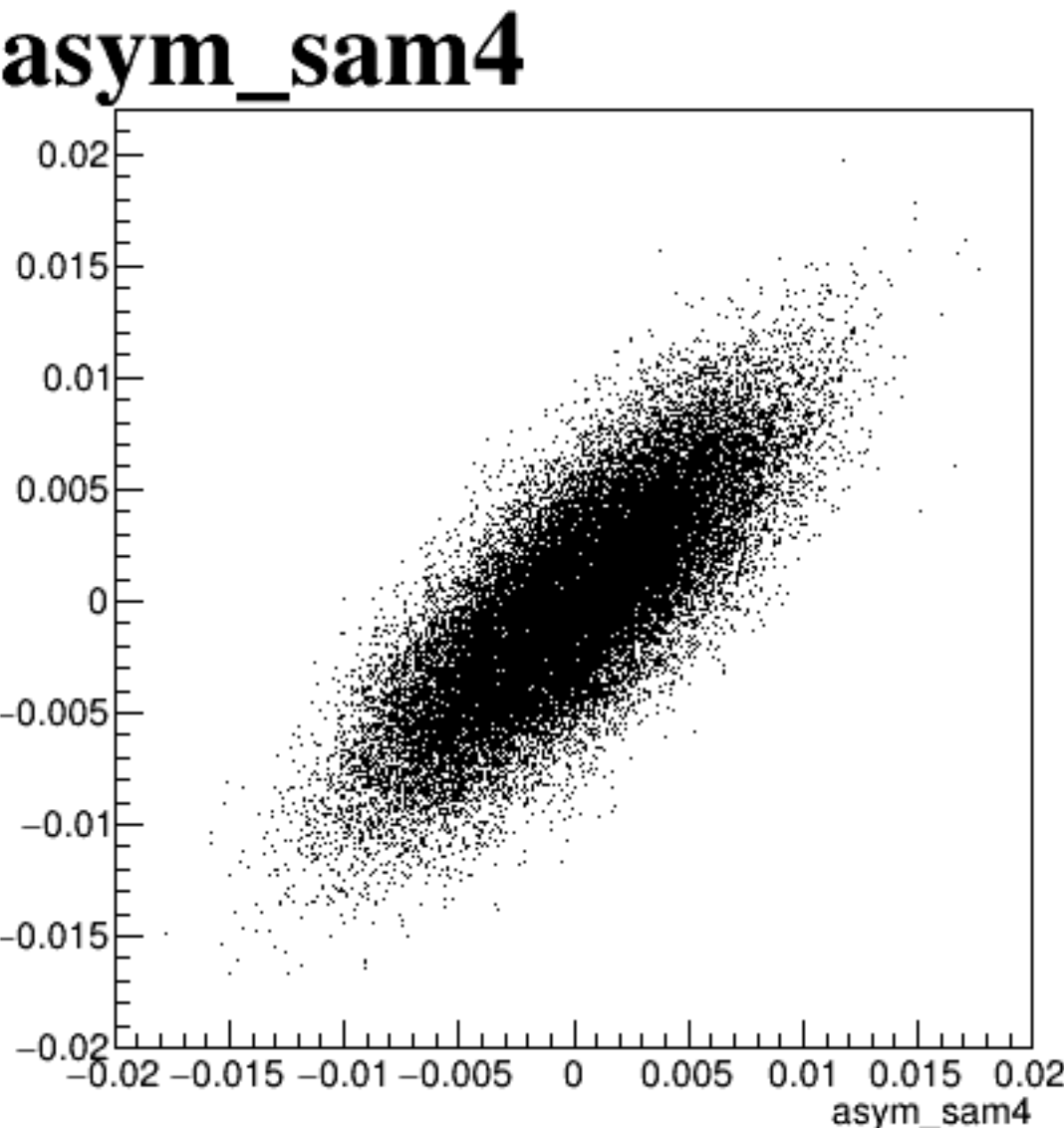
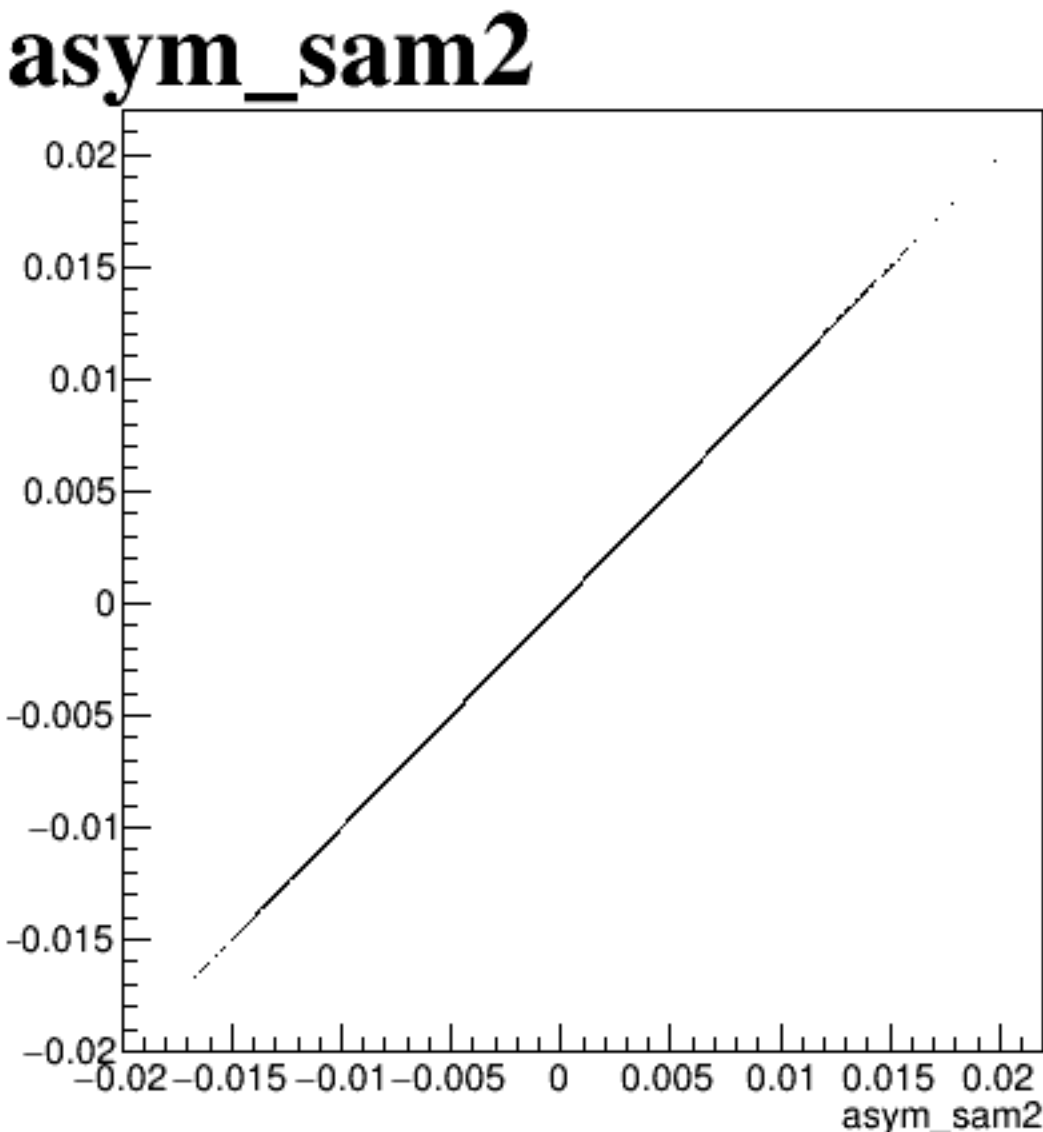
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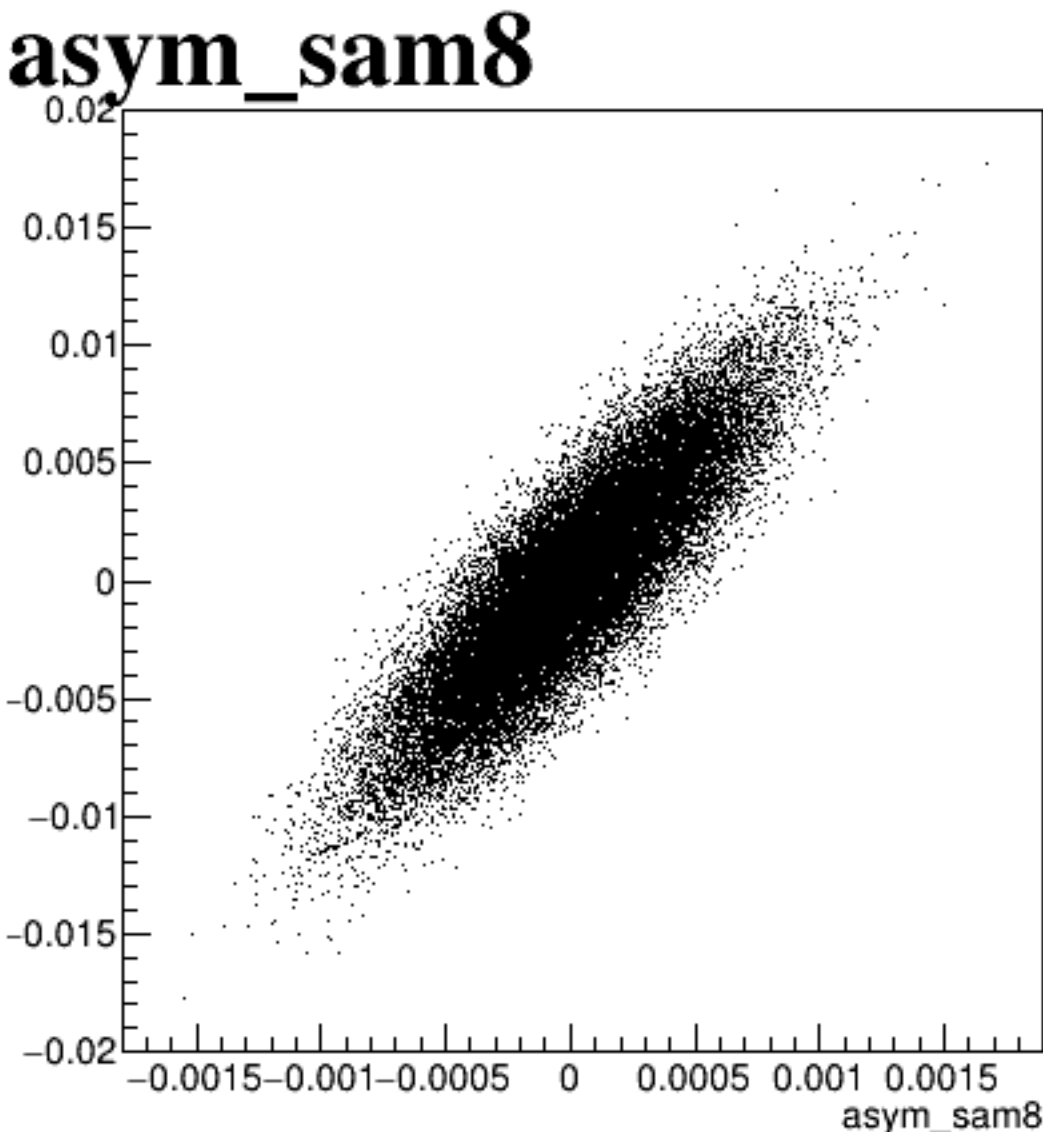
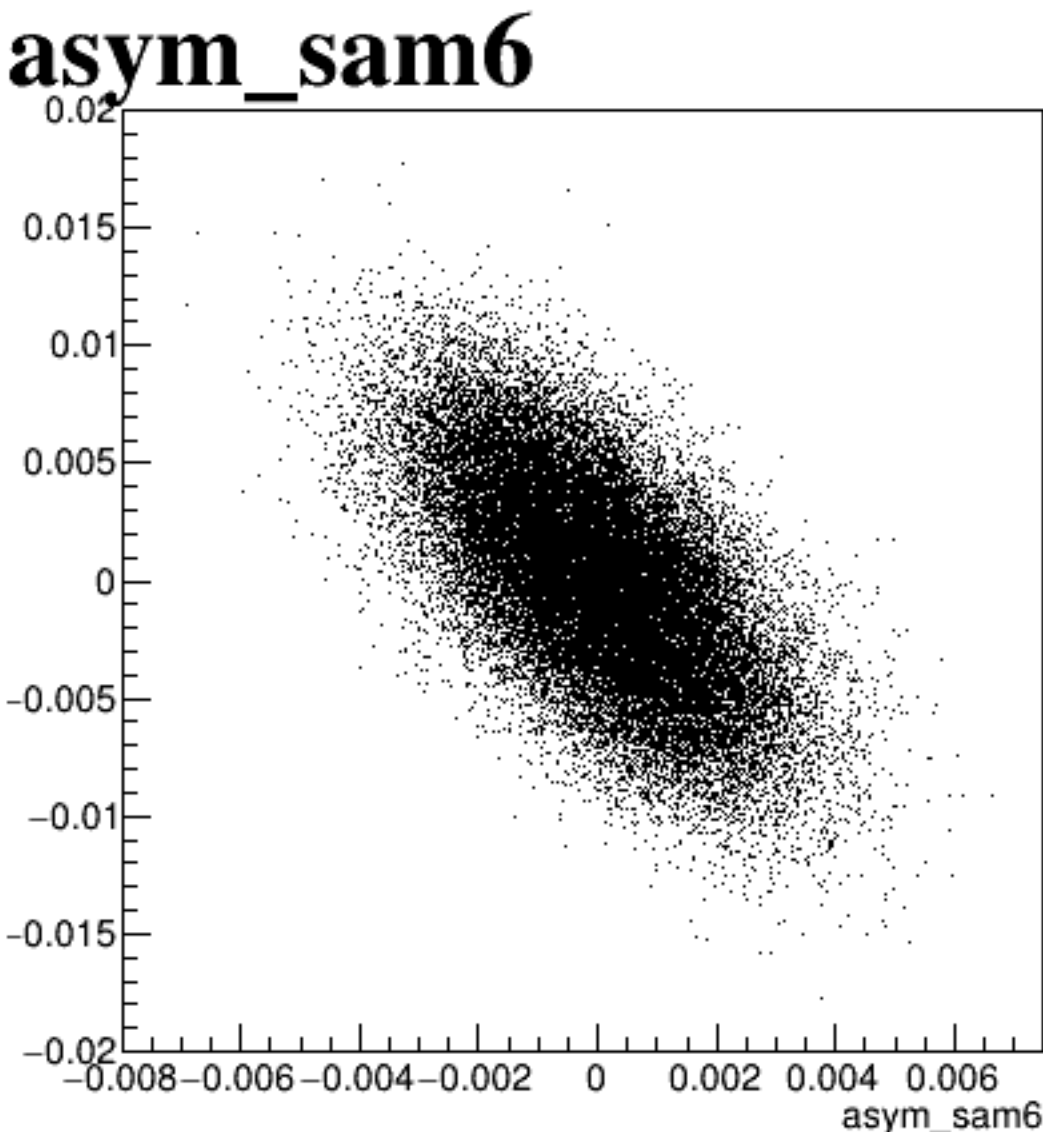
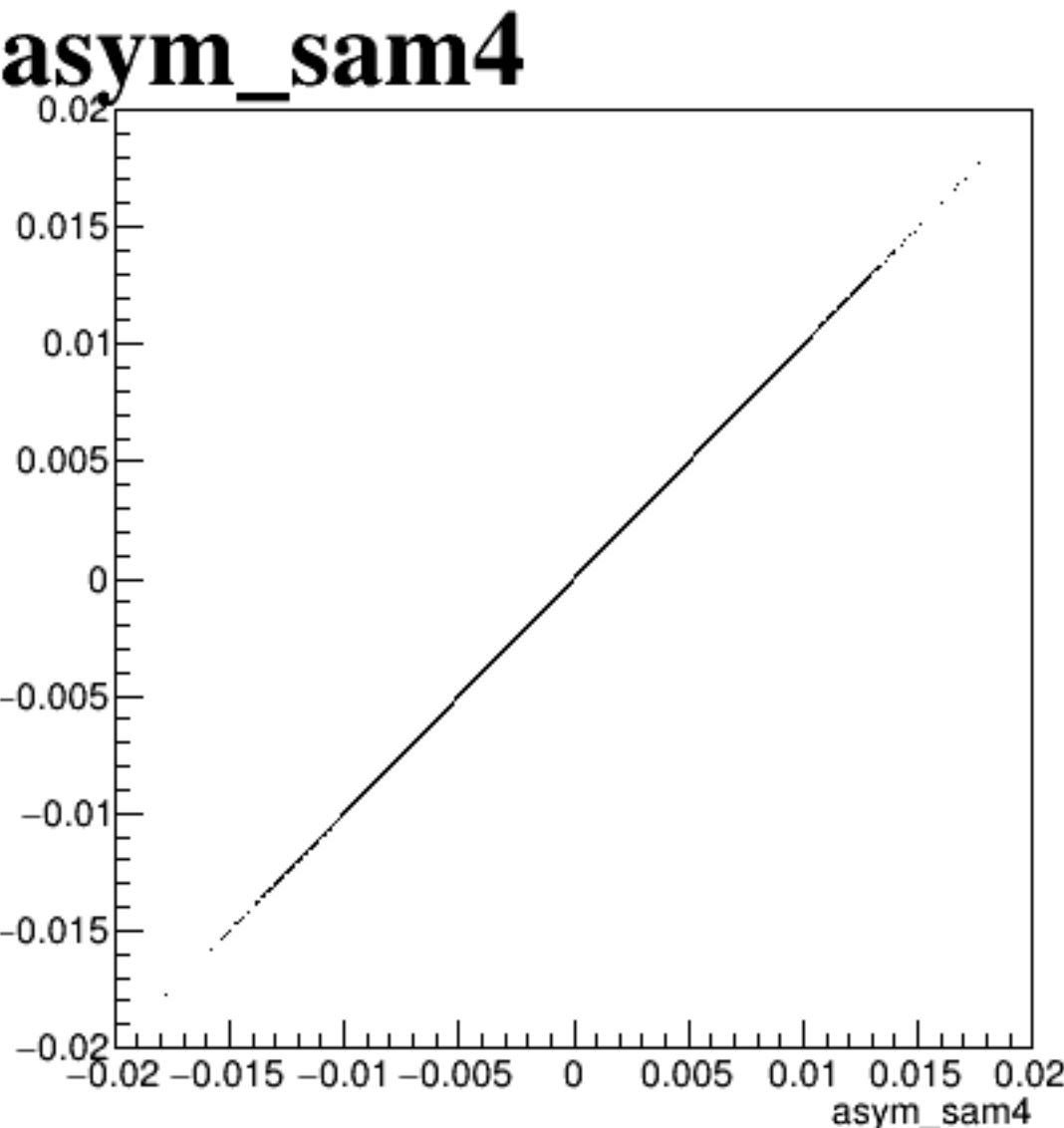
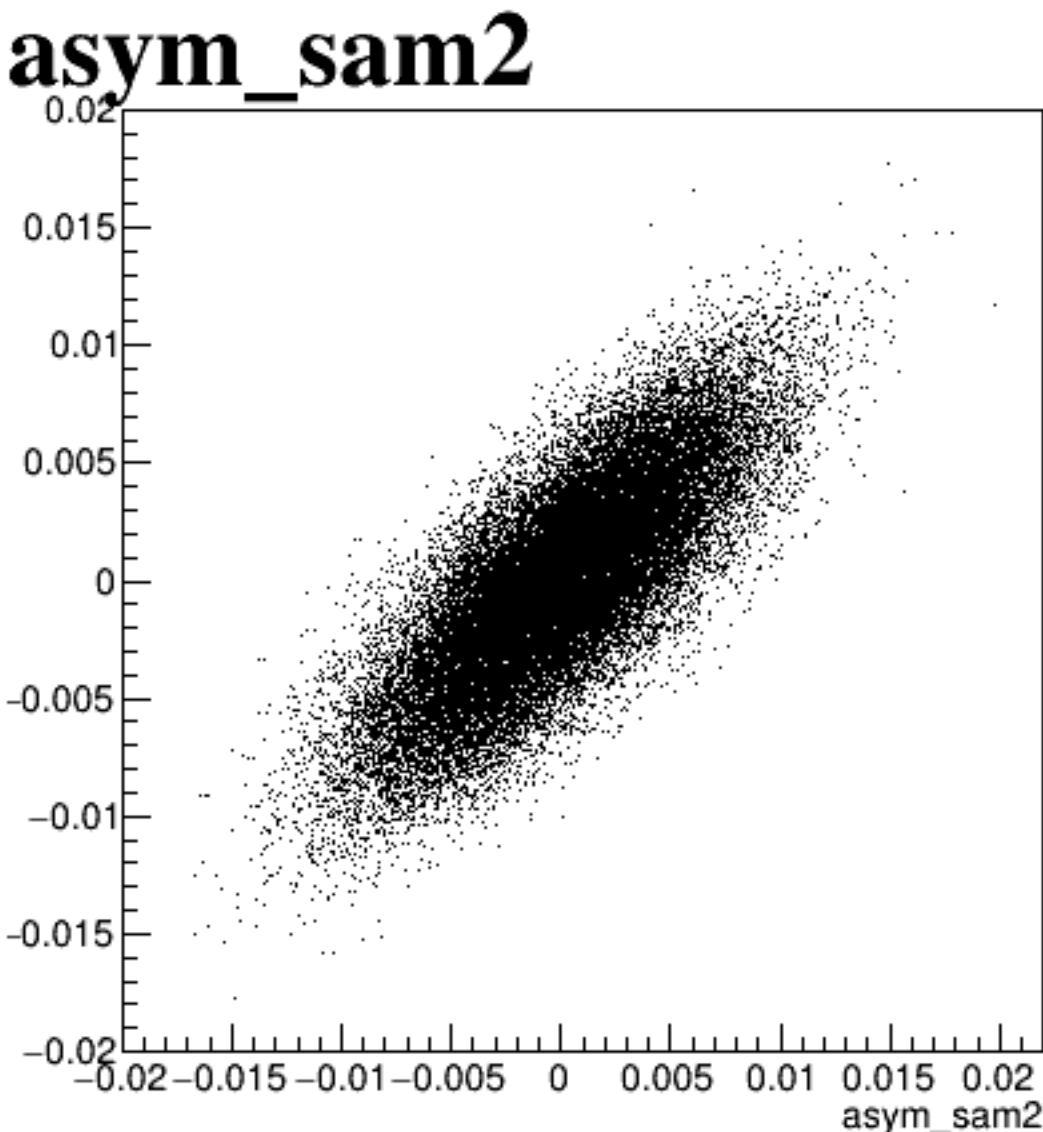
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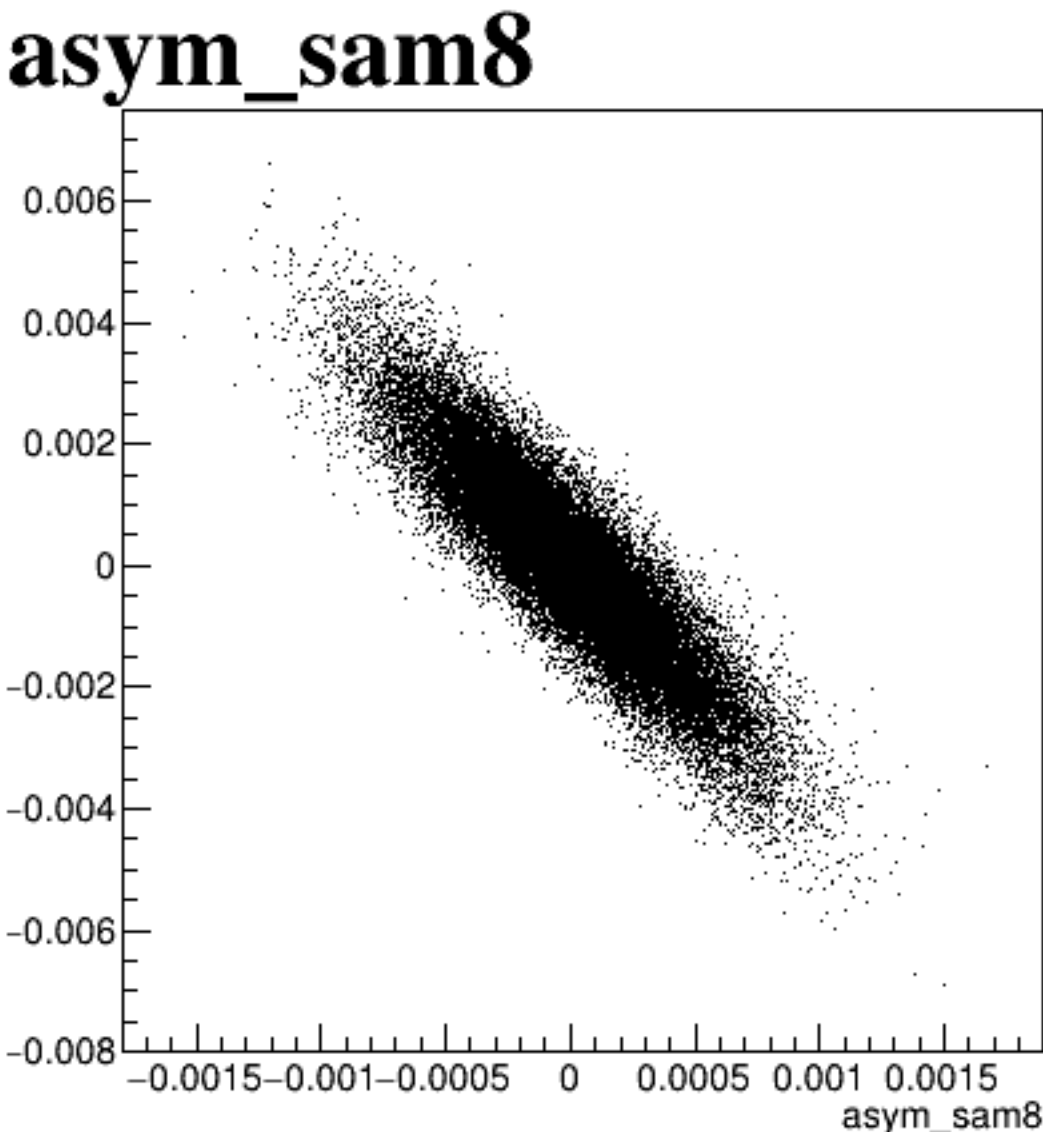
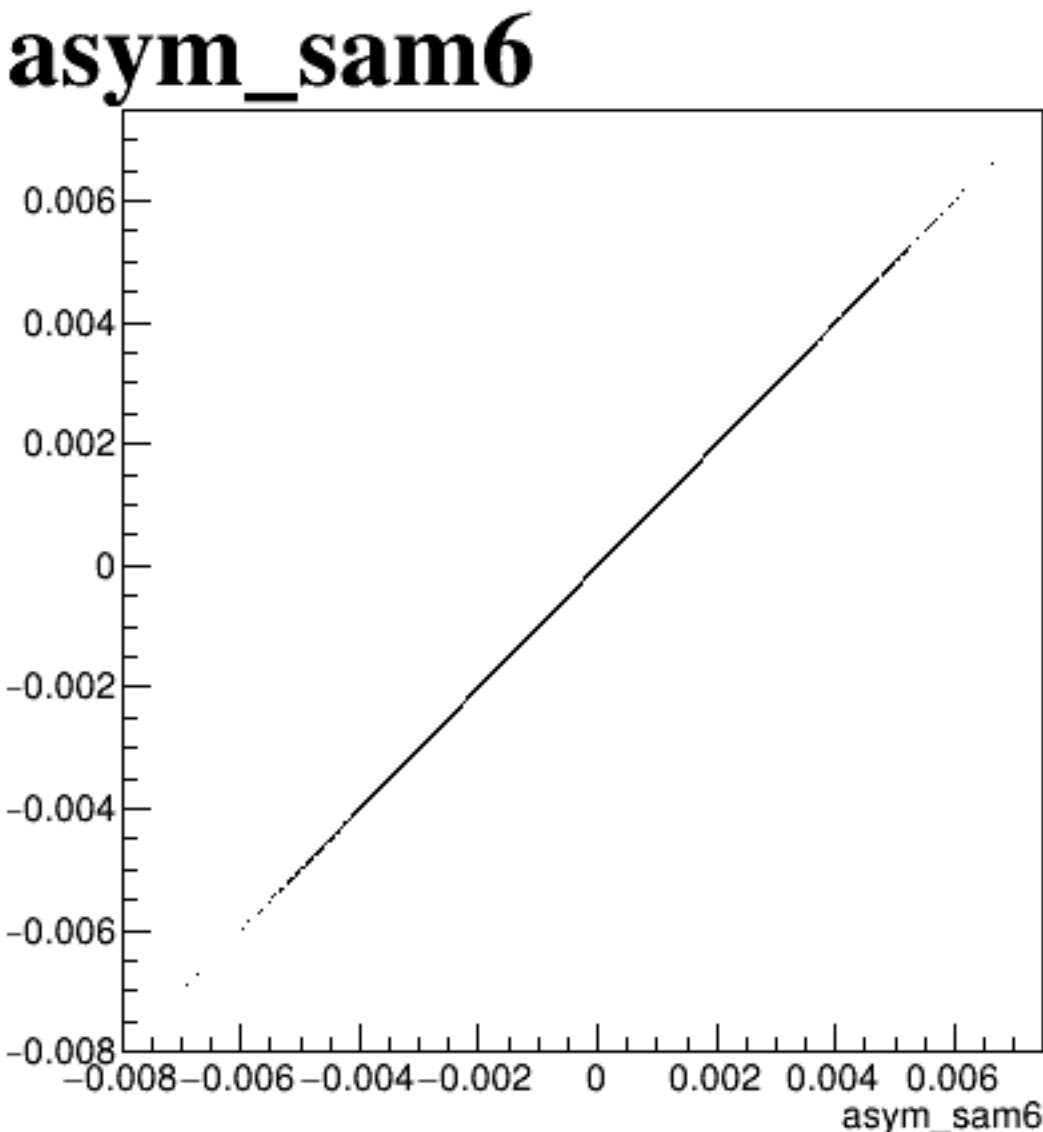
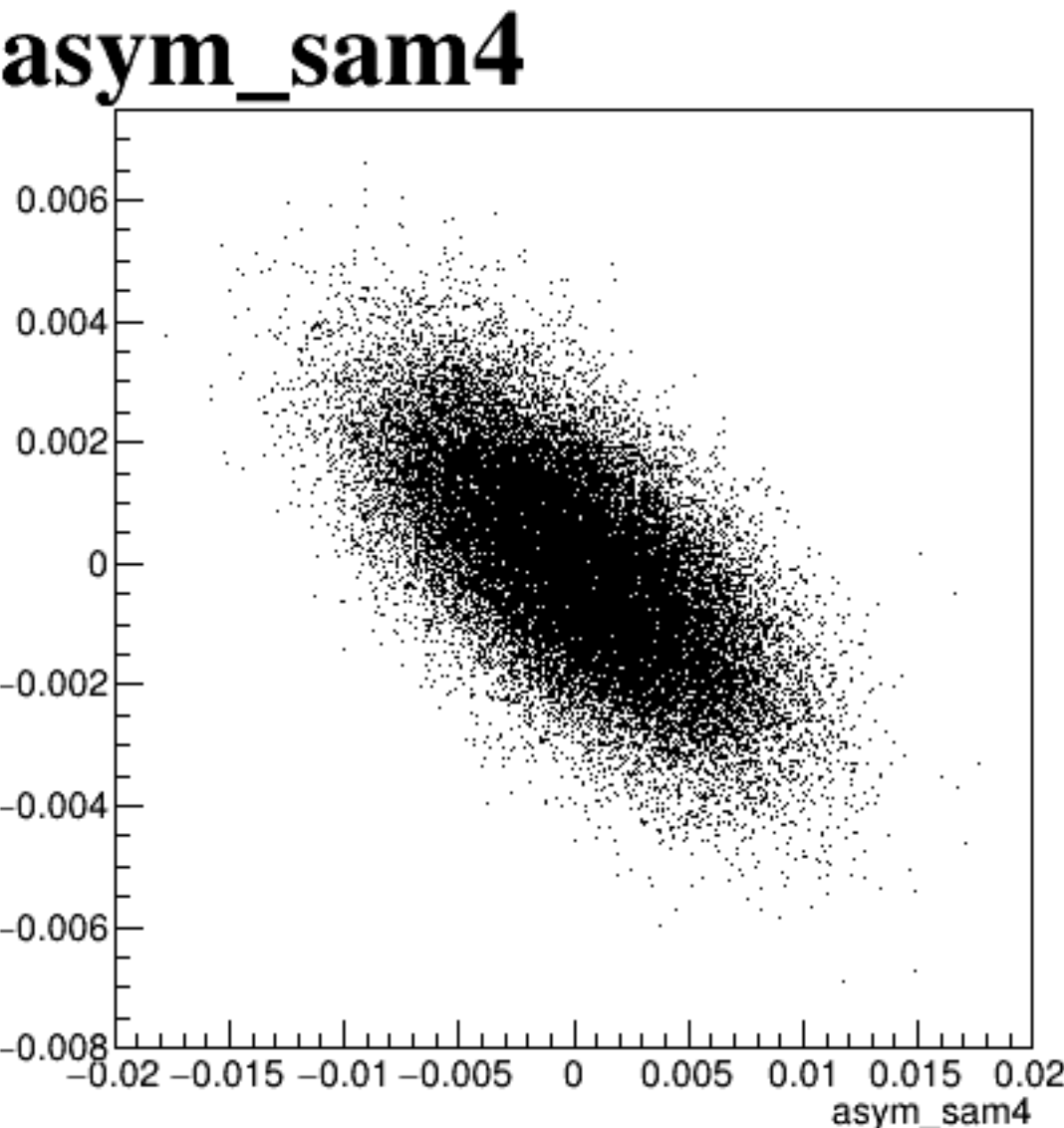
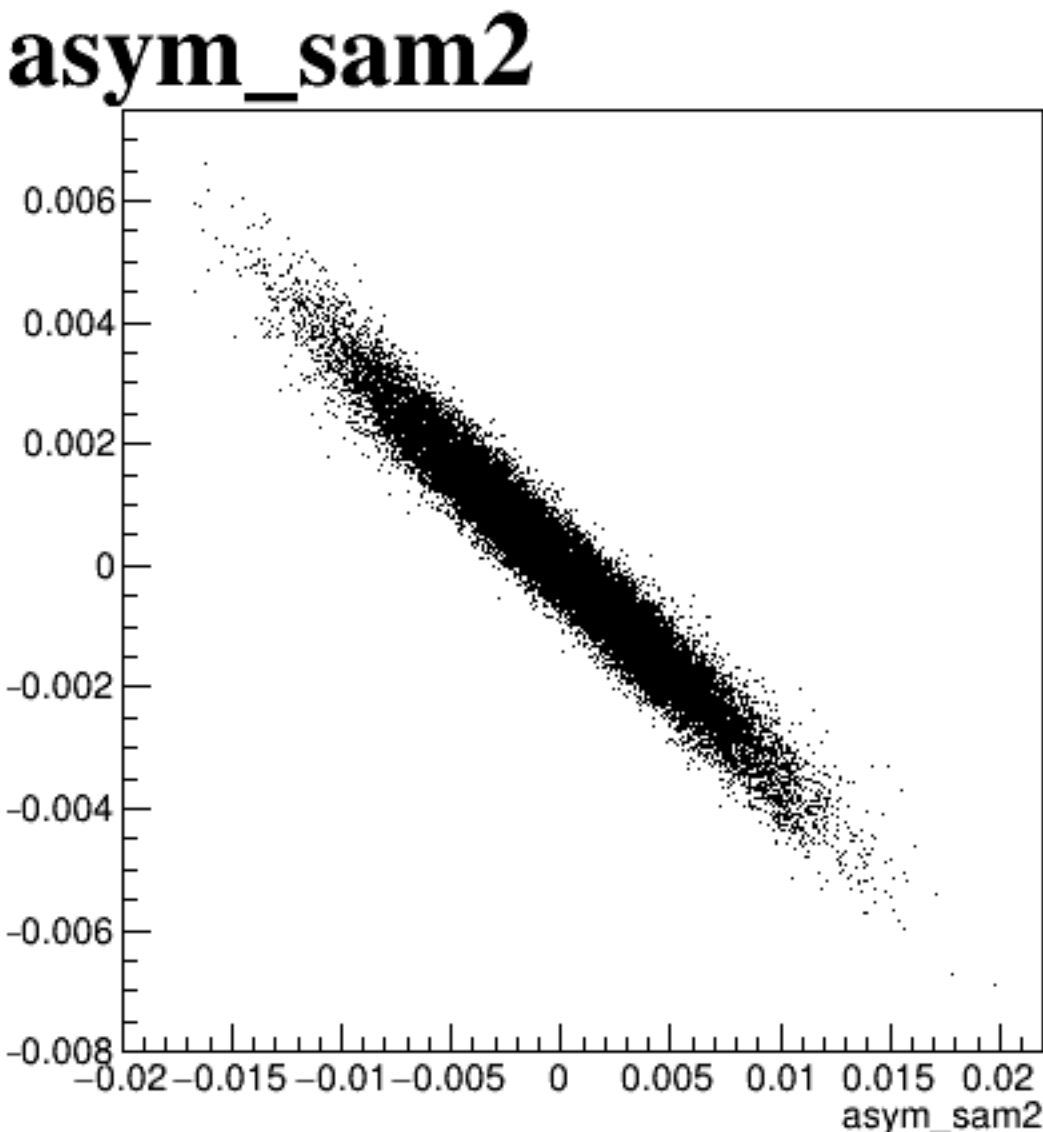
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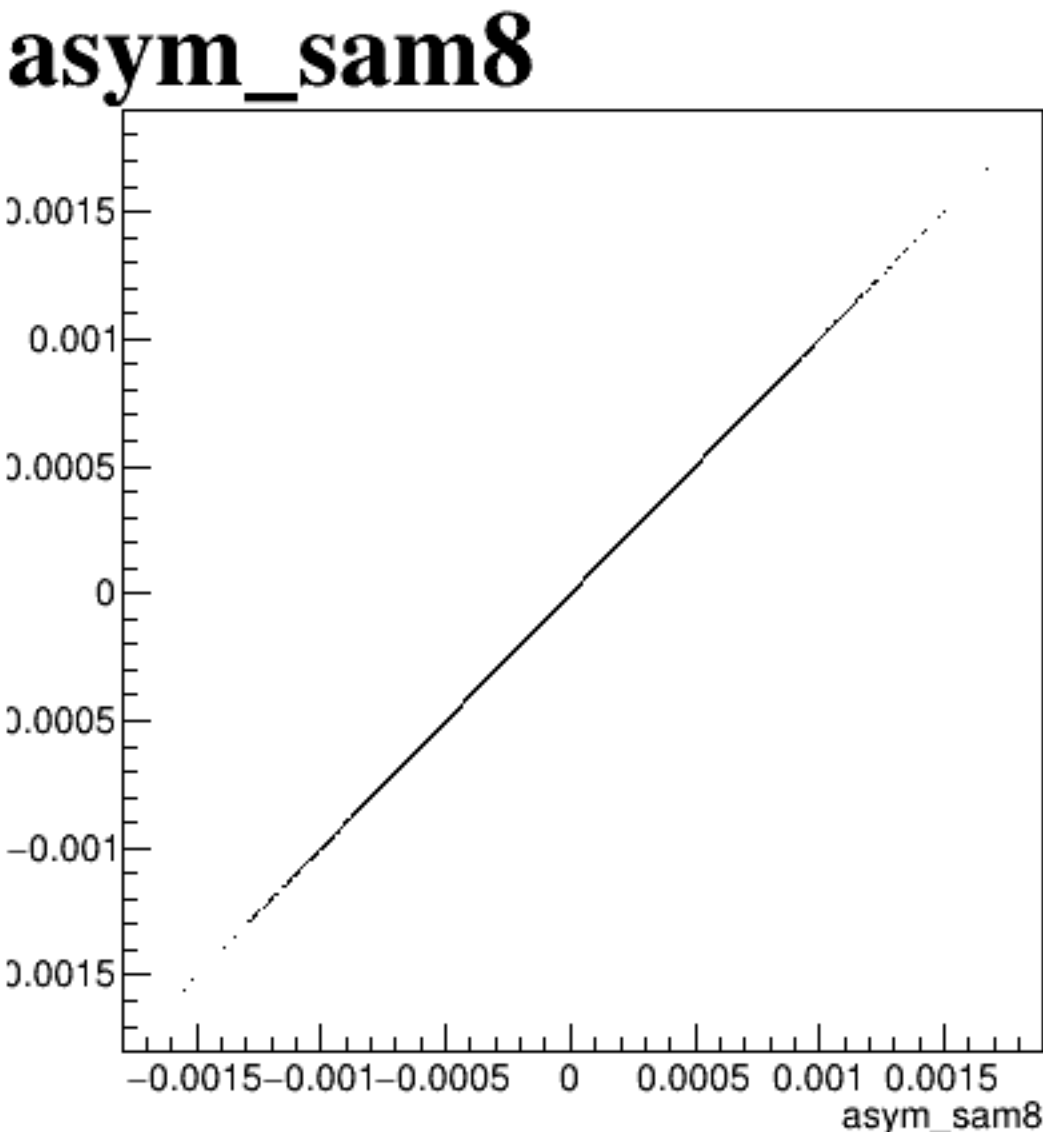
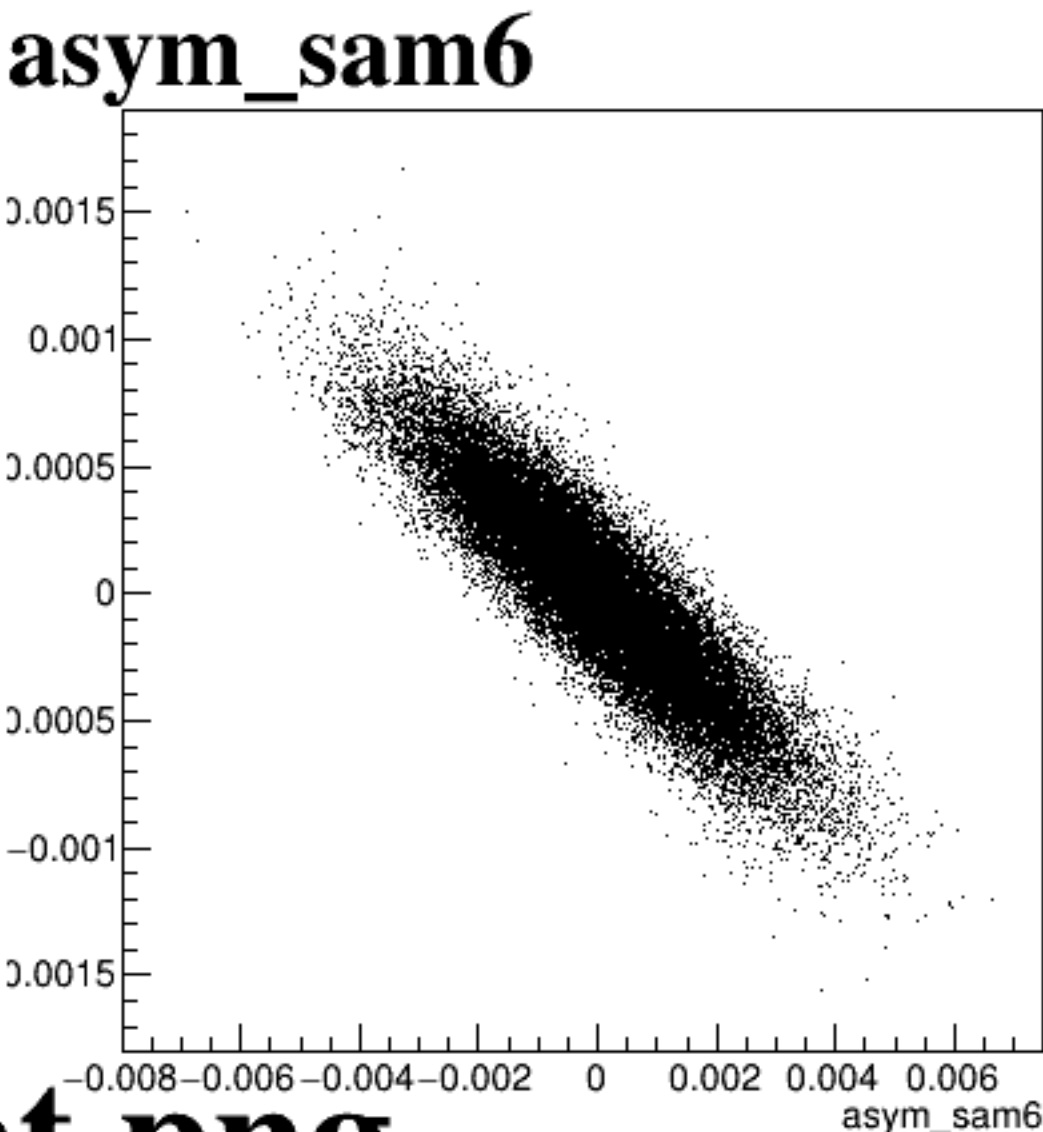
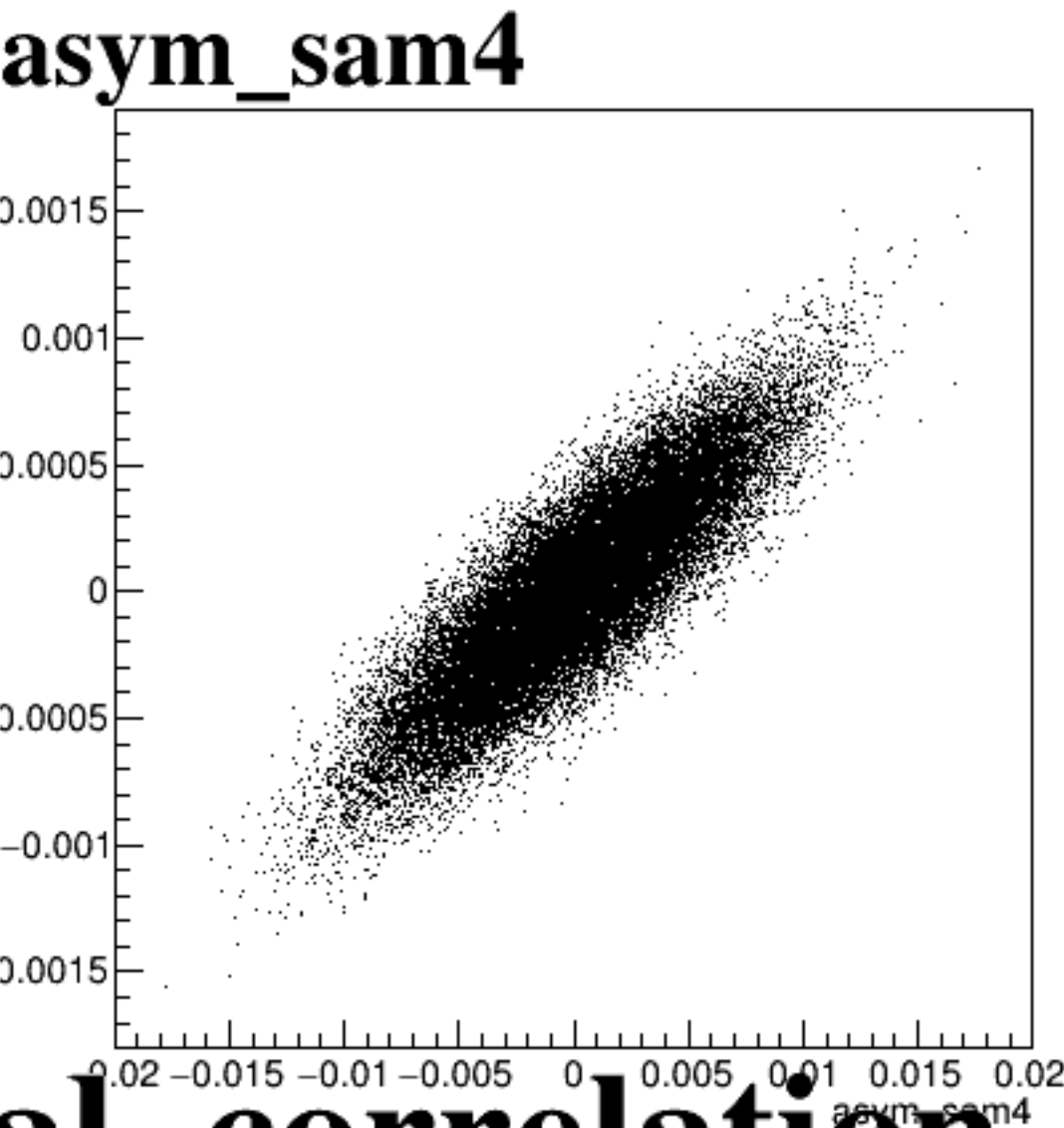
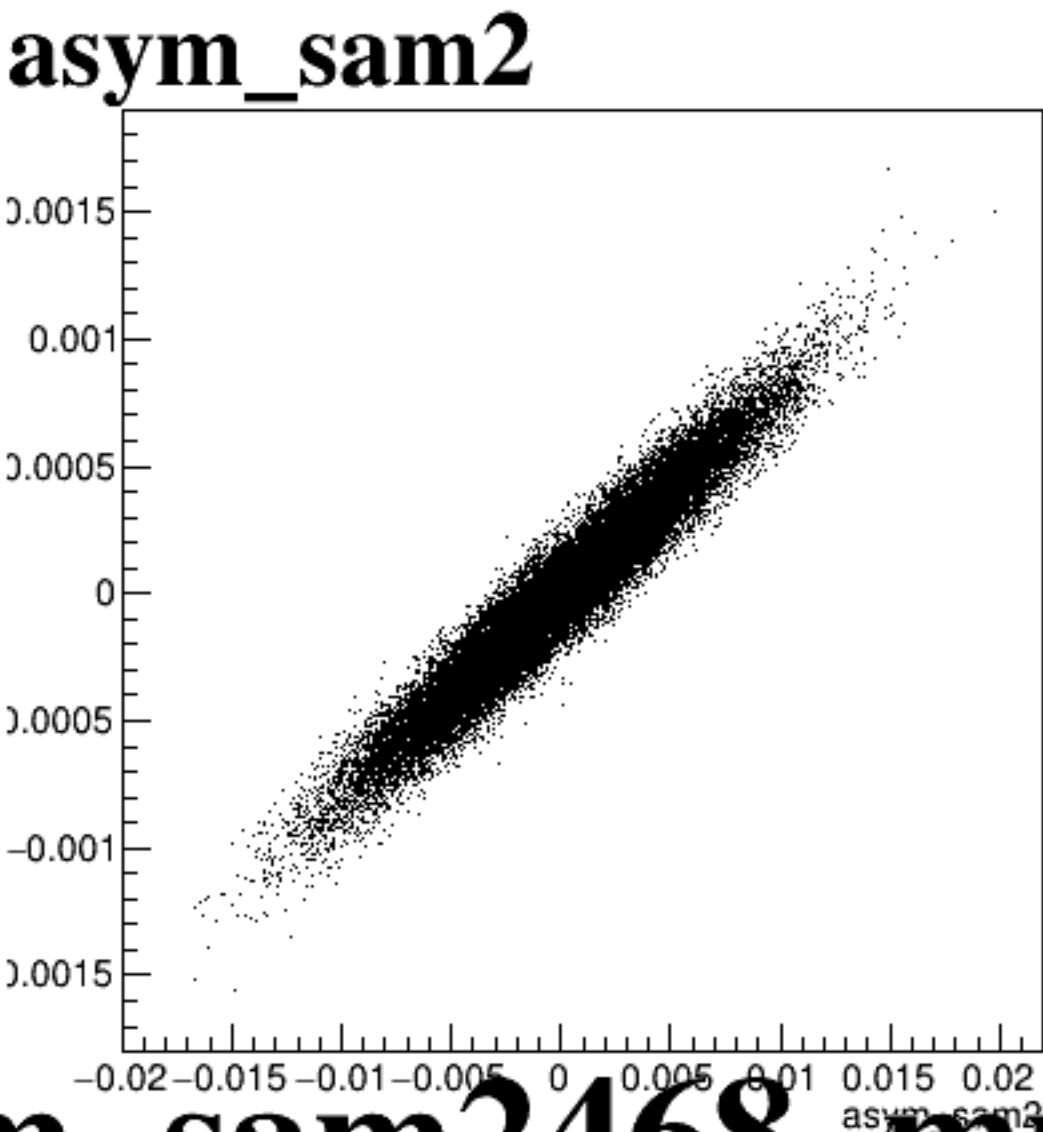
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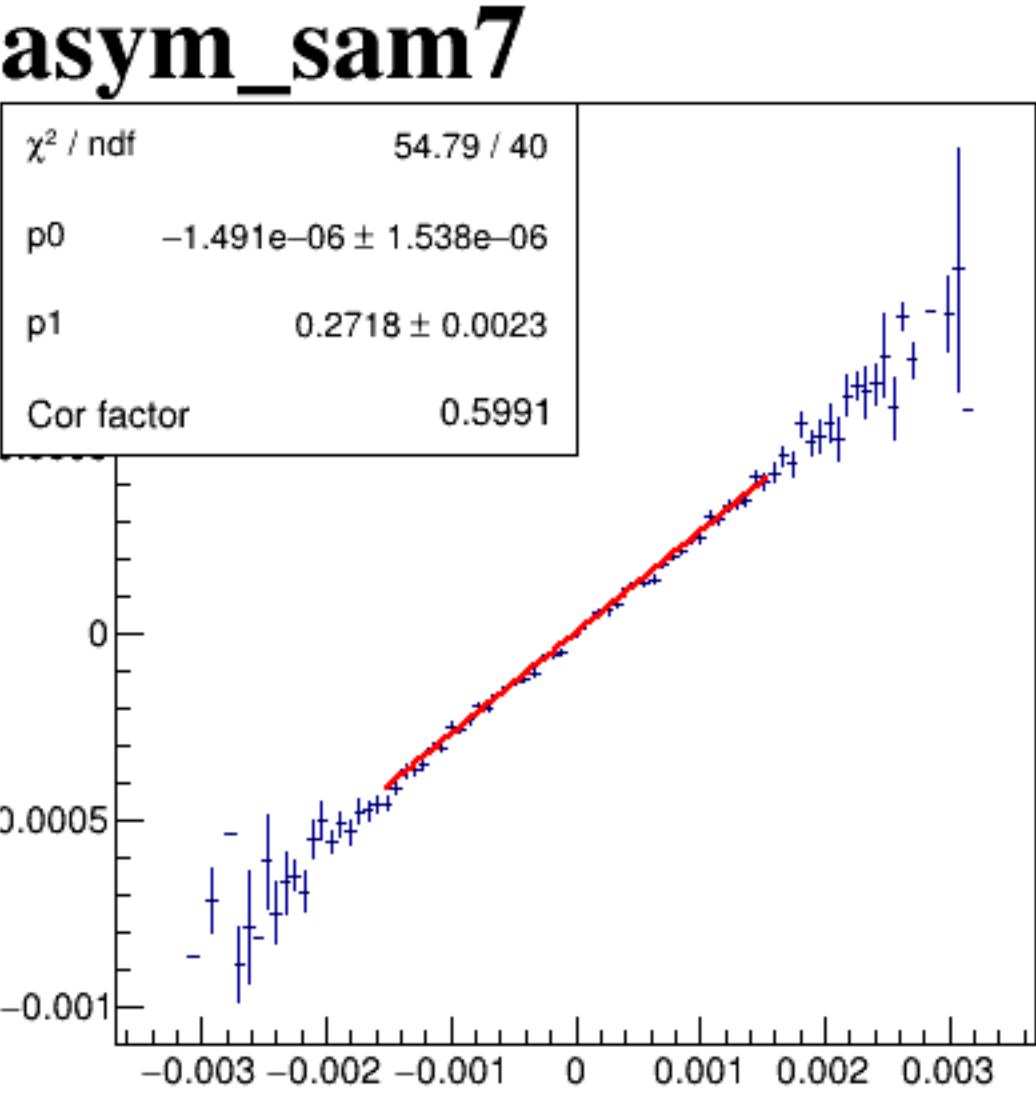
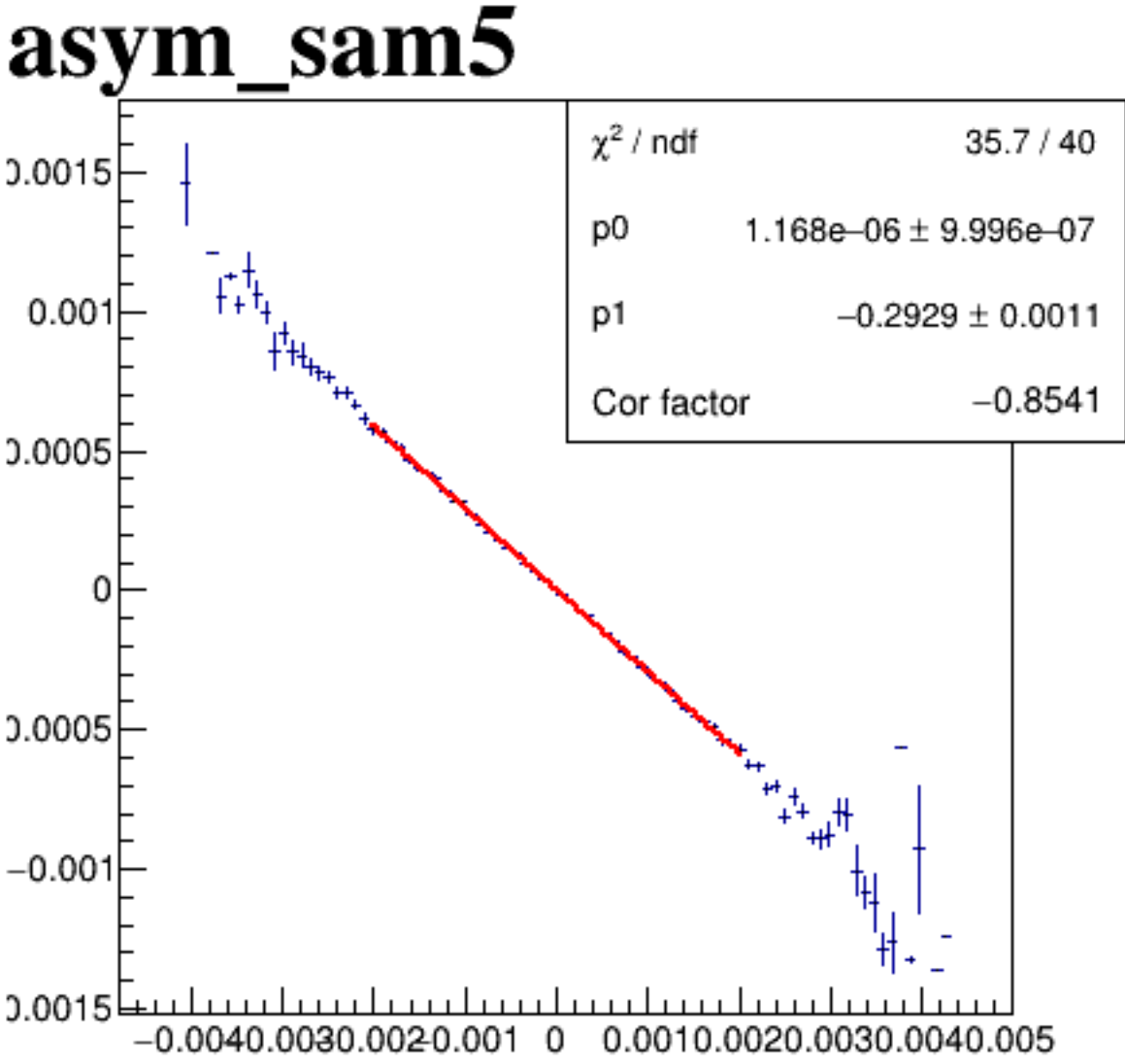
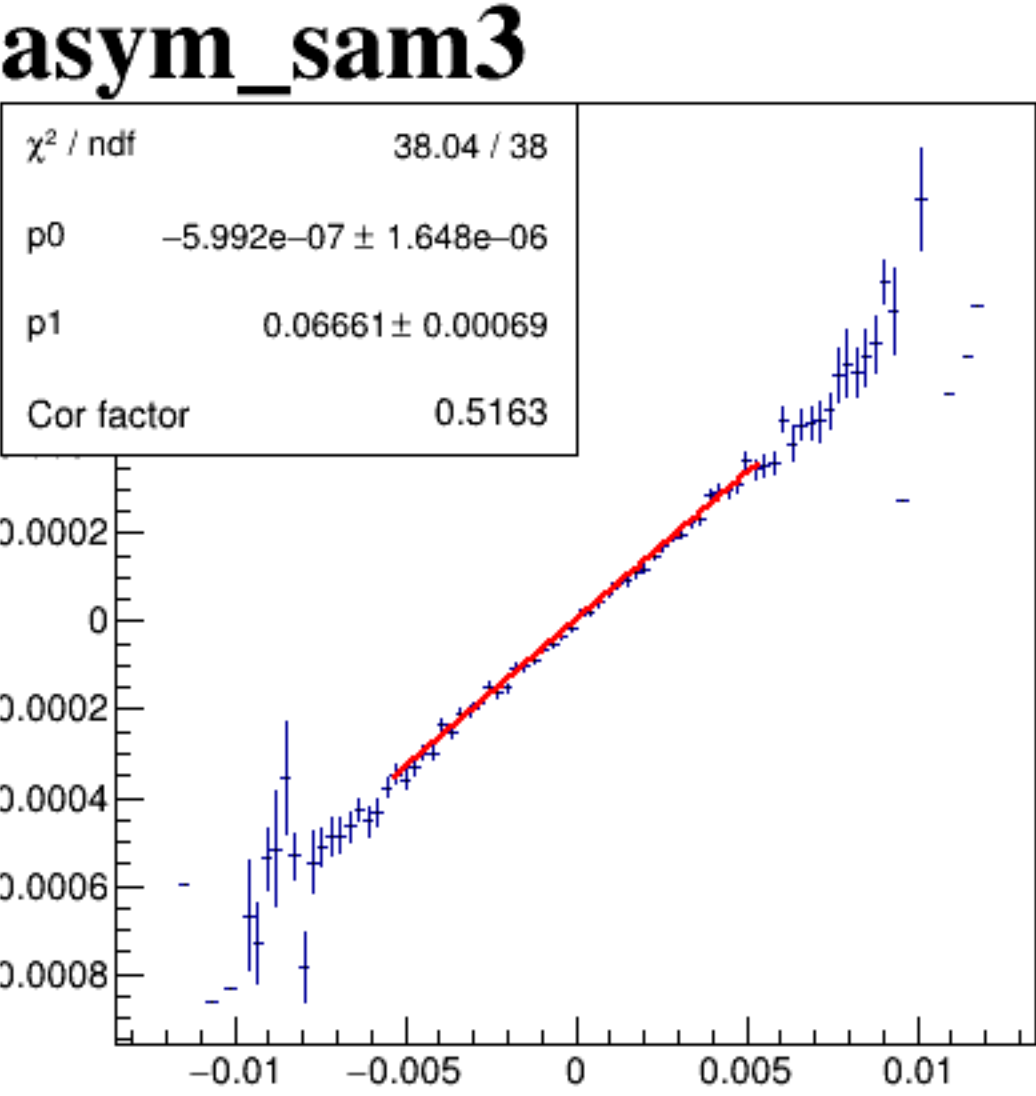
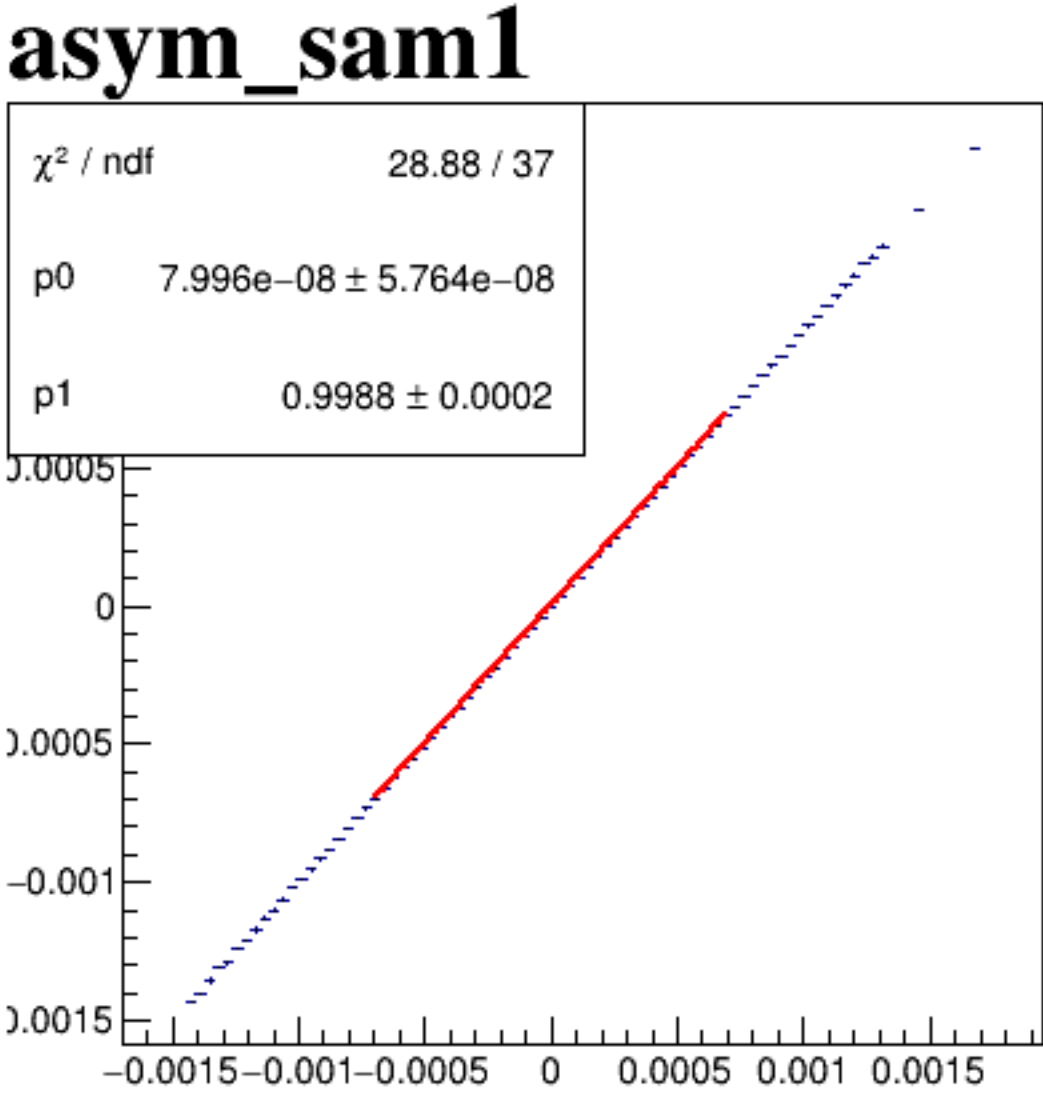
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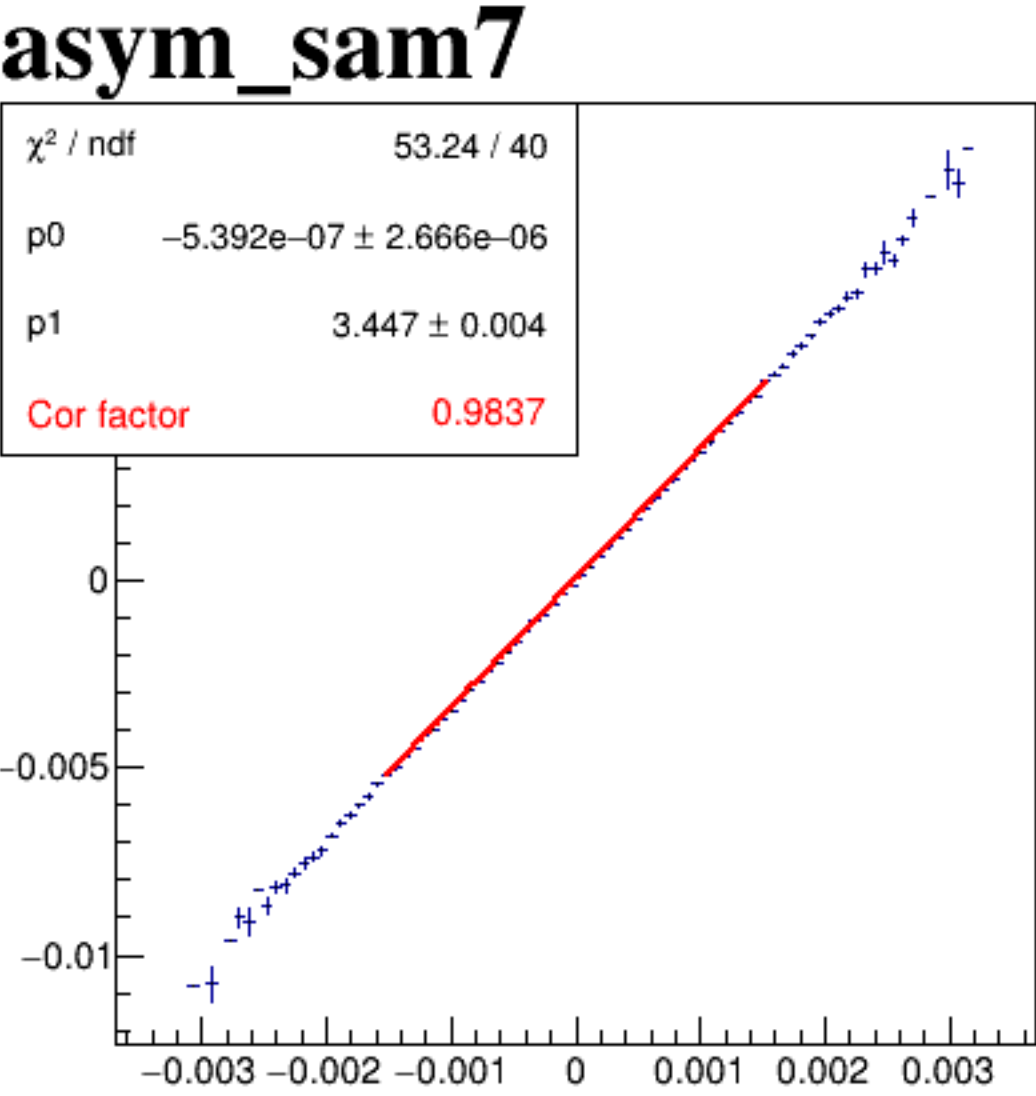
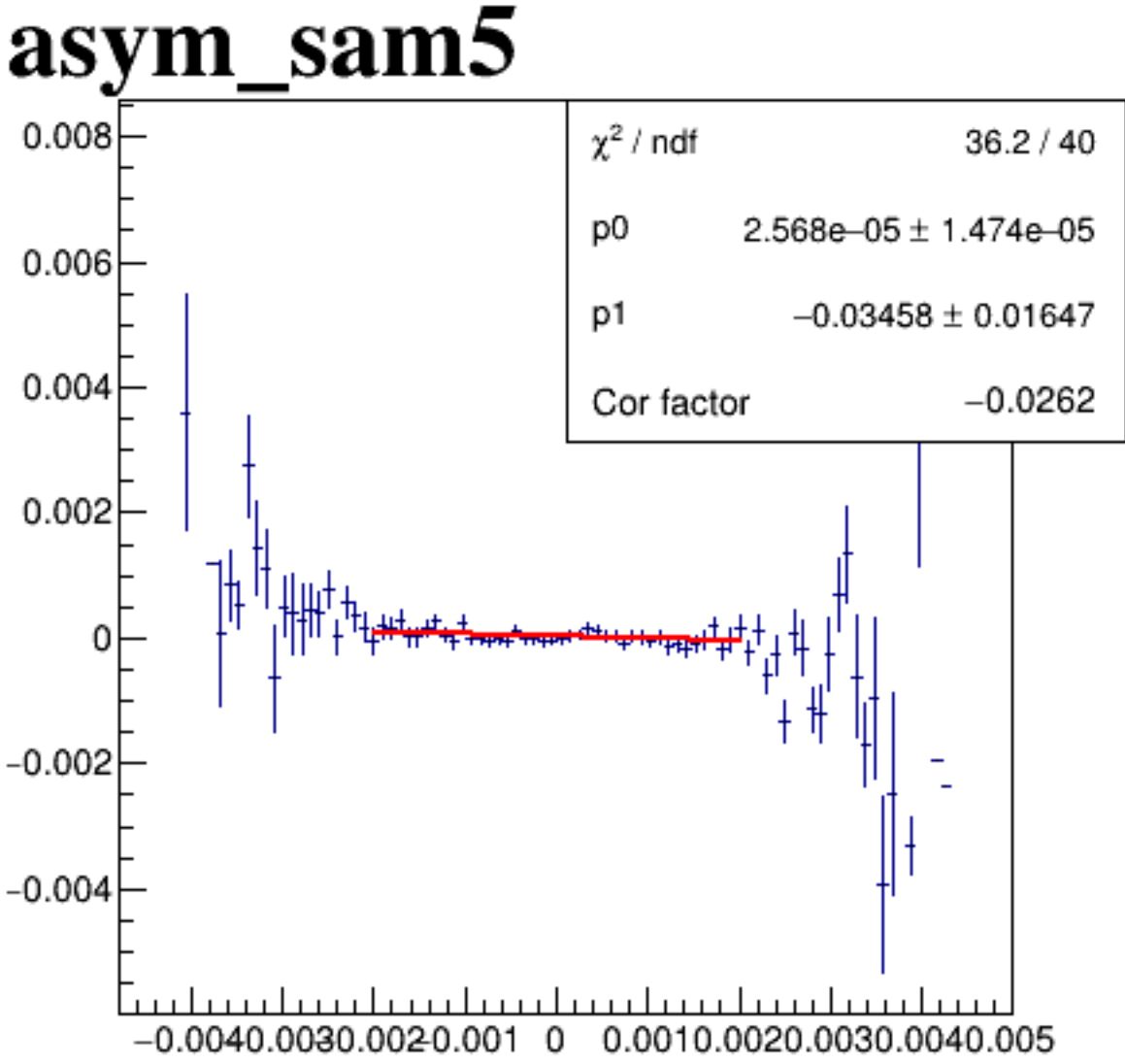
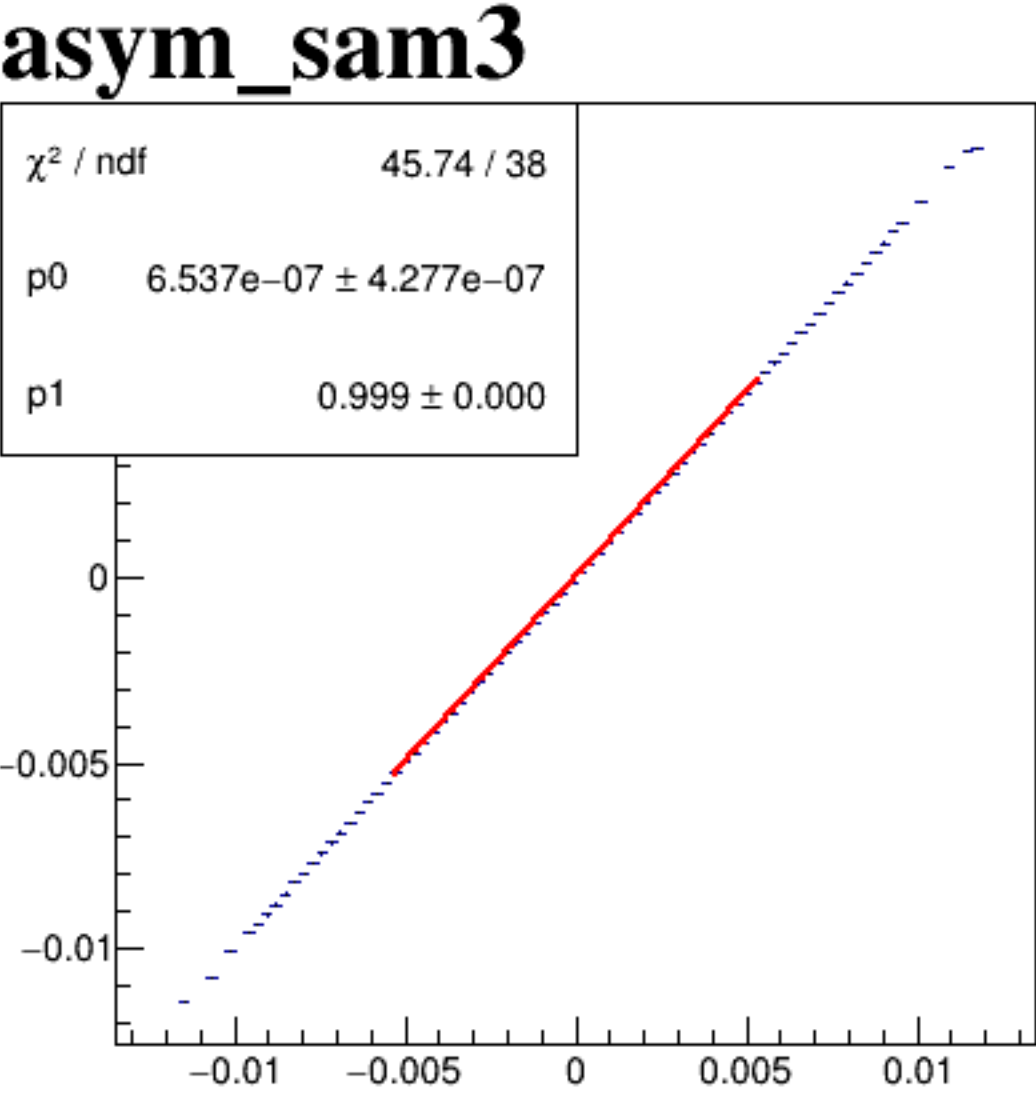
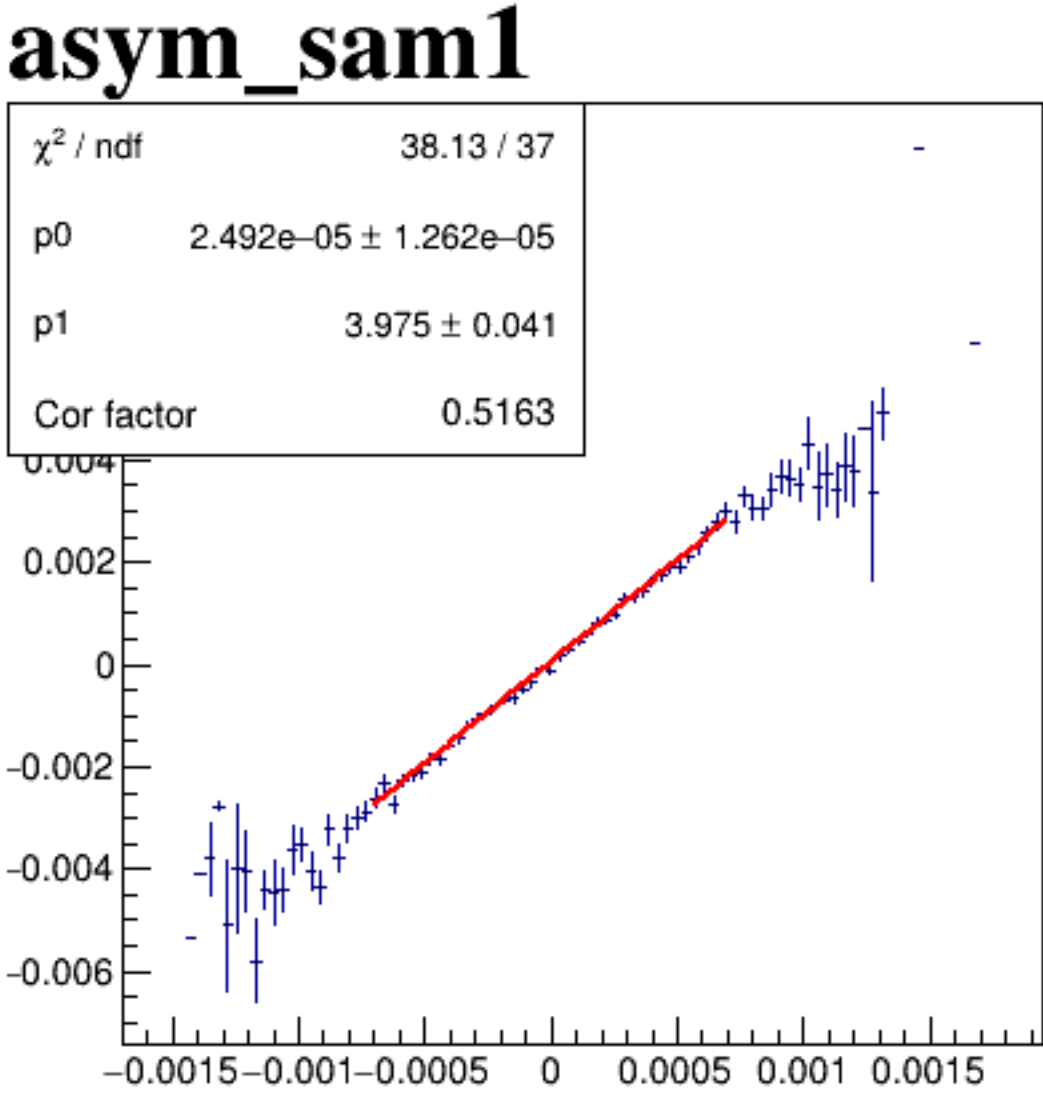
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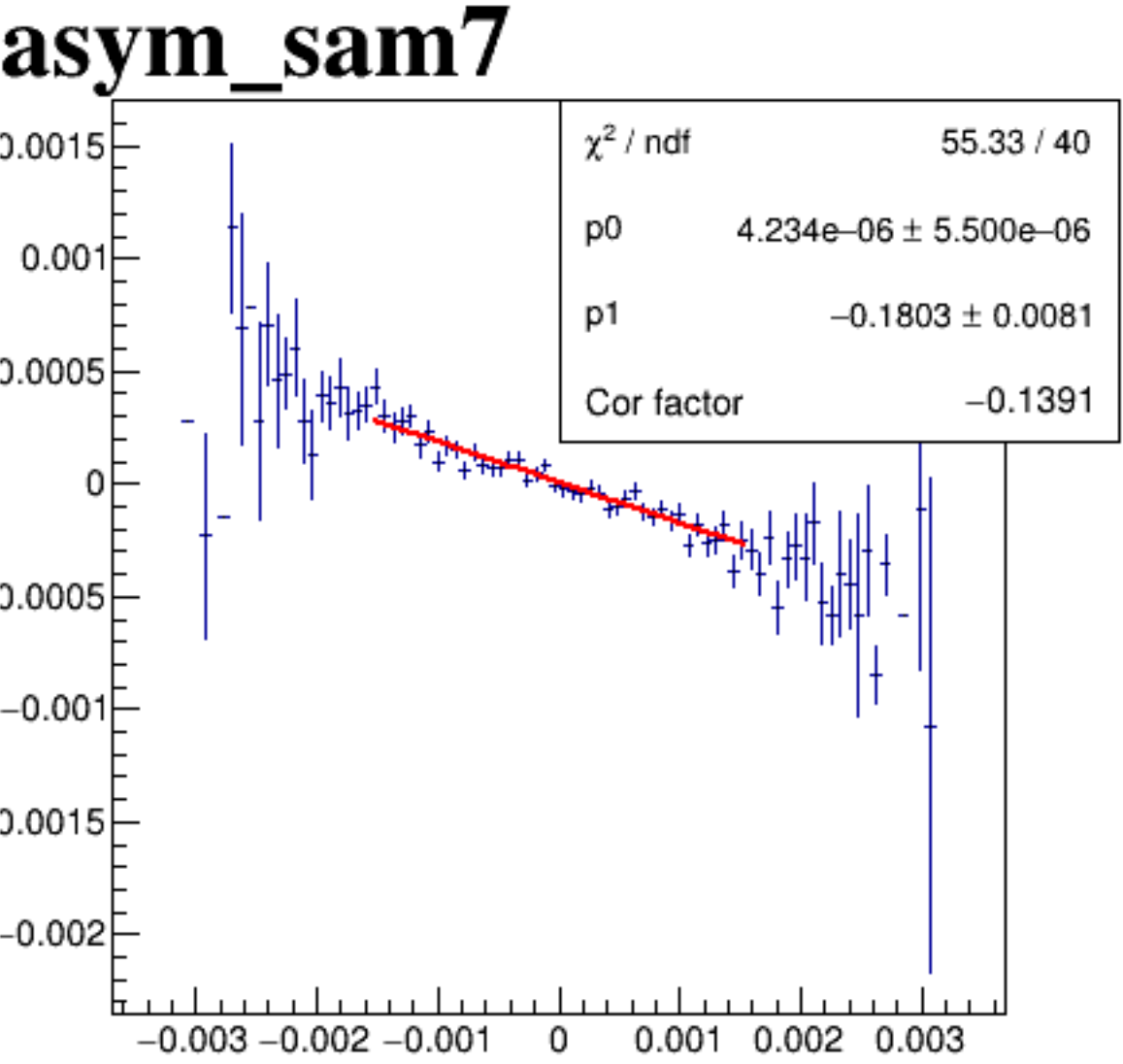
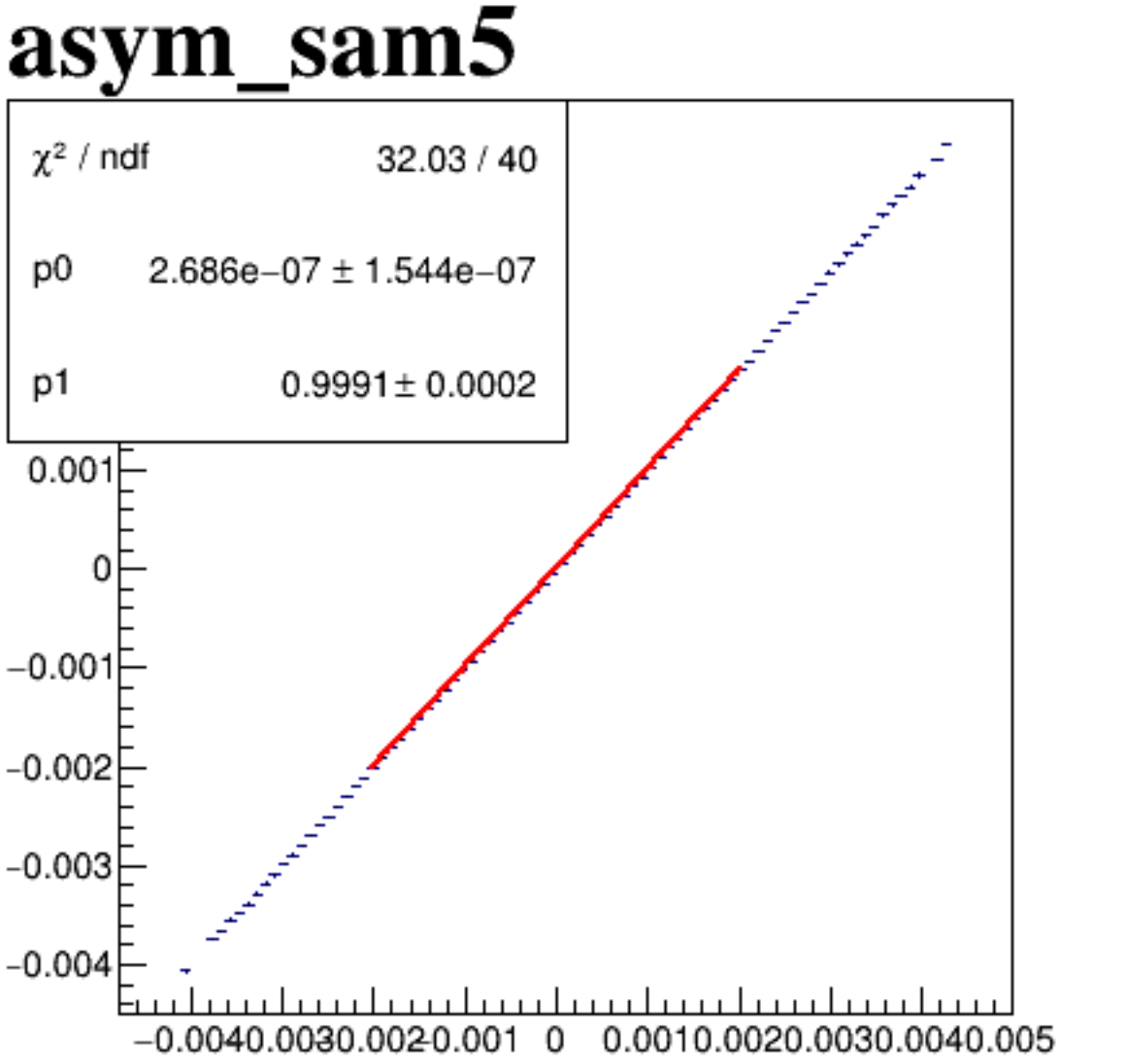
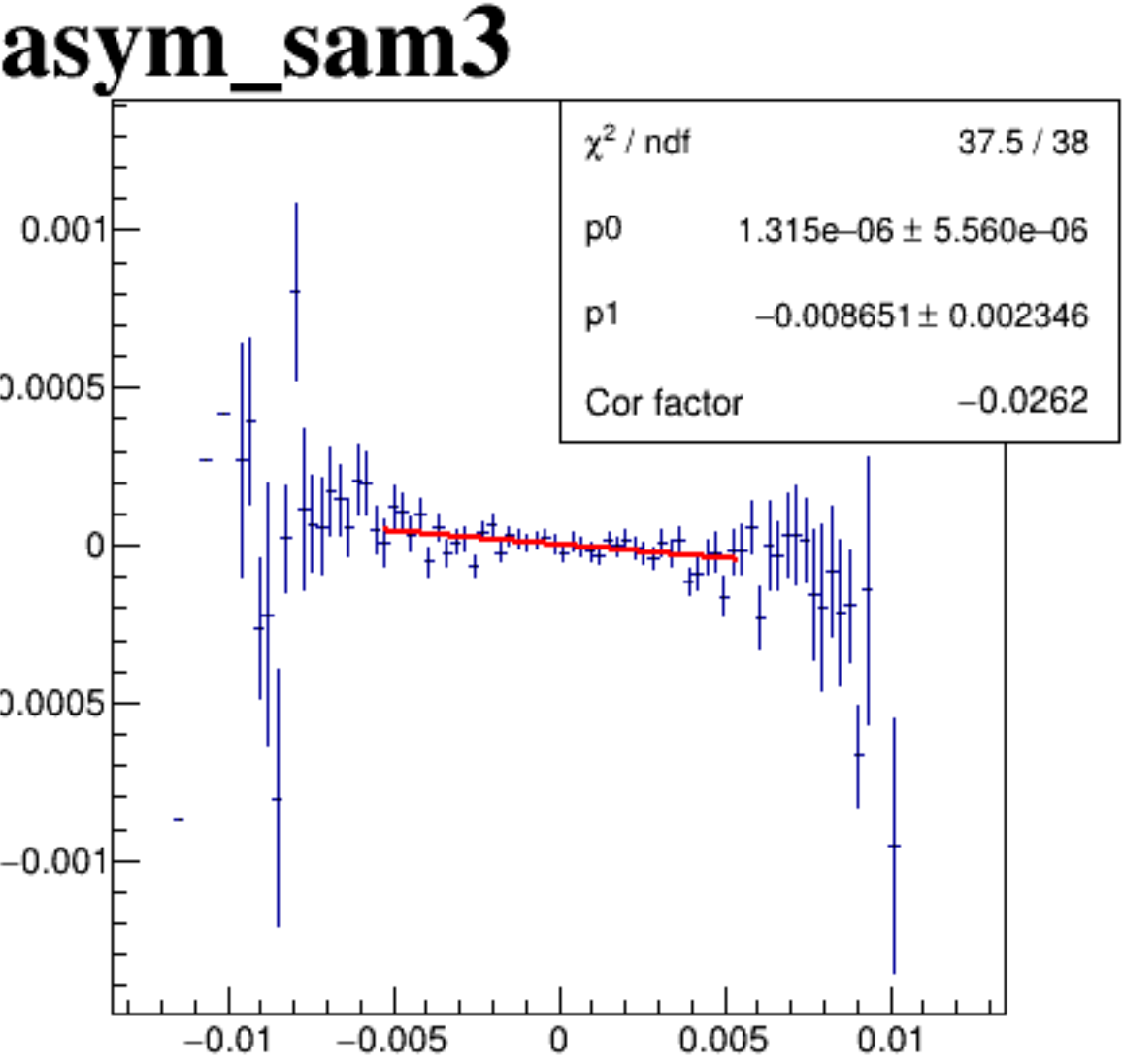
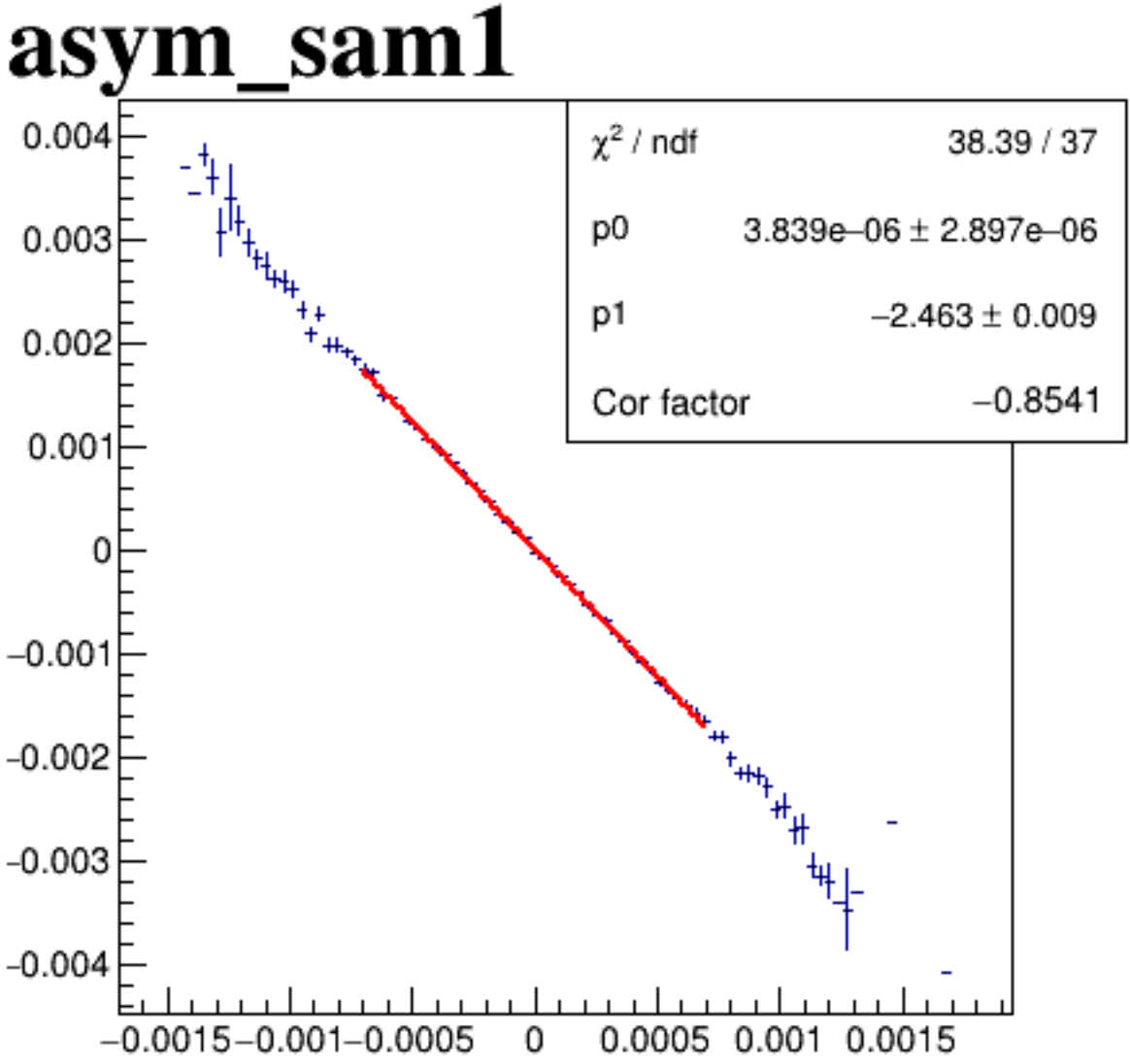
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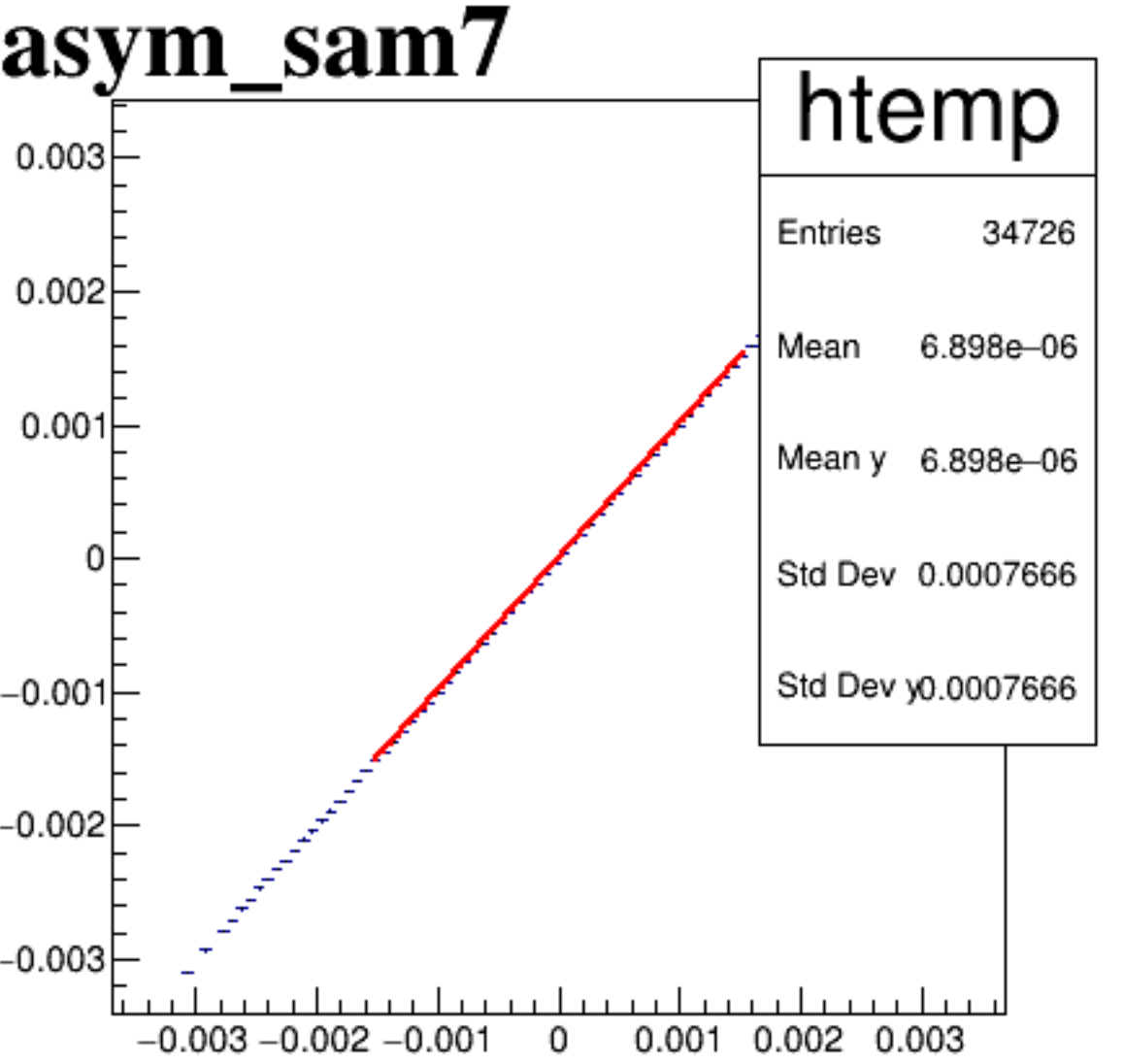
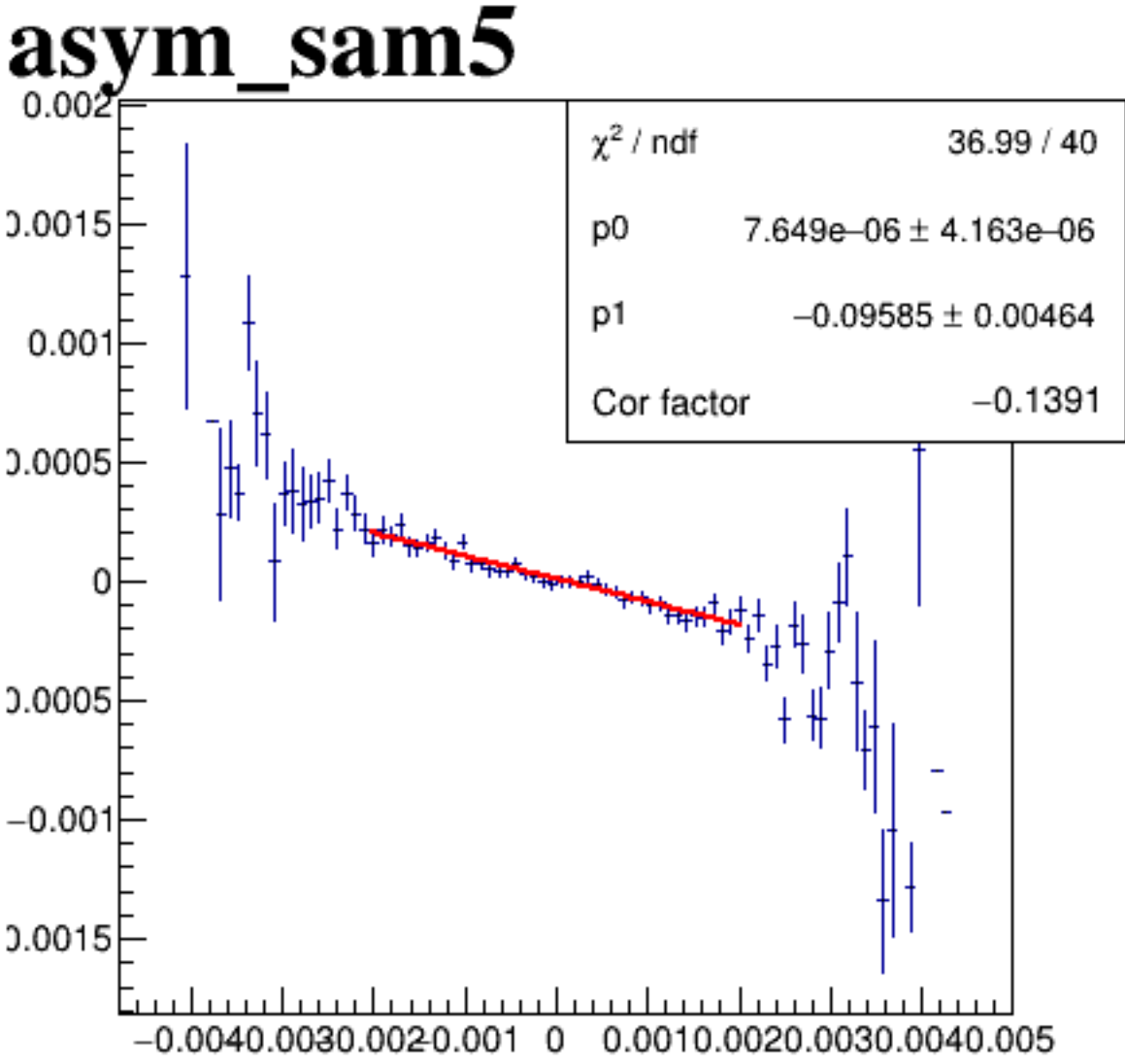
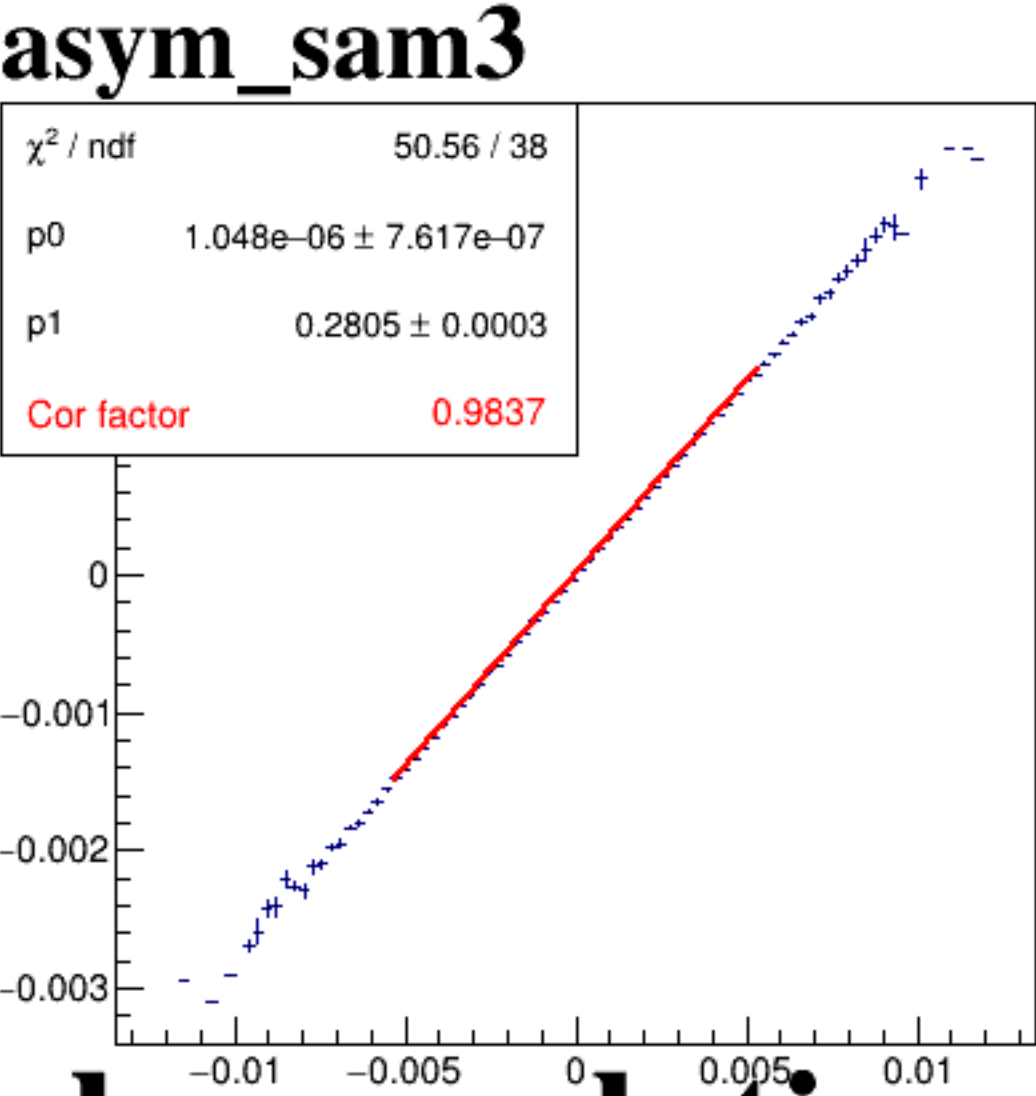
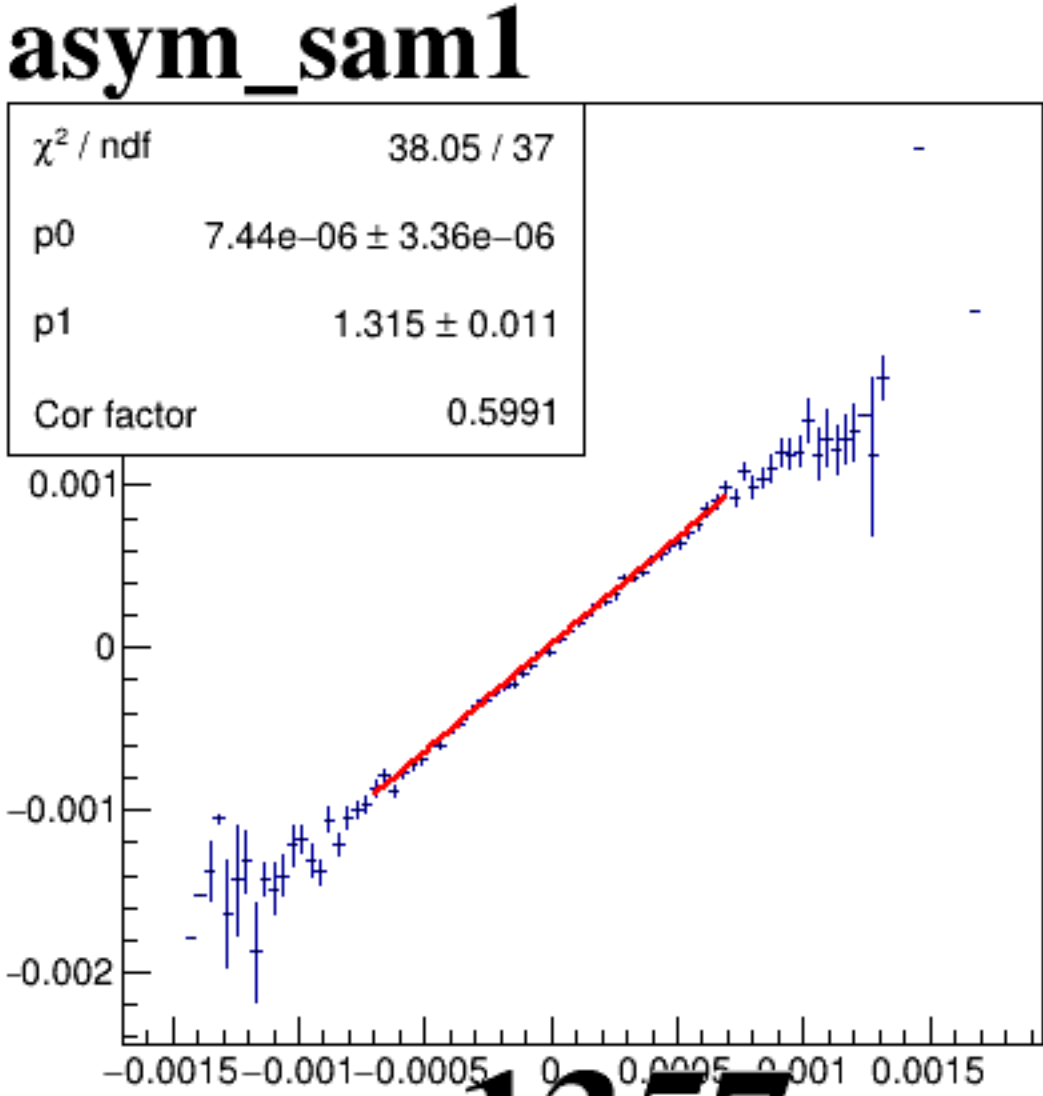
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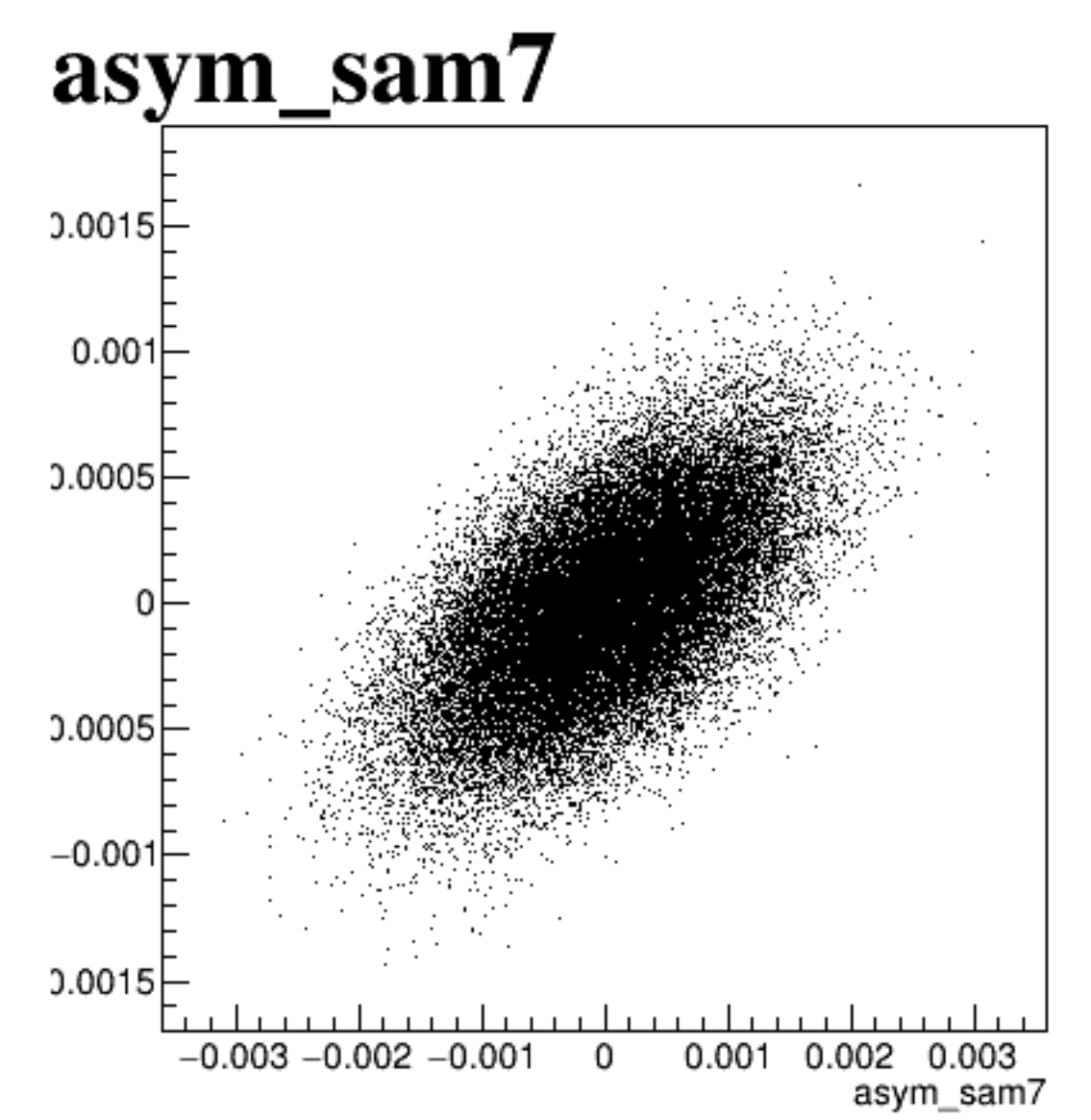
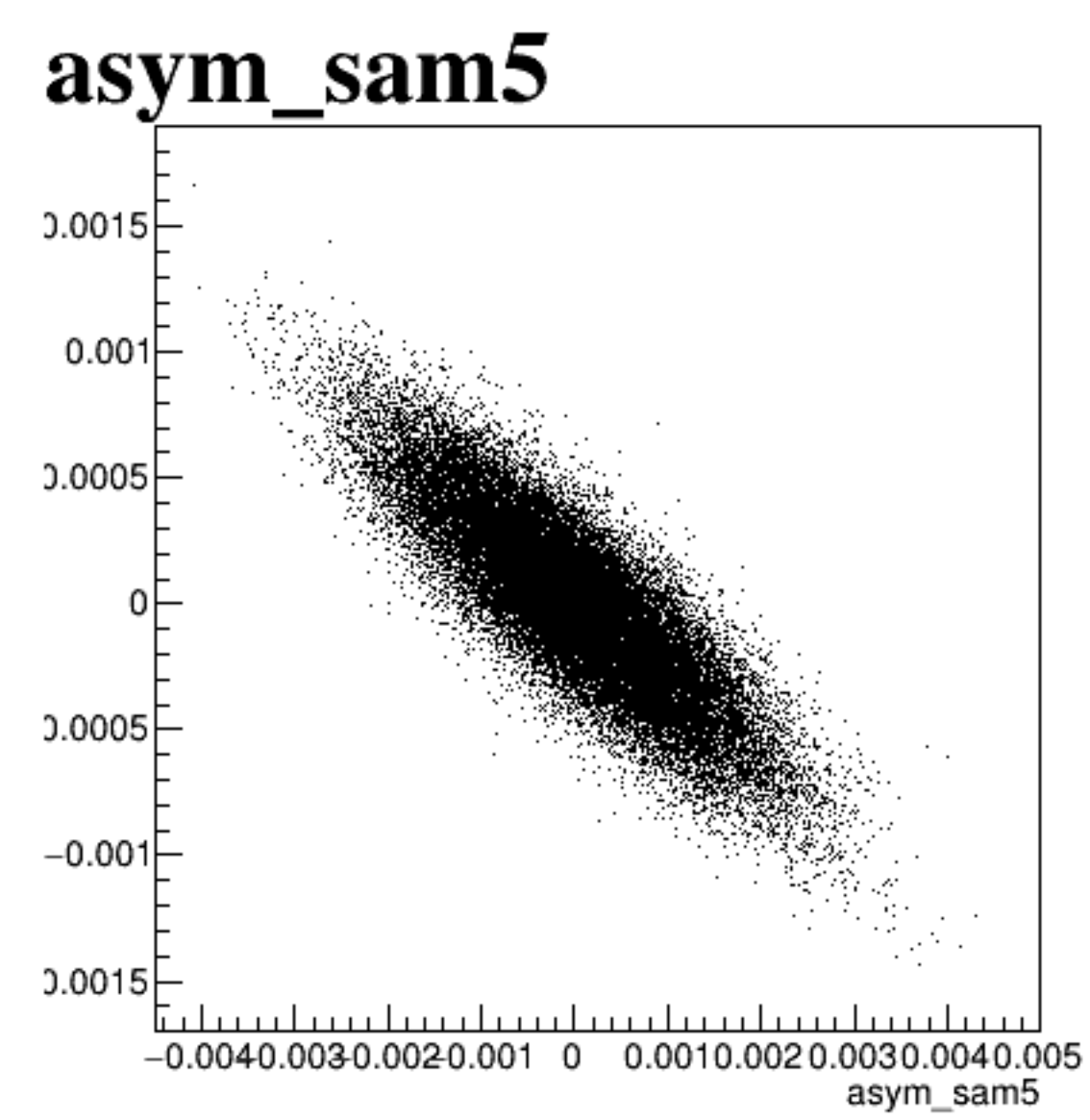
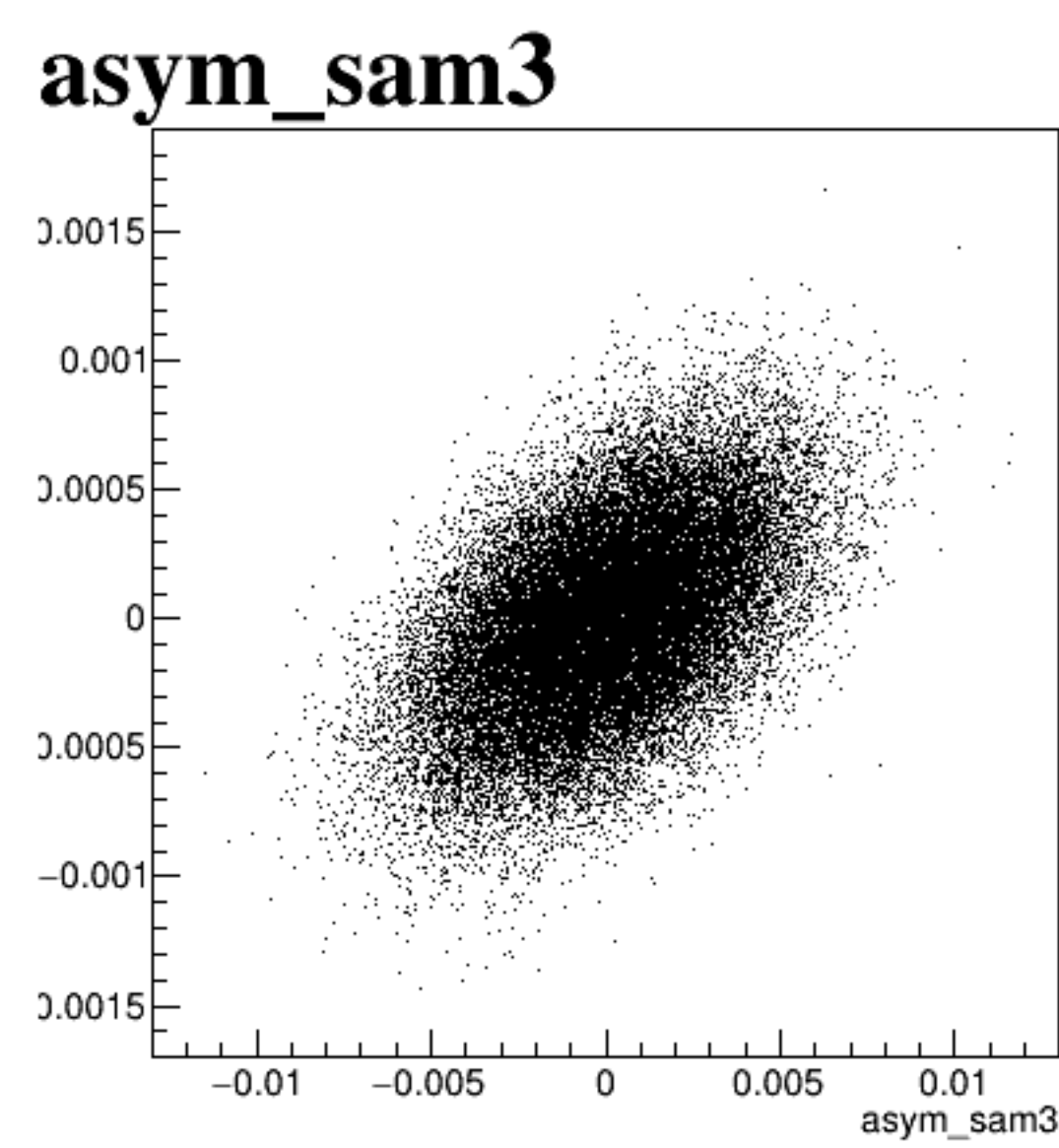
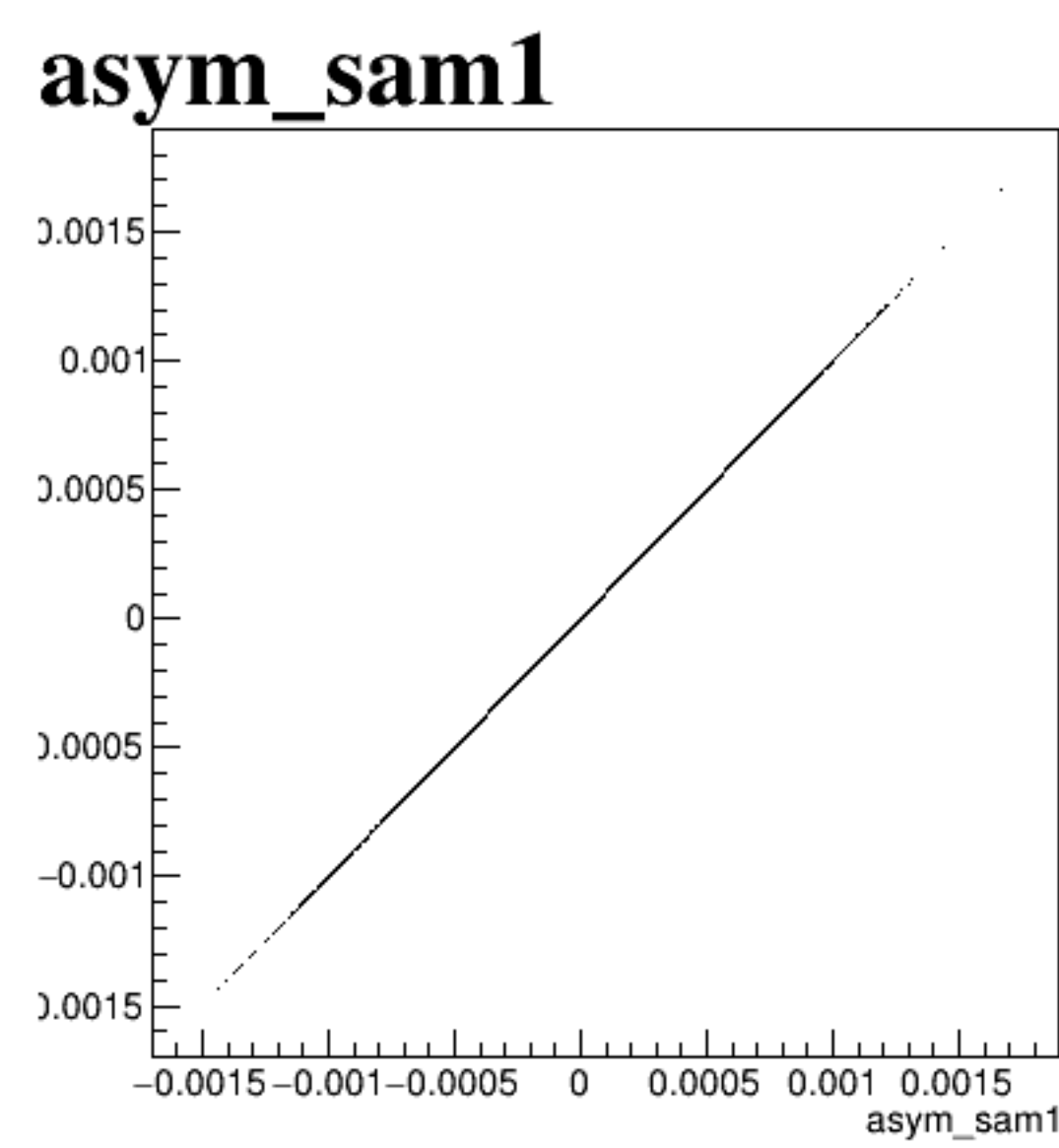


asym_sam5

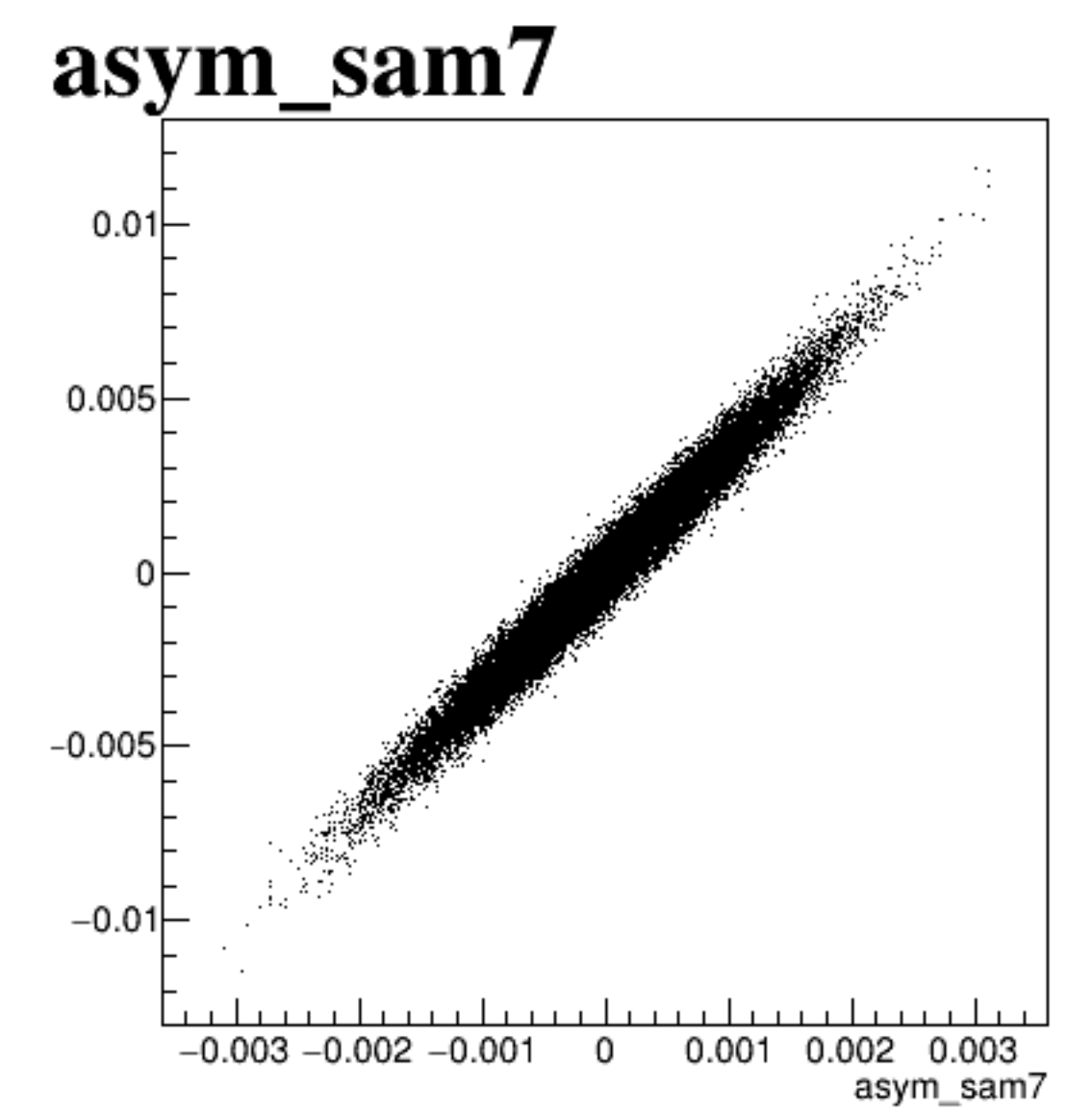
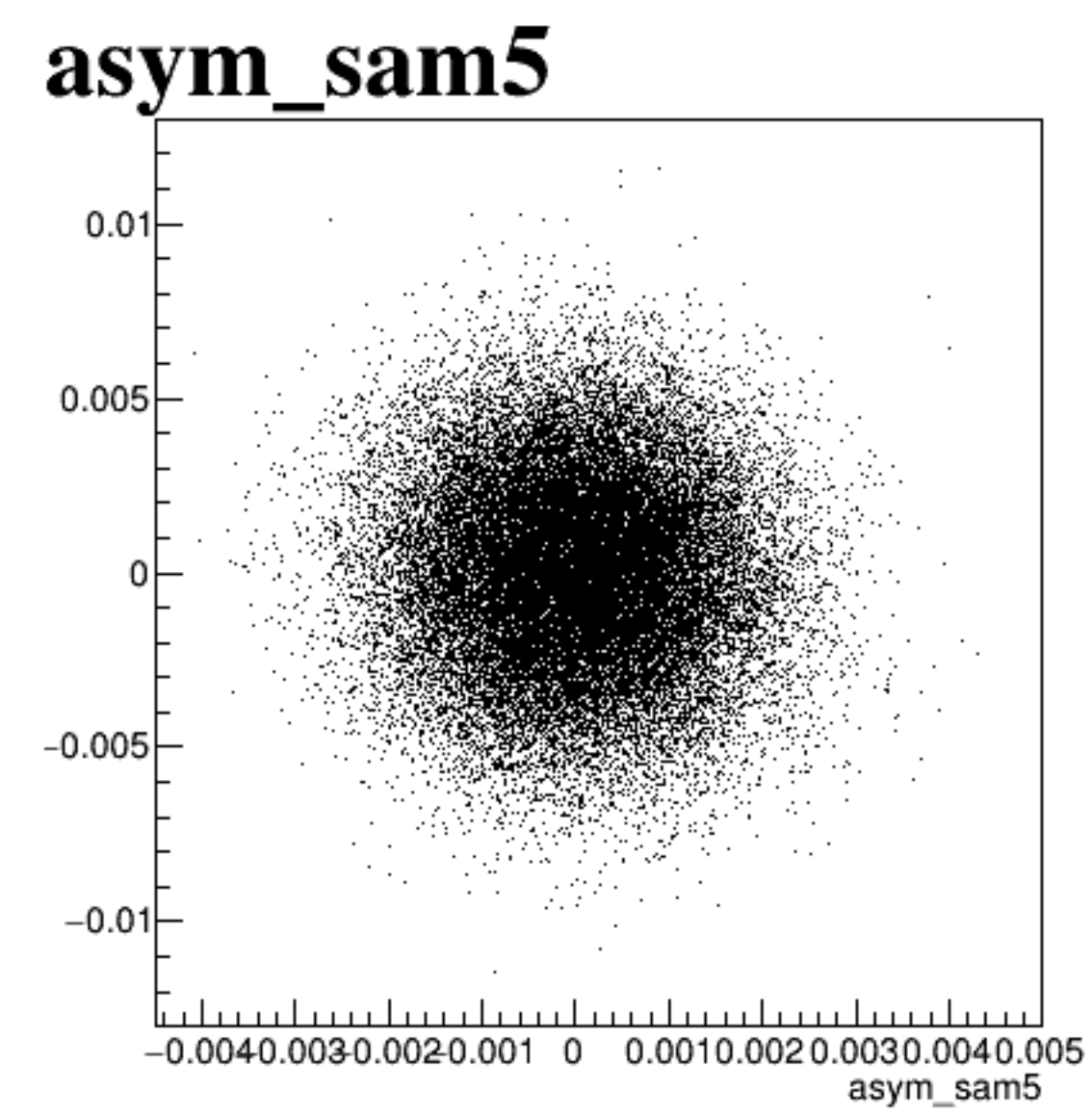
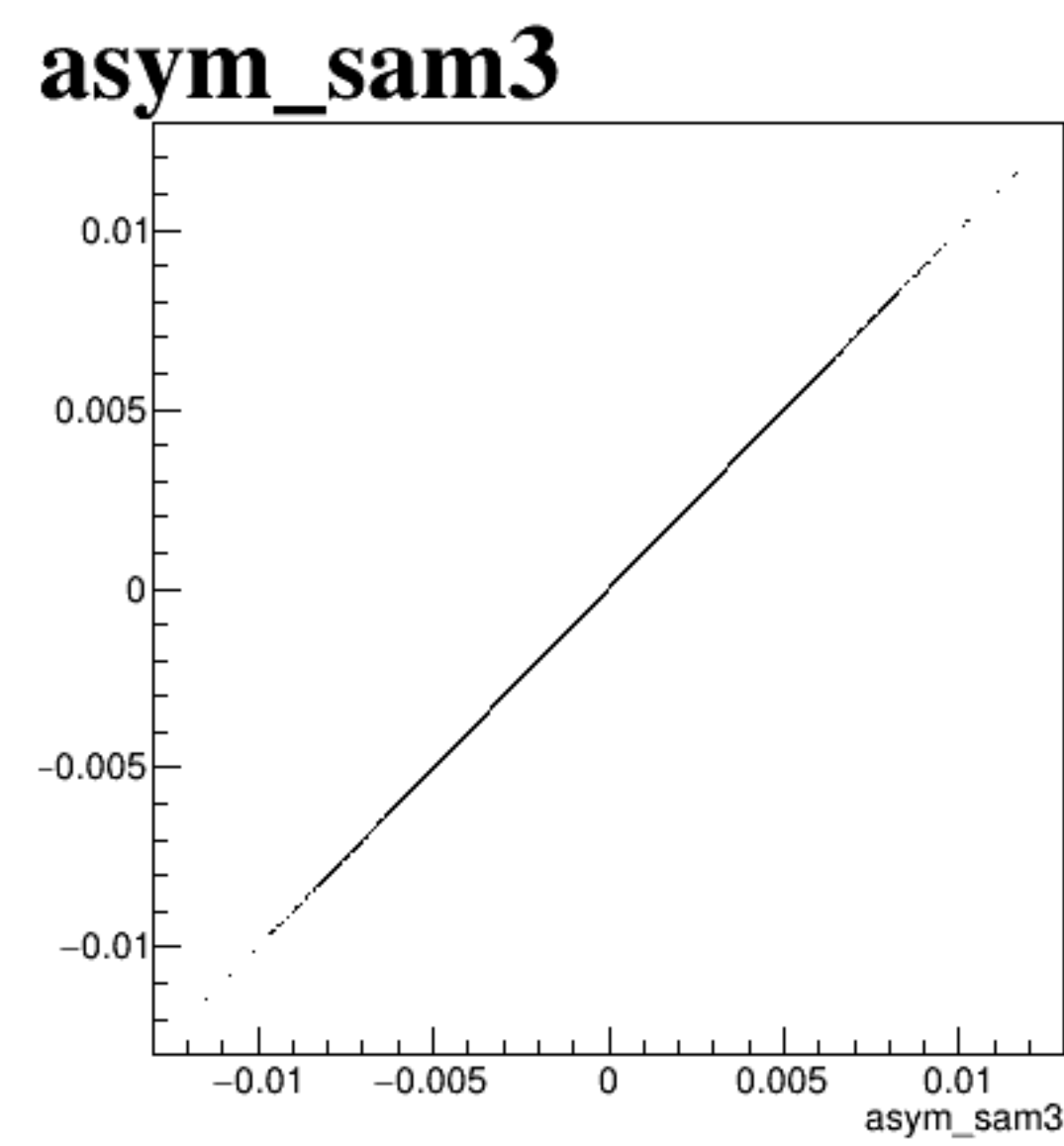
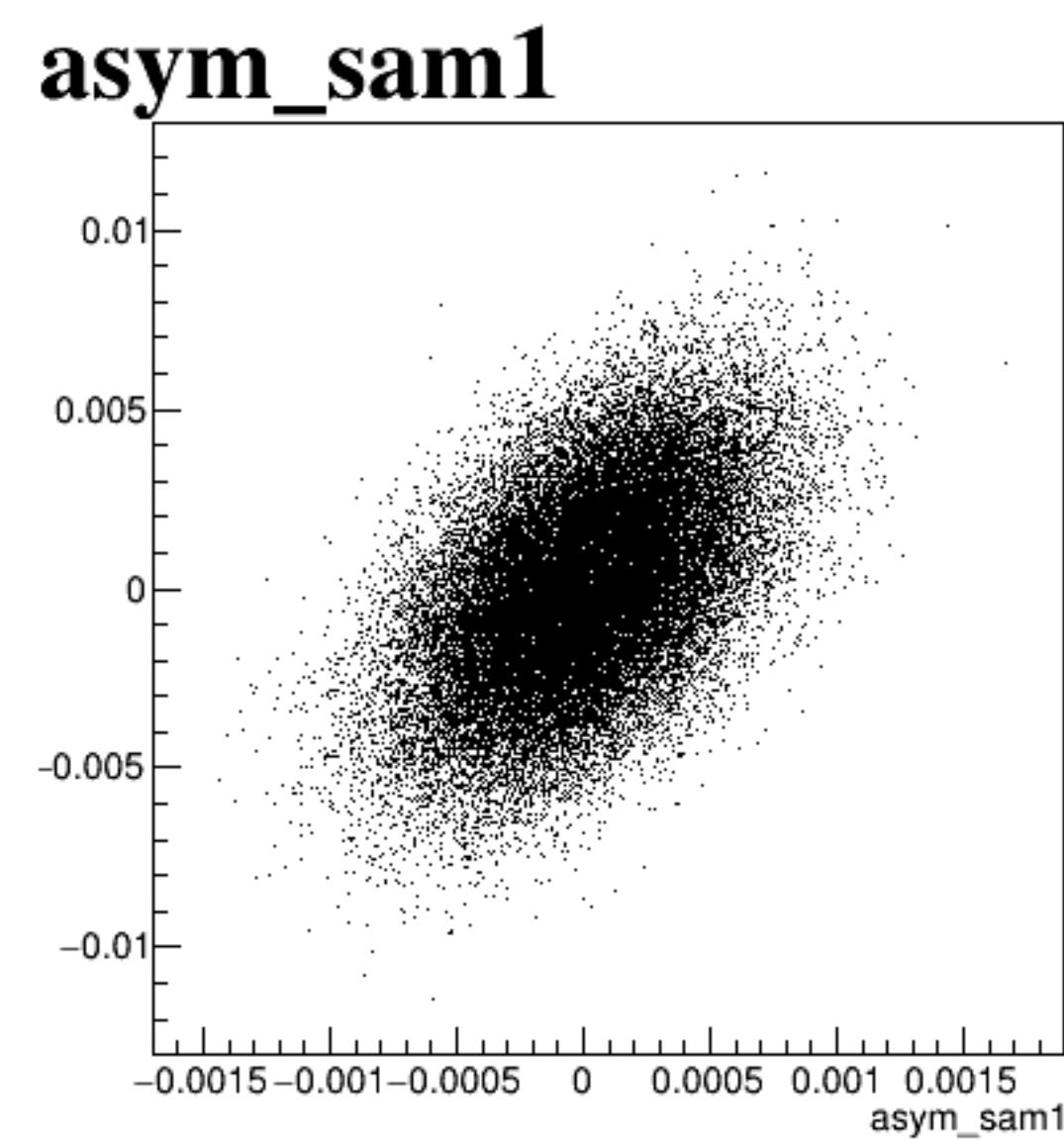
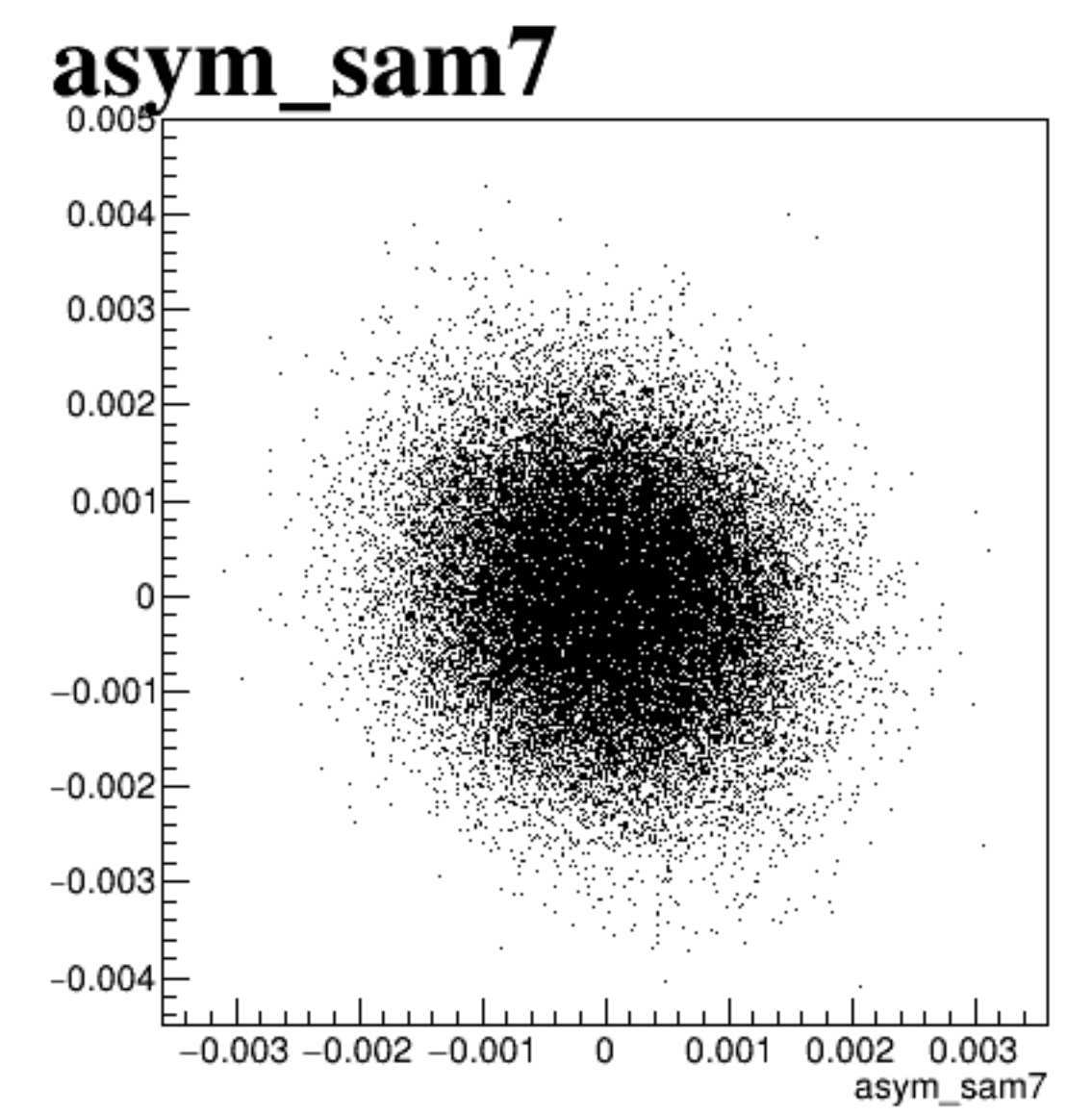
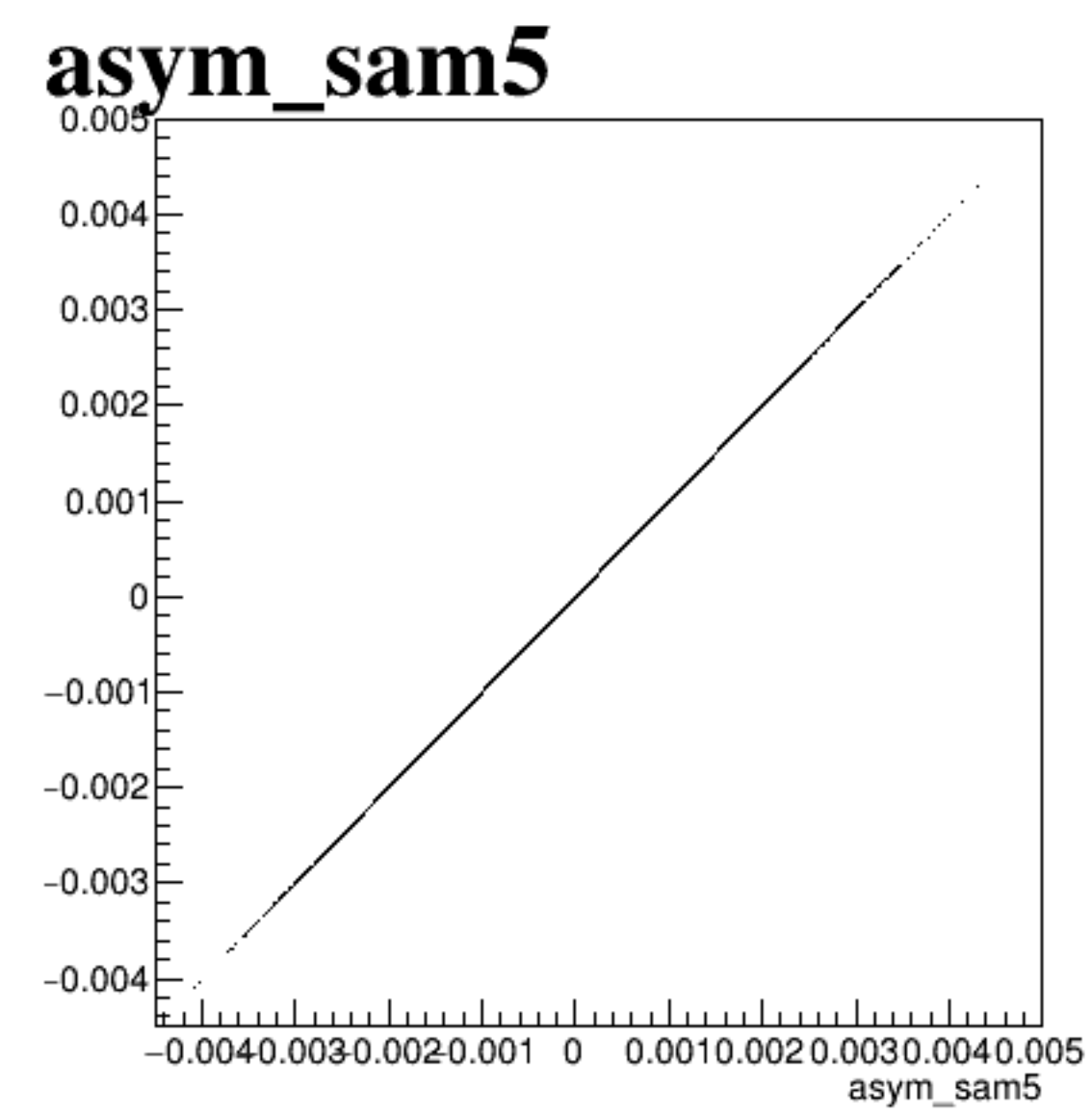
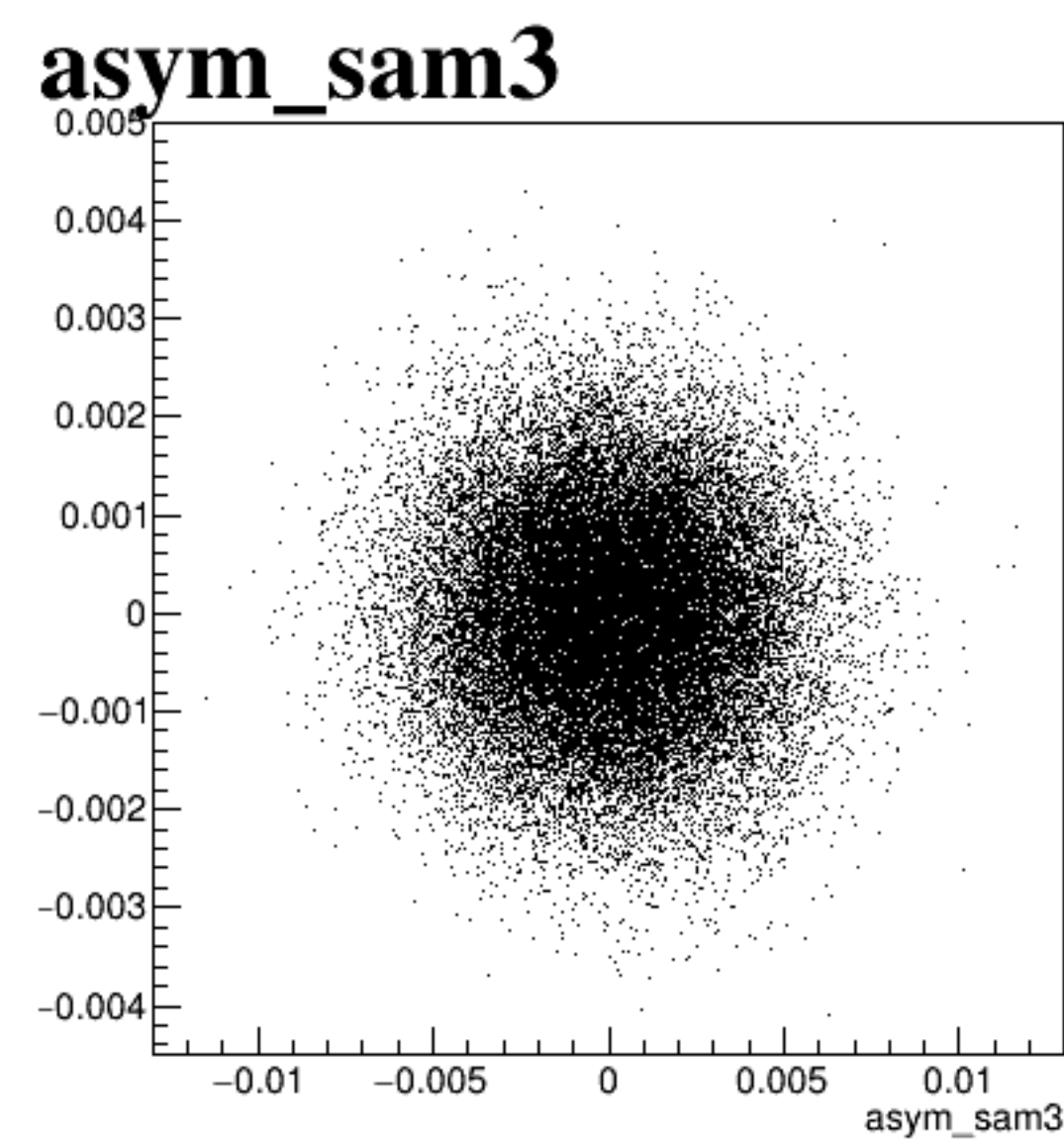
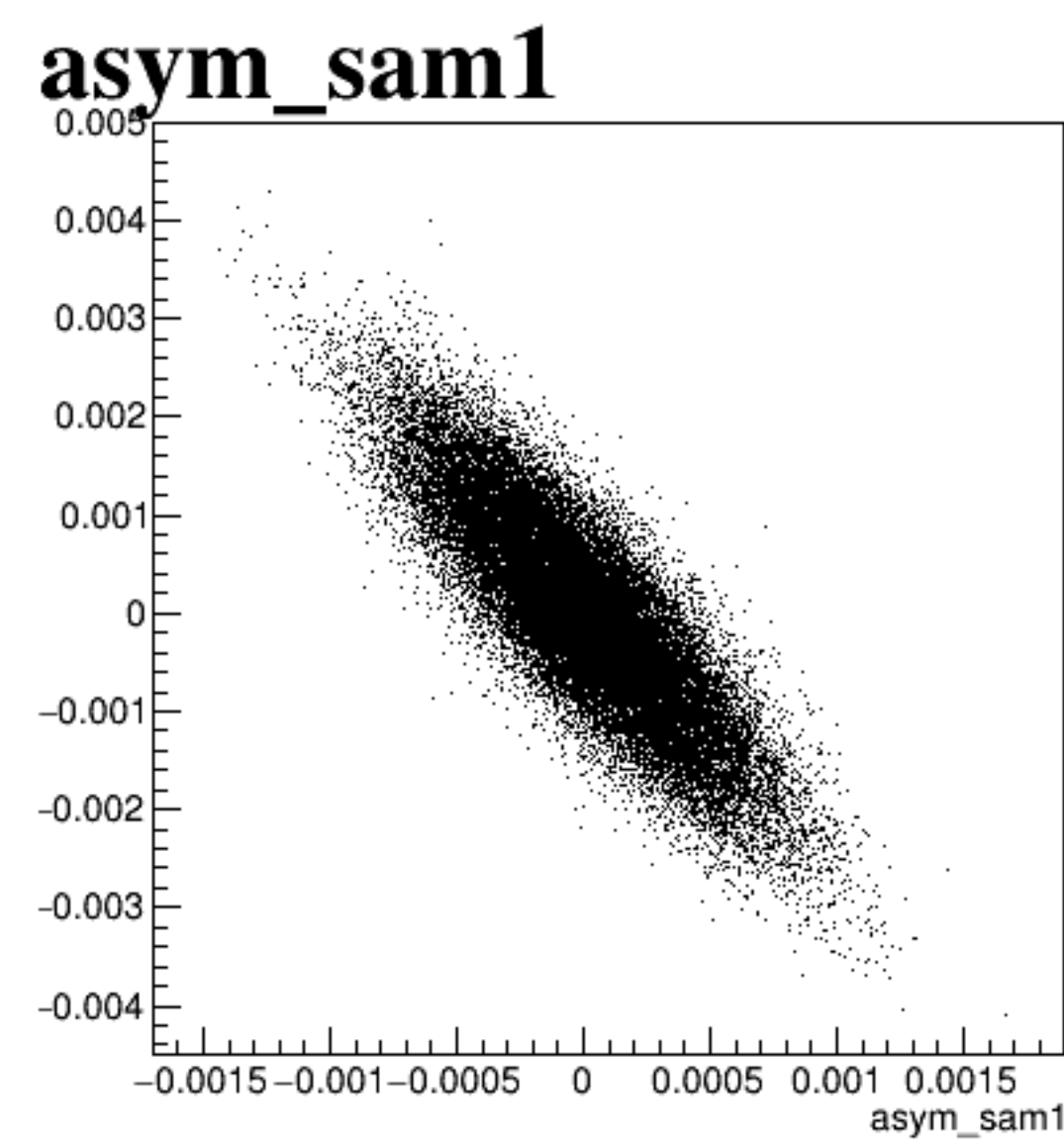


asym_sam7



asym_sam1

asym_sam3

**asym_sam5**

asym_sam7

