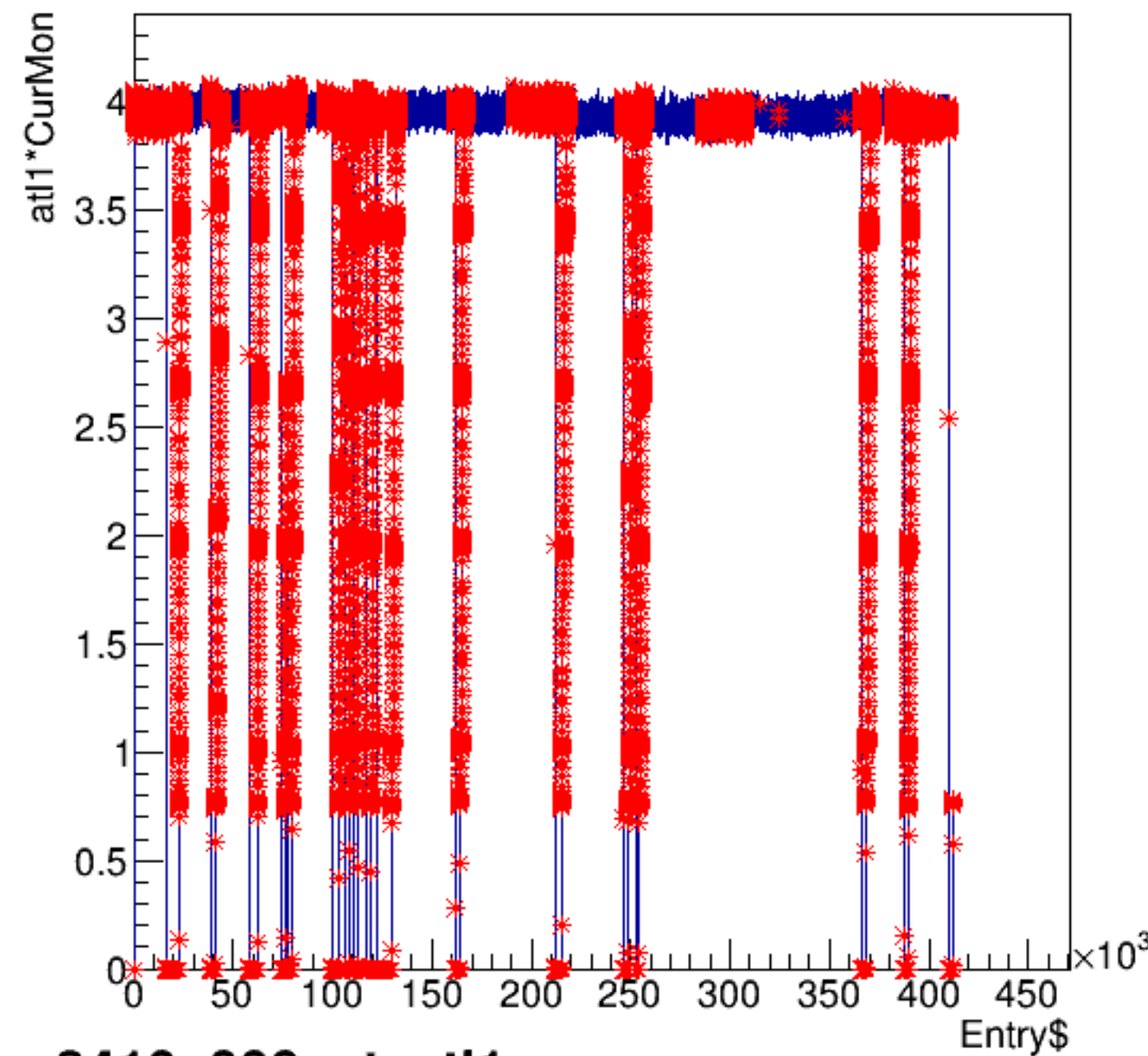
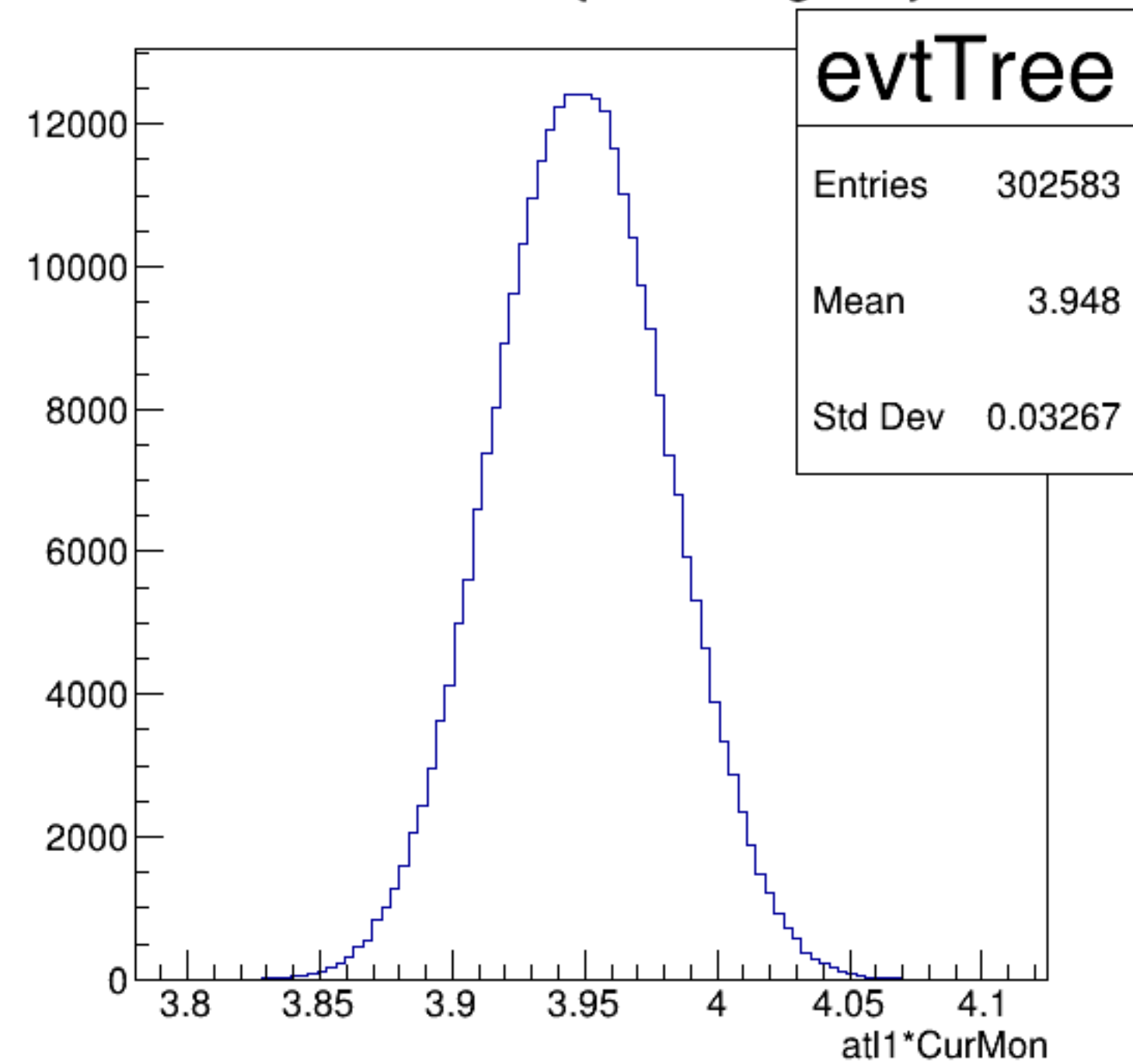


at1\*CurMon:Entry\$

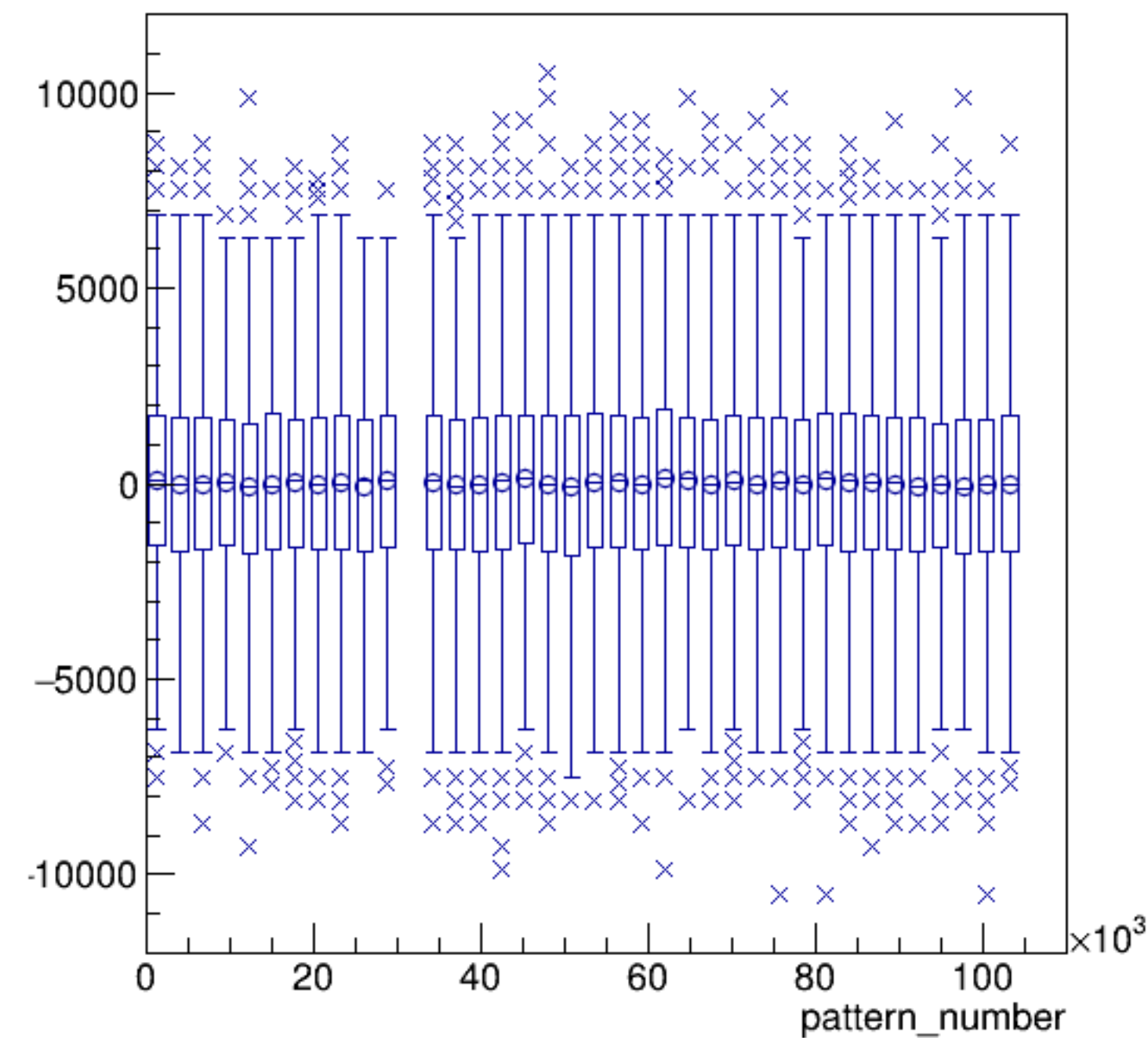


run8410\_000 at at1.png

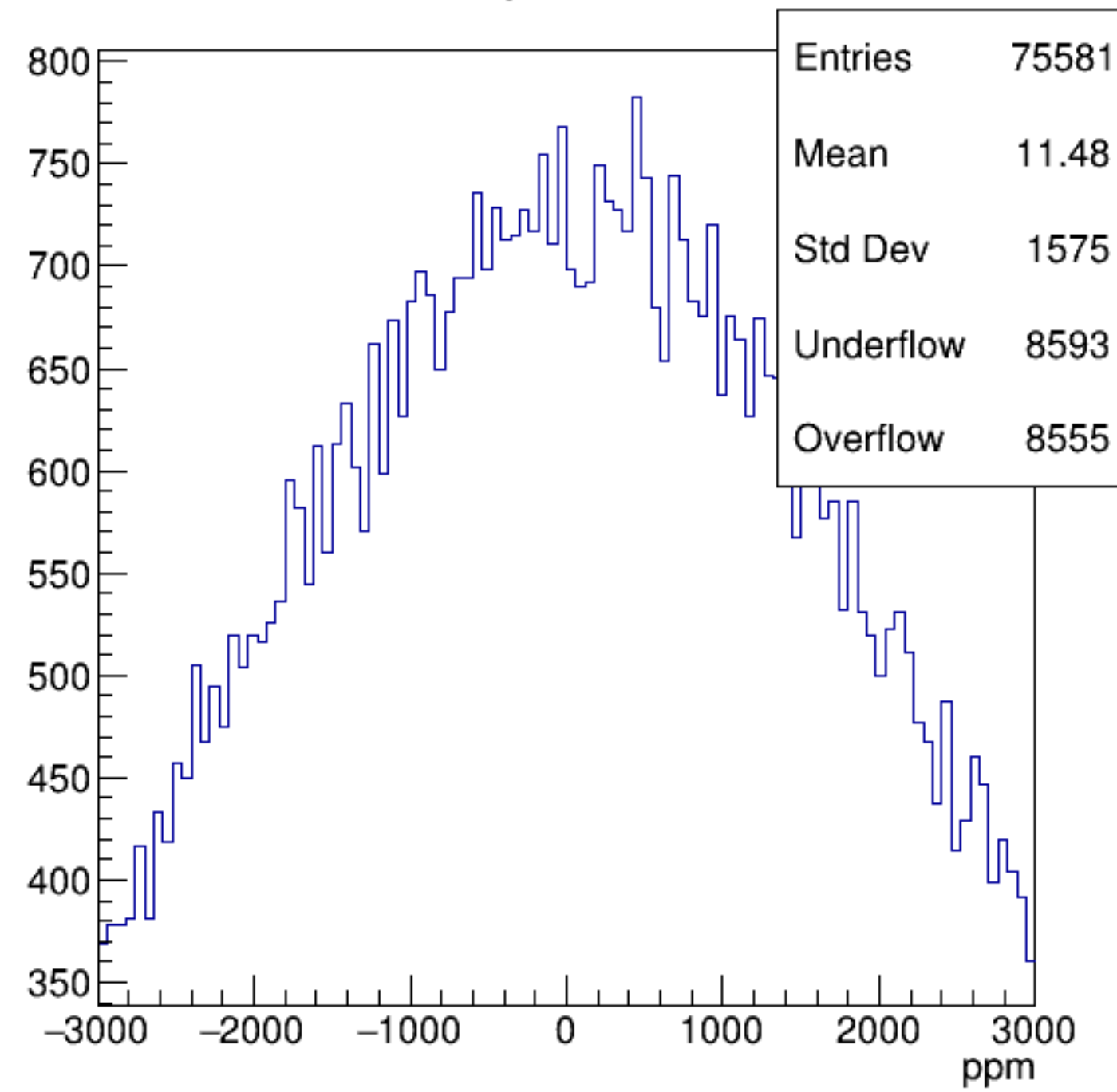
at1\*CurMon {ErrorFlag==0}



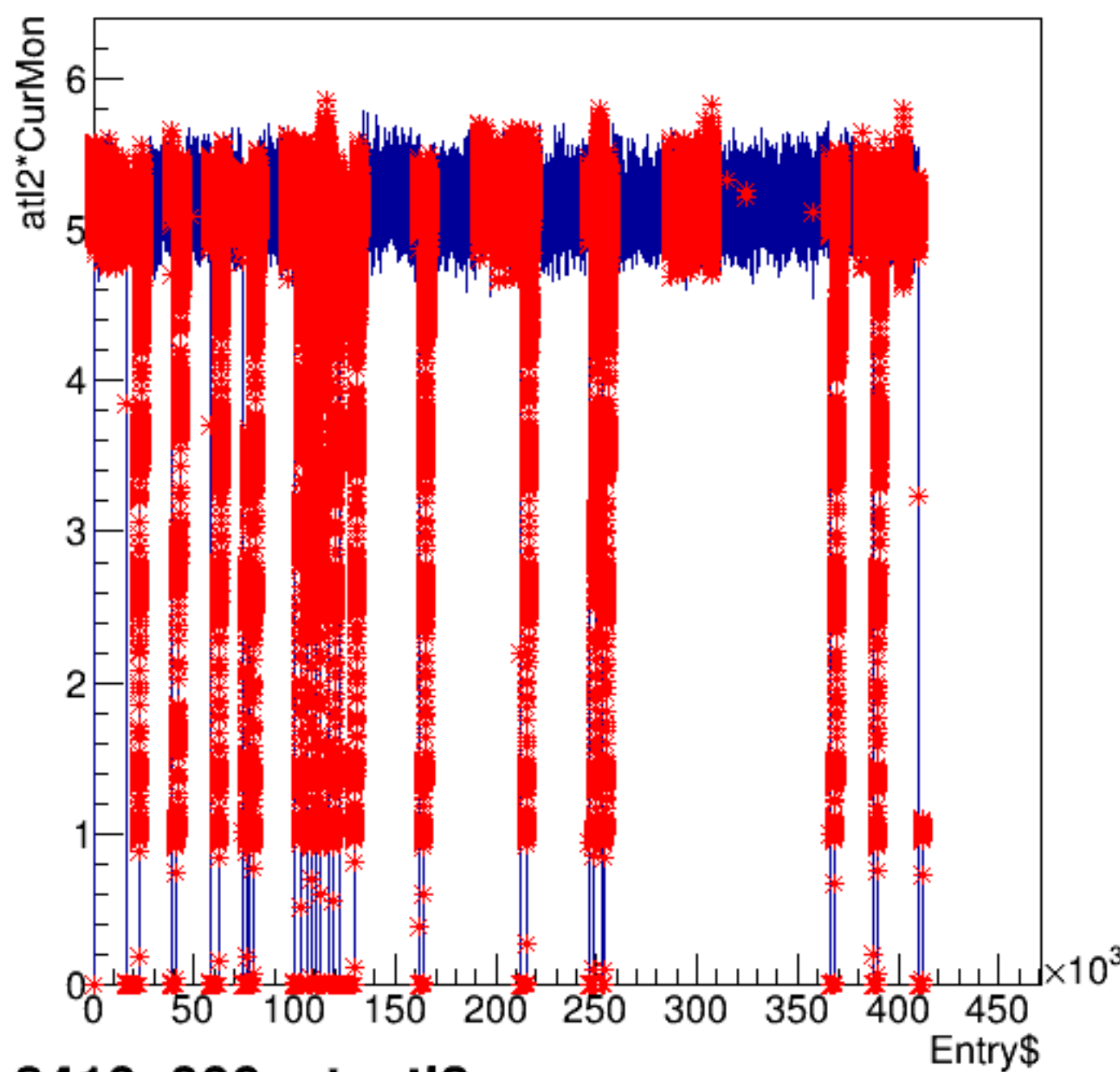
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asym\_at1

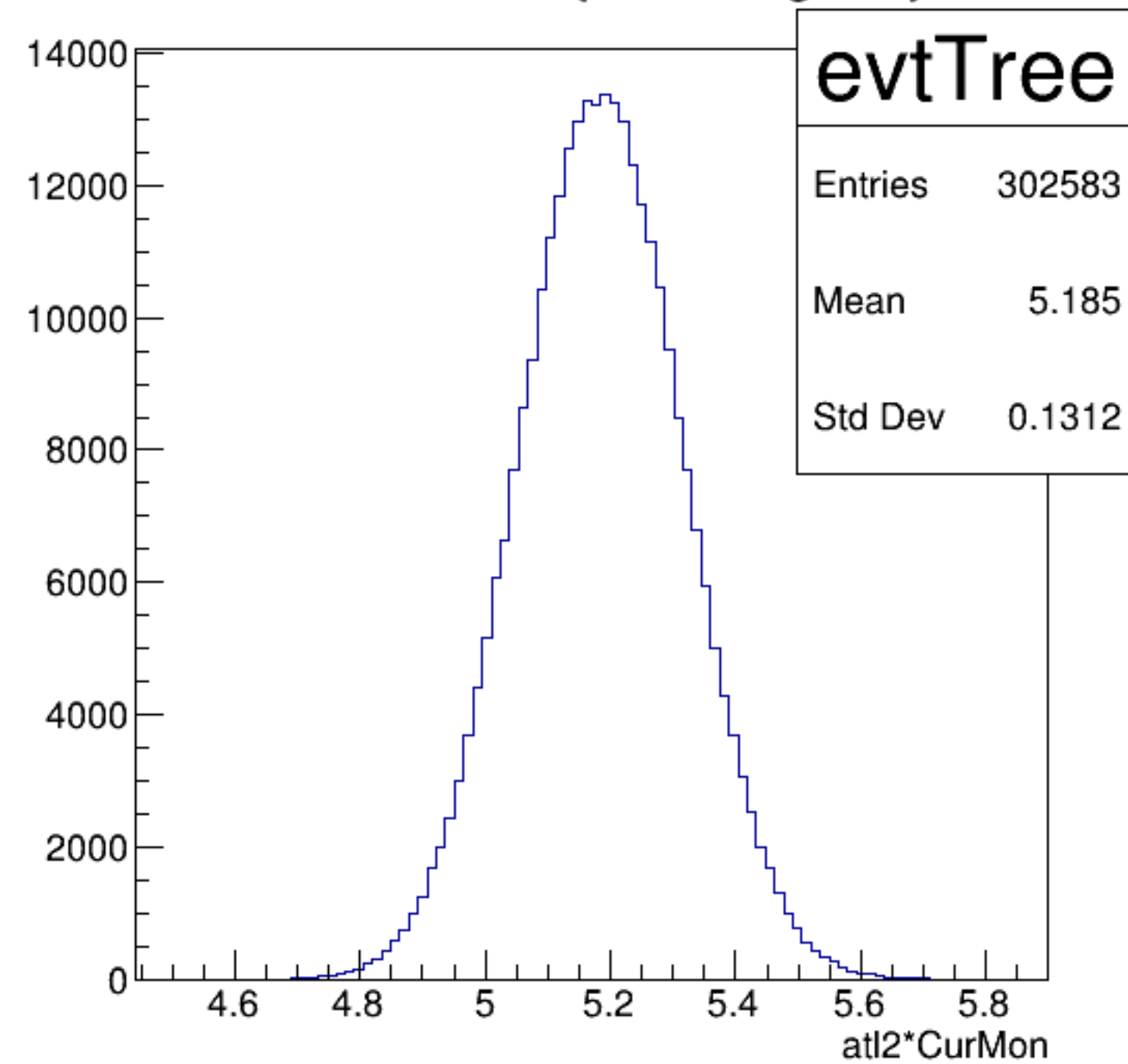


atl2\*CurMon:Entry\$

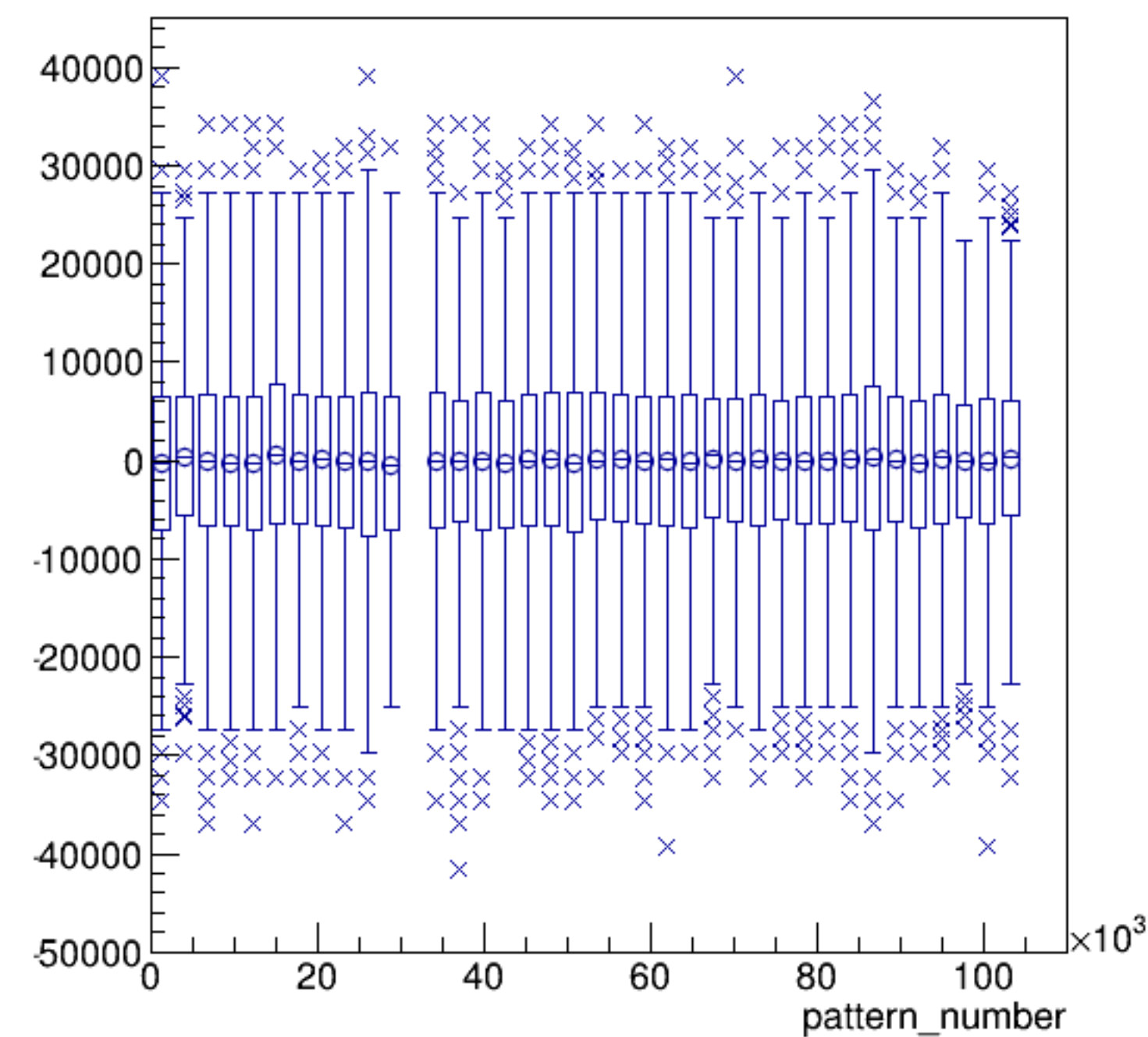


run8410\_000 at atl2.png

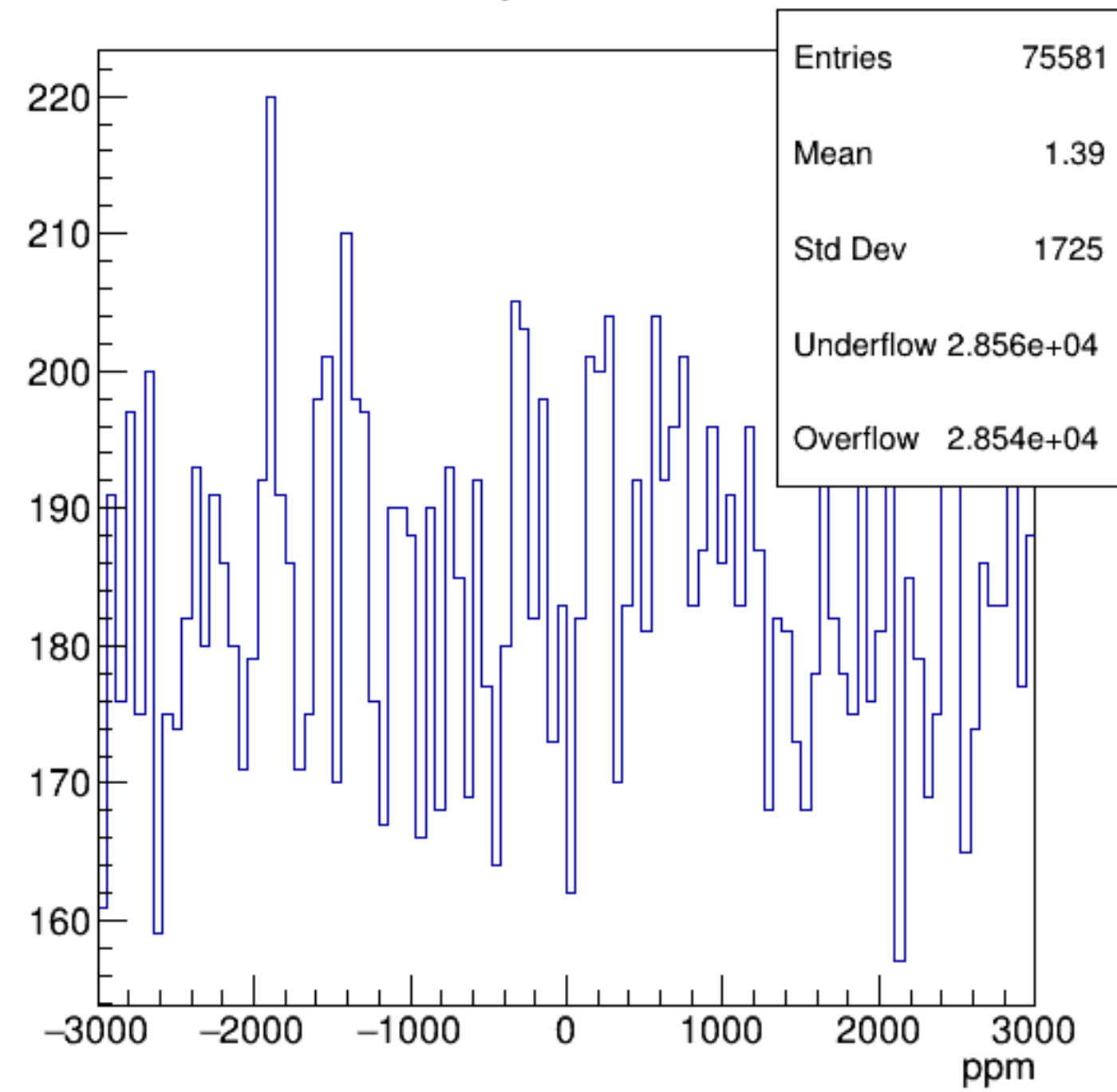
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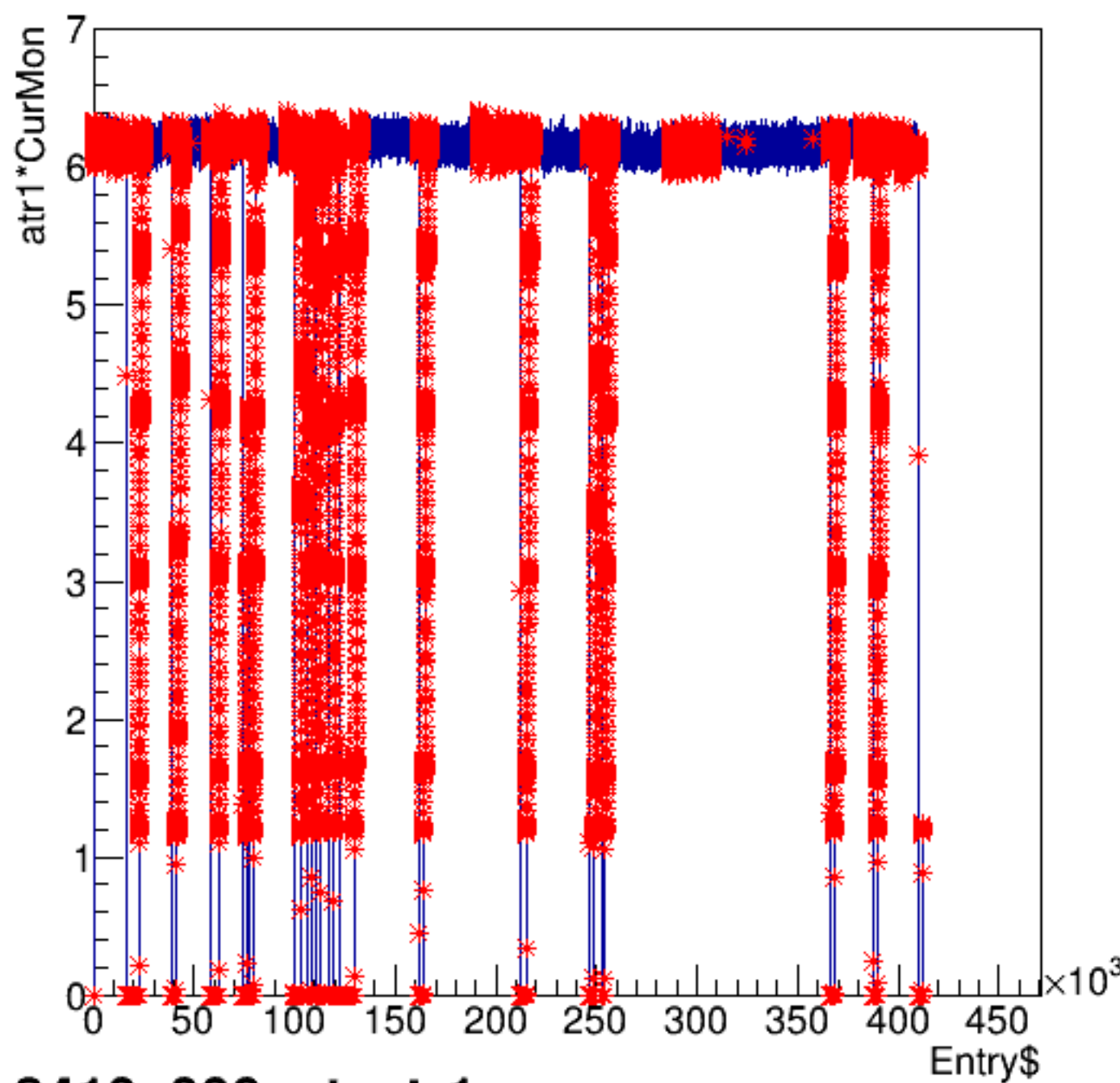
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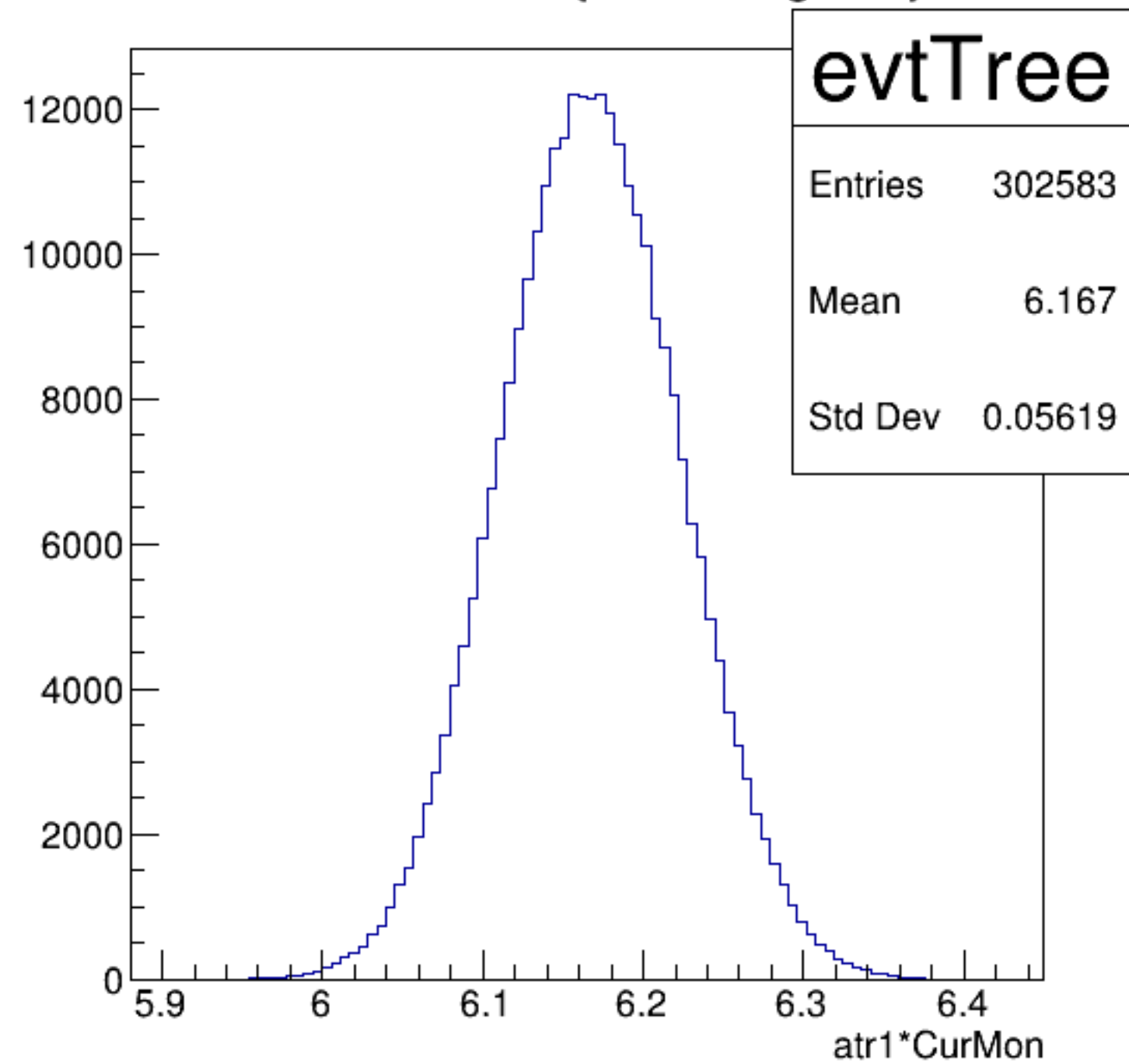
asym\_atl2



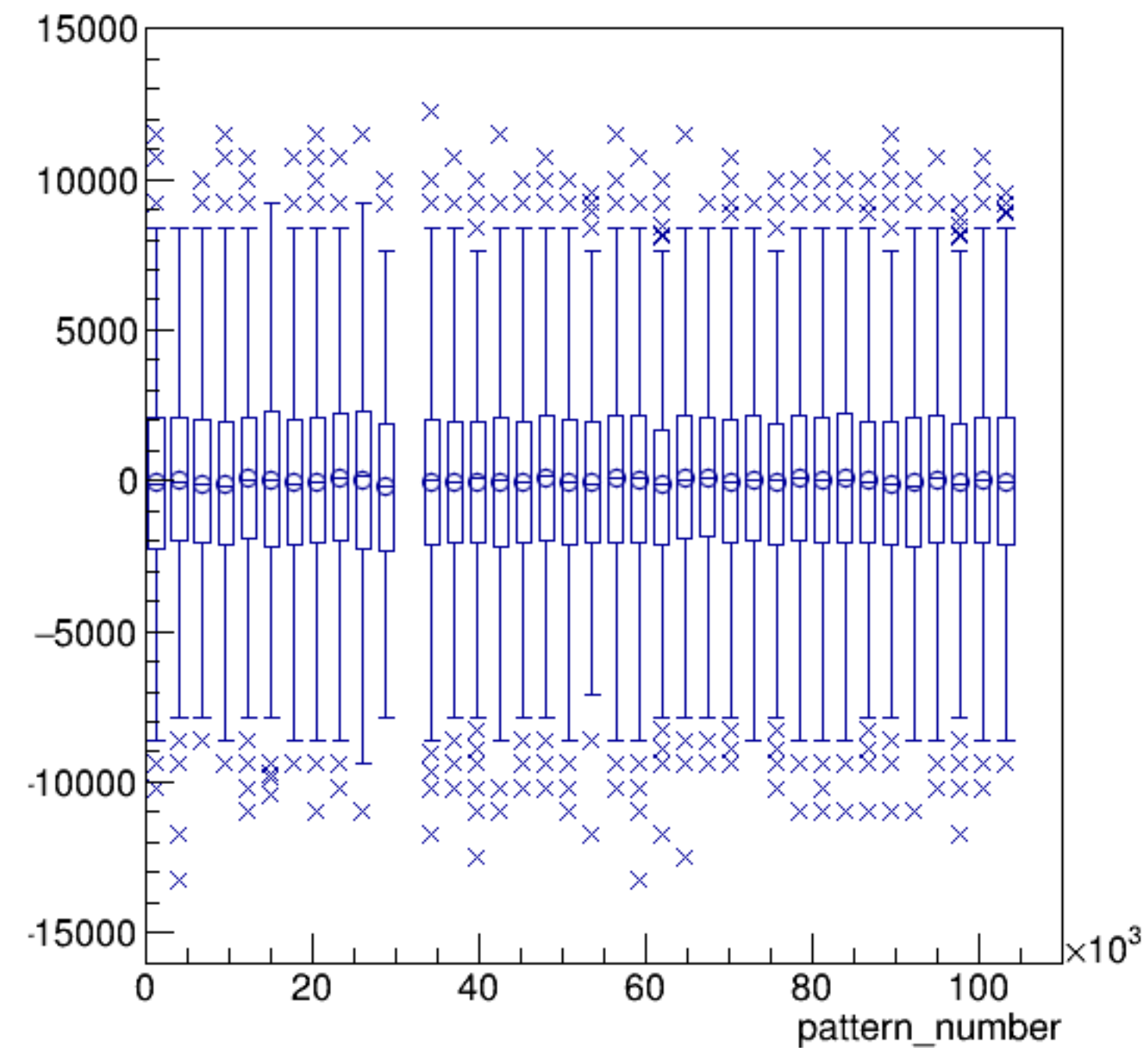
atr1\*CurMon:Entry\$



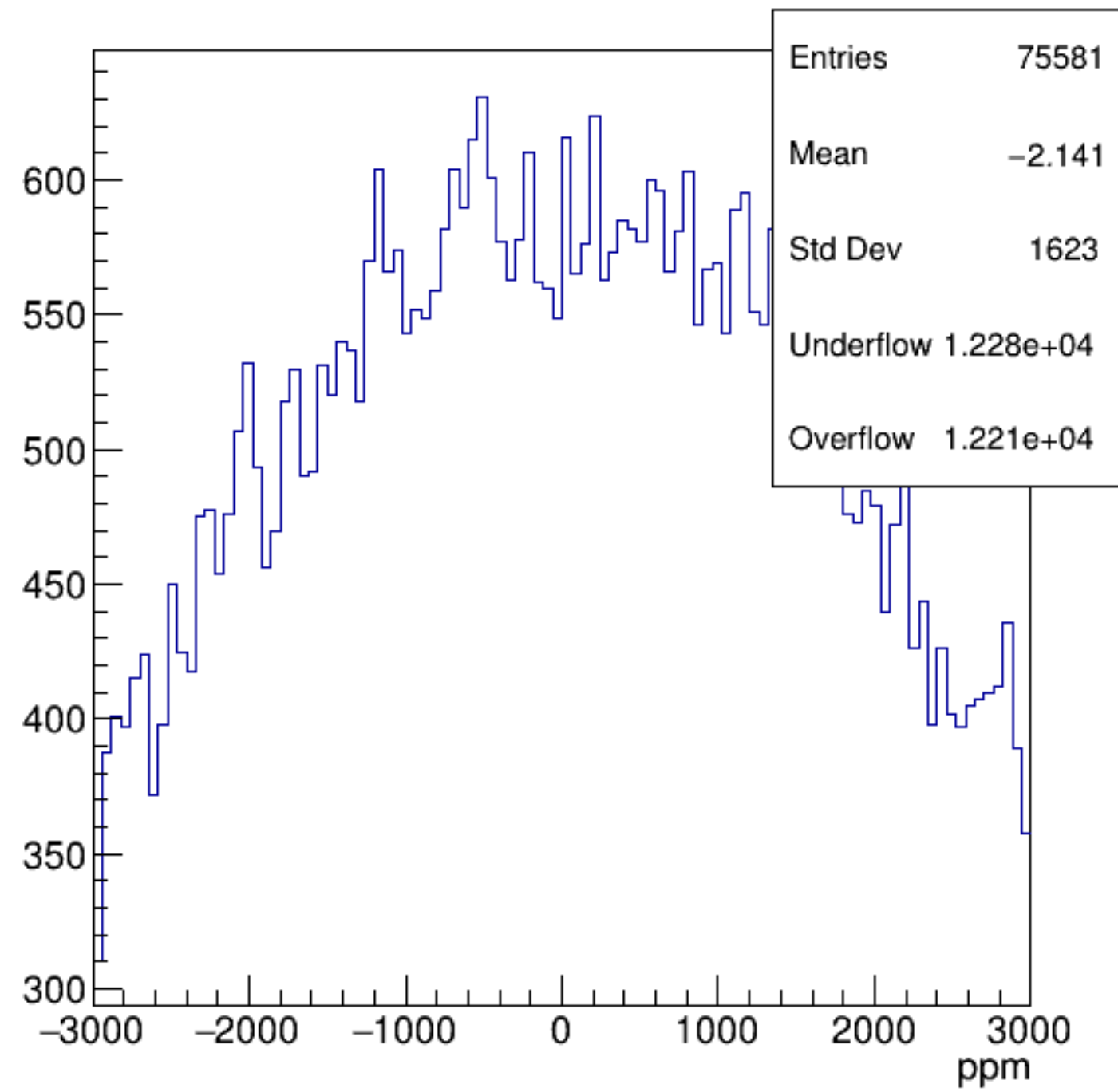
atr1\*CurMon {ErrorFlag==0}



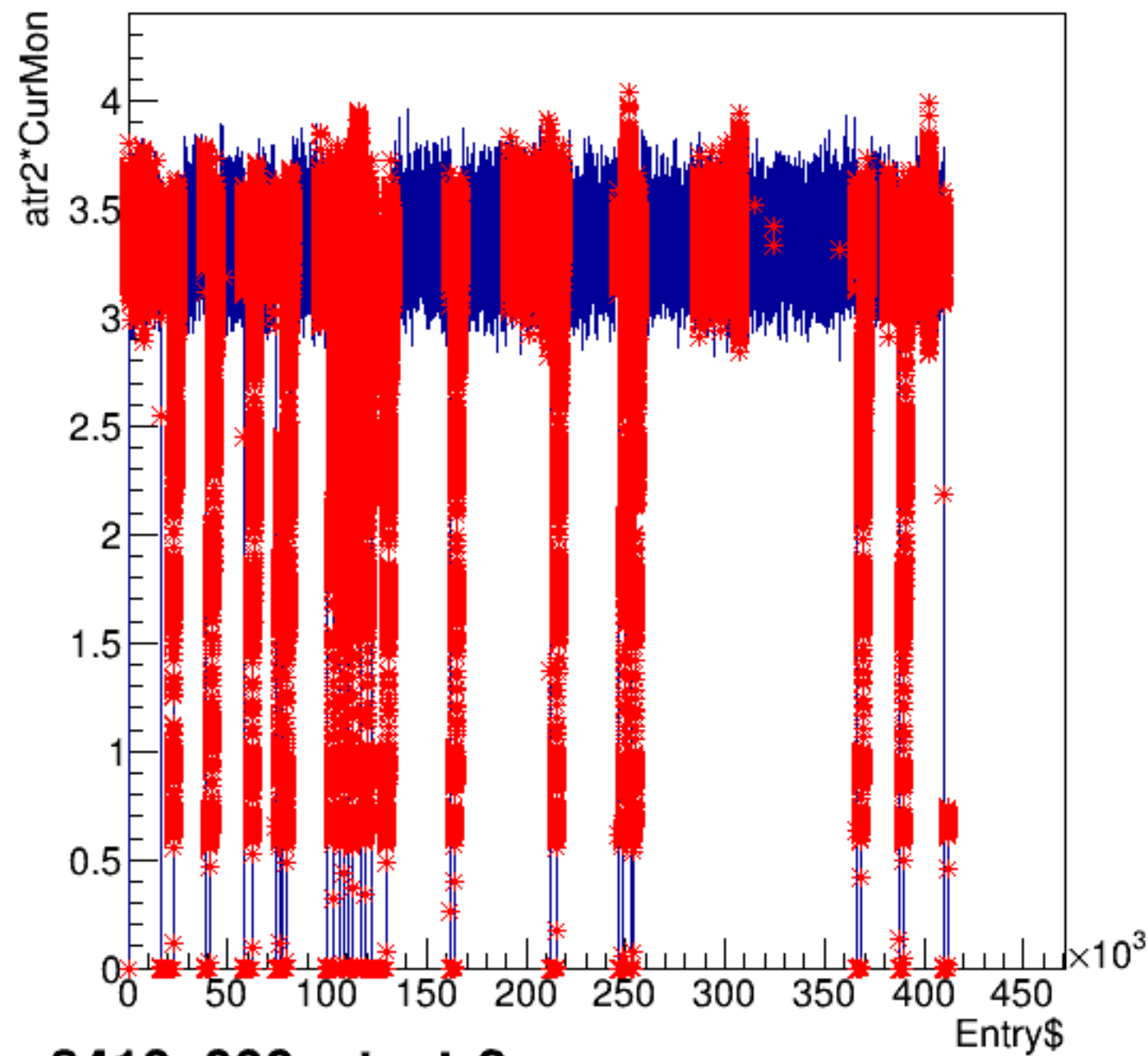
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asym\_atr1

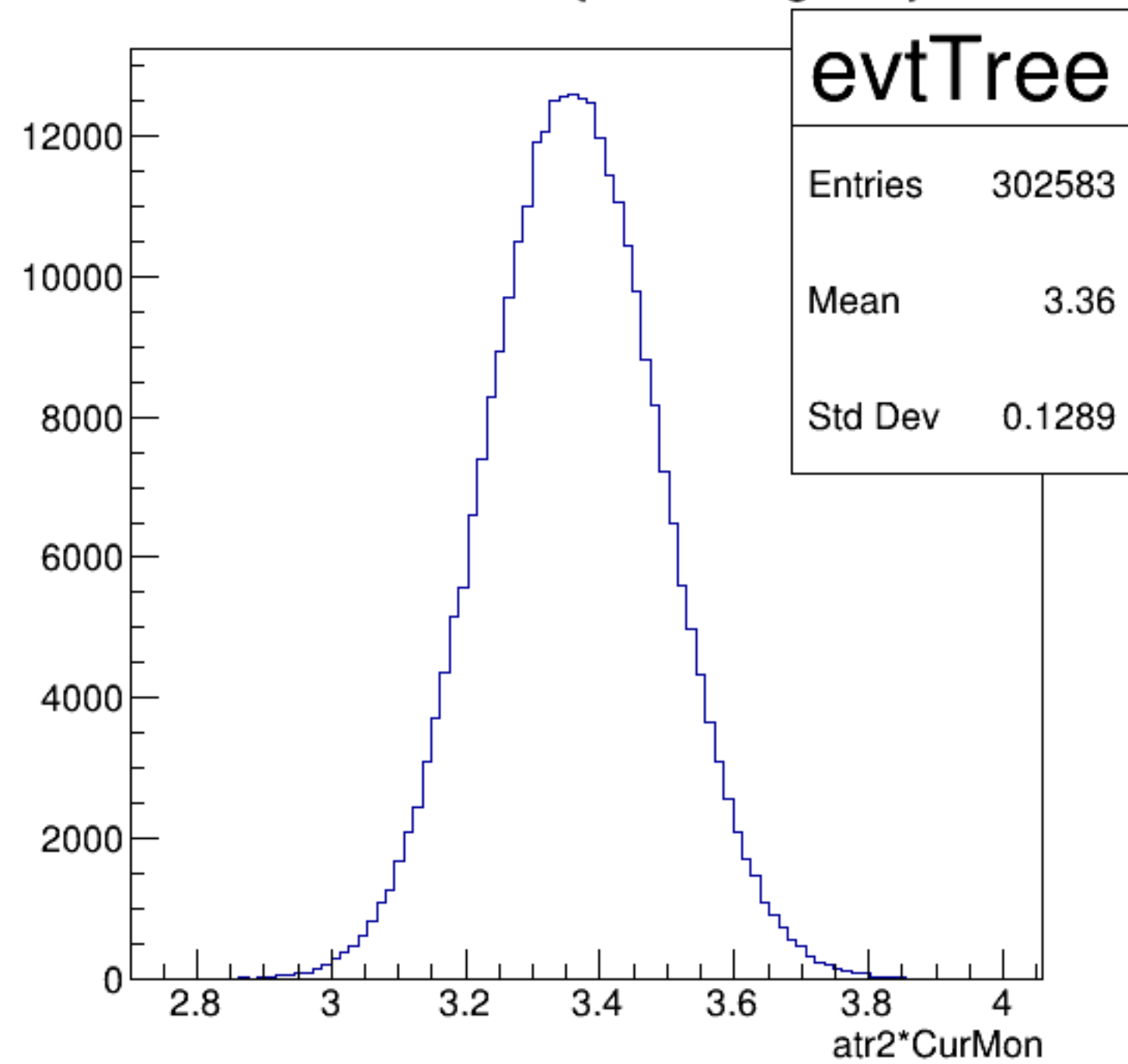


atr2\*CurMon:Entry\$

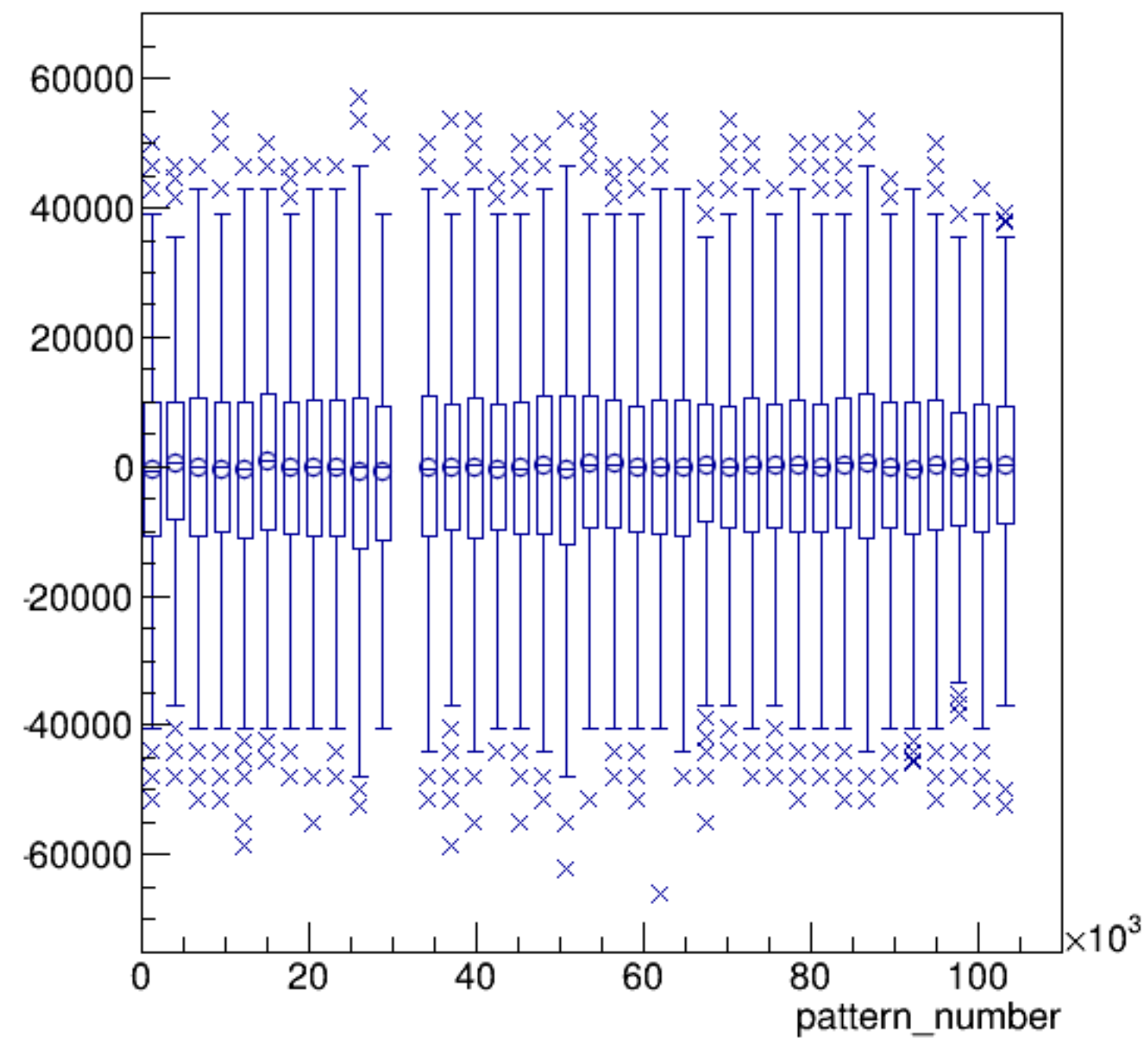


run8410\_000 at atr2.png

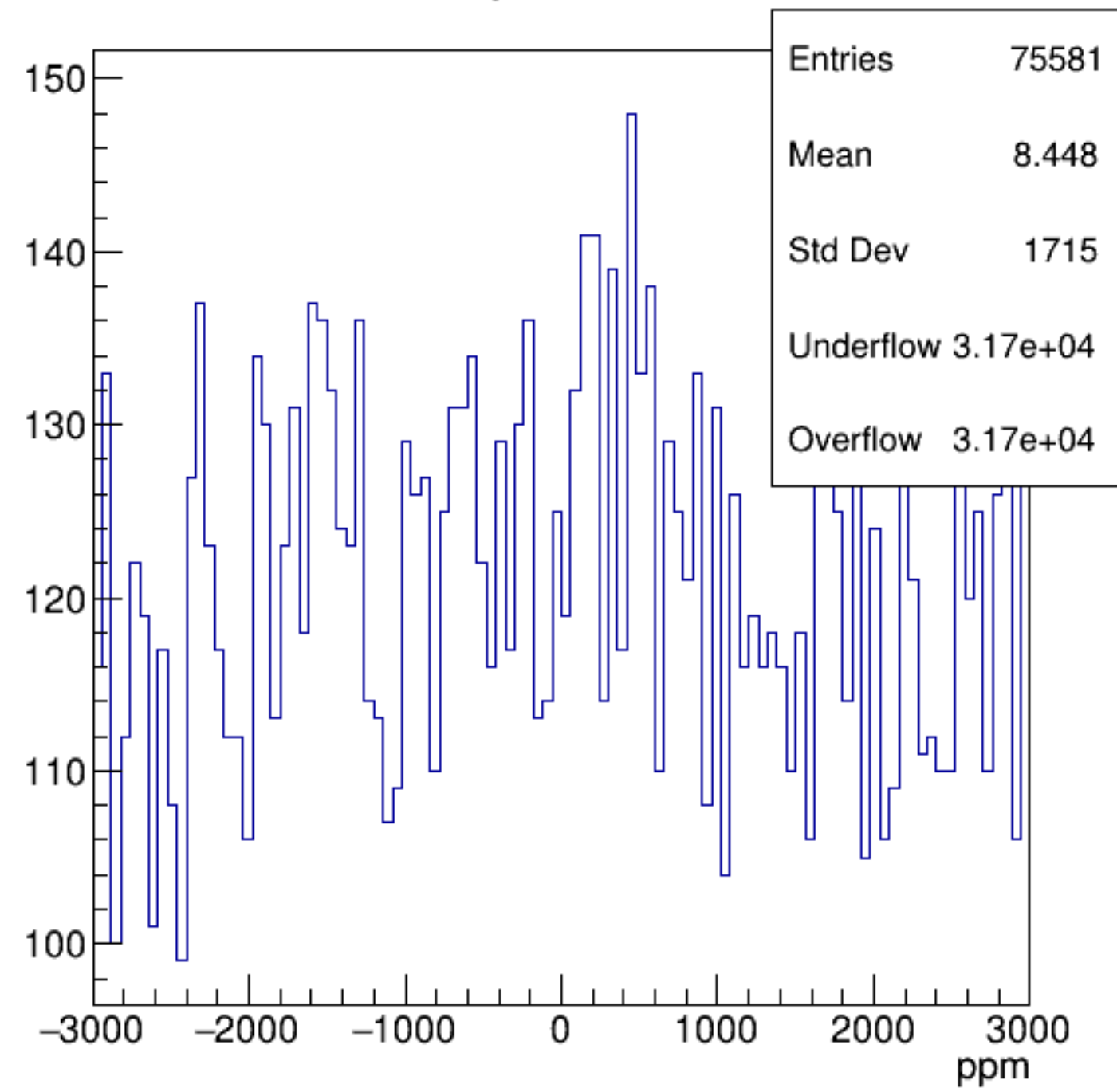
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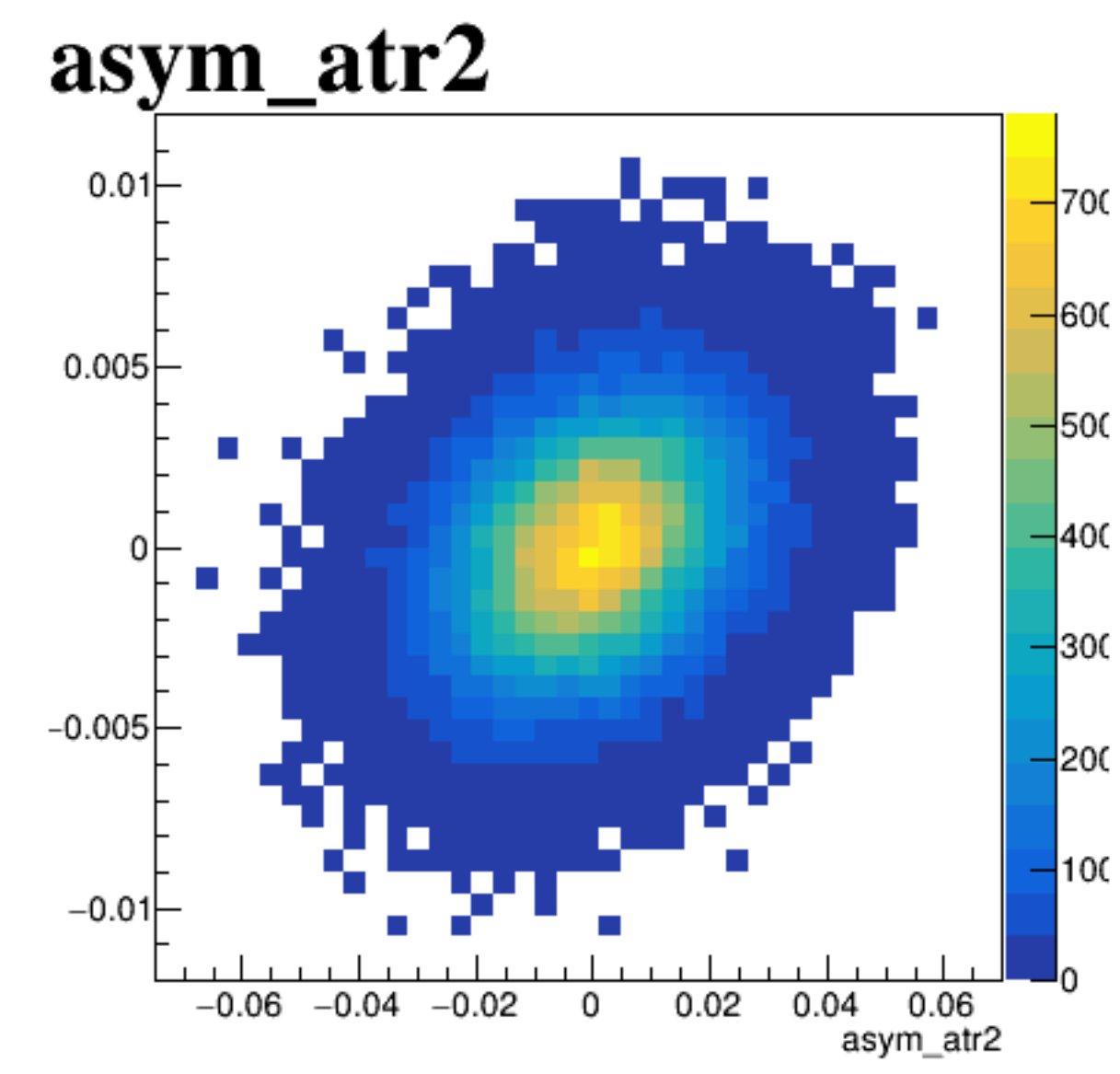
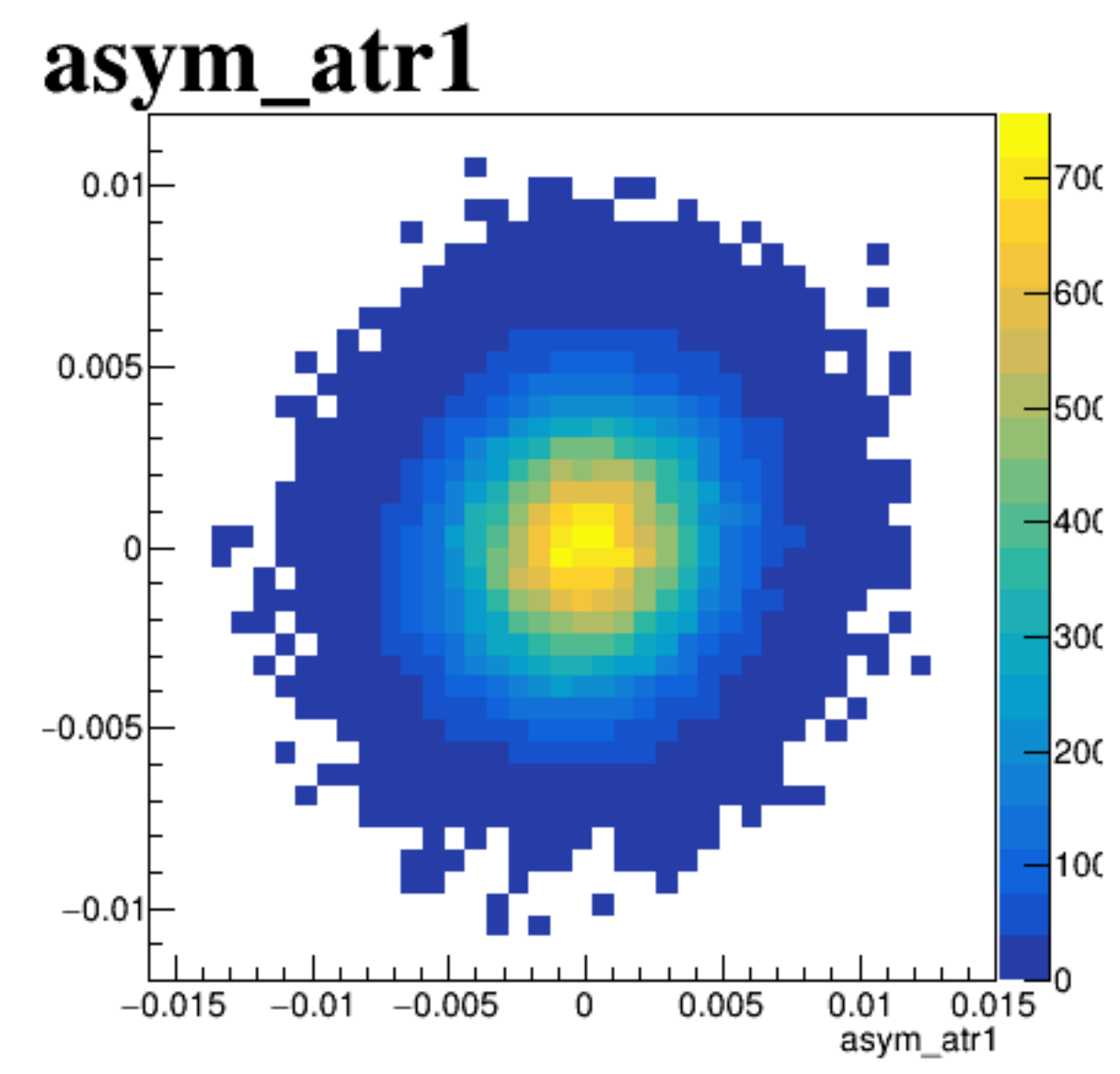
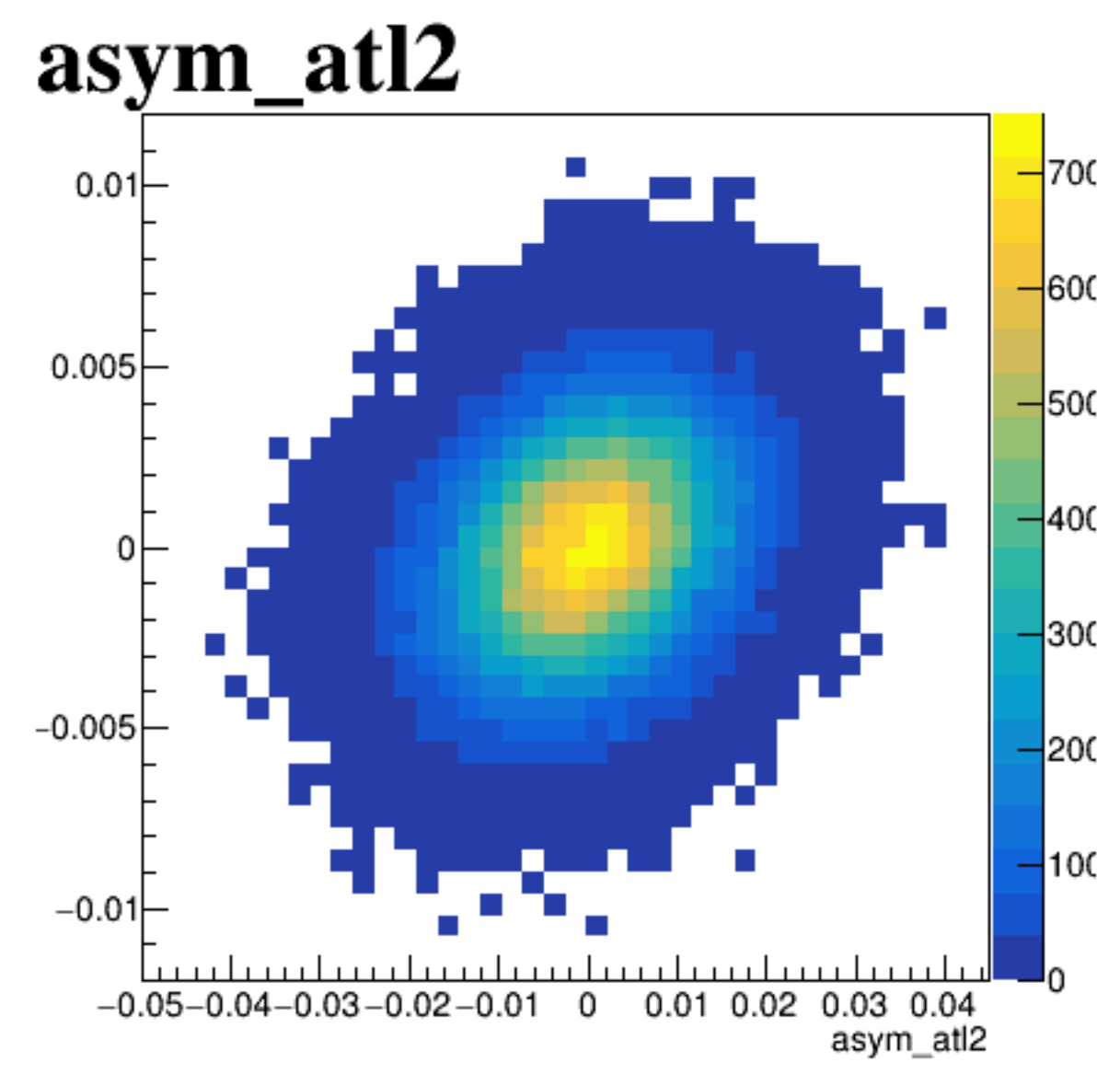
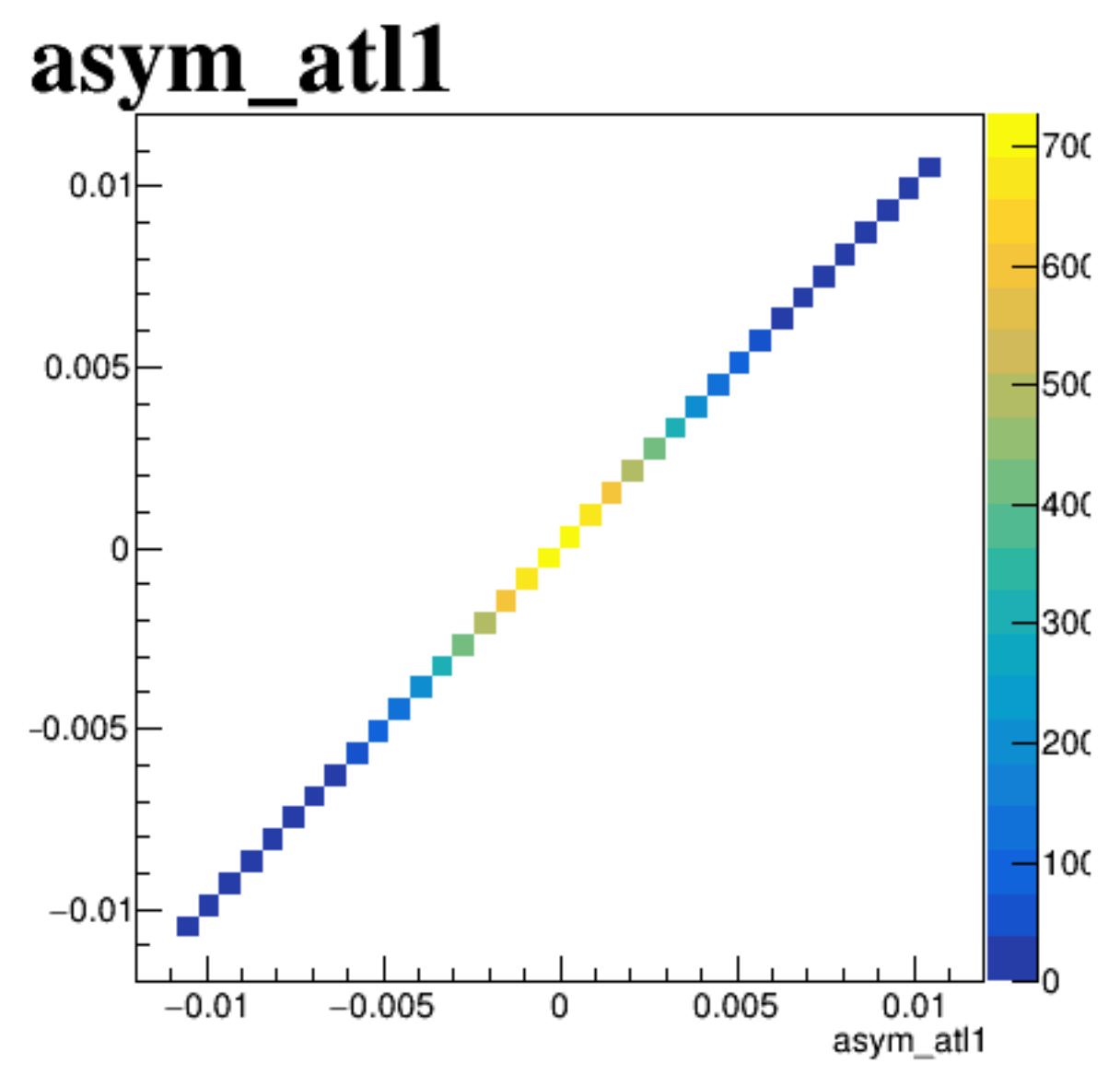
asym\_atr2/ppm:pattern\_number {ErrorFlag==0}



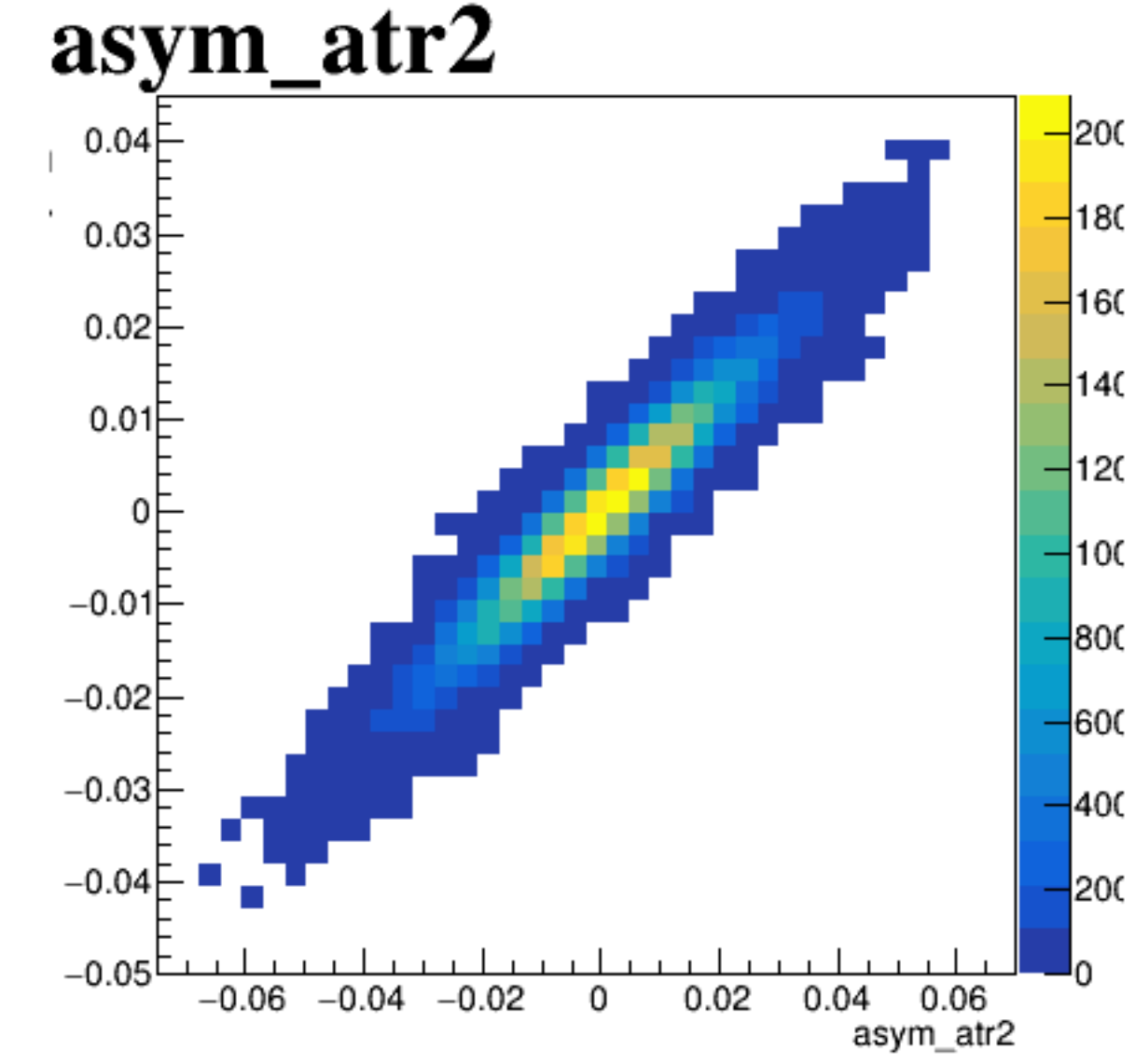
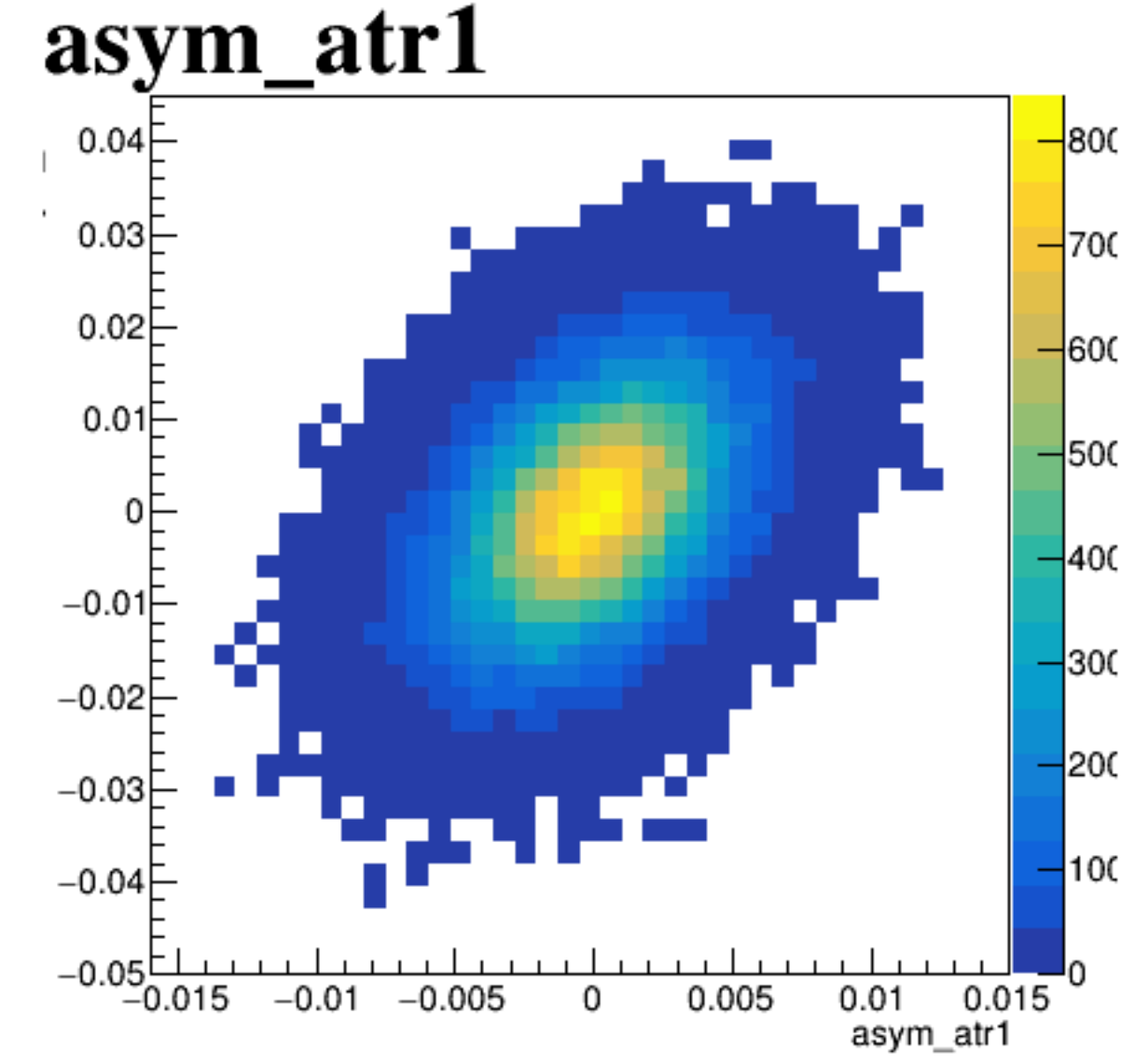
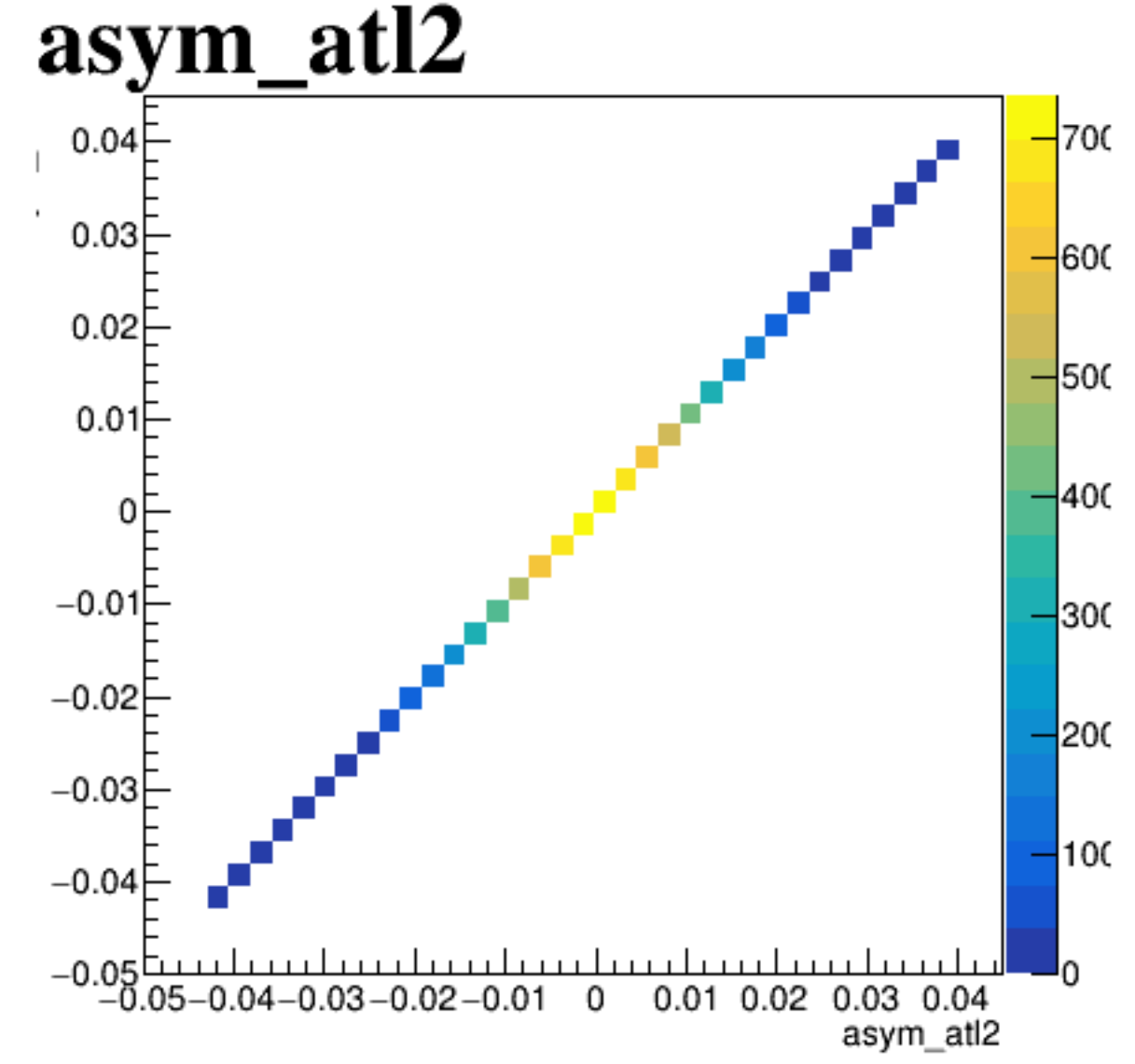
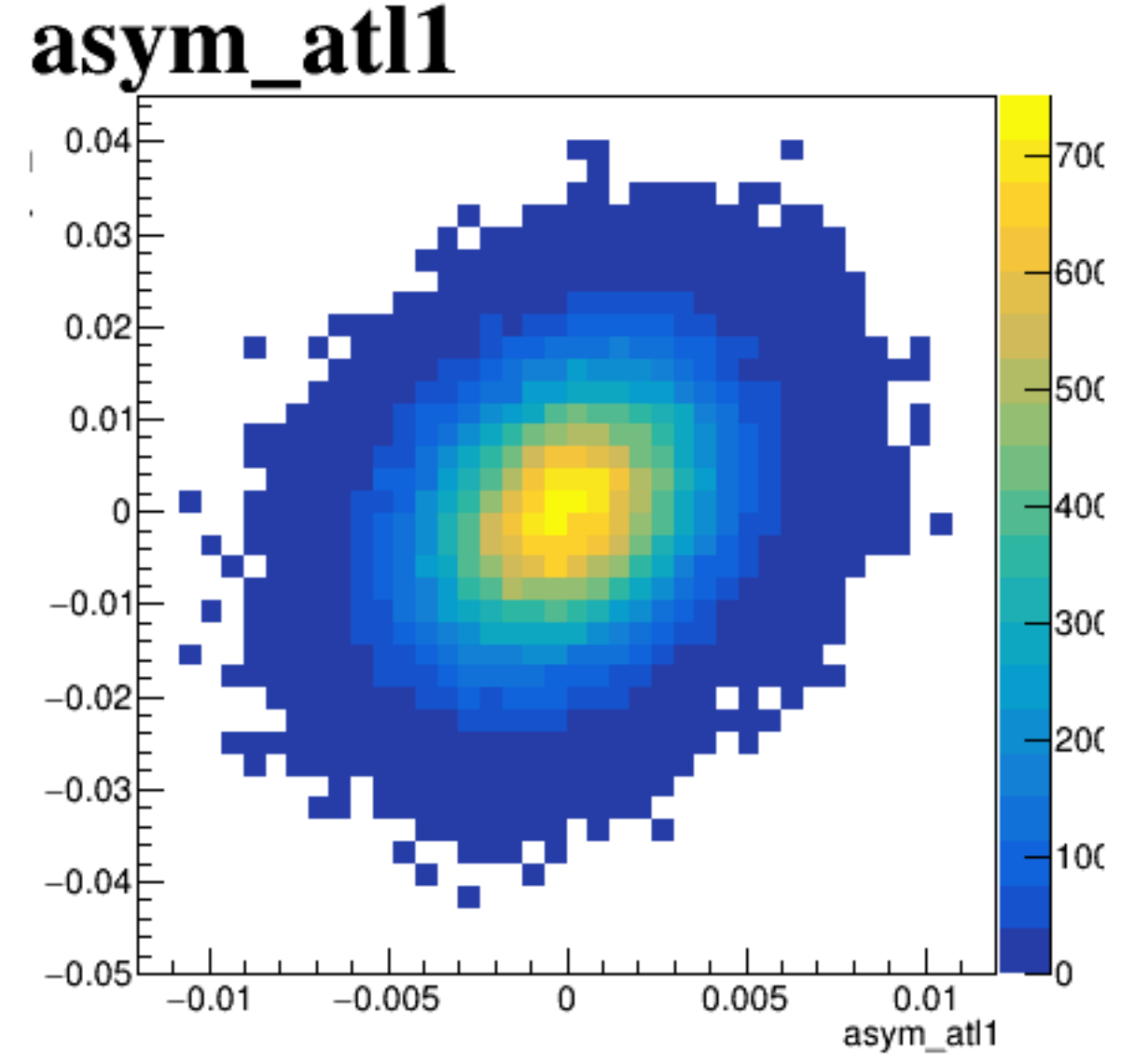
asym\_atr2



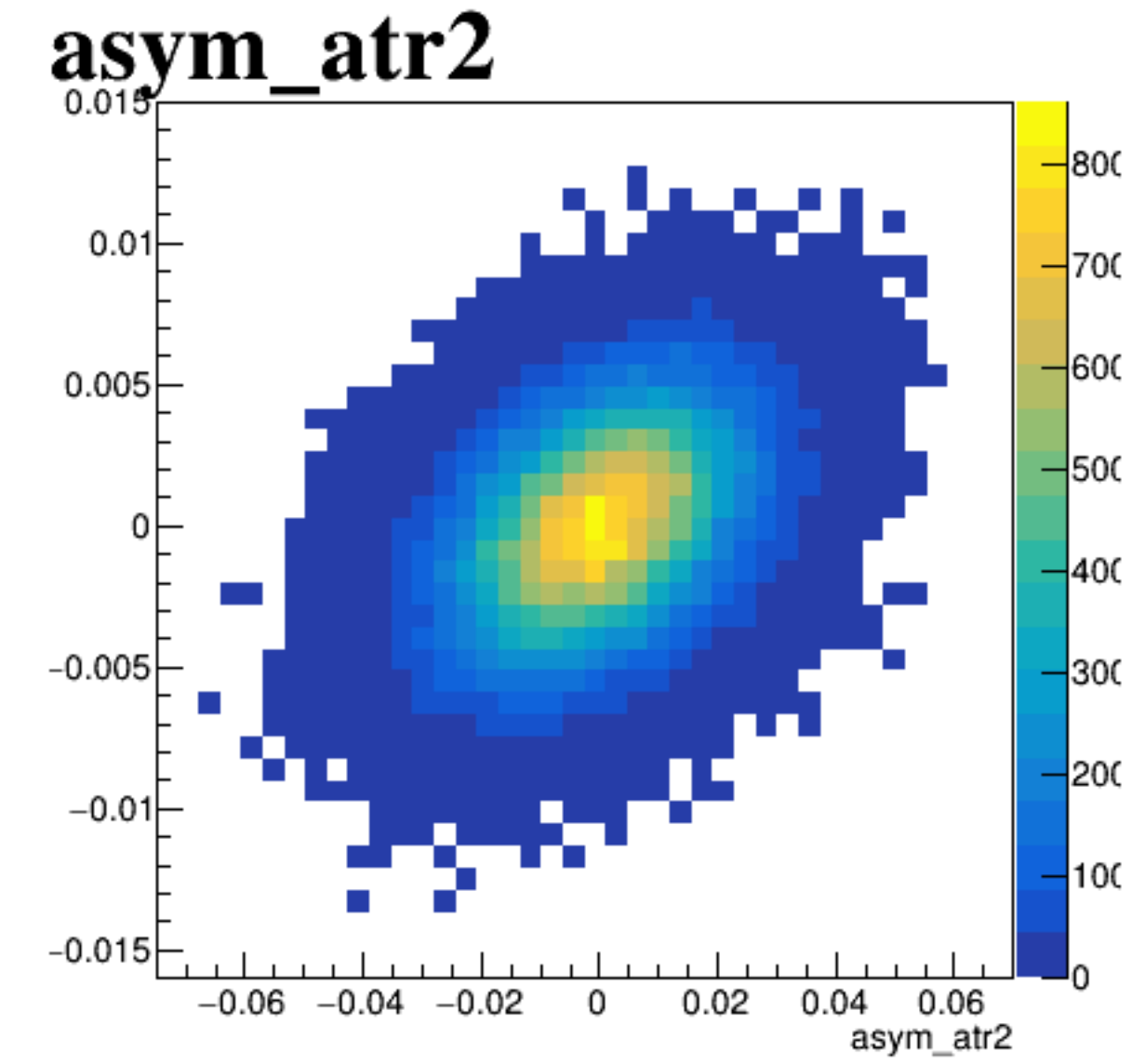
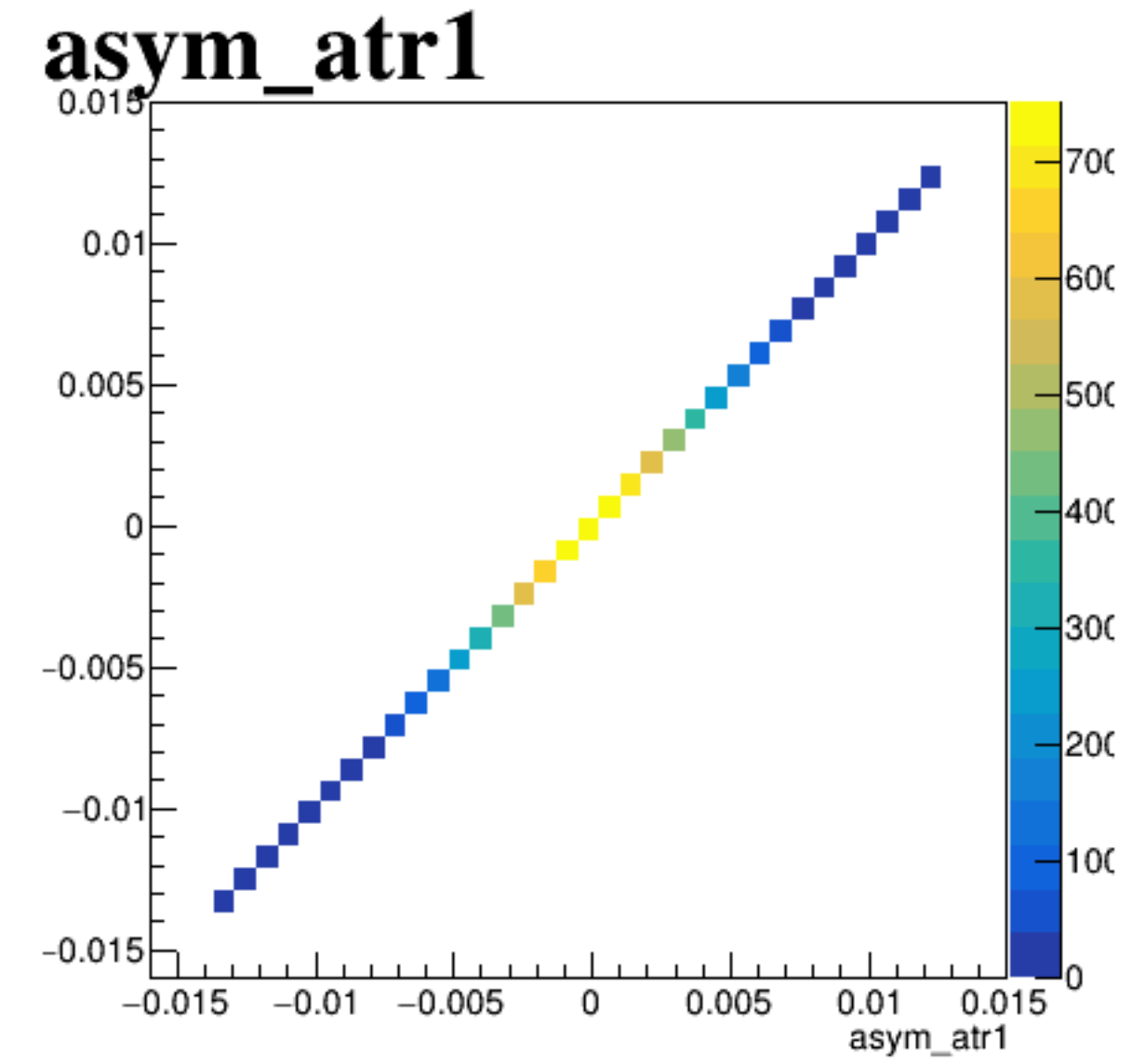
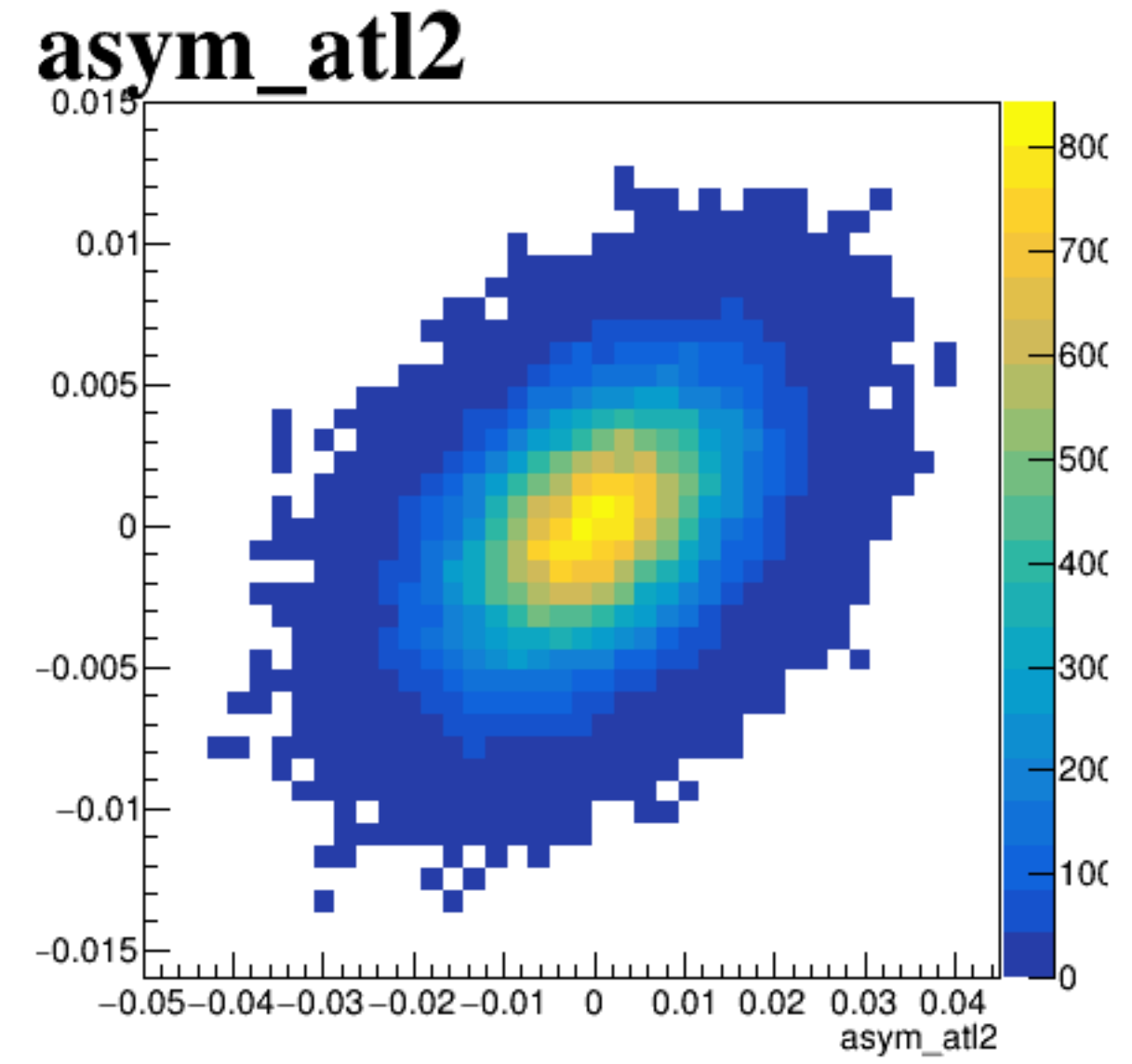
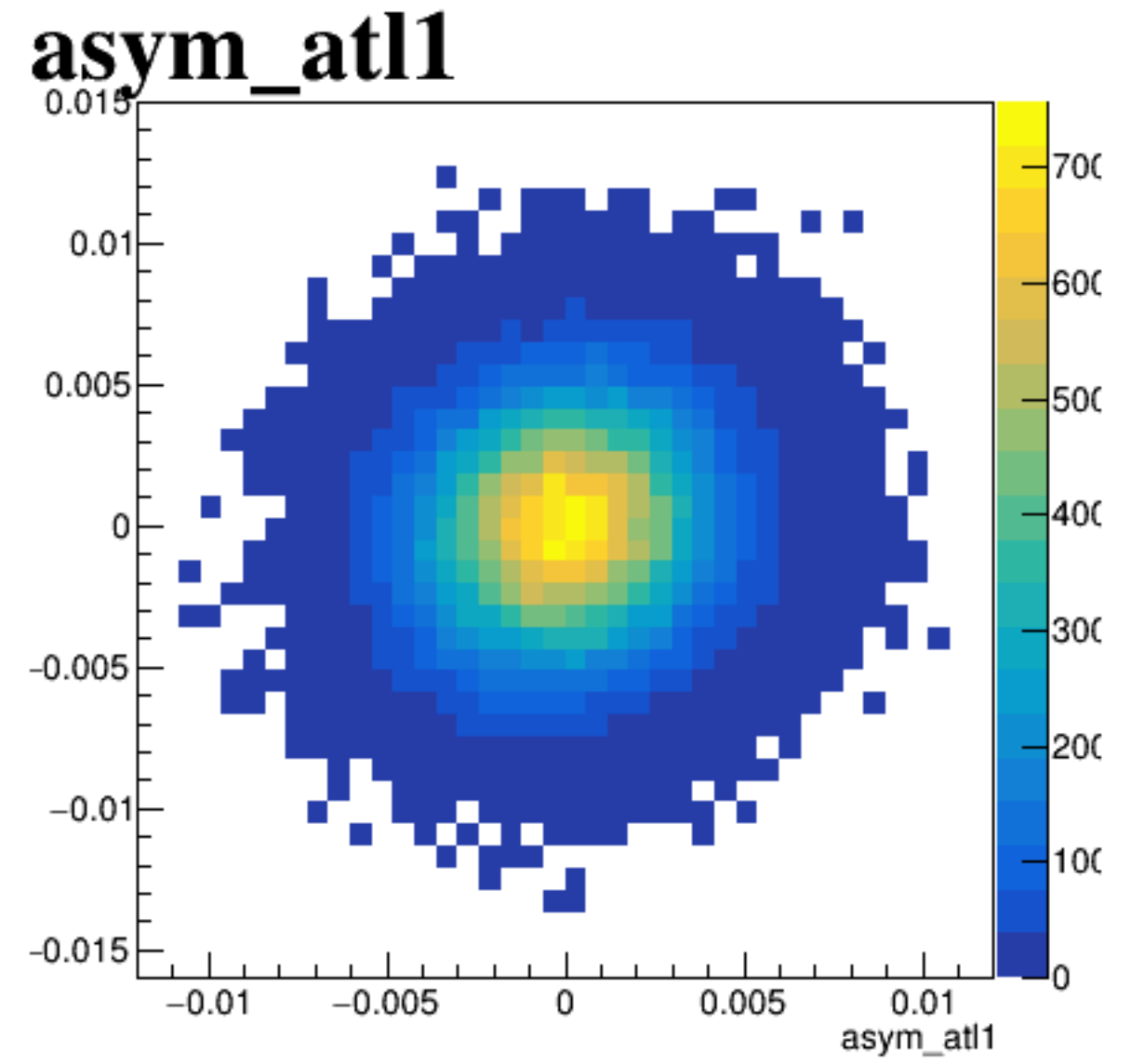
**asym\_atl1**



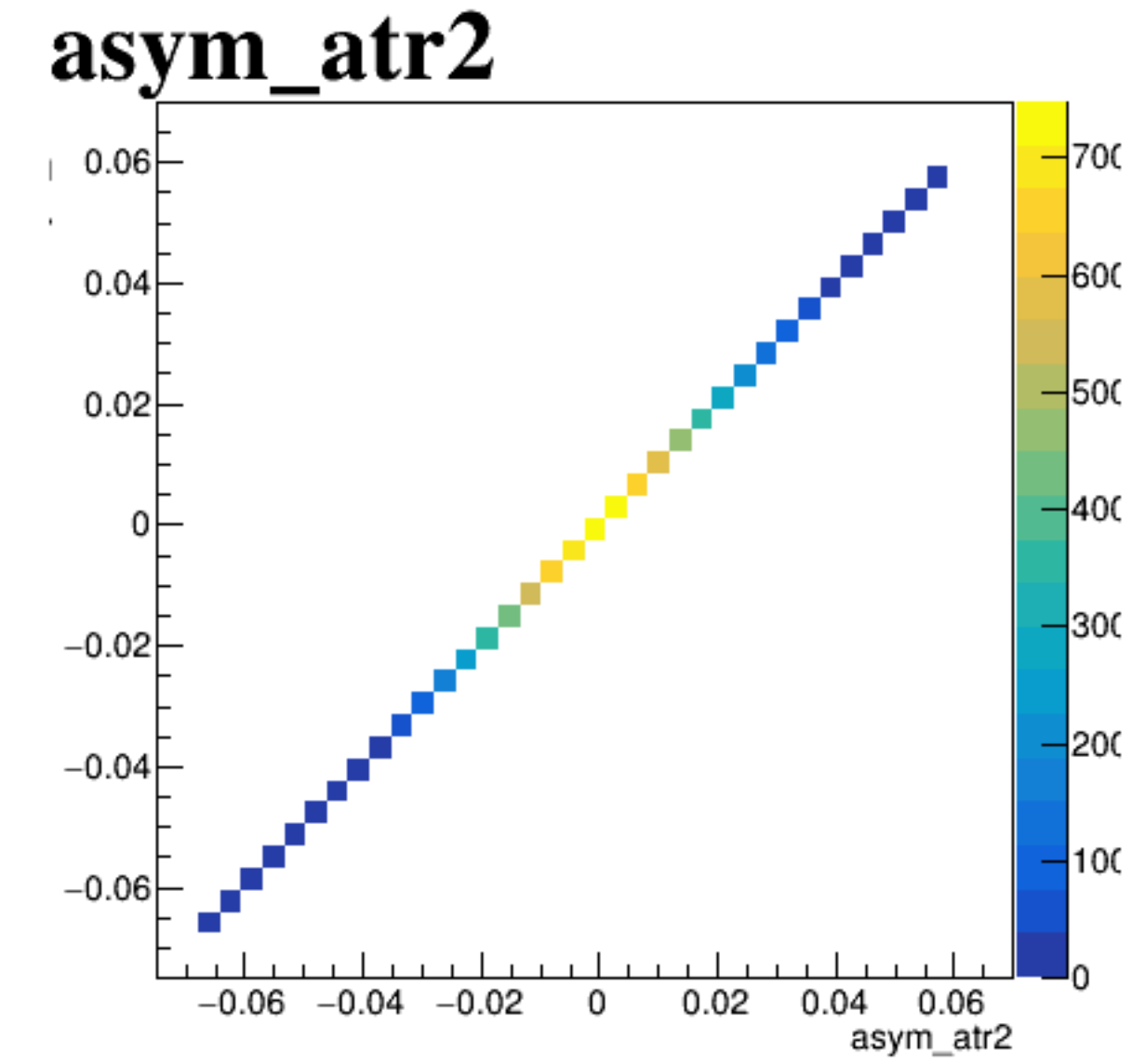
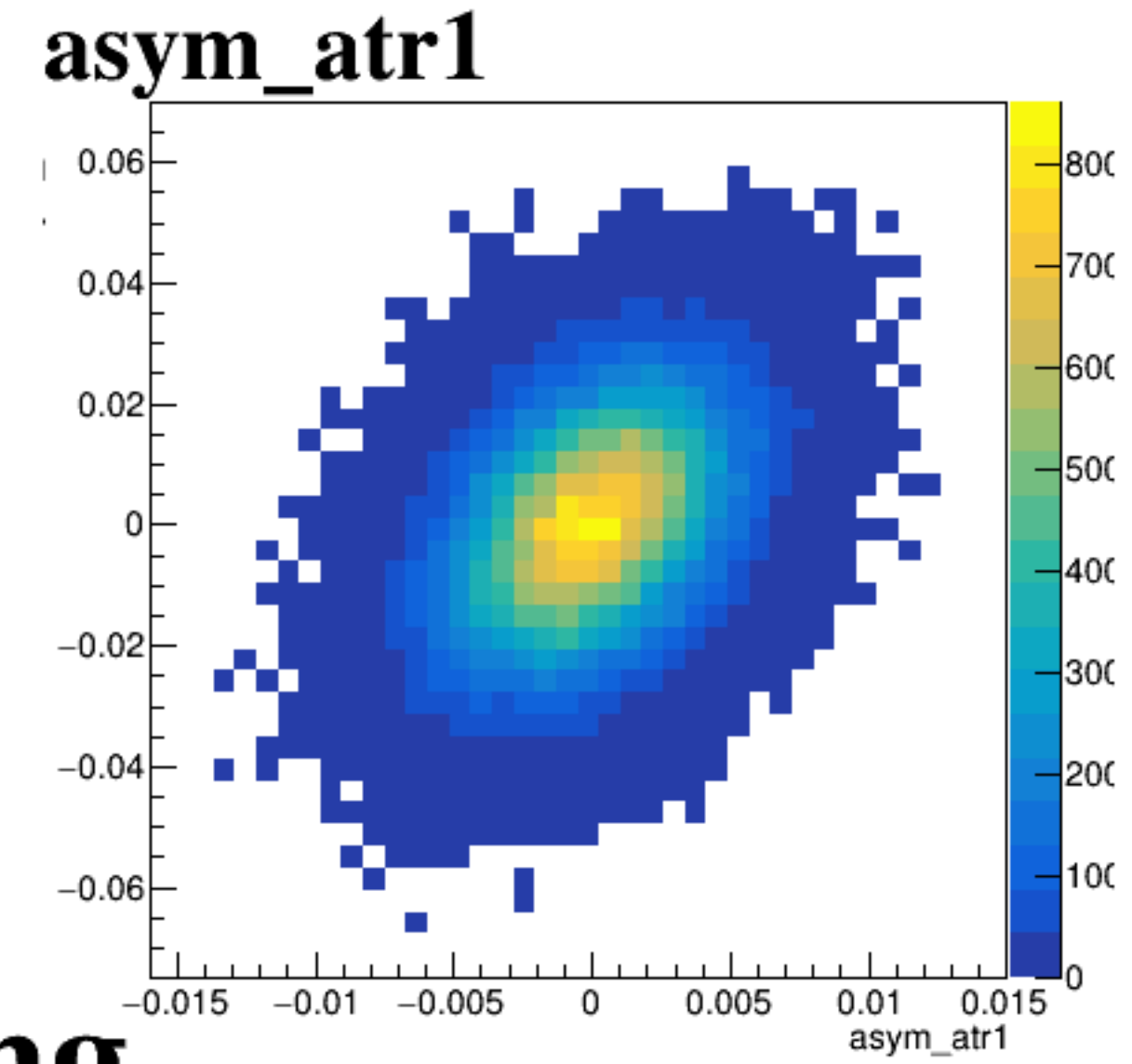
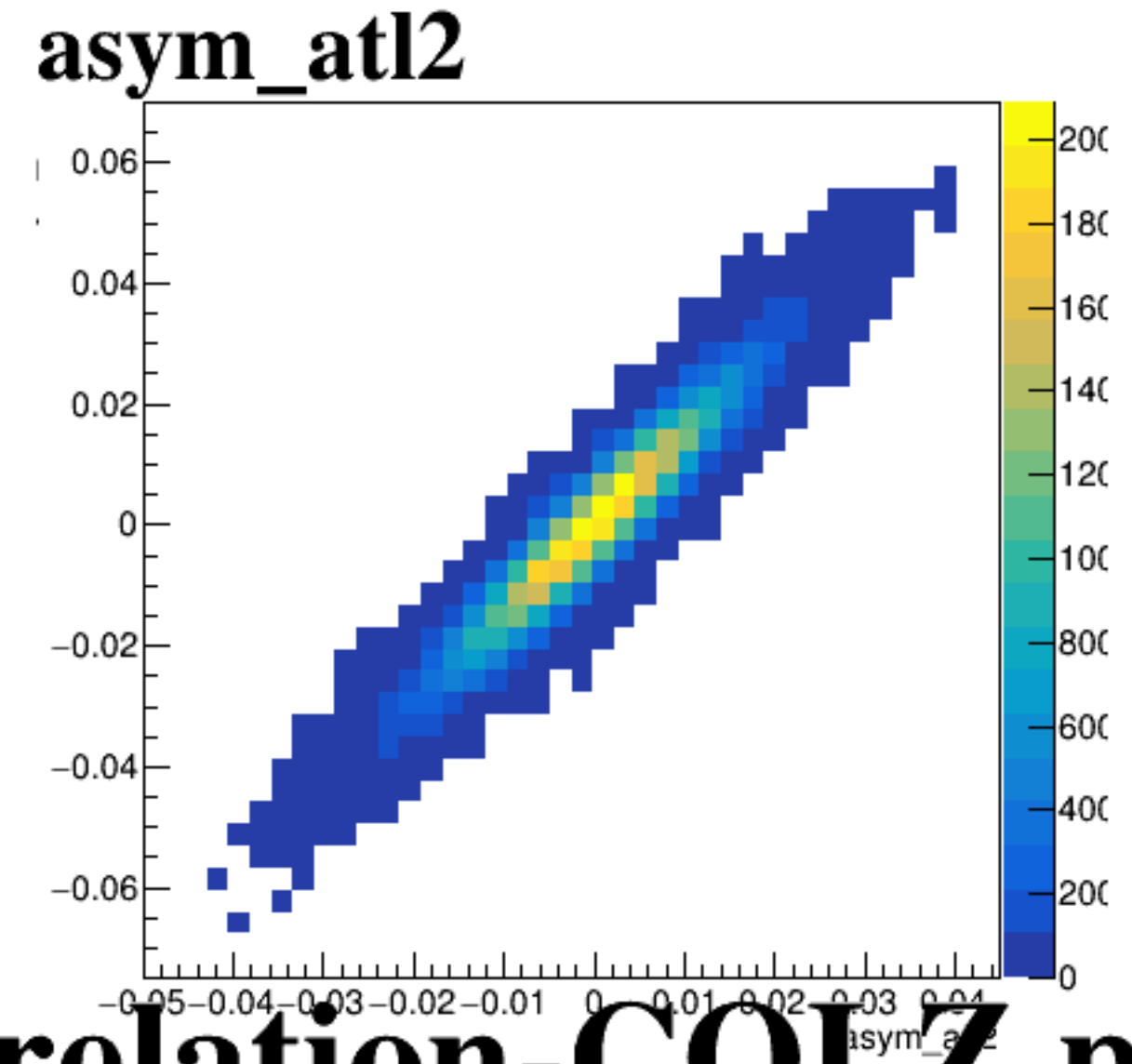
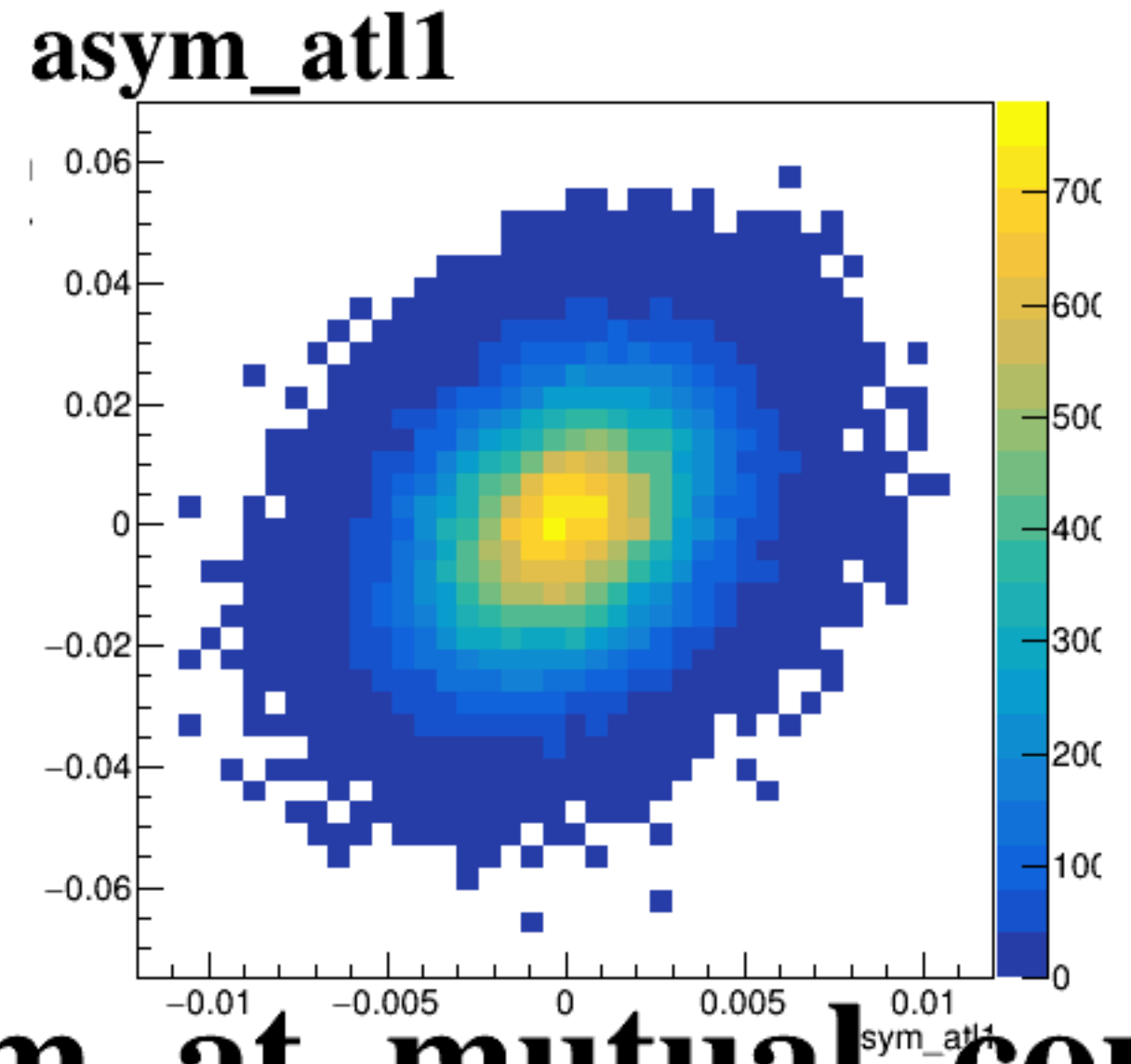
**asym\_atl2**



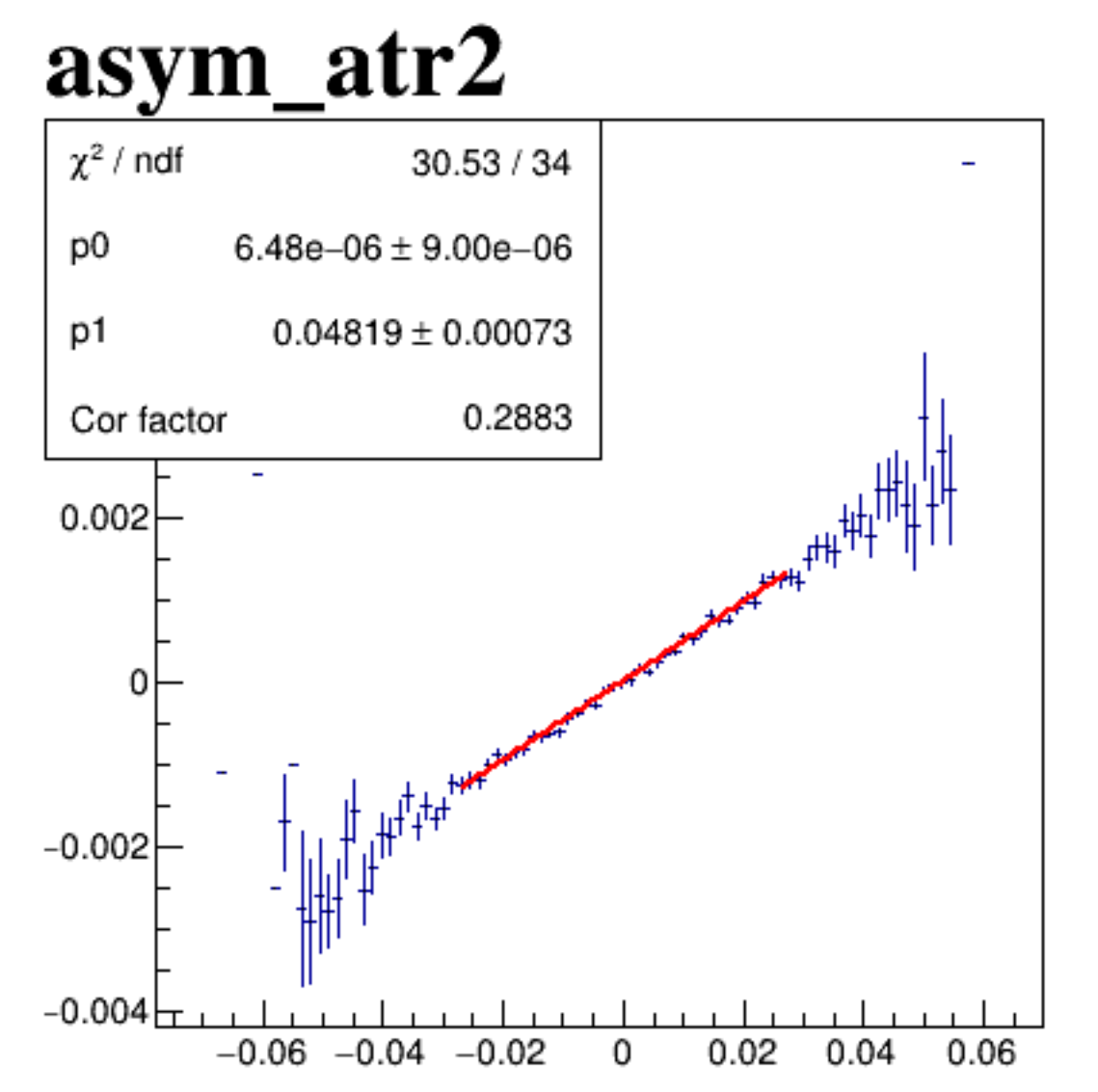
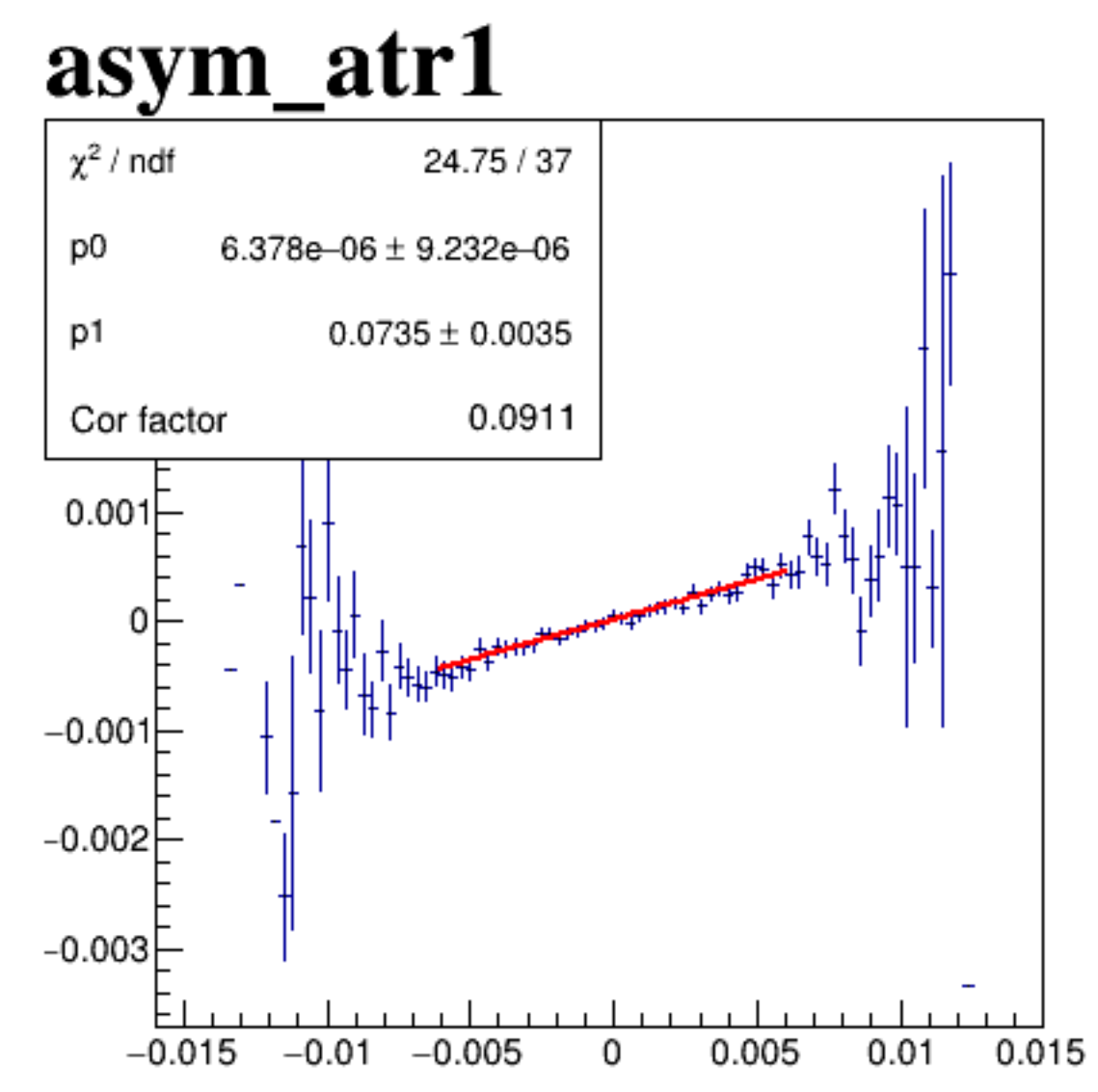
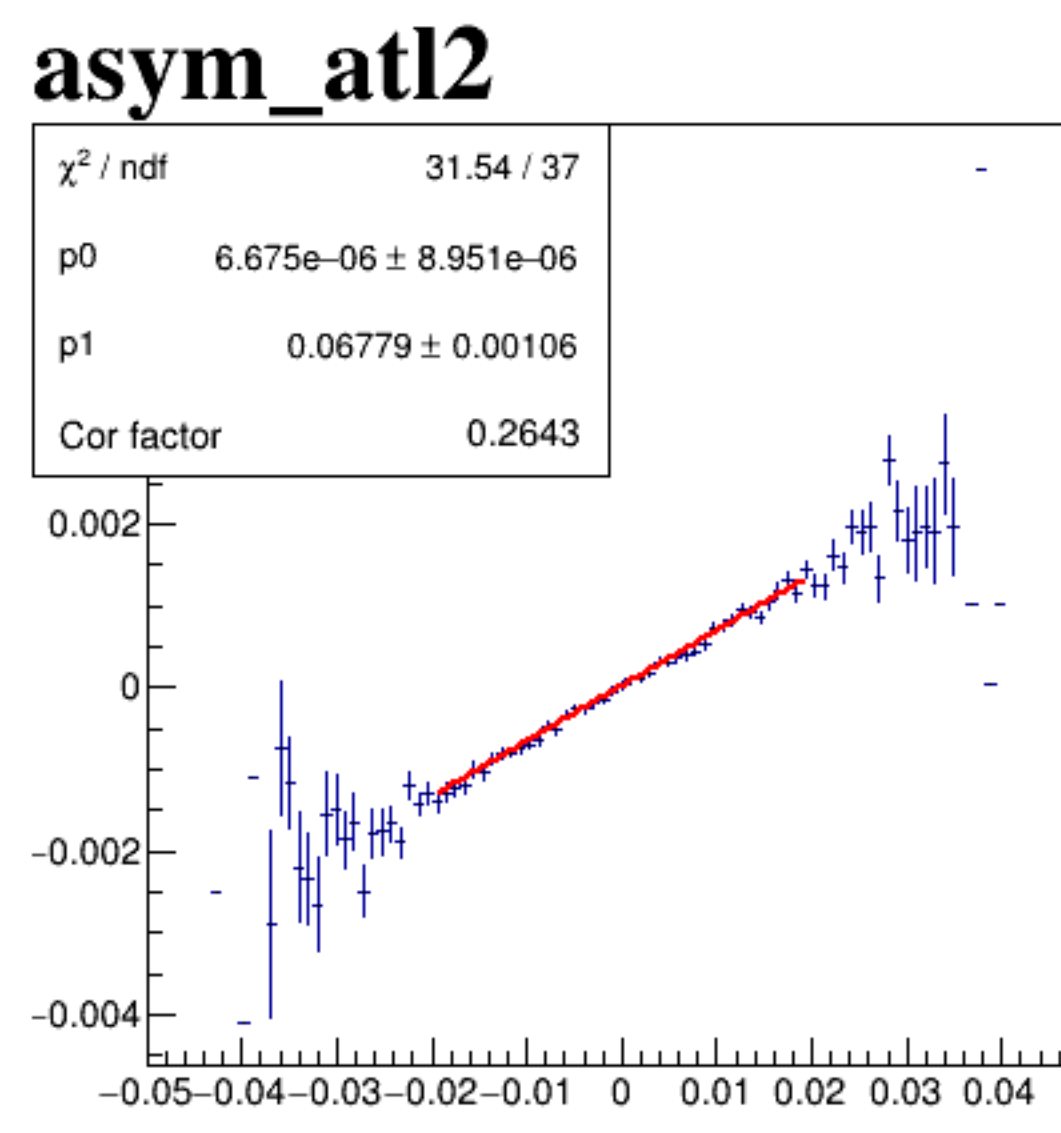
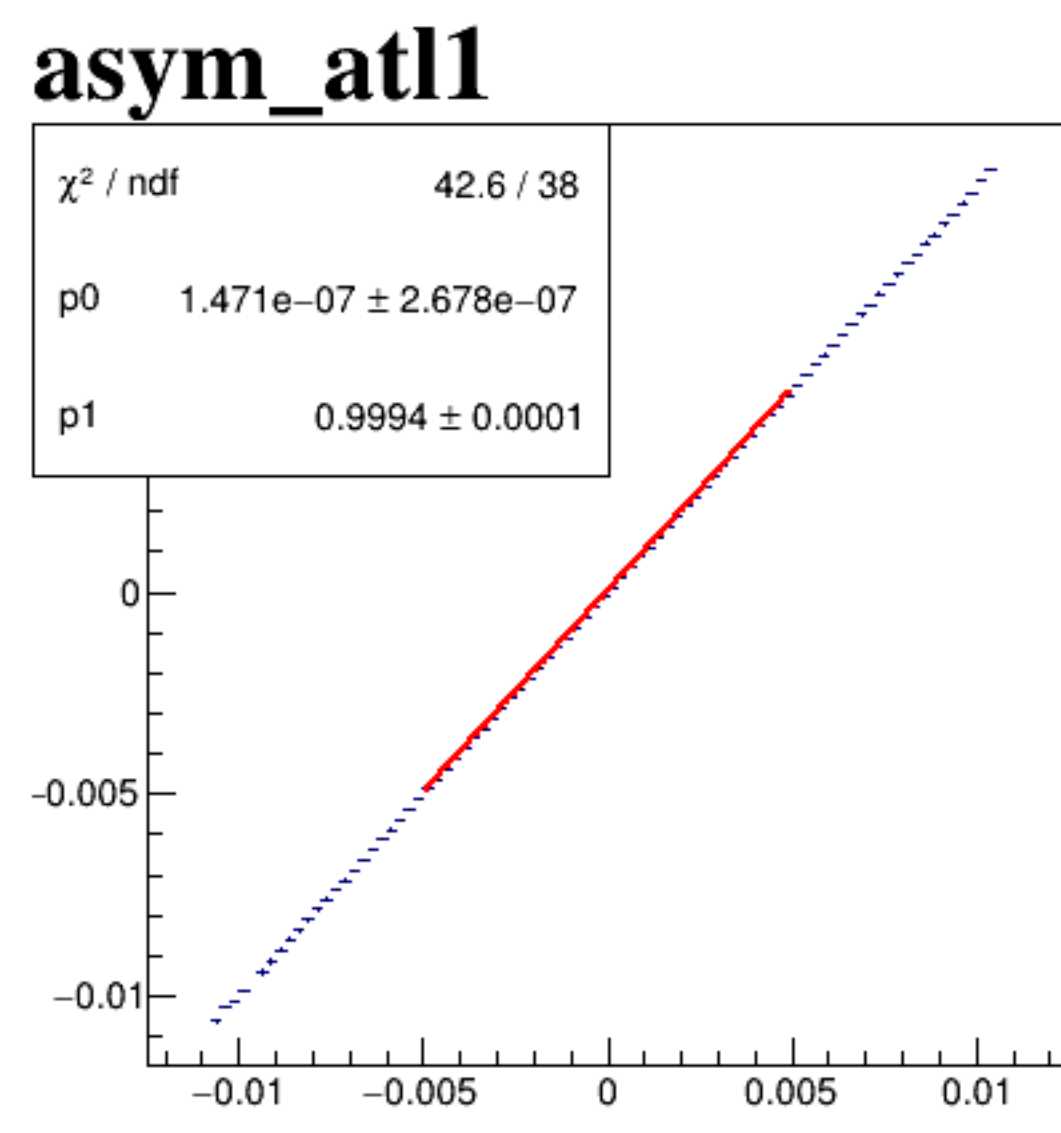
**asym\_atr1**



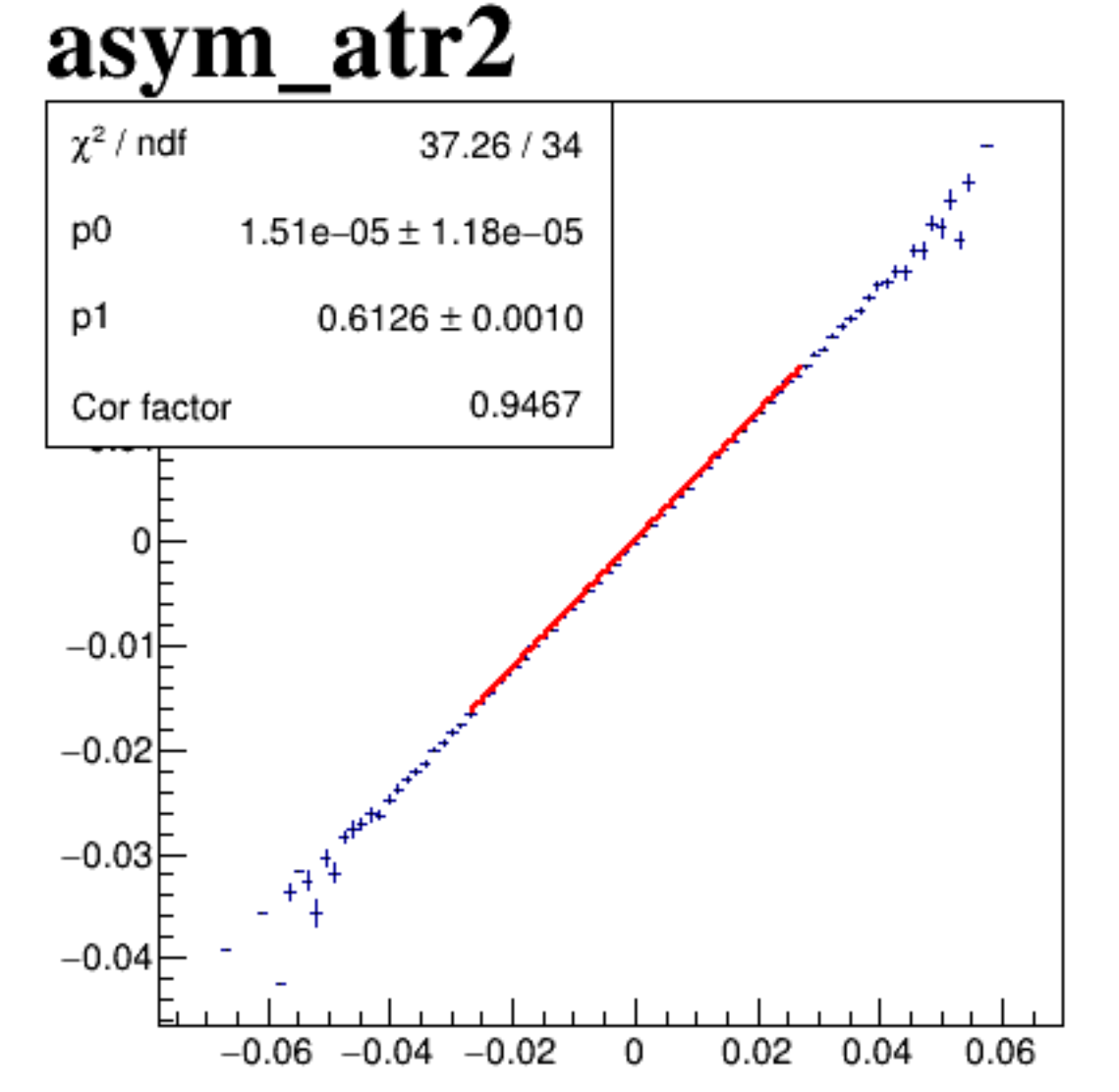
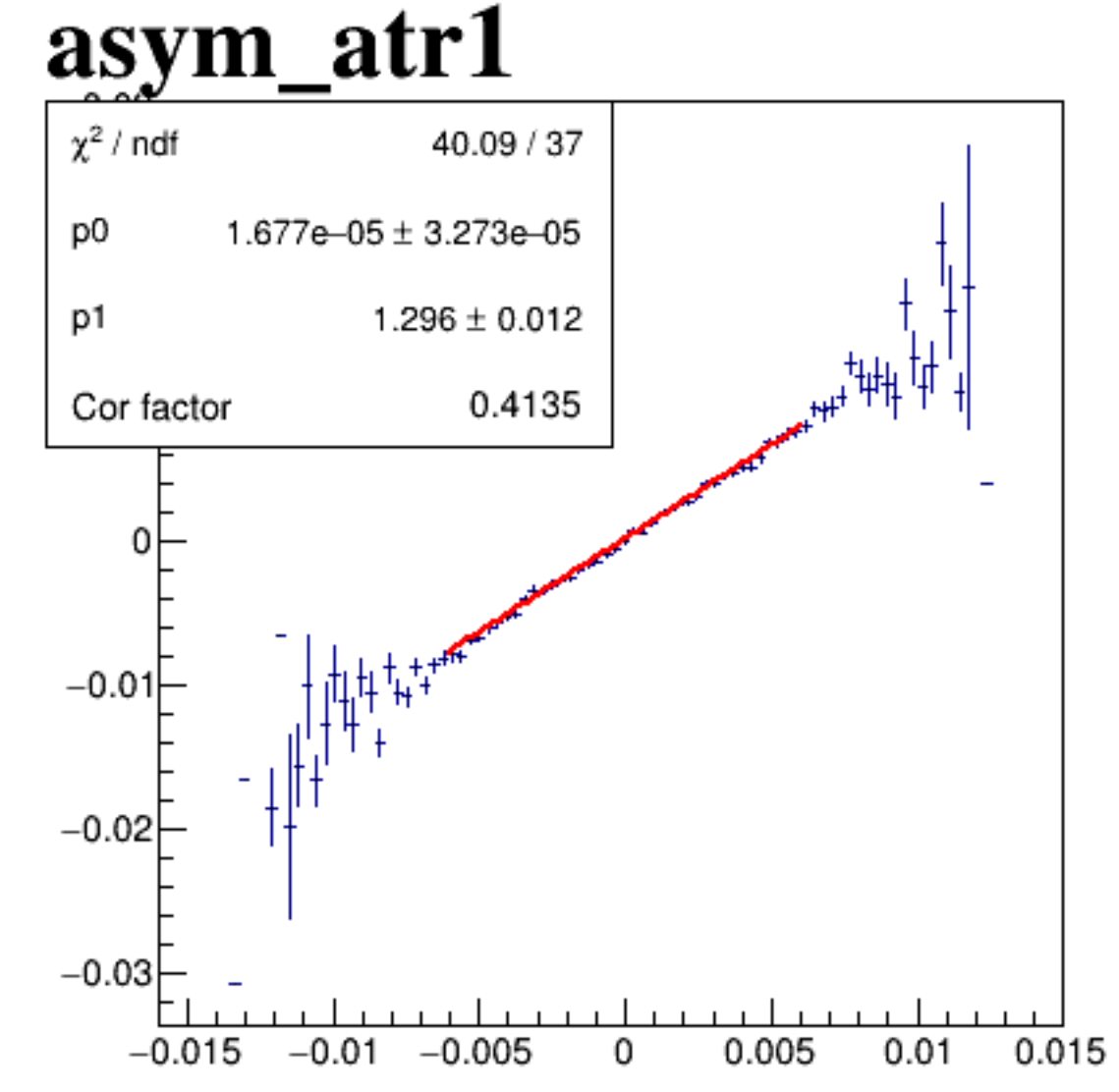
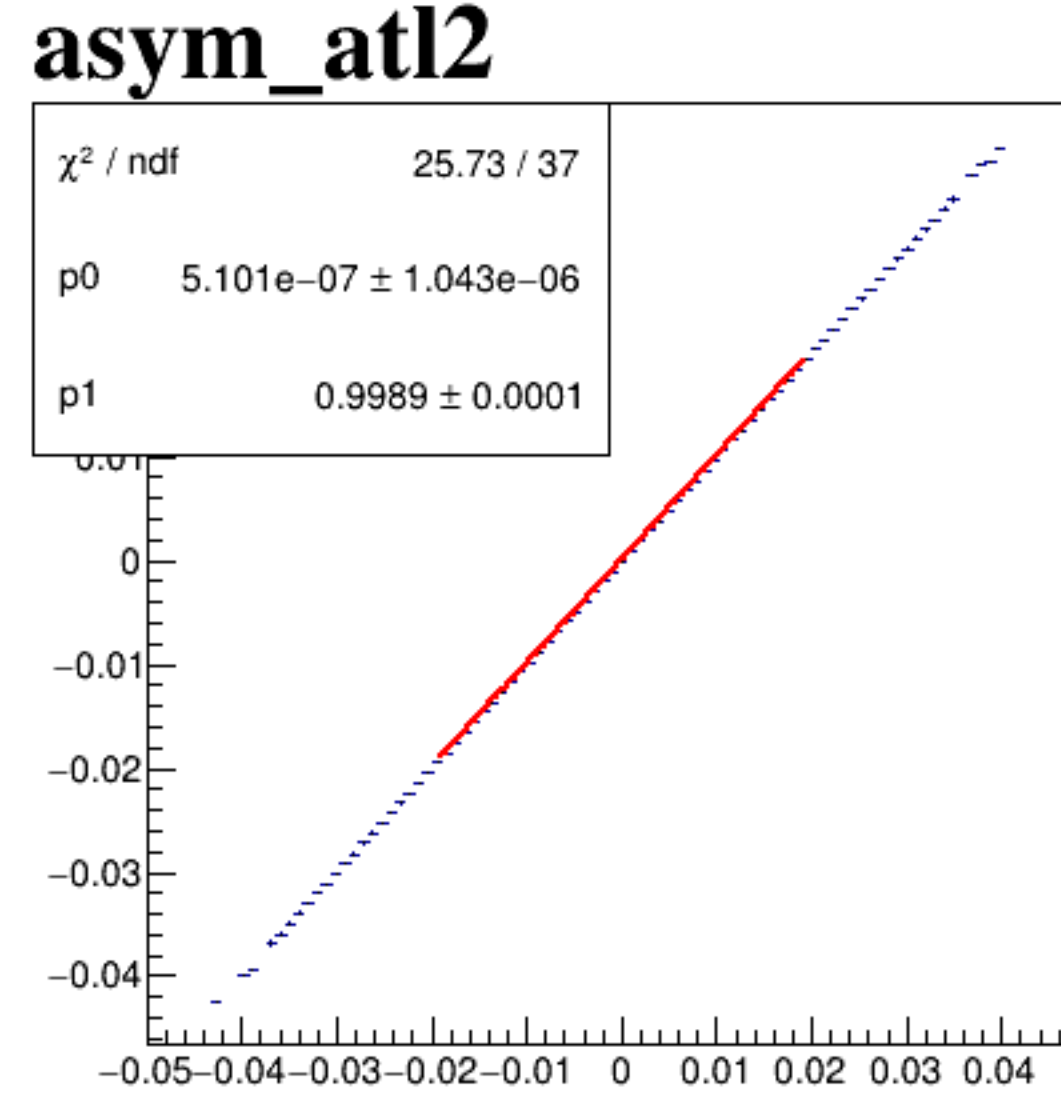
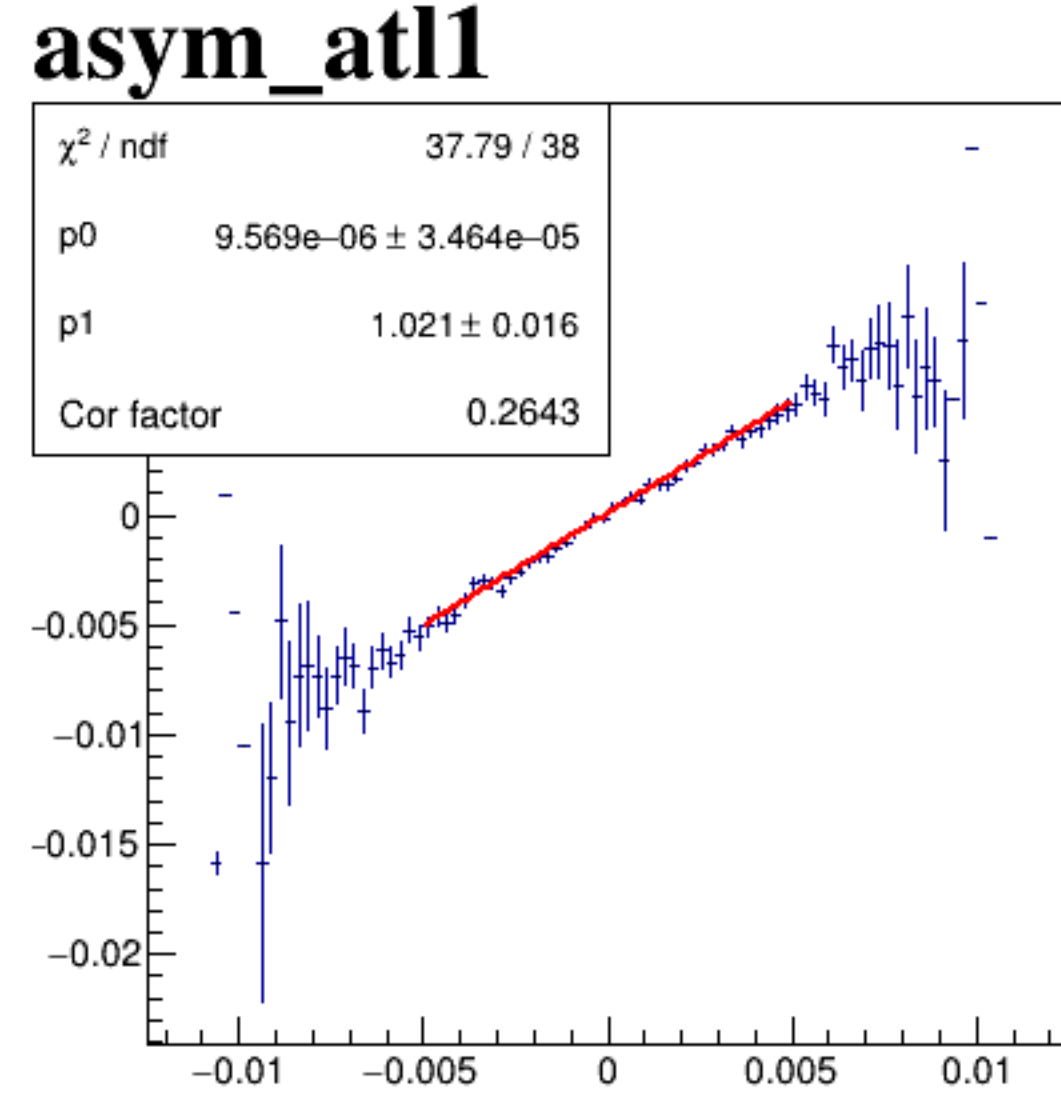
**asym\_atr2**



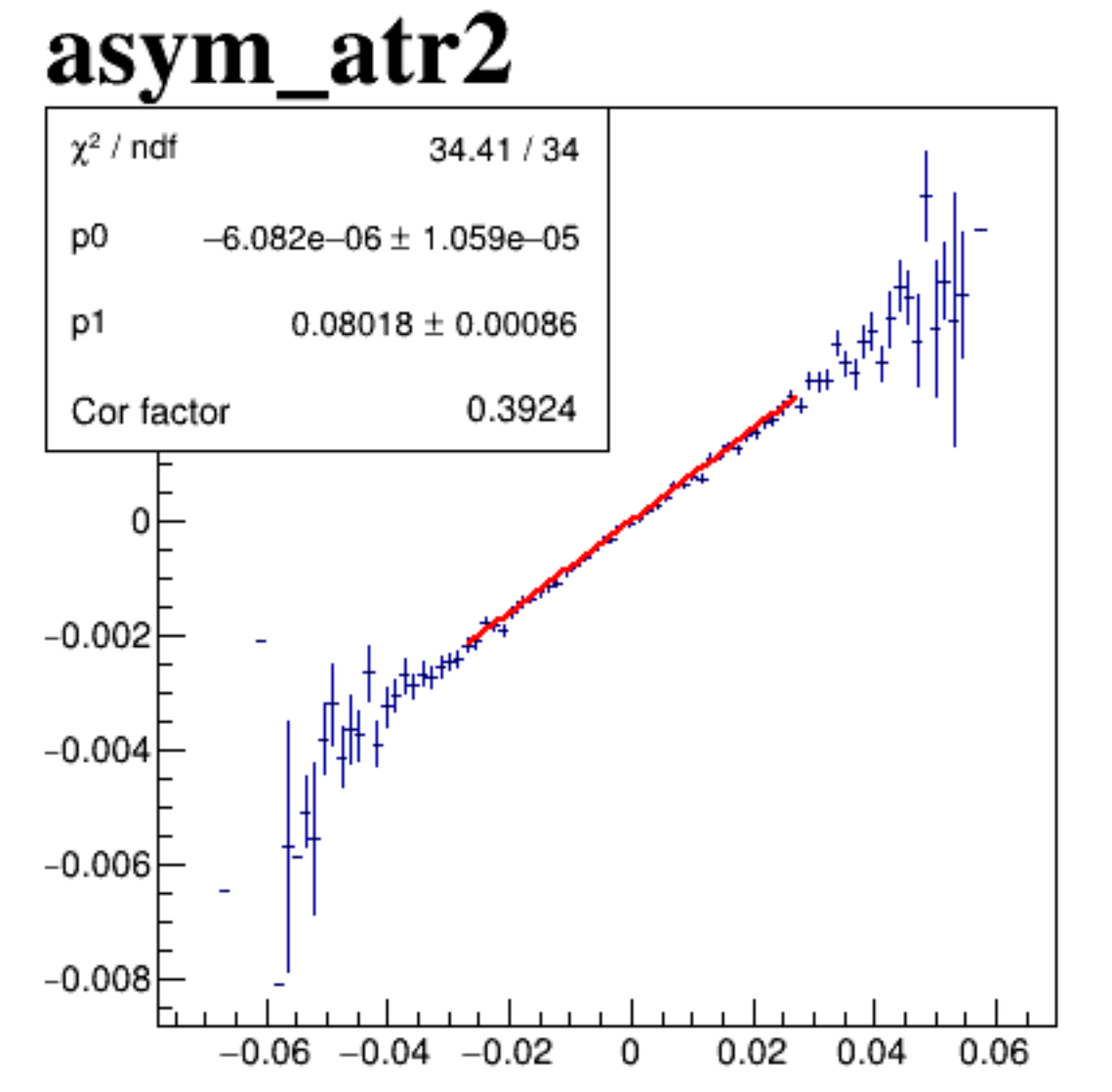
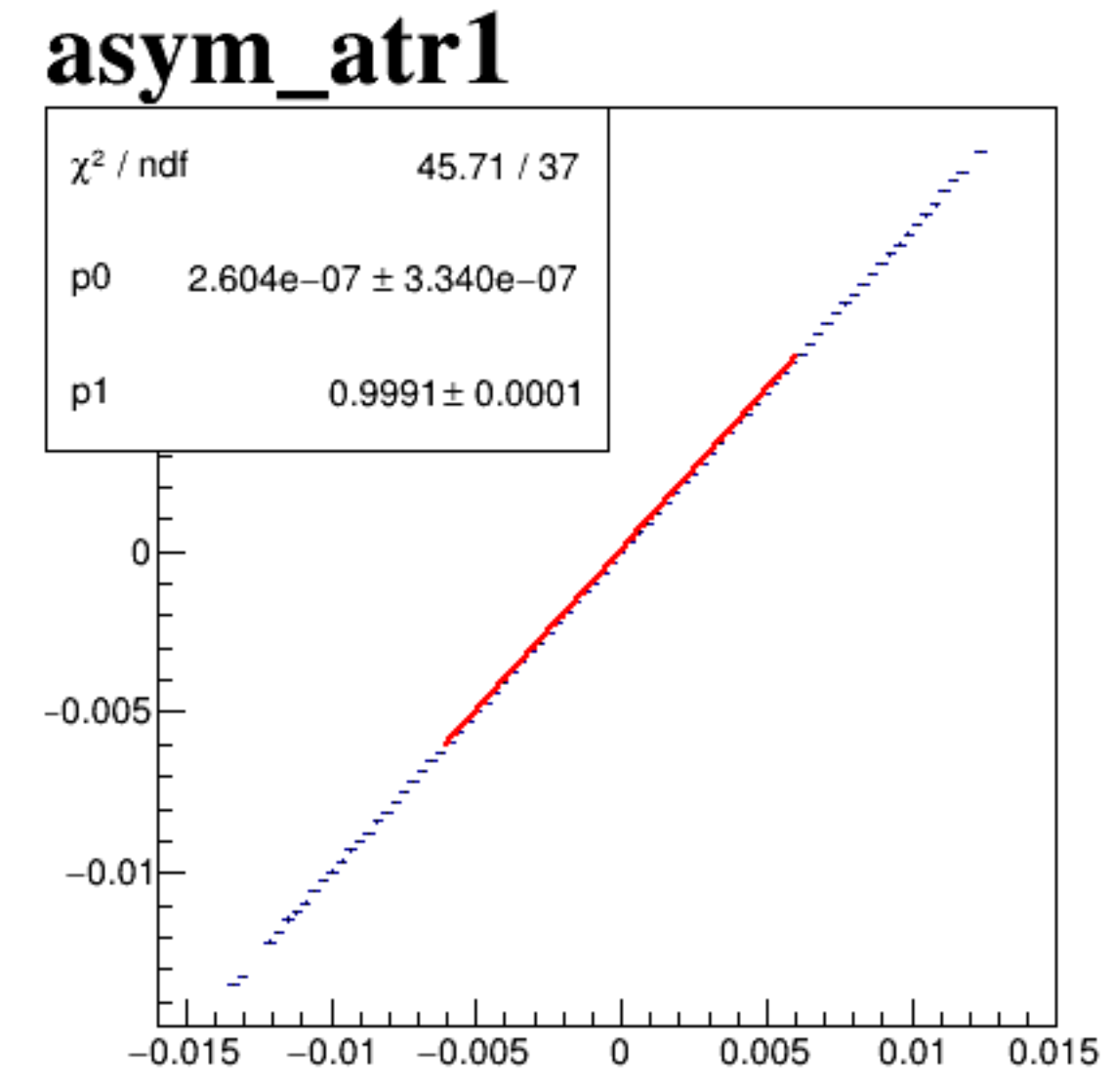
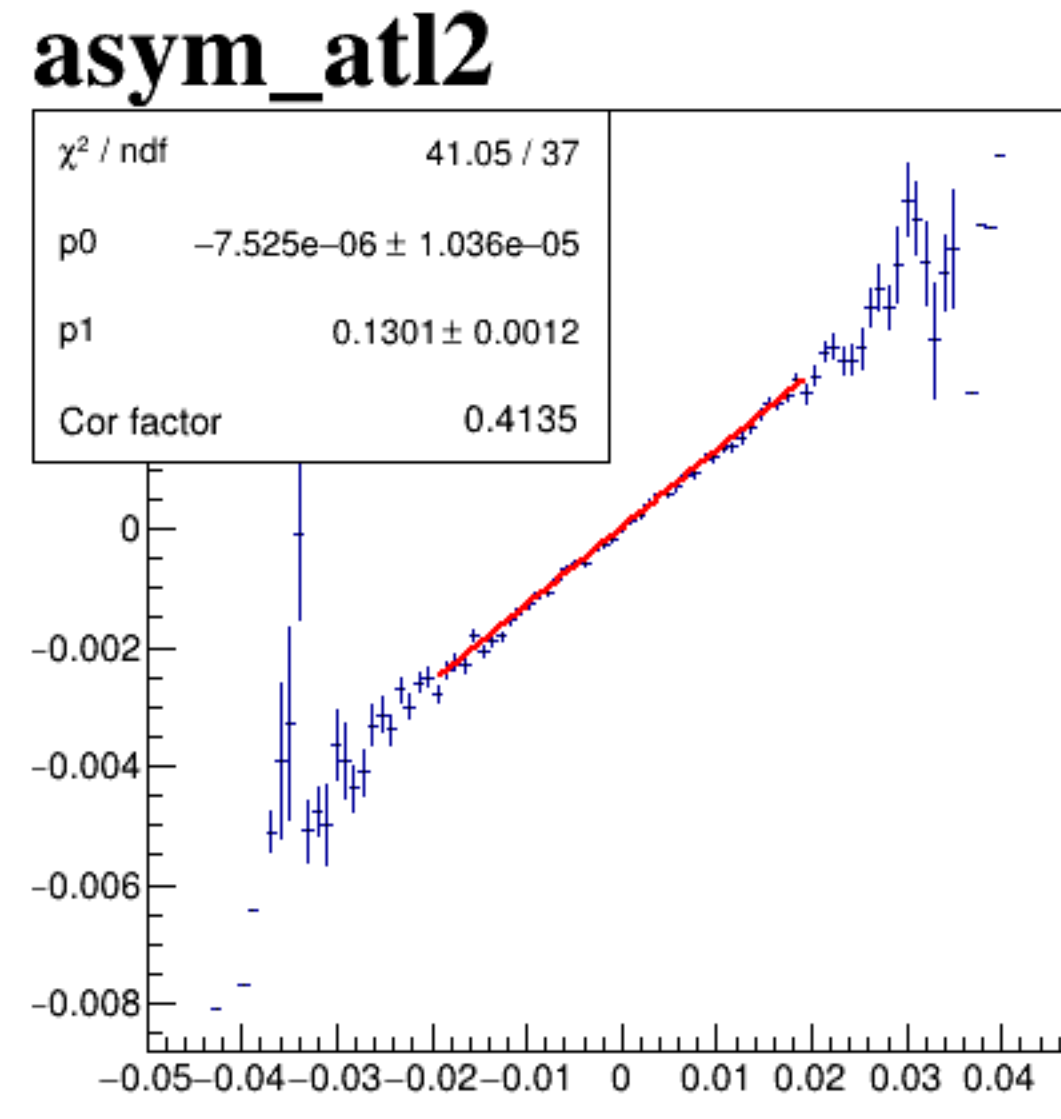
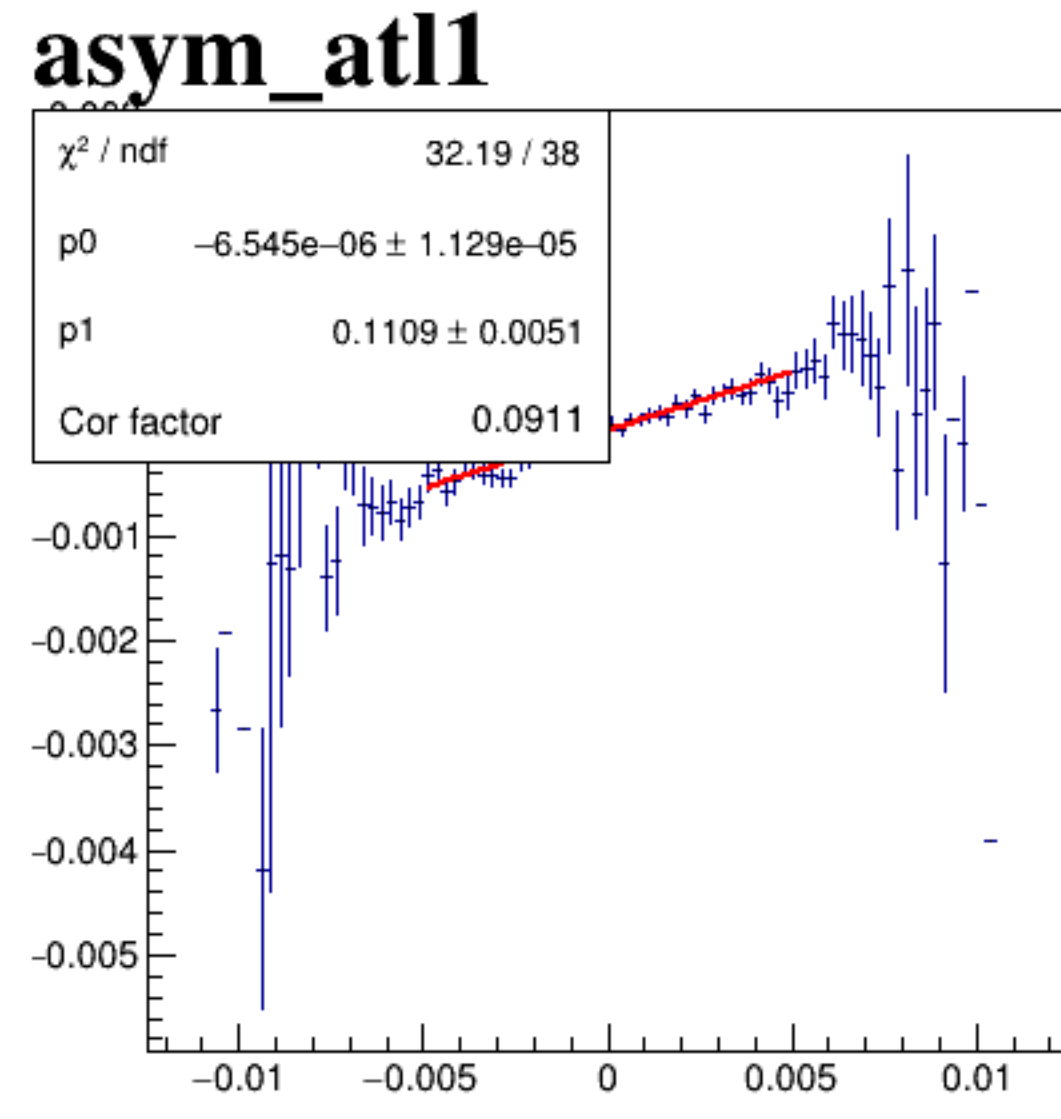
asym\_atl1



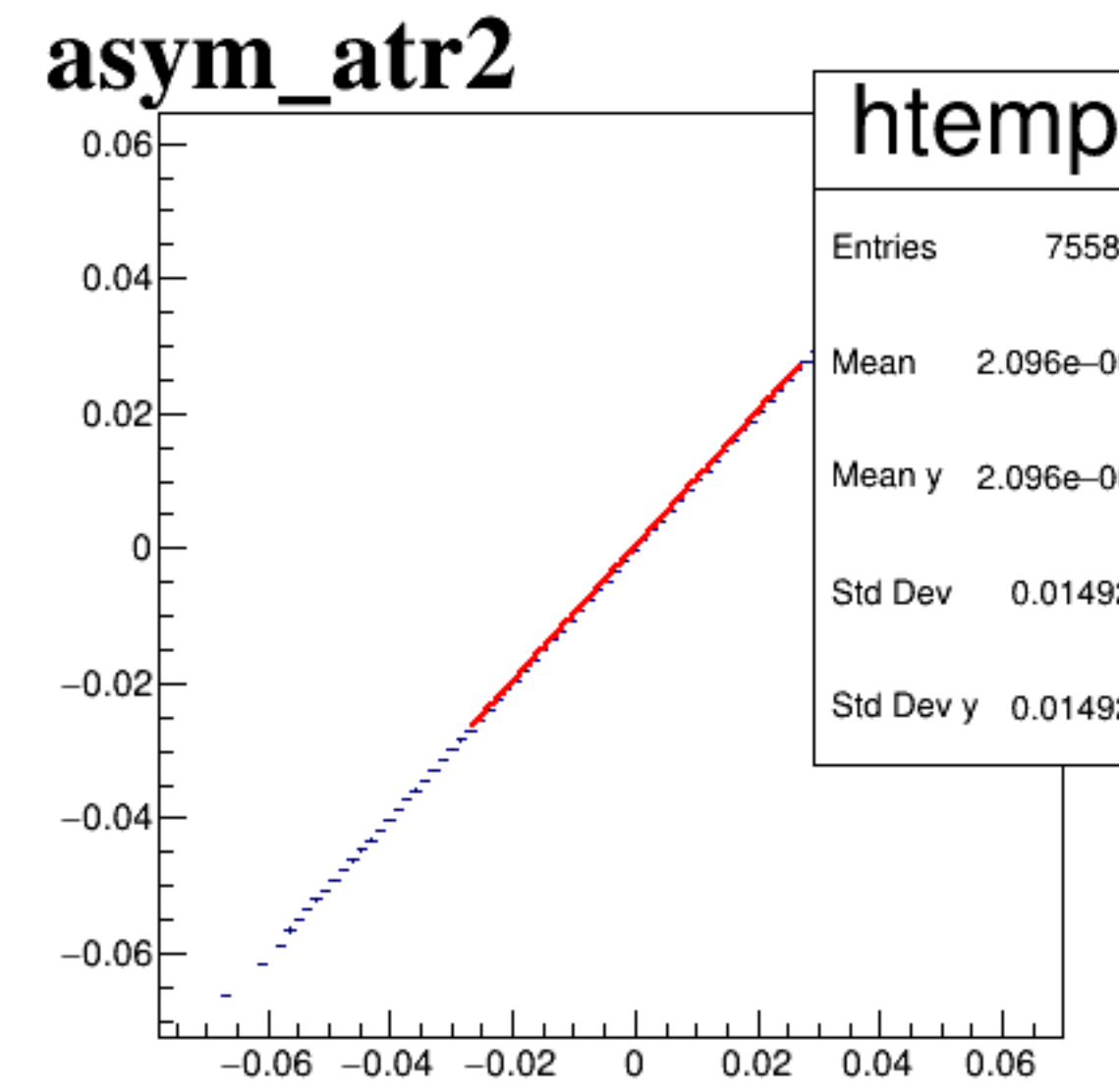
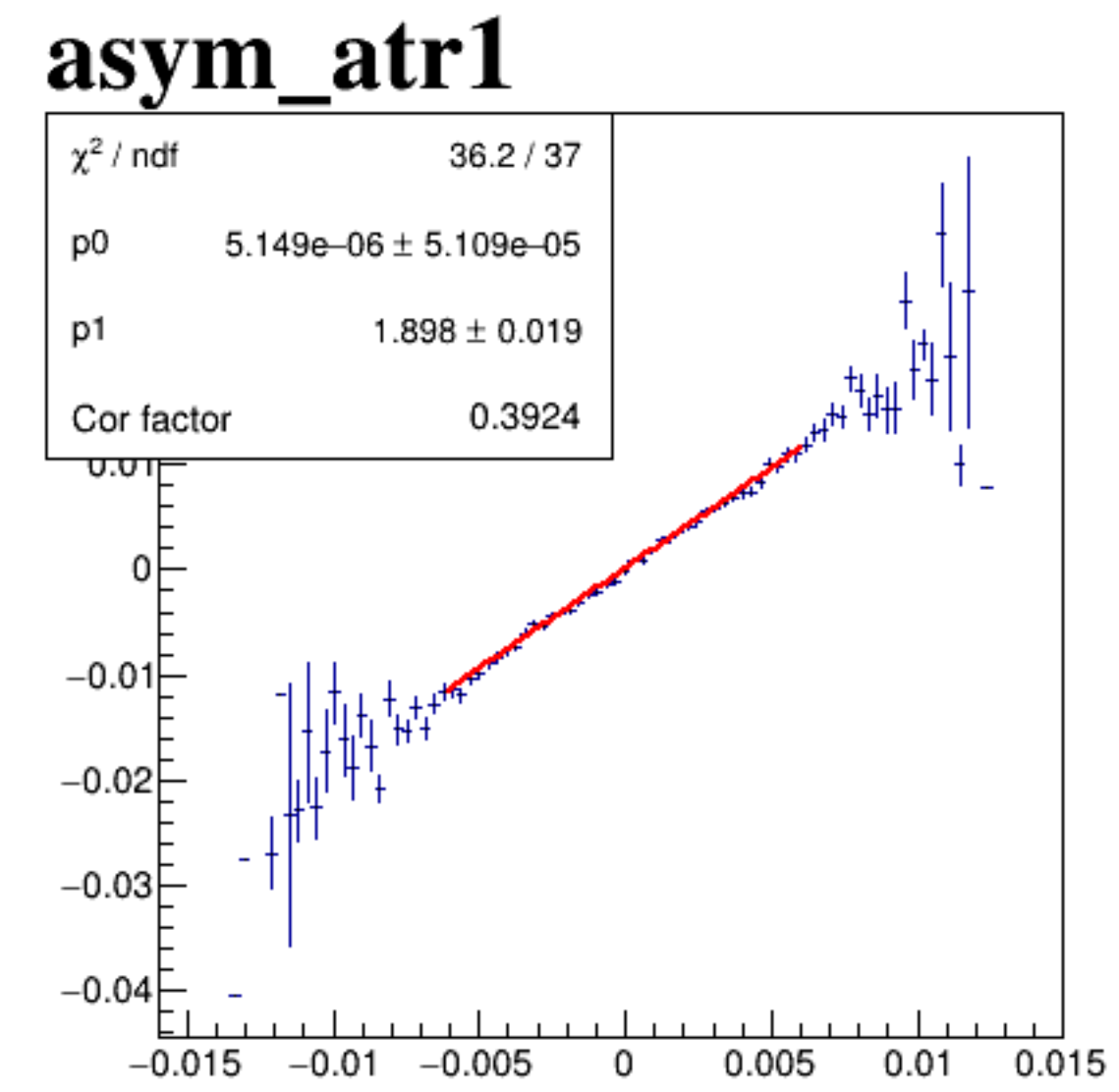
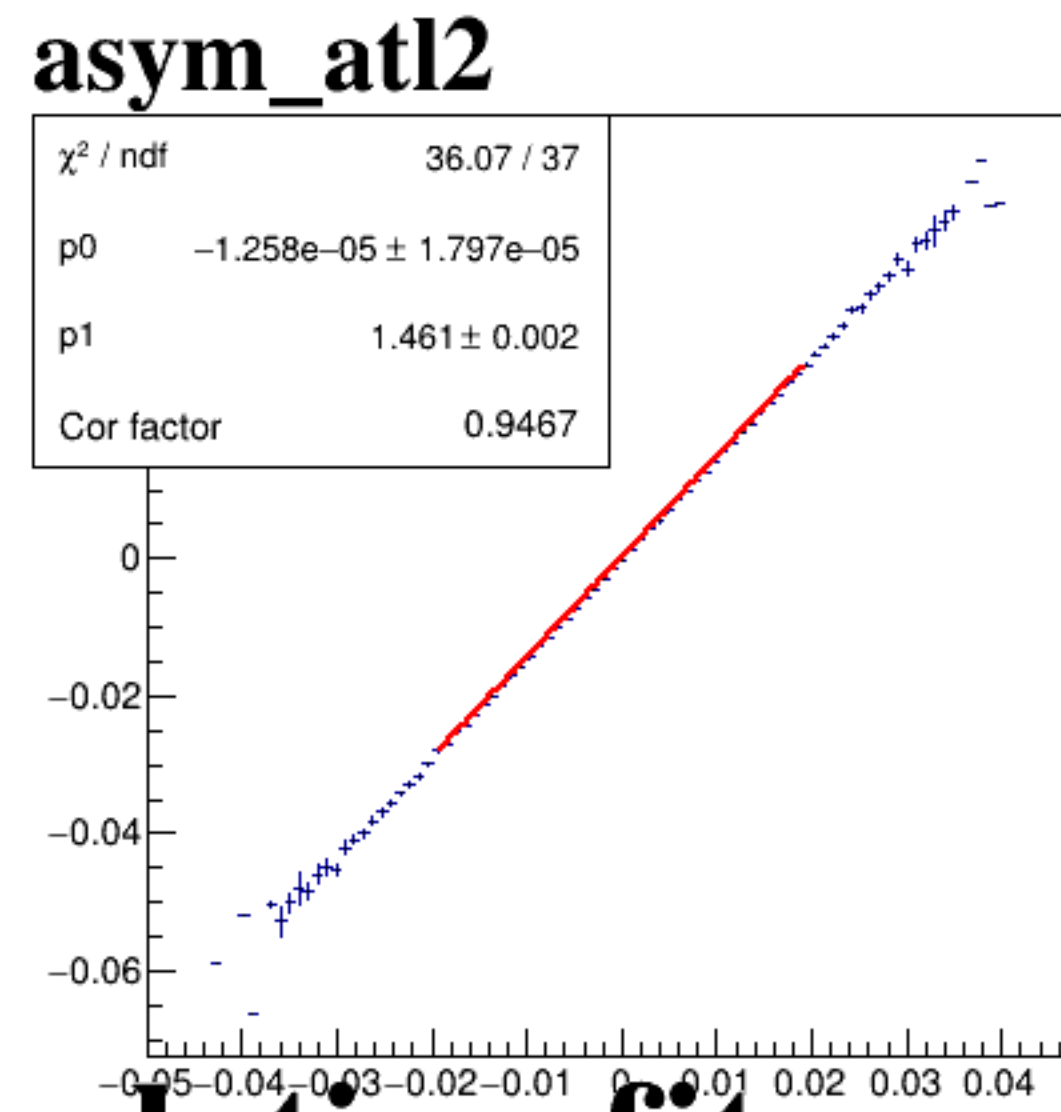
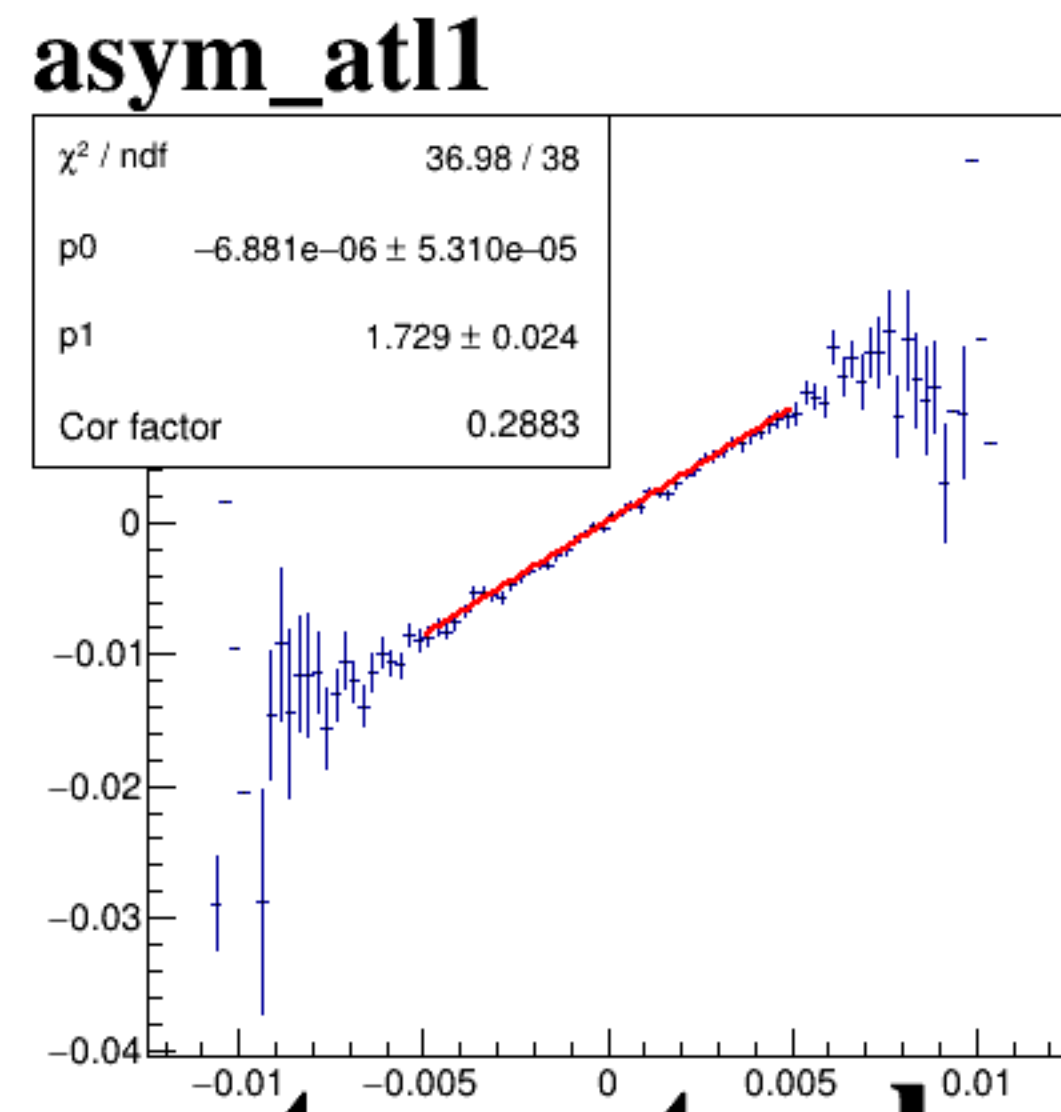
asym\_atl2



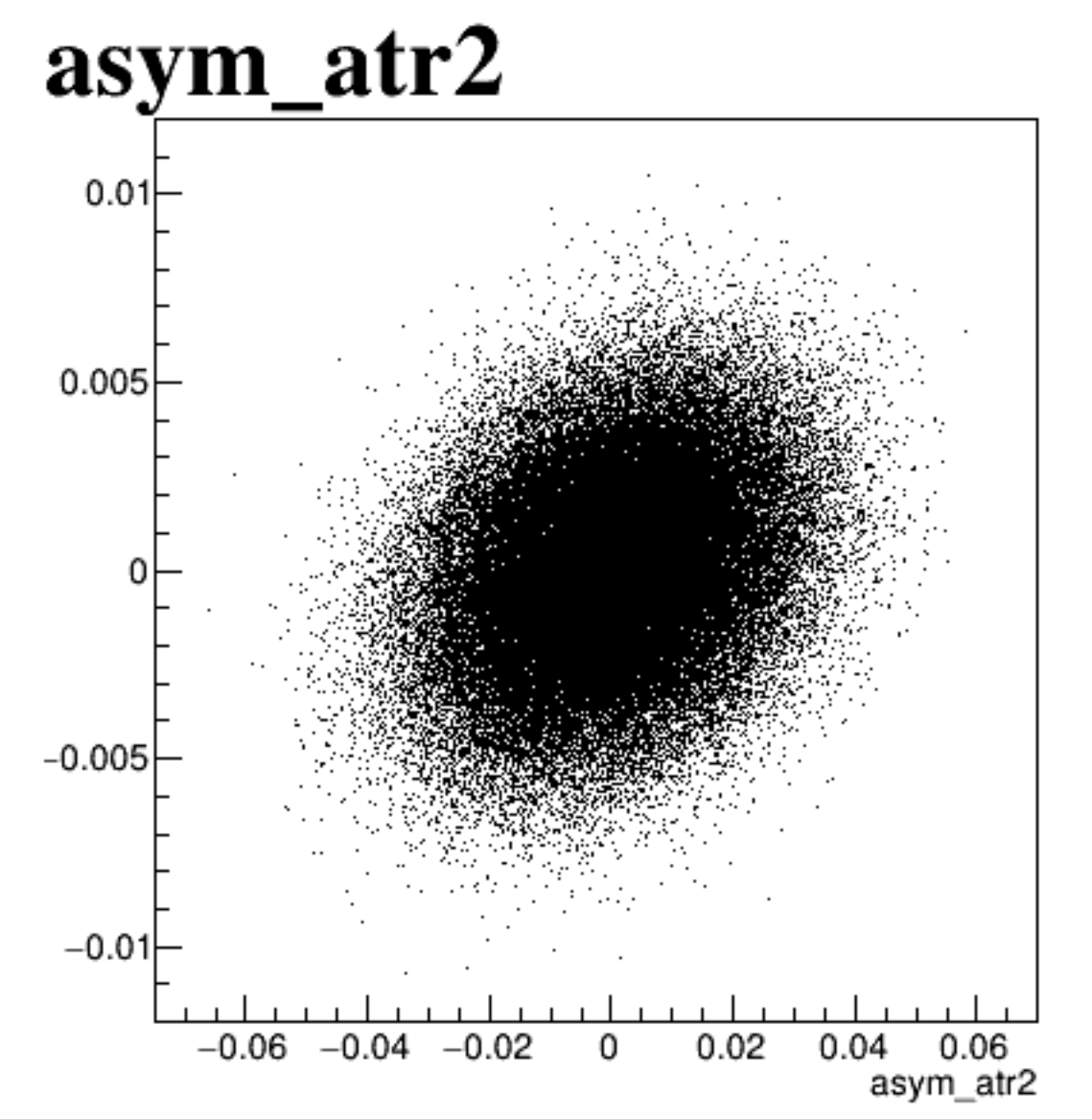
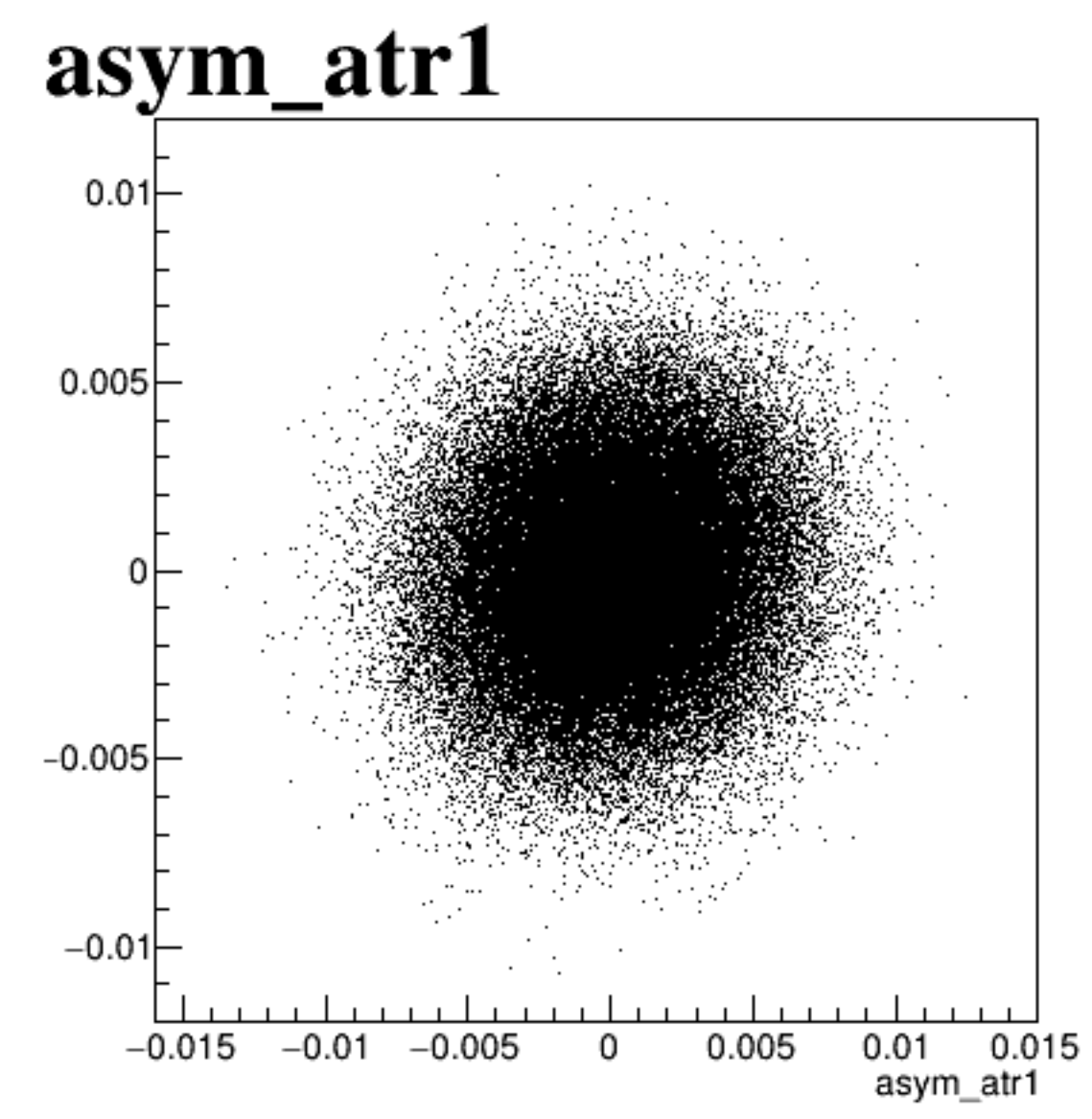
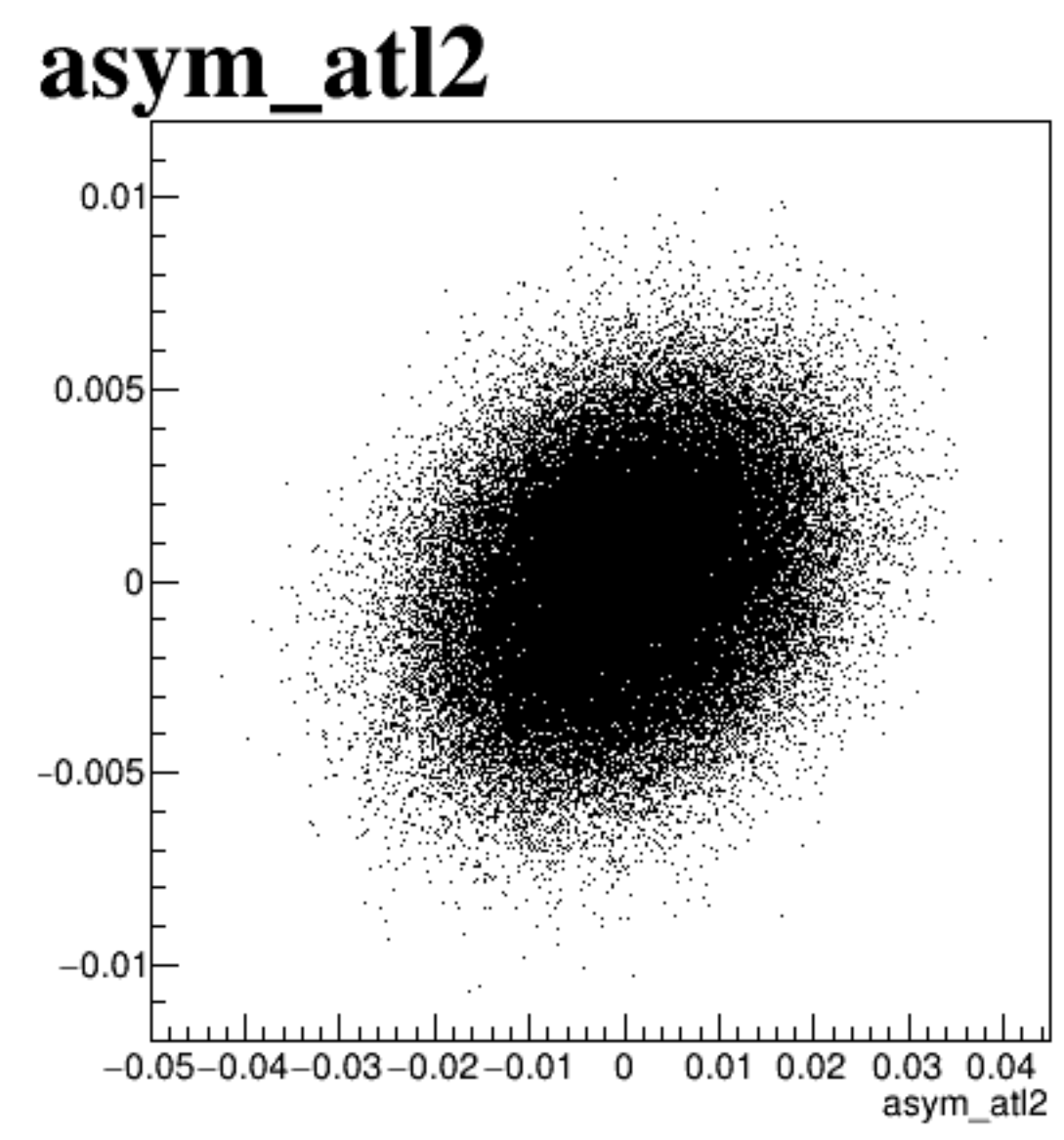
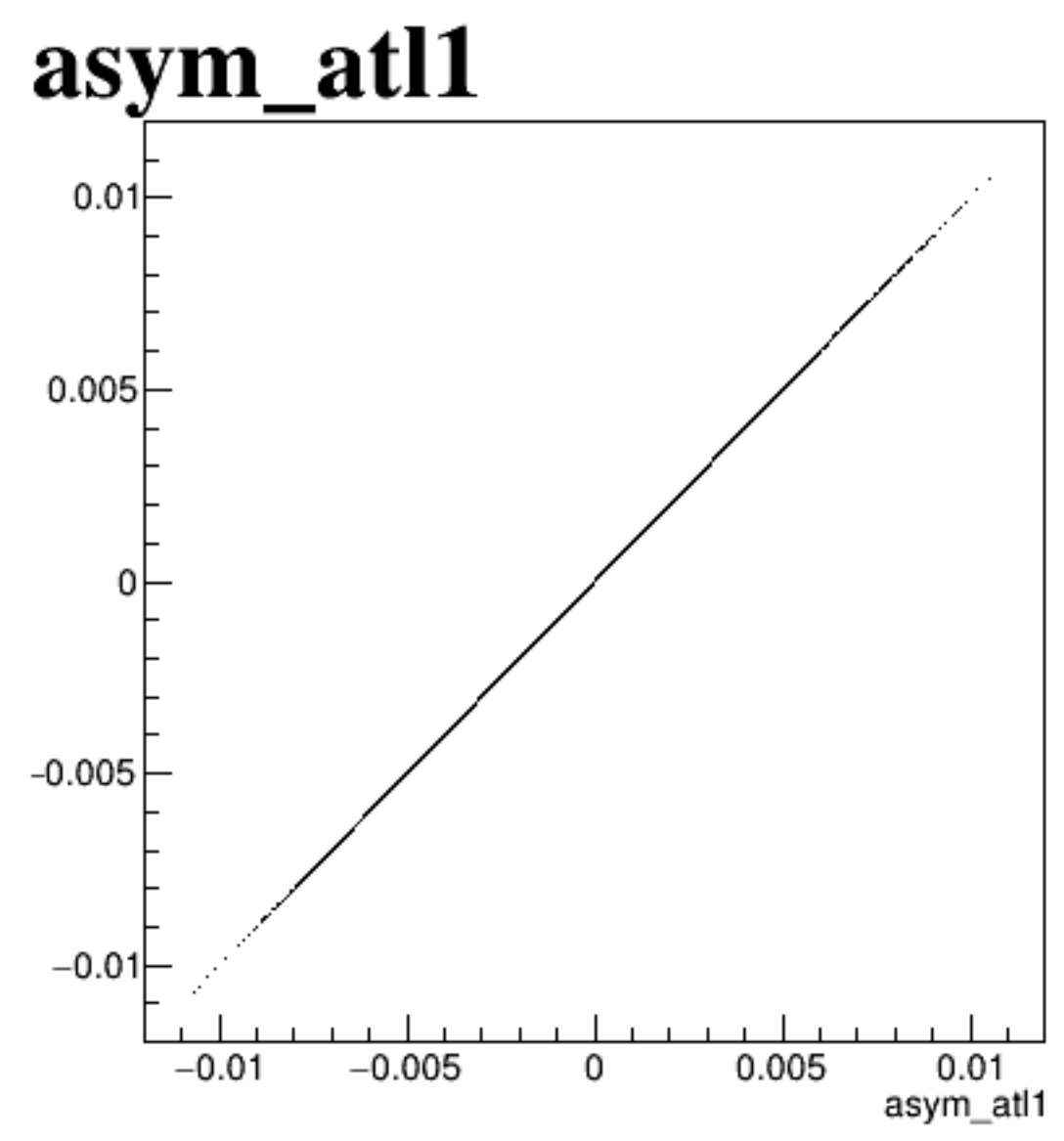
asym\_atr1



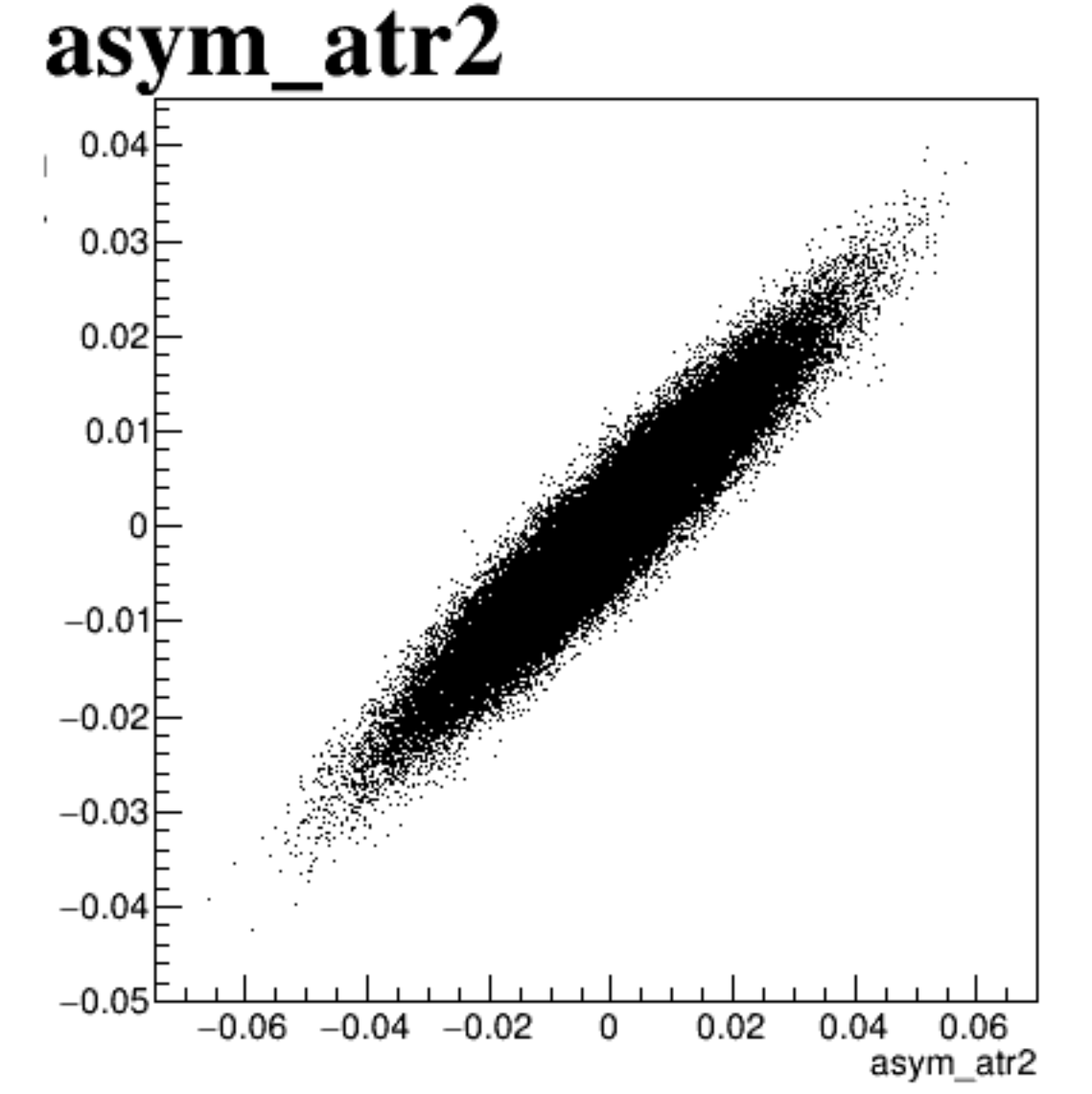
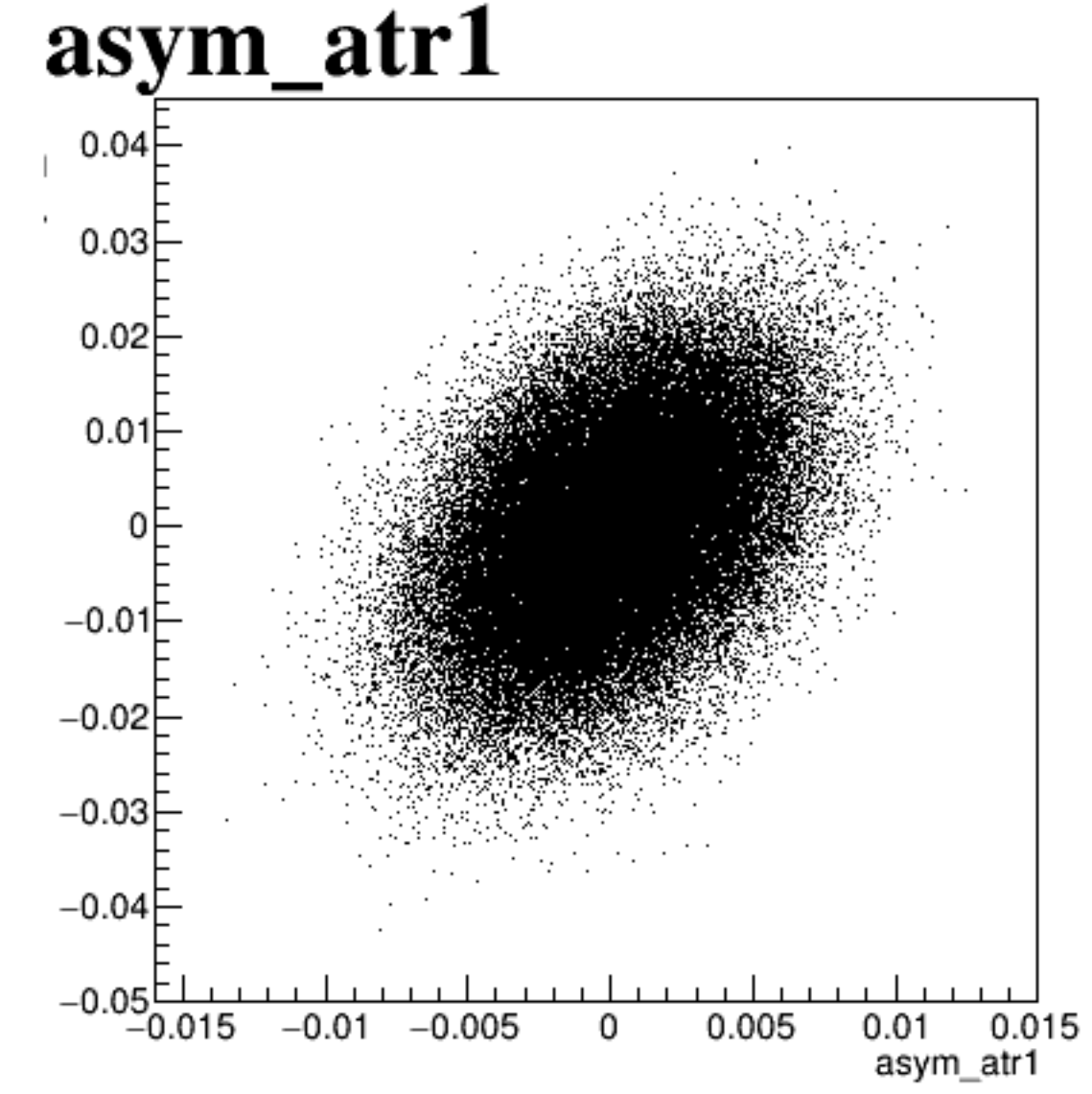
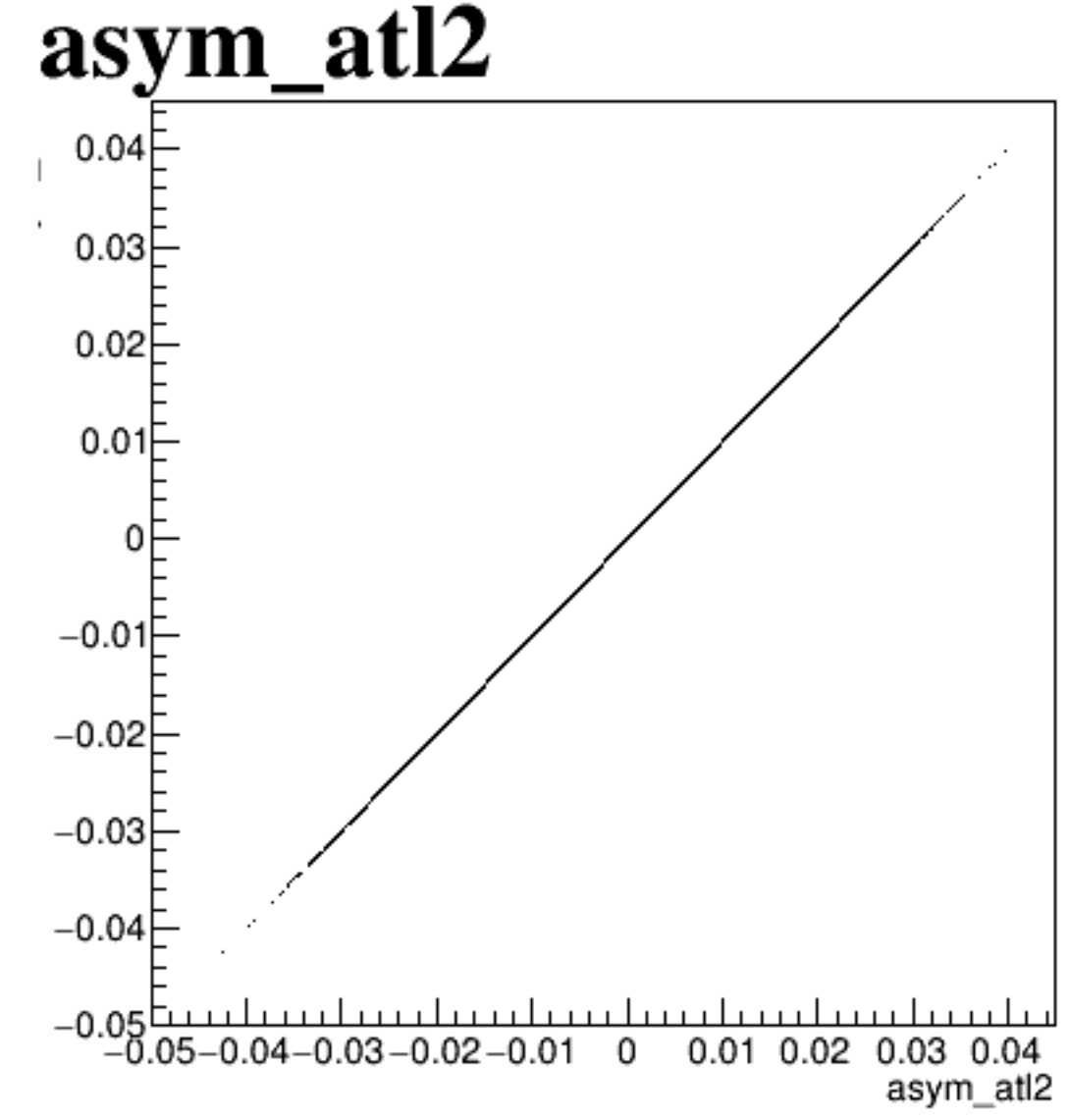
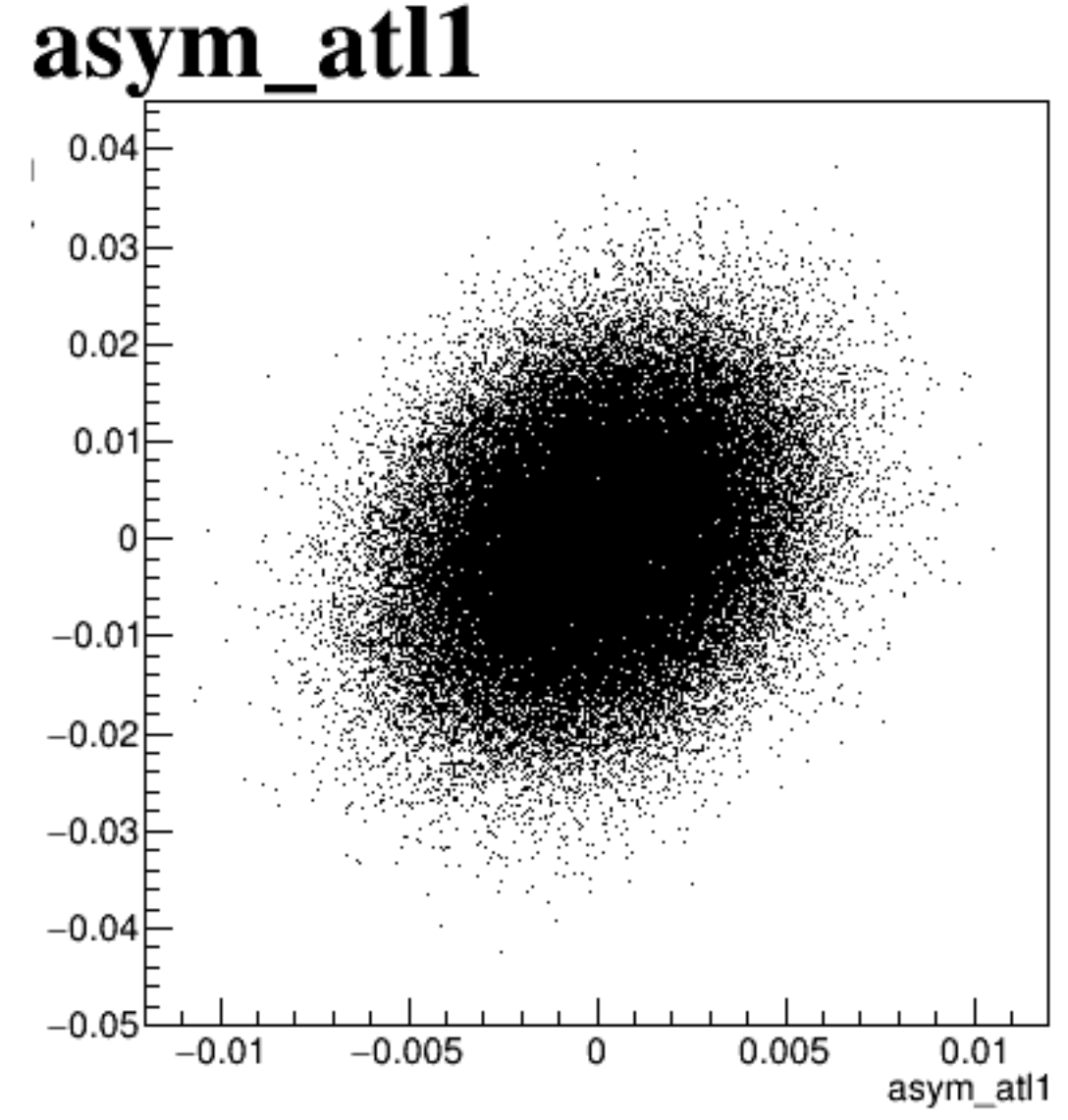
asym\_atr2



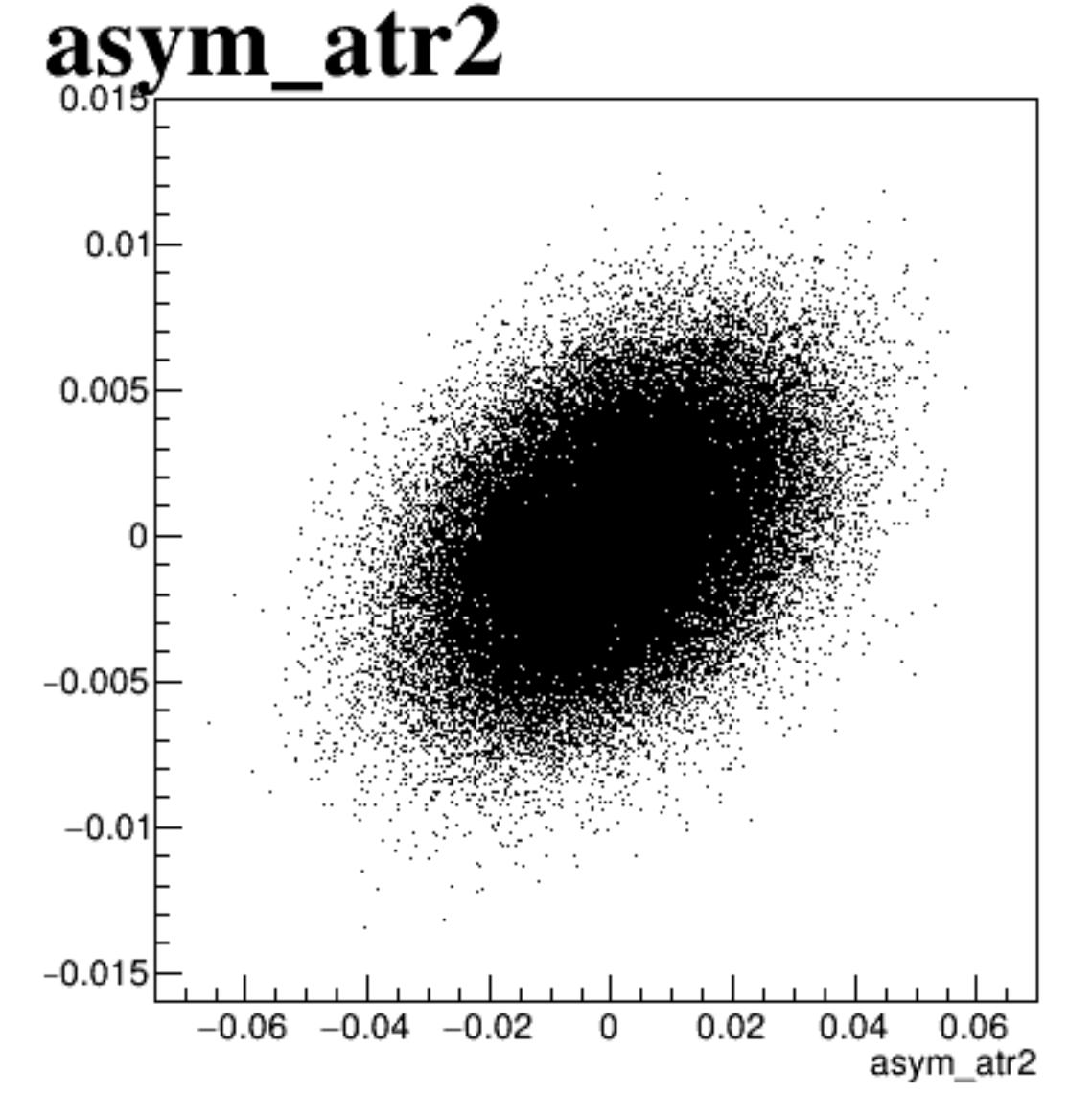
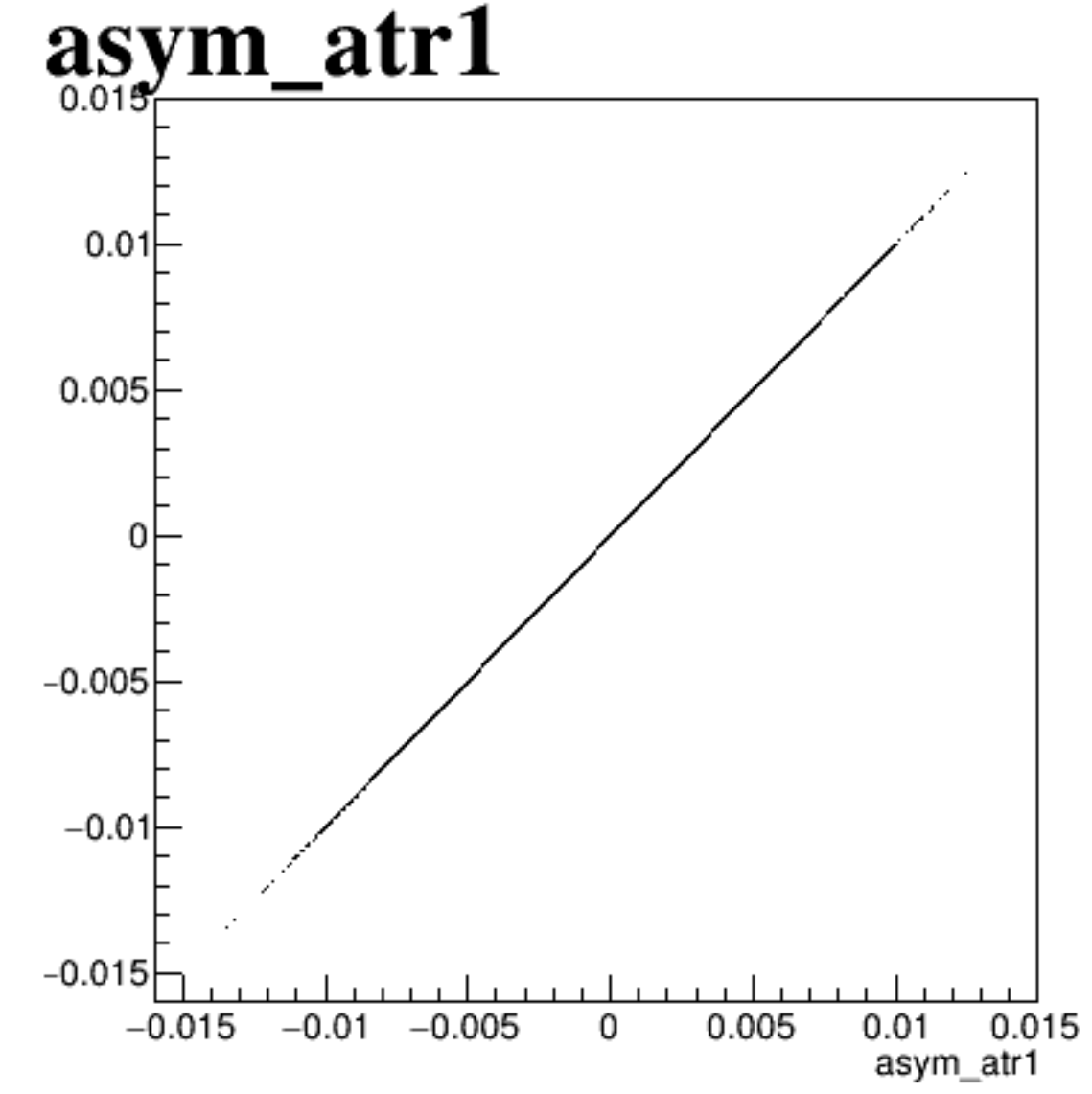
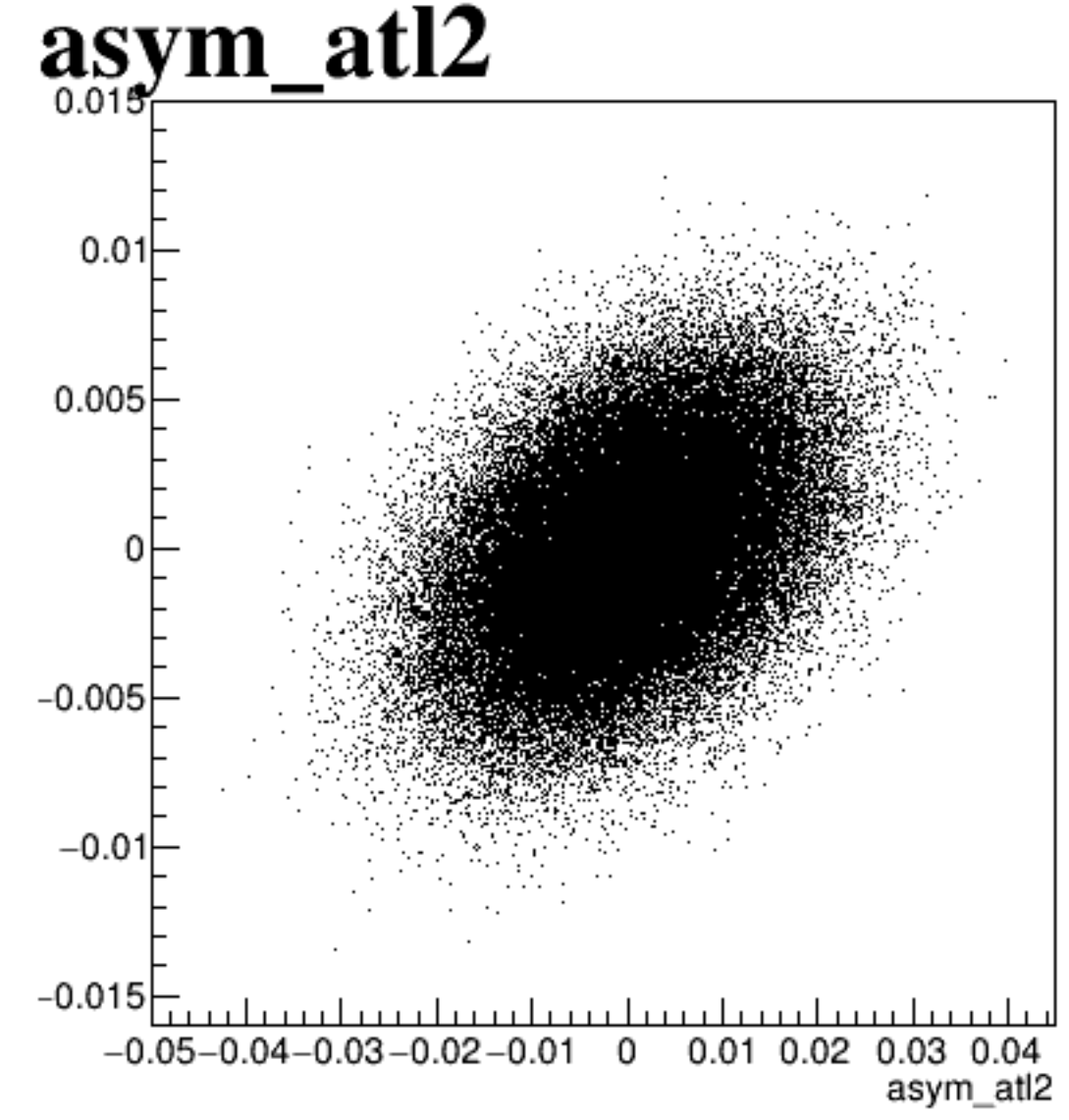
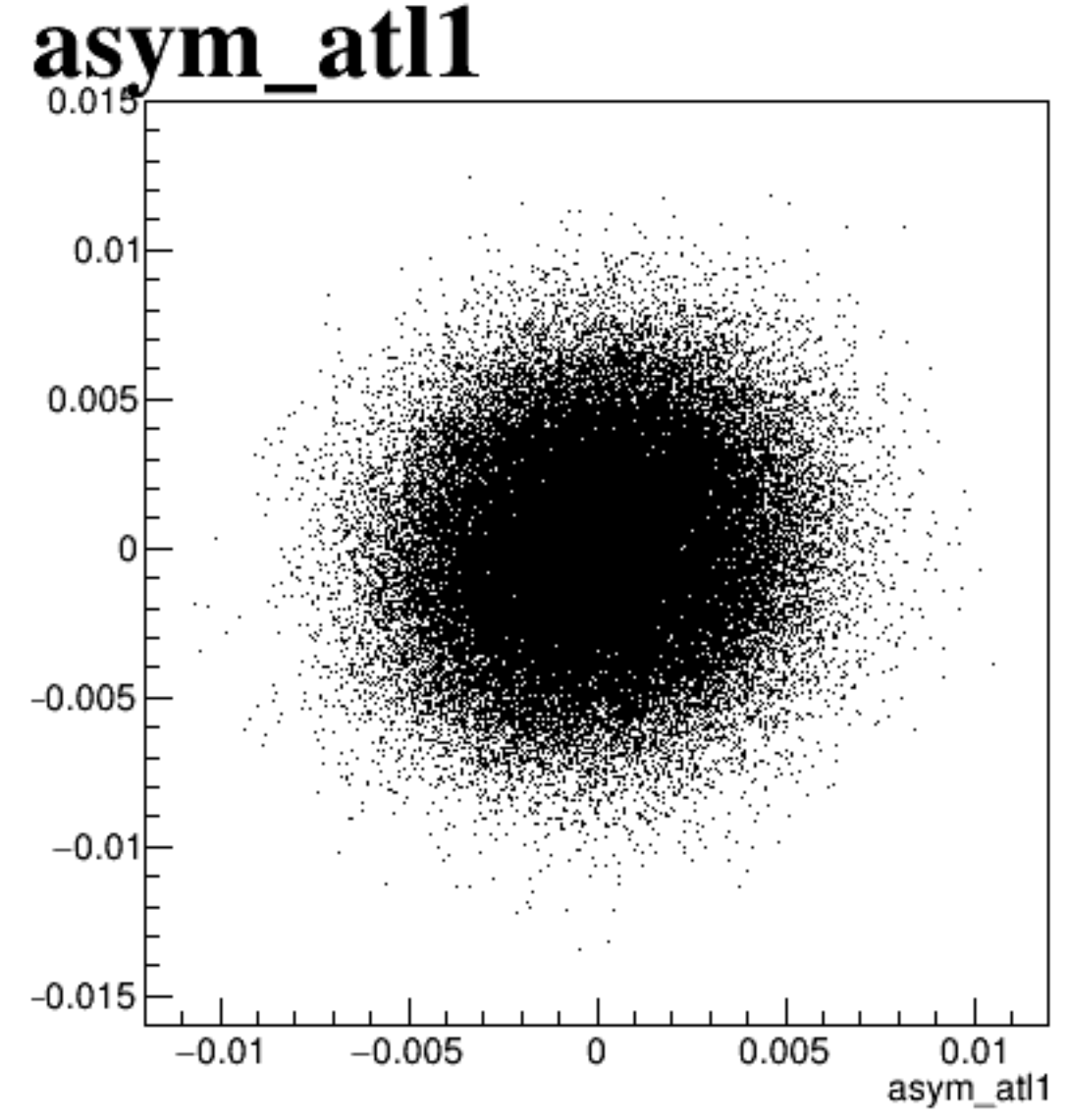
**asym\_atl1**



**asym\_atl2**



**asym\_atr1**



**asym\_atr2**

