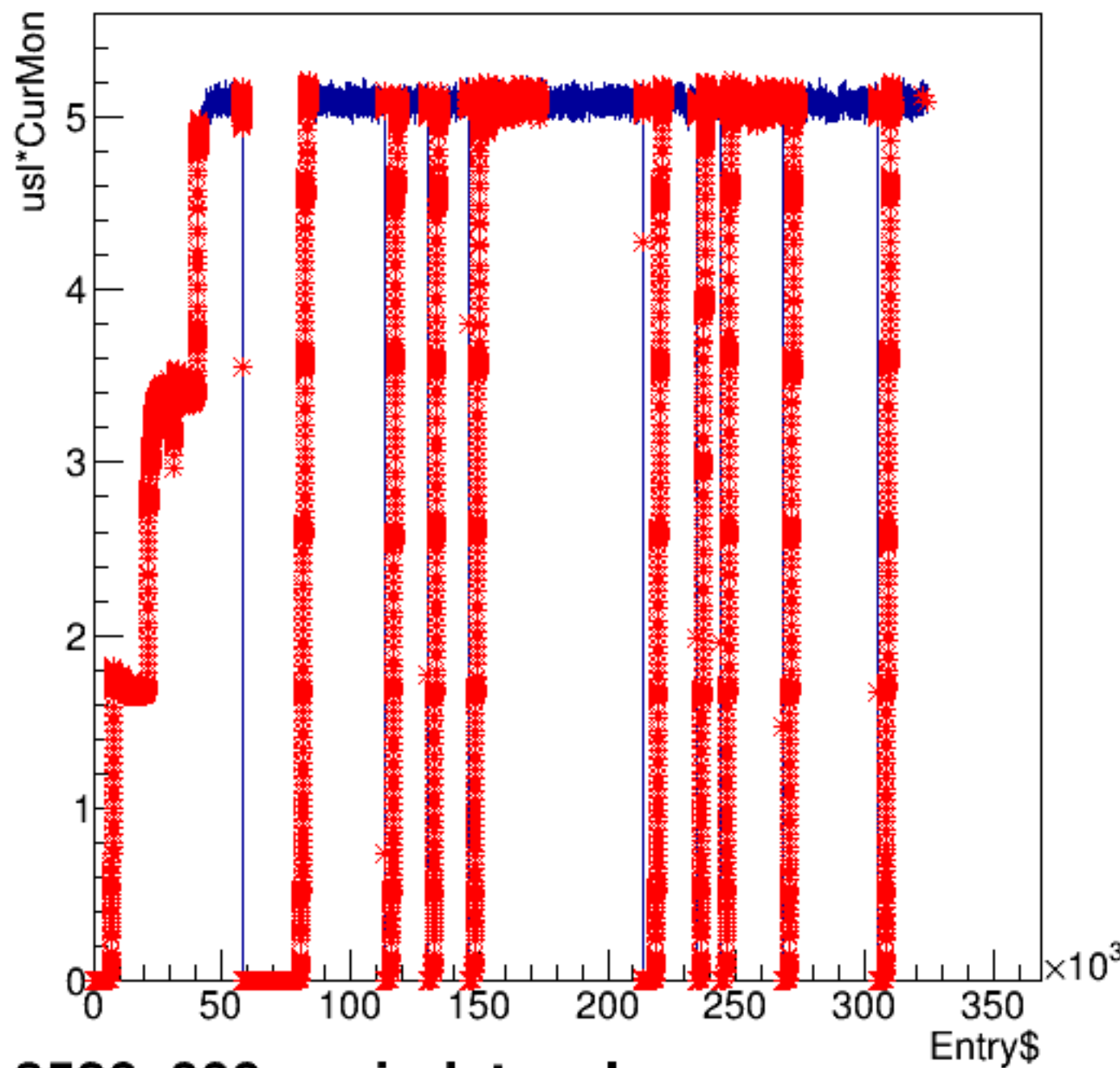
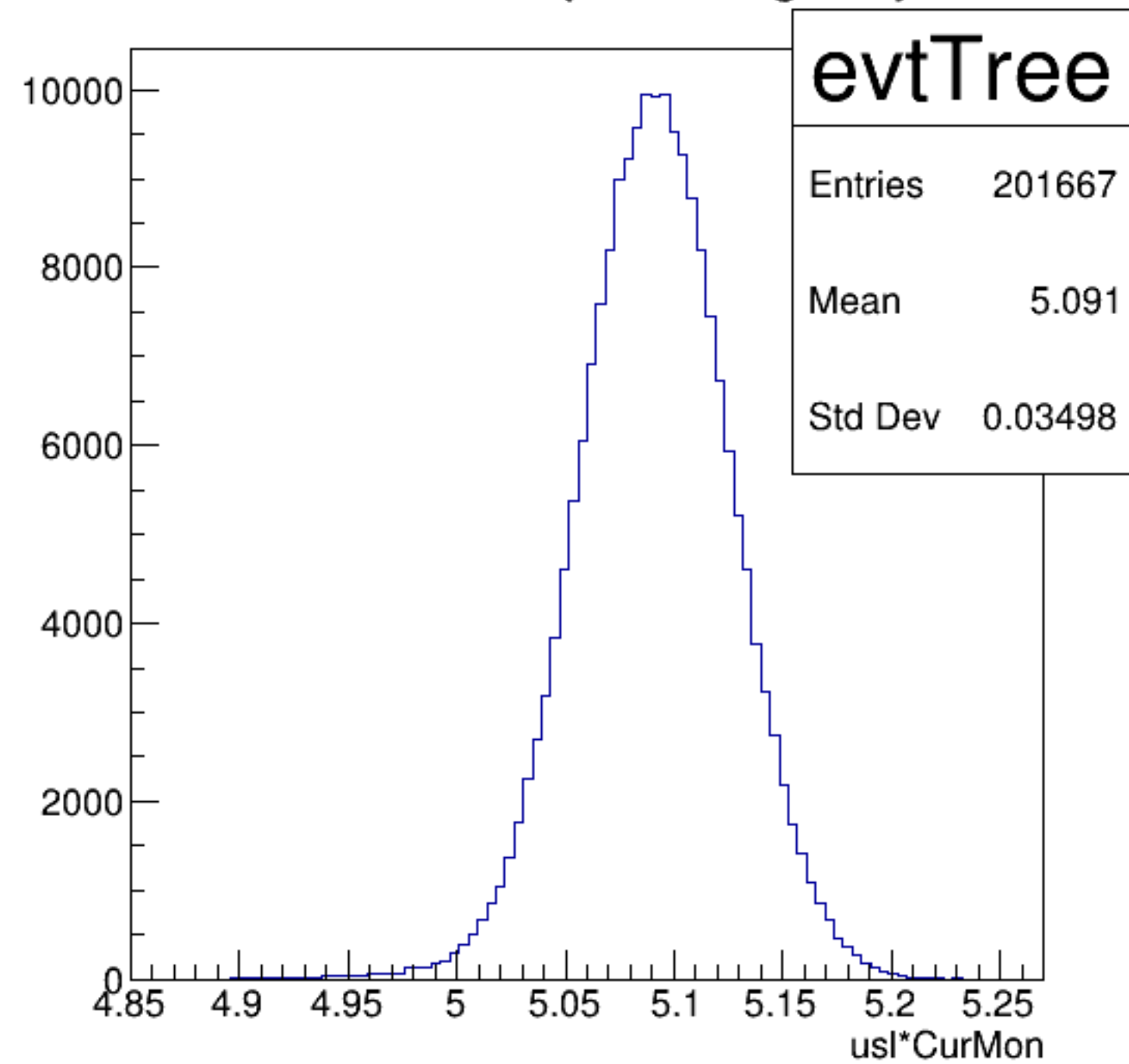


usl*CurMon:Entry\$

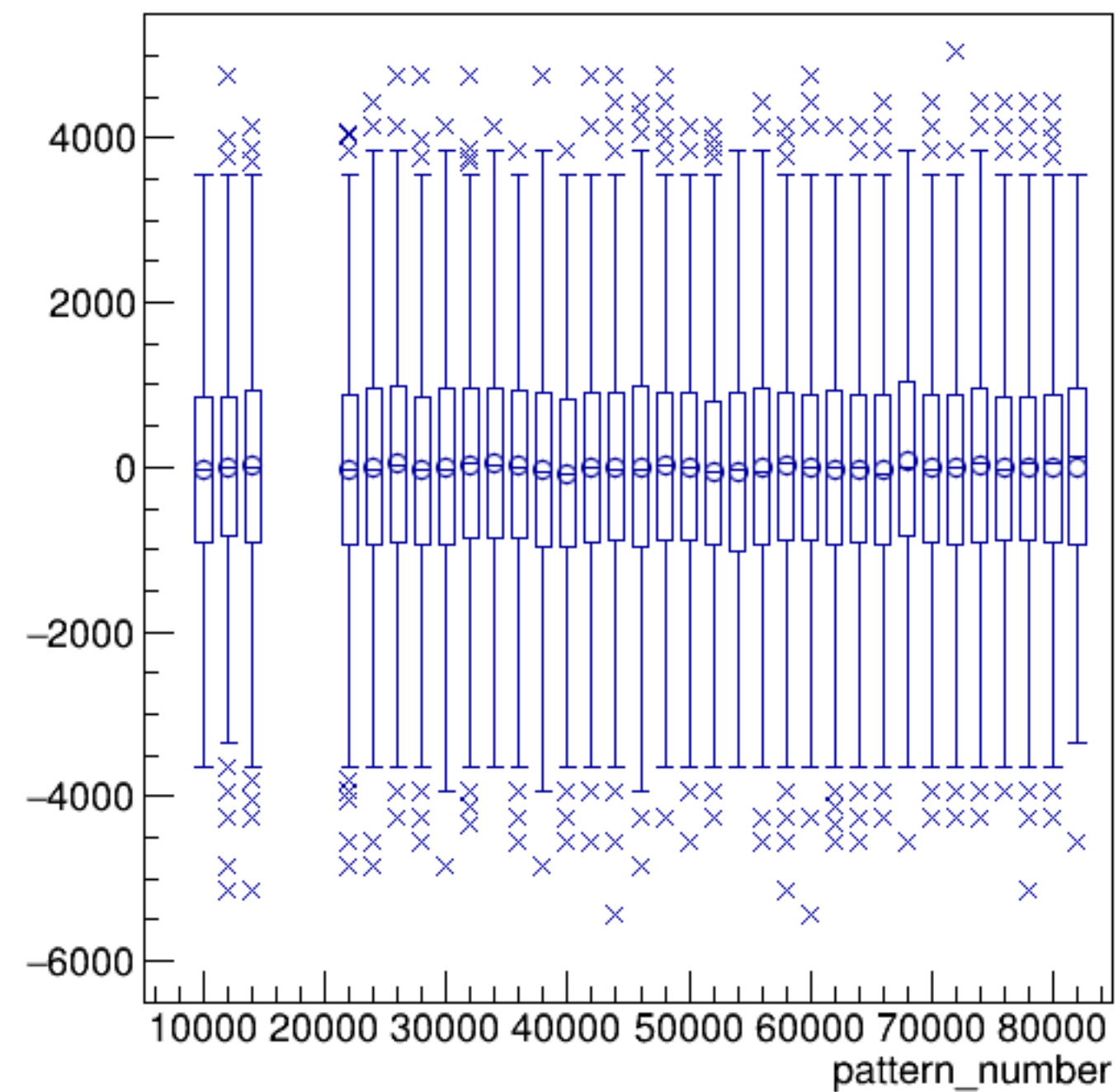


run8526_000_maintet_usl.png

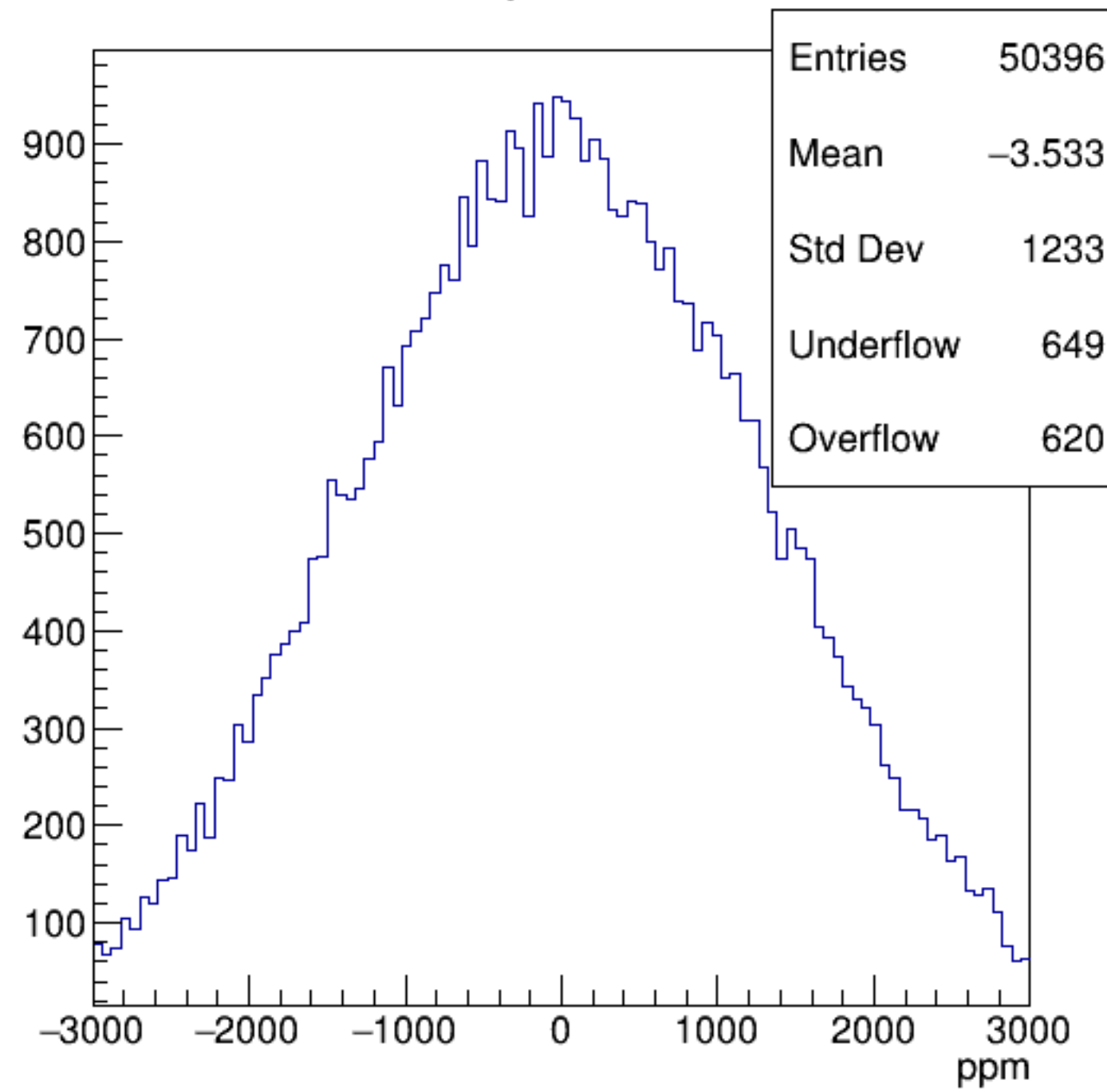
usl*CurMon {ErrorFlag==0}



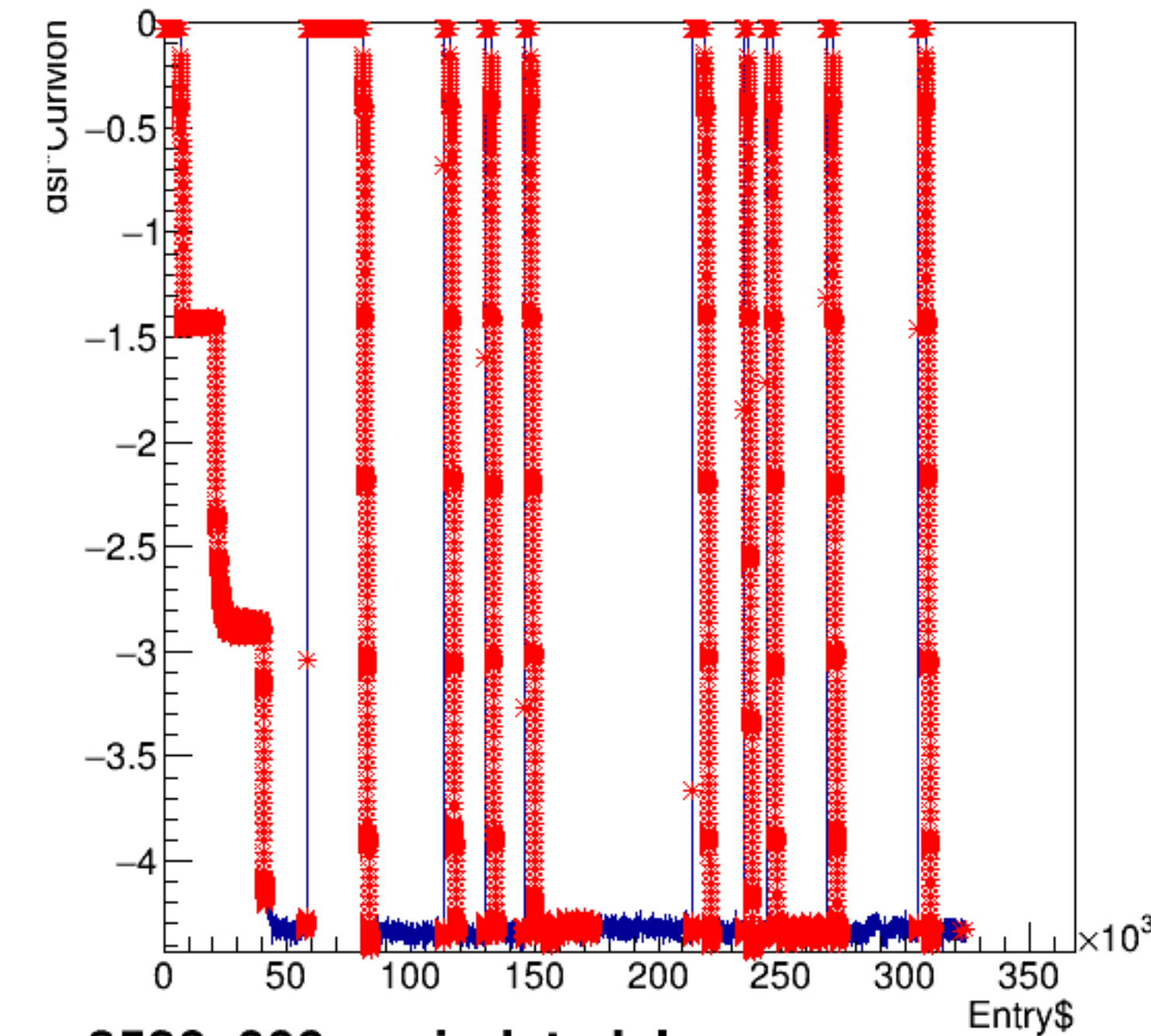
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl

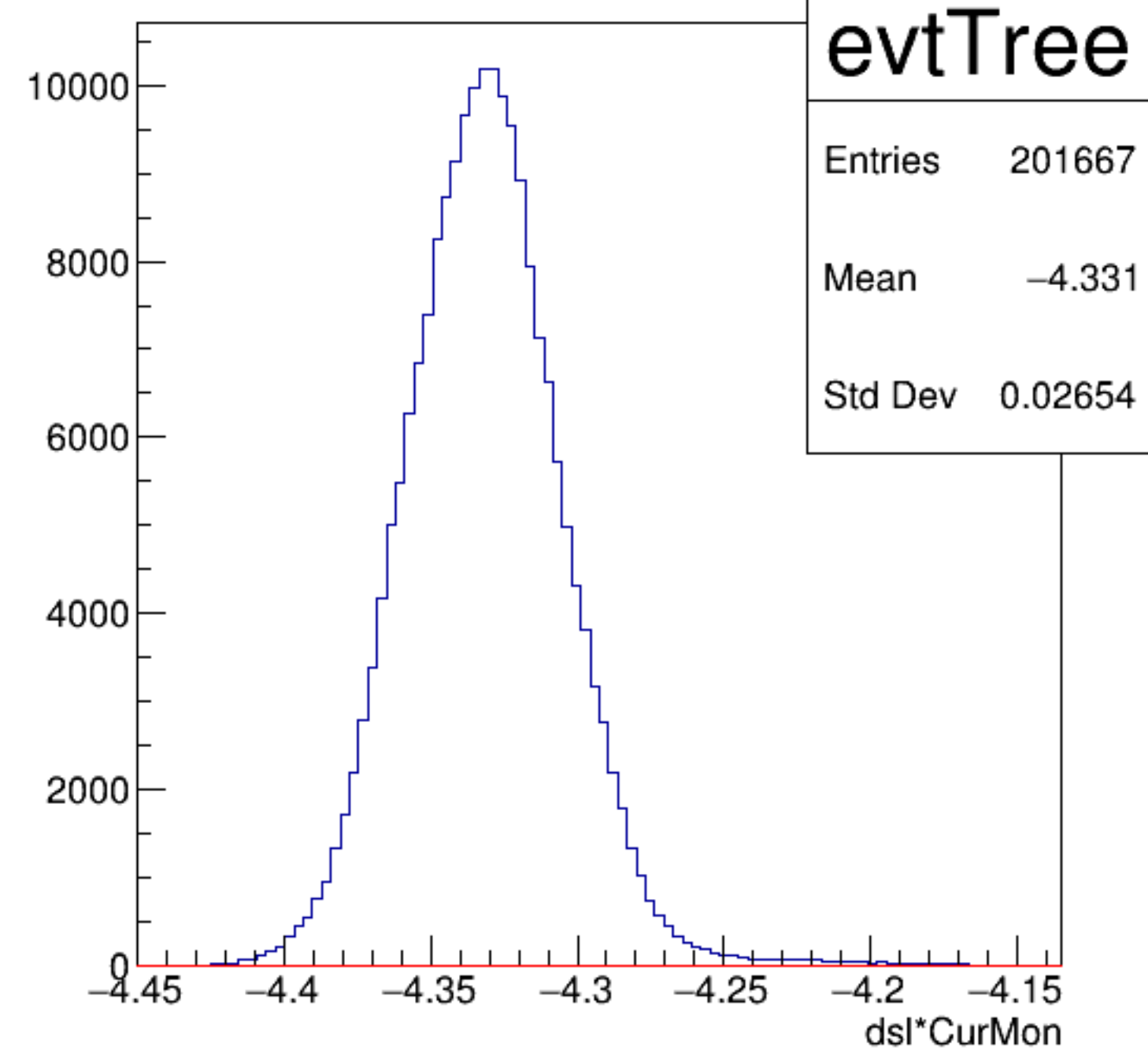


dsl*CurMon:Entry\$



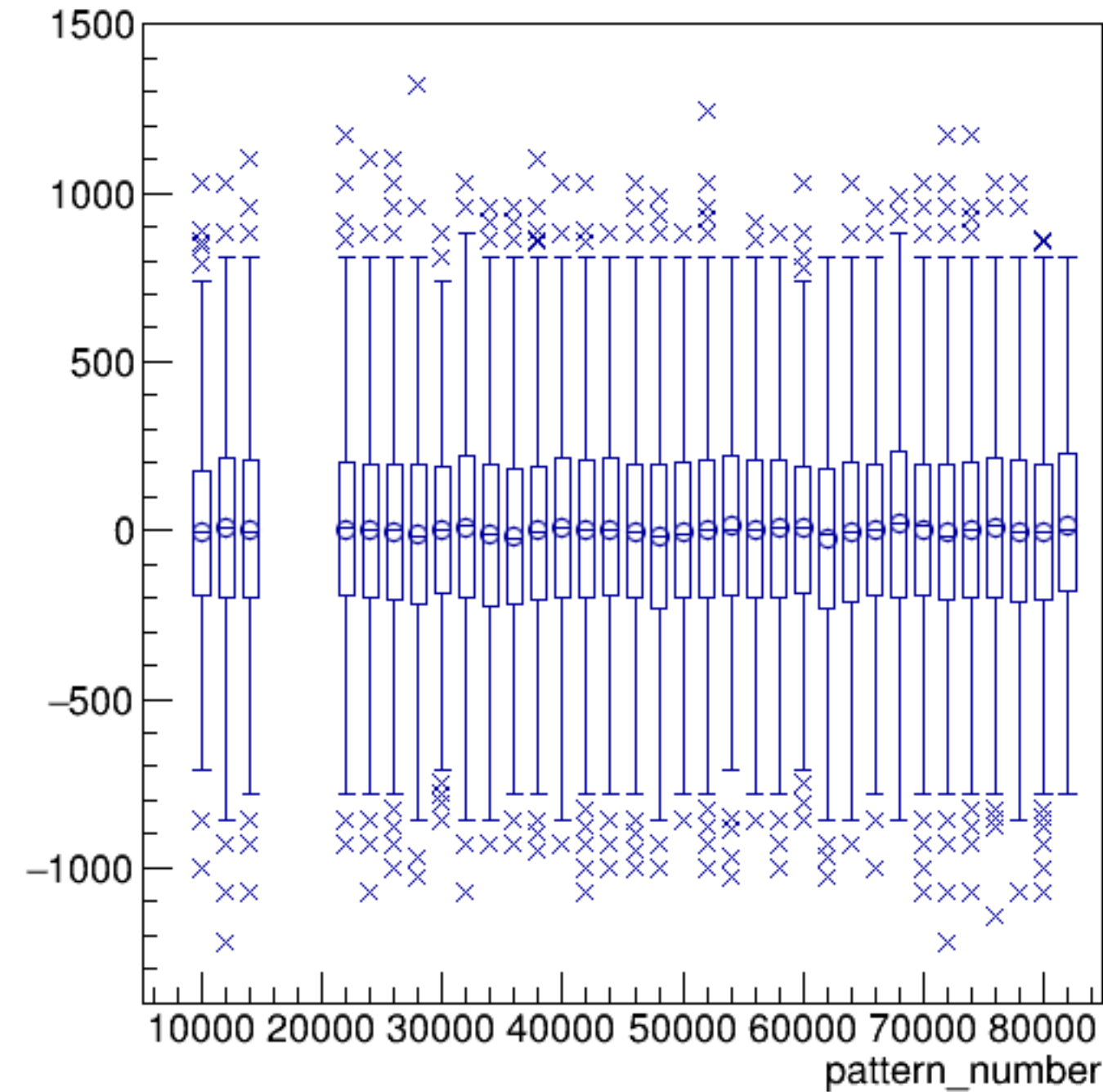
run8526_000_maindet_dsl.png

dsl*CurMon {ErrorFlag==0}

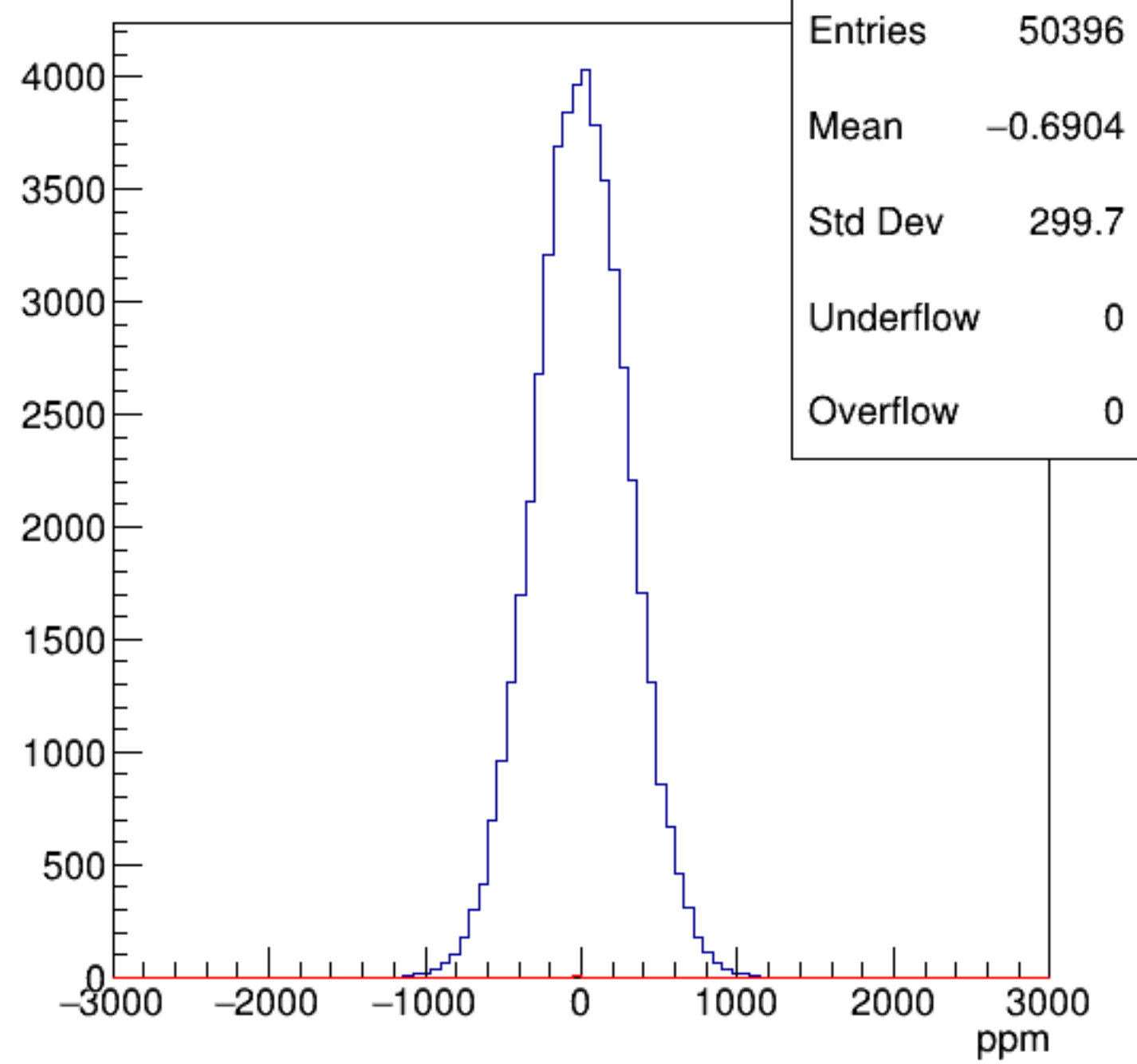


evtTree	
Entries	201667
Mean	-4.331
Std Dev	0.02654

asym_dsl/ppm:pattern_number {ErrorFlag==0}

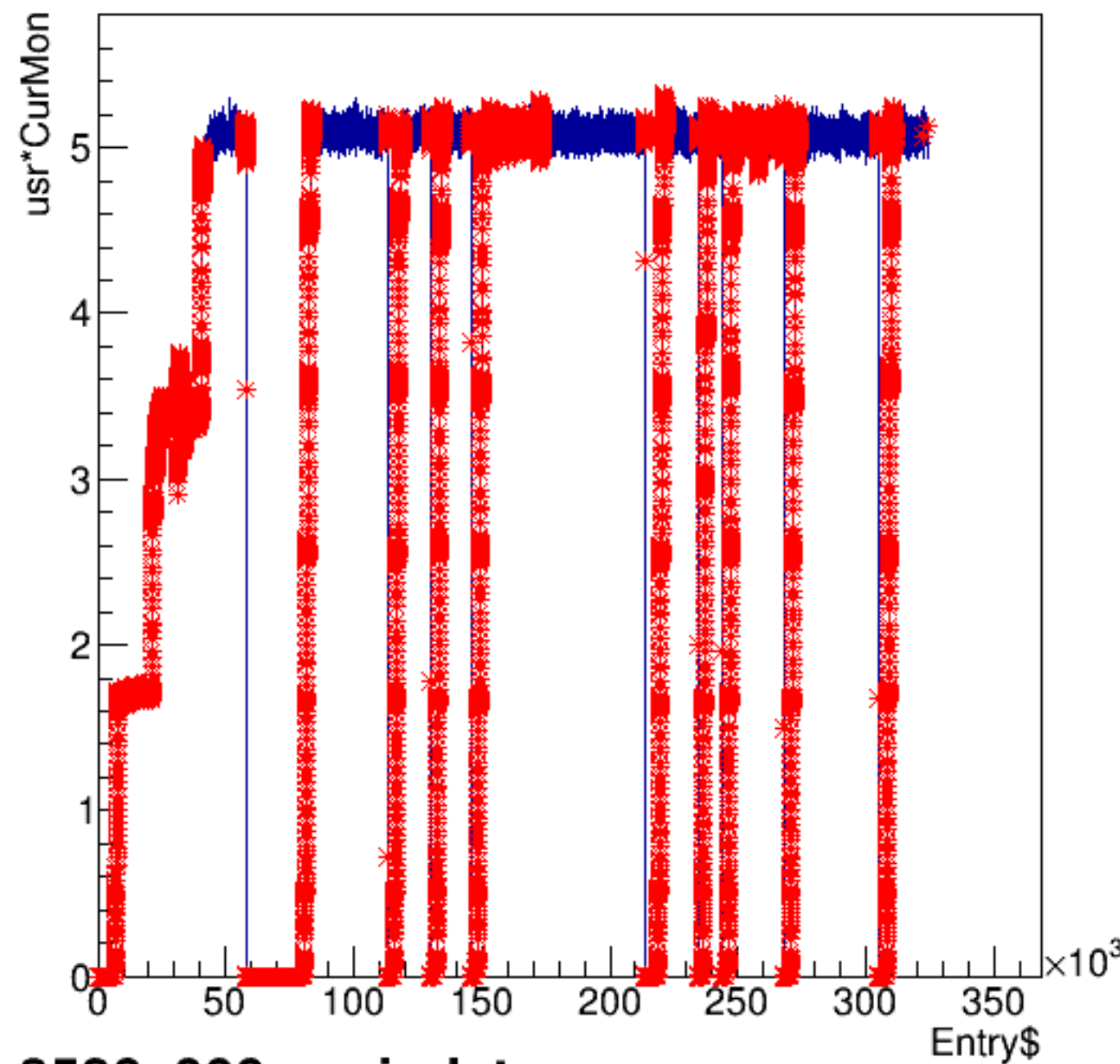


asym_dsl



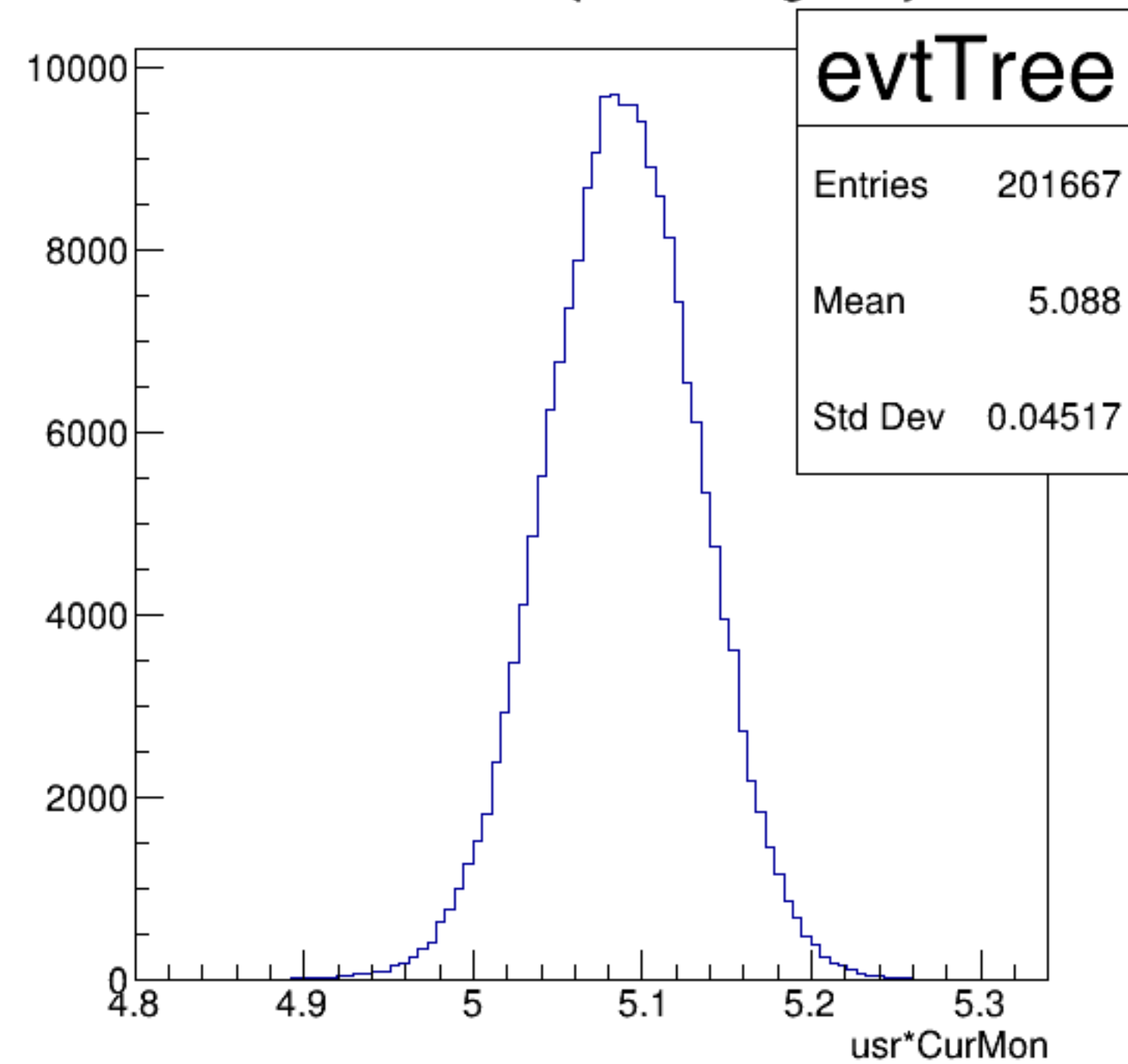
Entries	50396
Mean	-0.6904
Std Dev	299.7
Underflow	0
Overflow	0

usr*CurMon:Entry\$

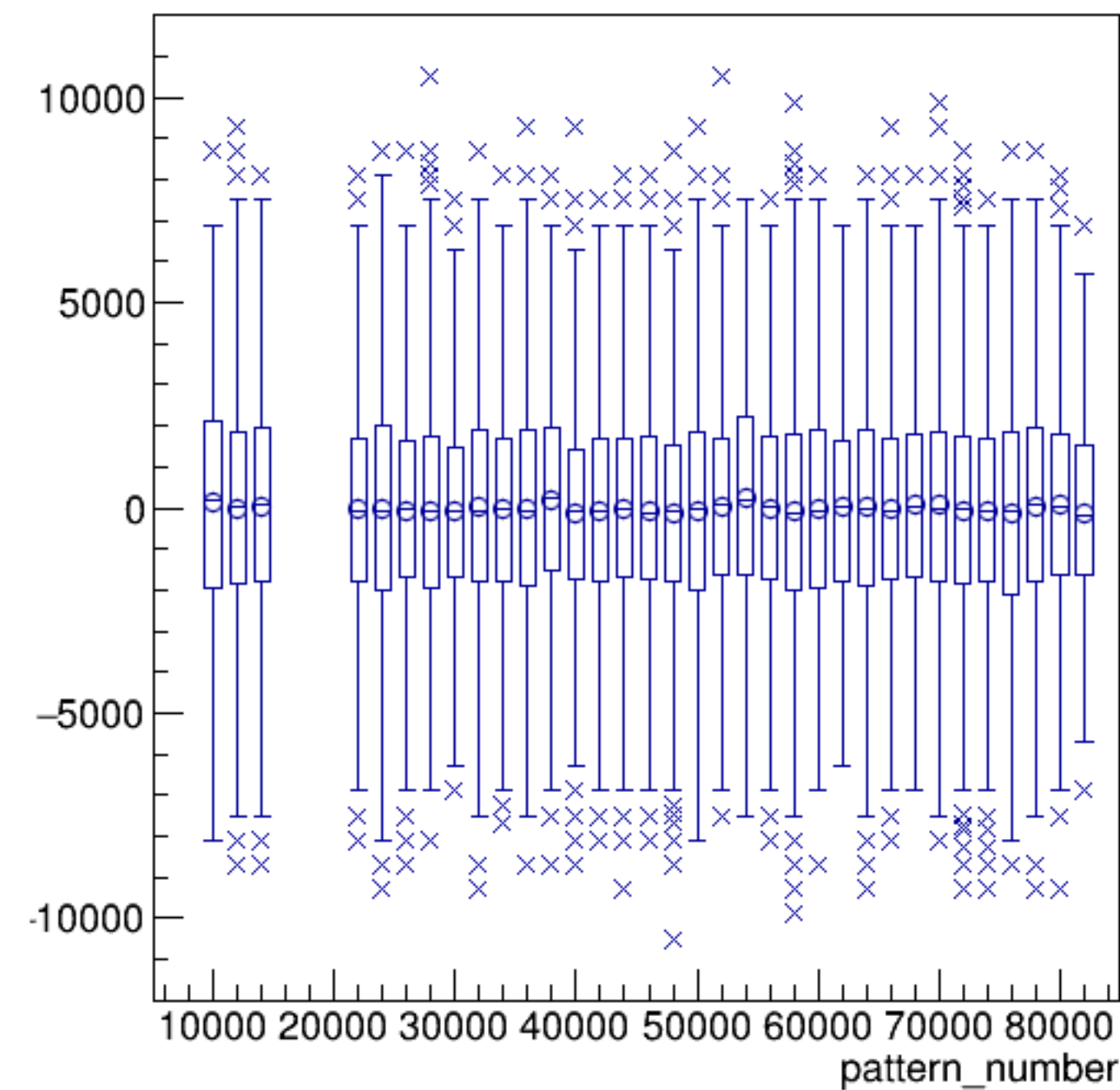


run8526_000_maindet_usr.png

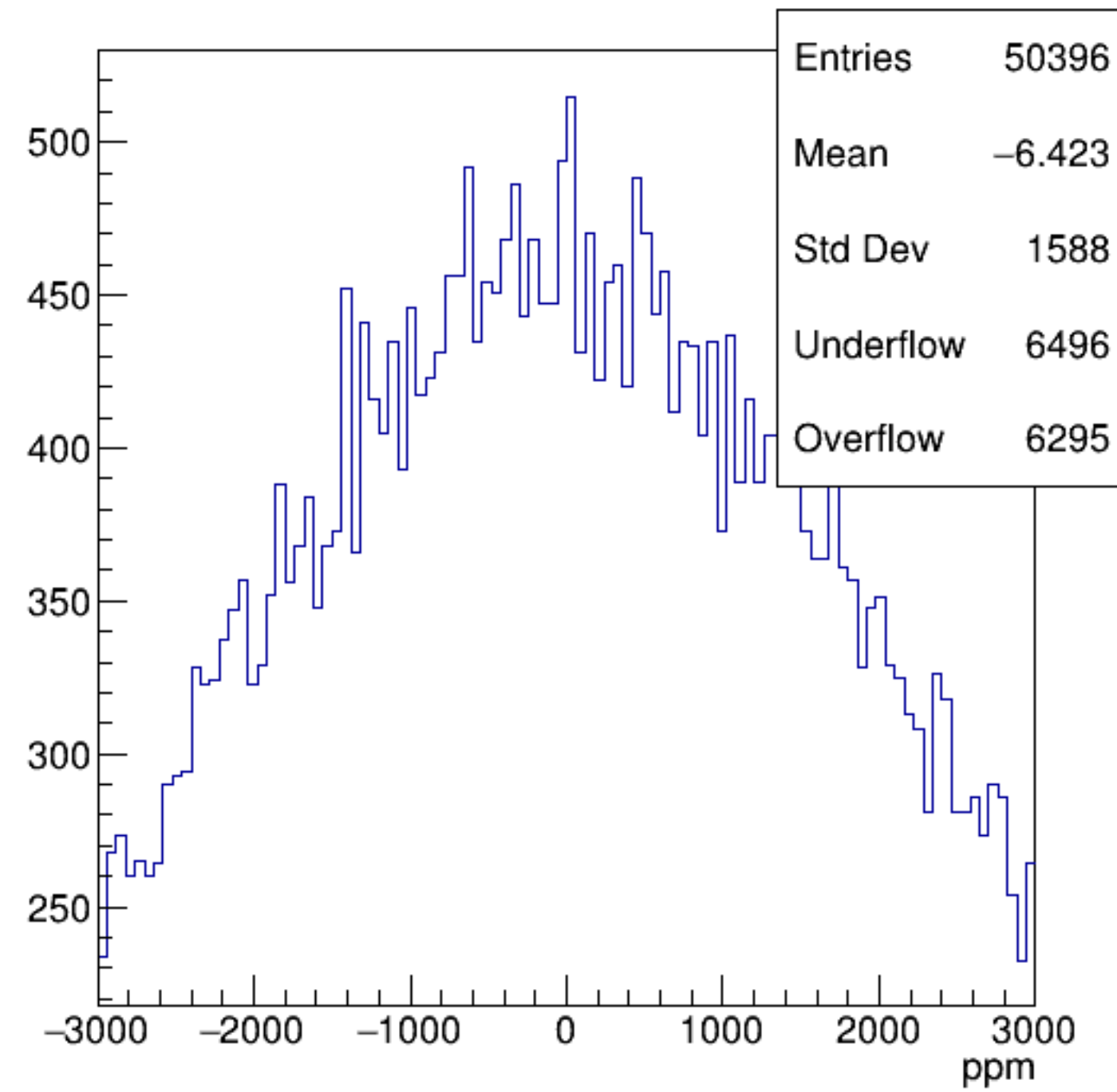
usr*CurMon {ErrorFlag==0}



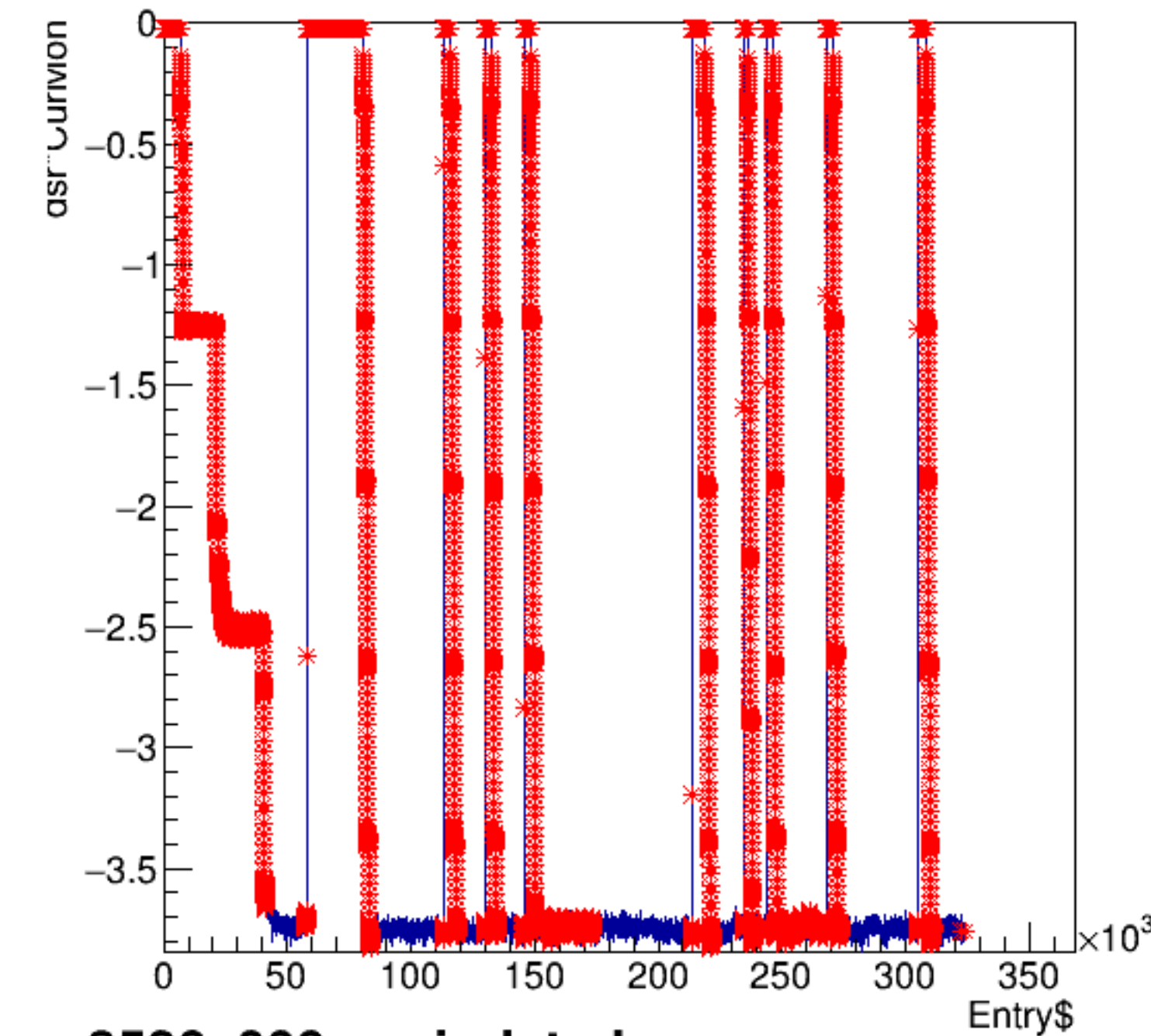
asym_usr/ppm:pattern_number {ErrorFlag==0}



asym_usr

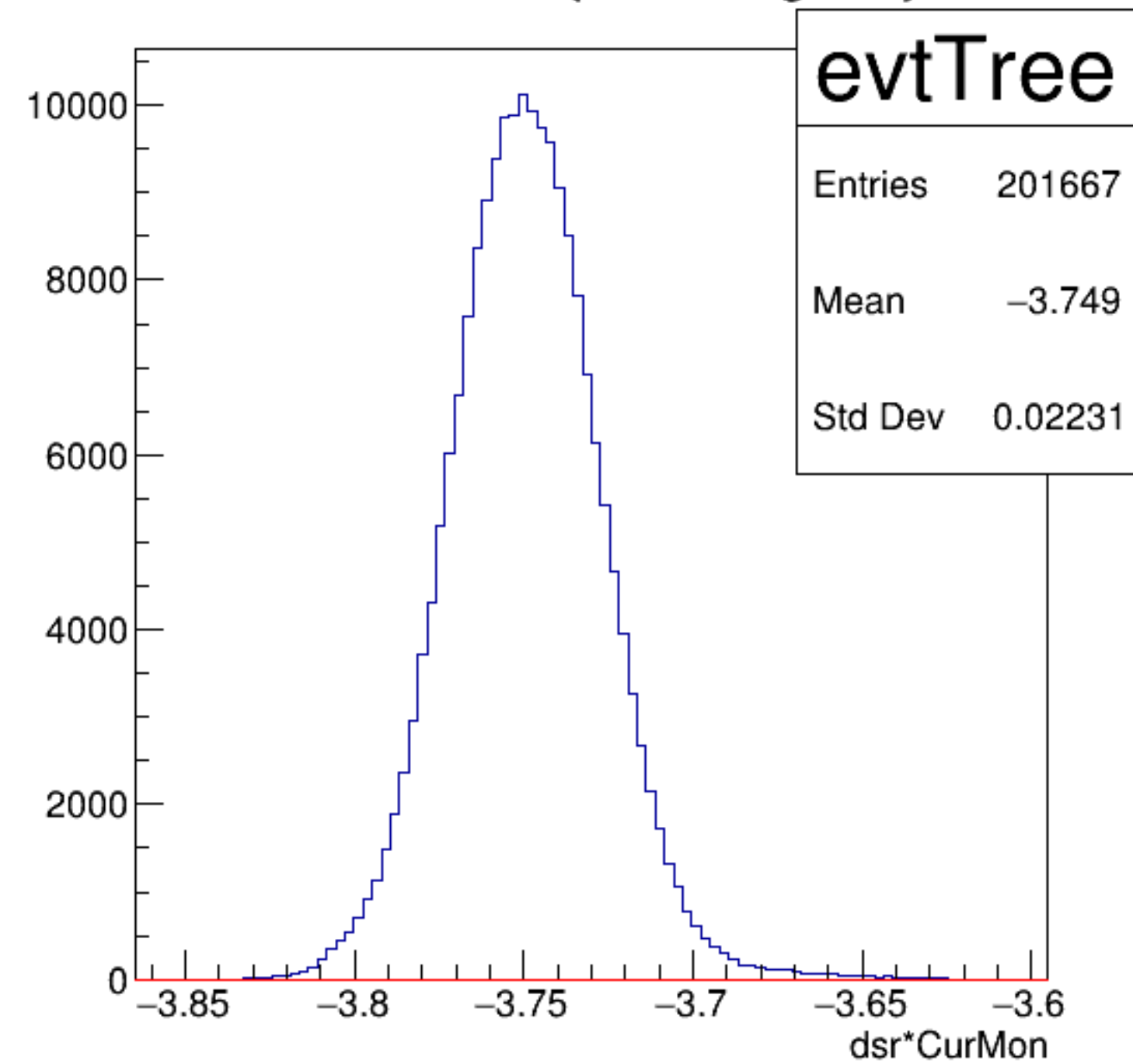


dsr*CurMon:Entry\$

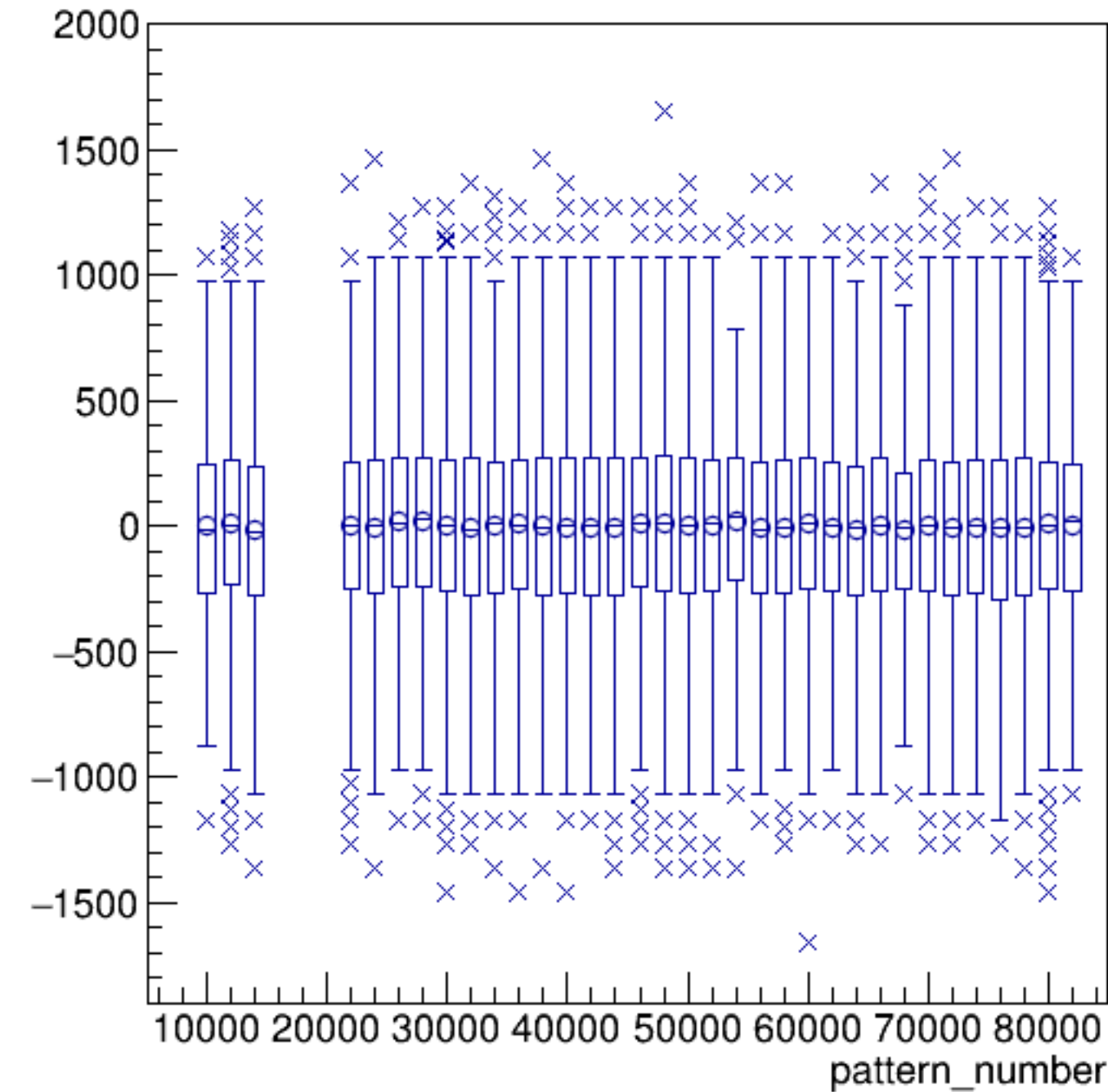


run8526_000_maindet_dsr.png

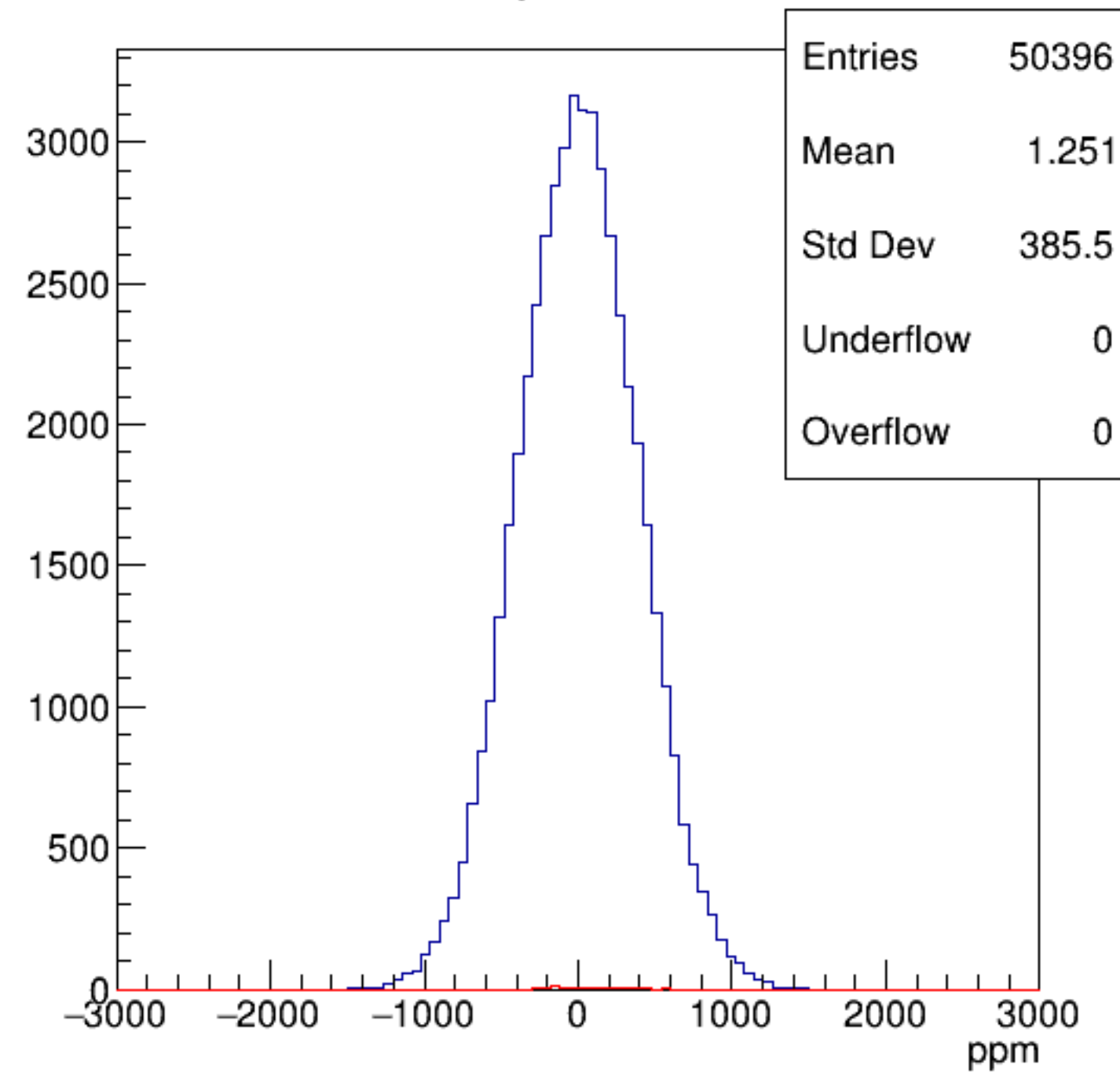
dsr*CurMon {ErrorFlag==0}



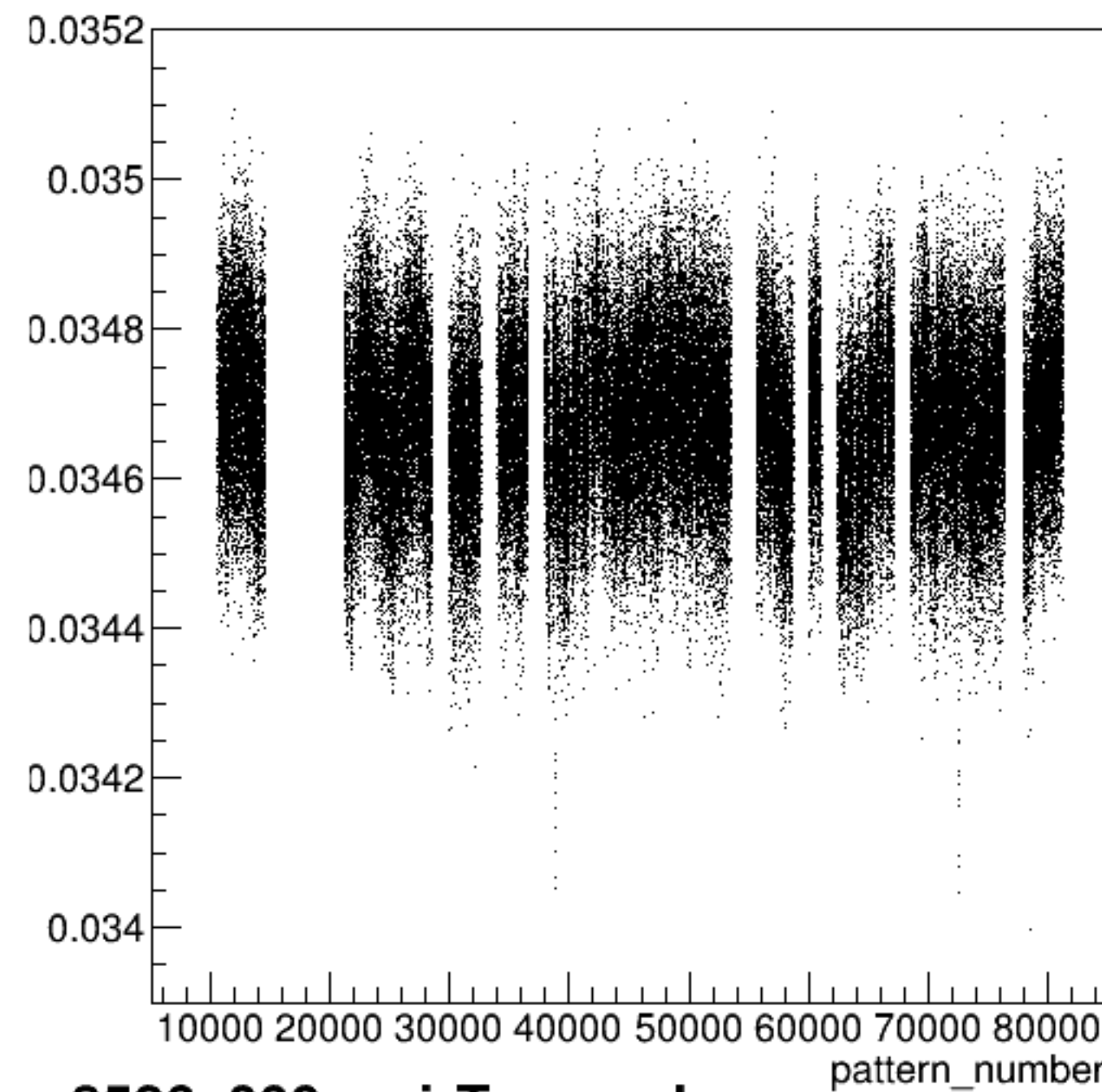
asym_dsr/ppm:pattern_number {ErrorFlag==0}



asym_dsr

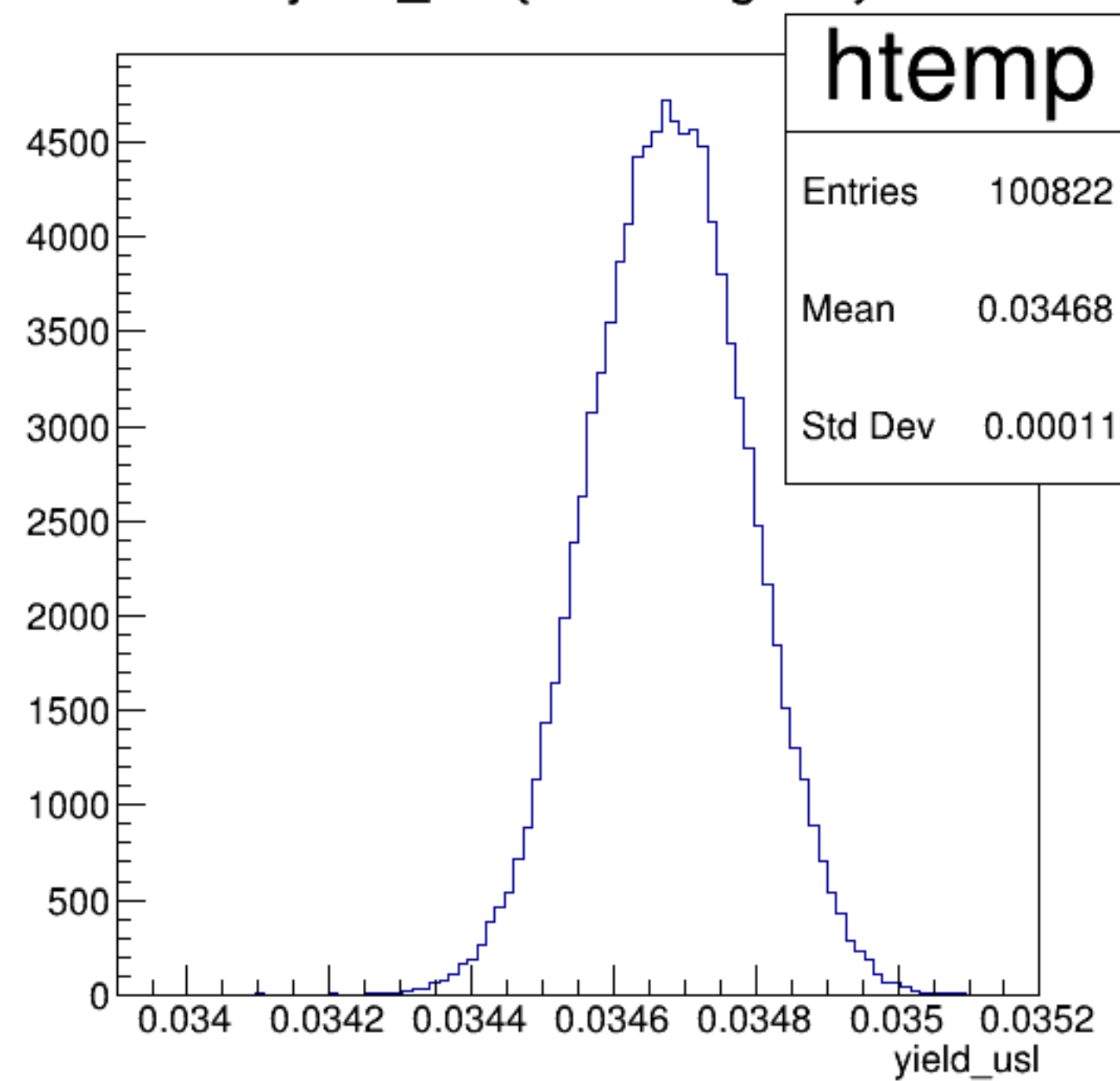


yield_usl:pattern_number {ErrorFlag==0}

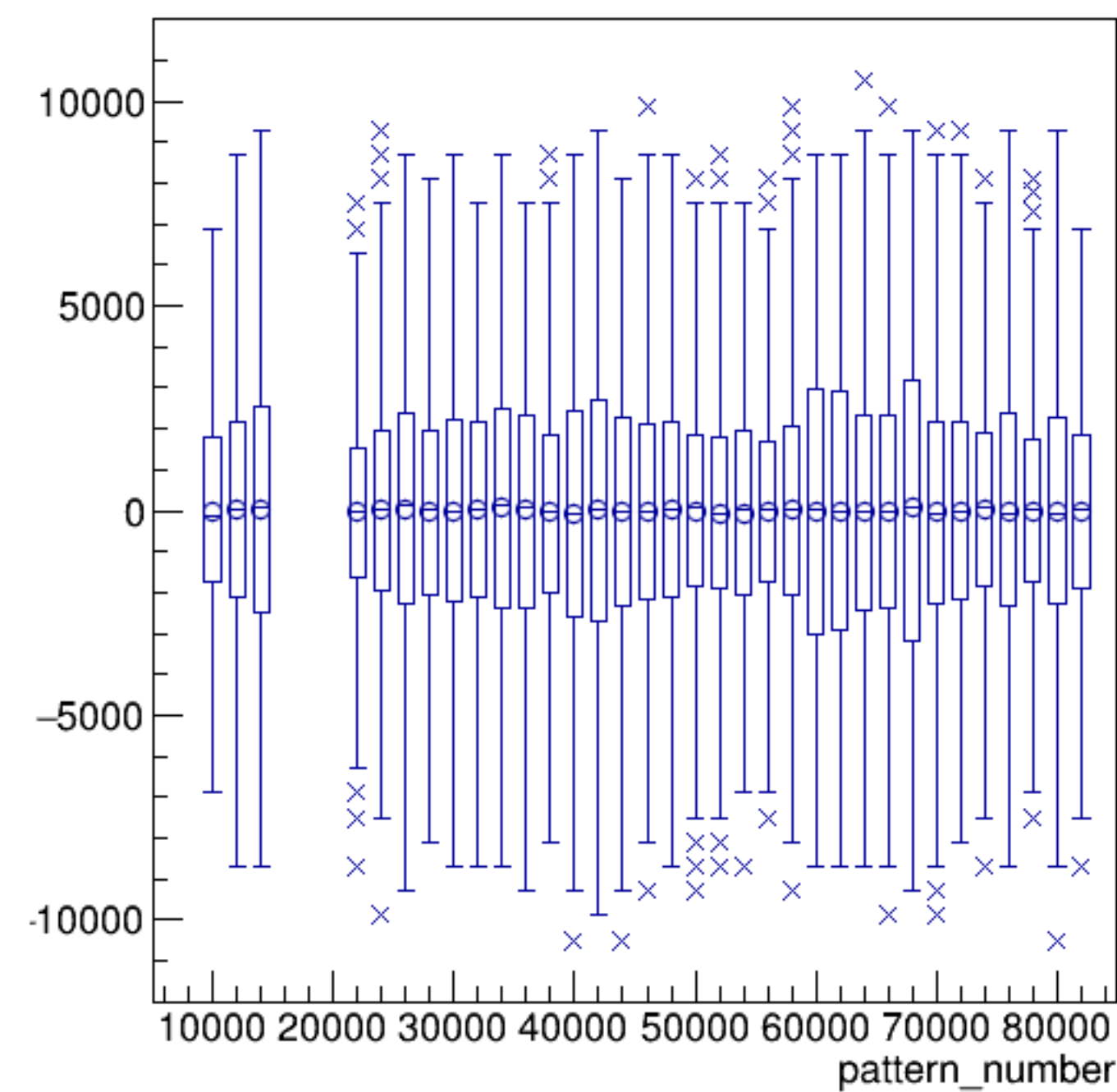


run8526_000_pairTree_usl.png

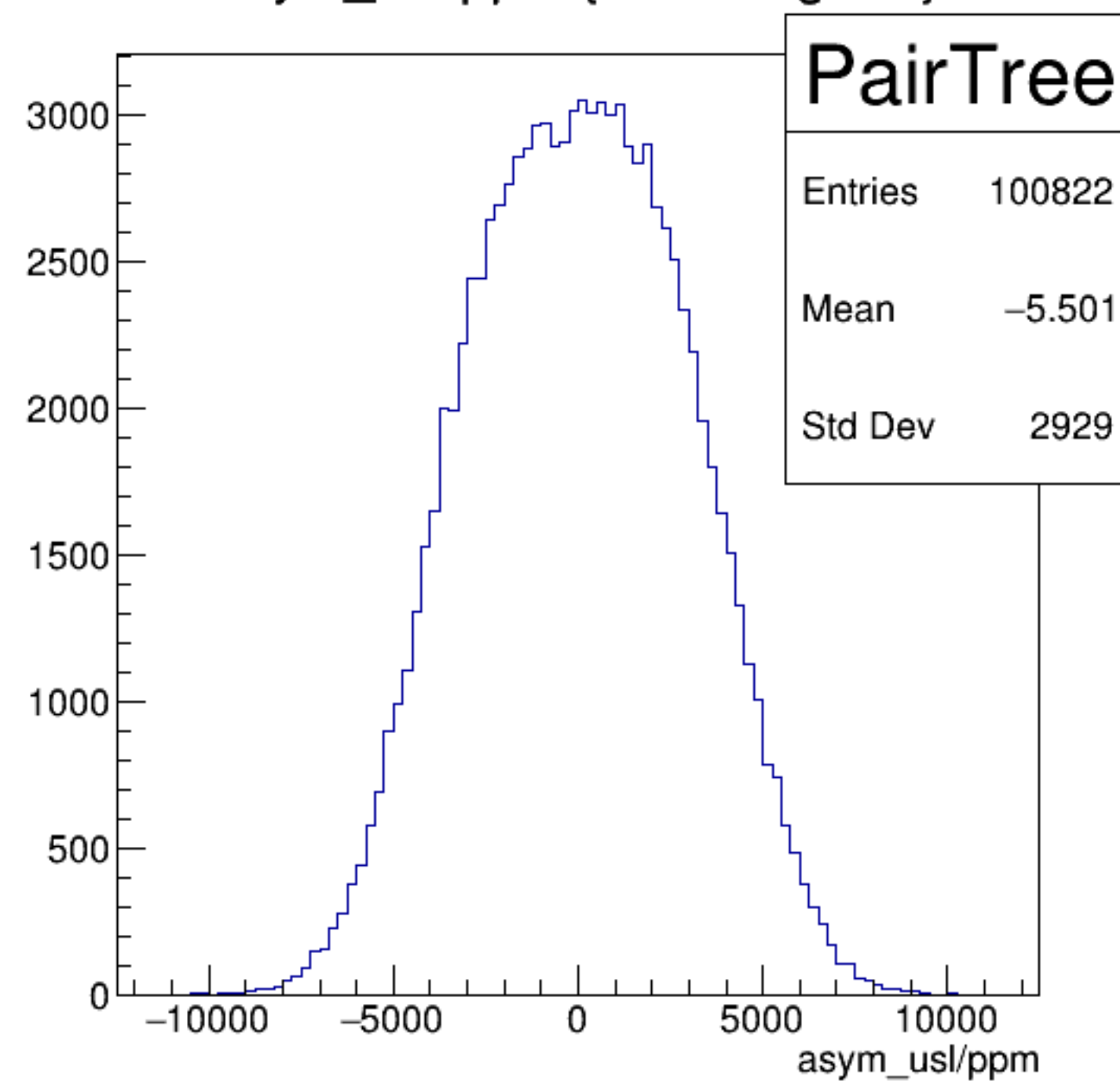
yield_usl {ErrorFlag==0}



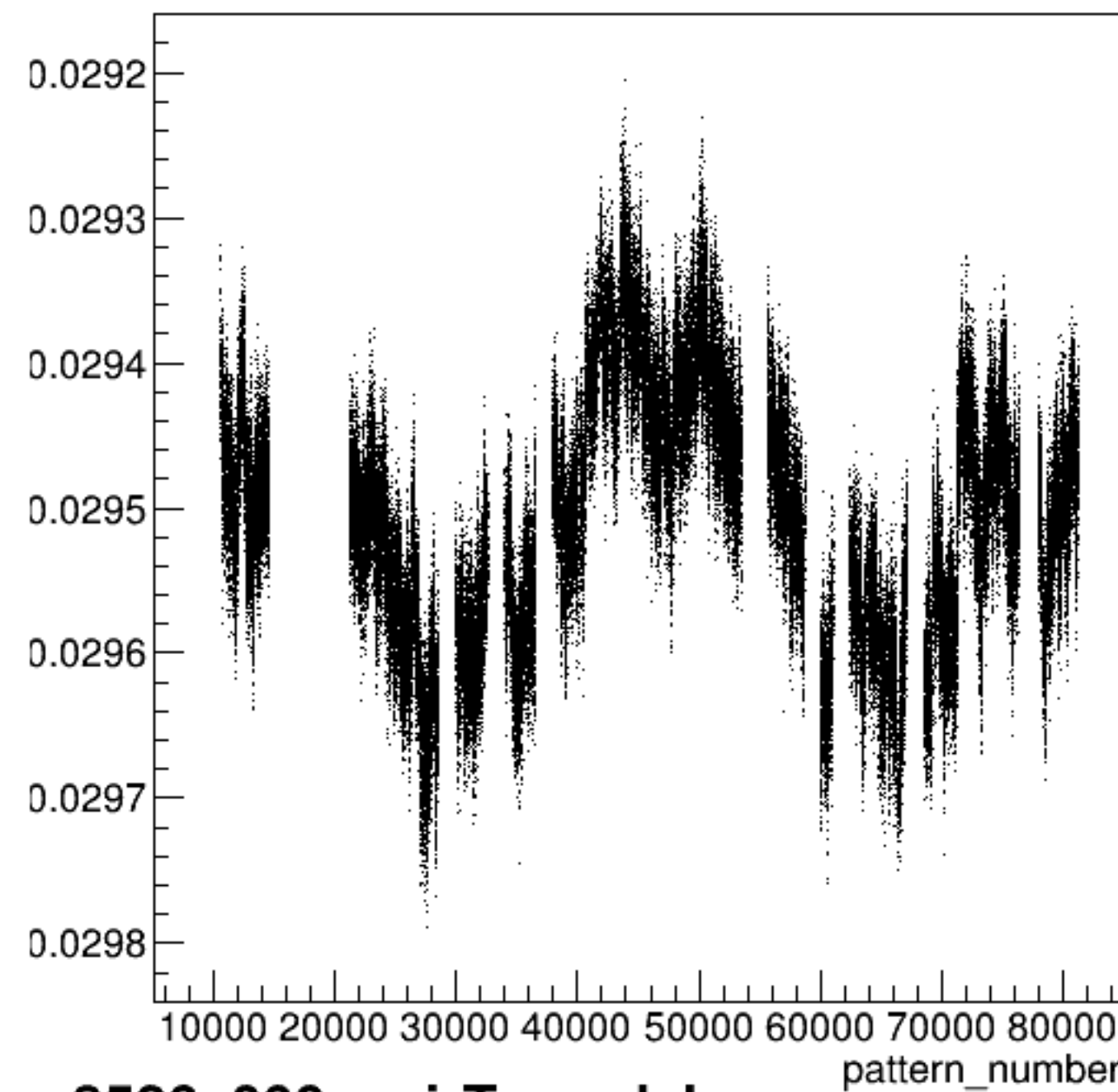
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl/ppm {ErrorFlag==0}

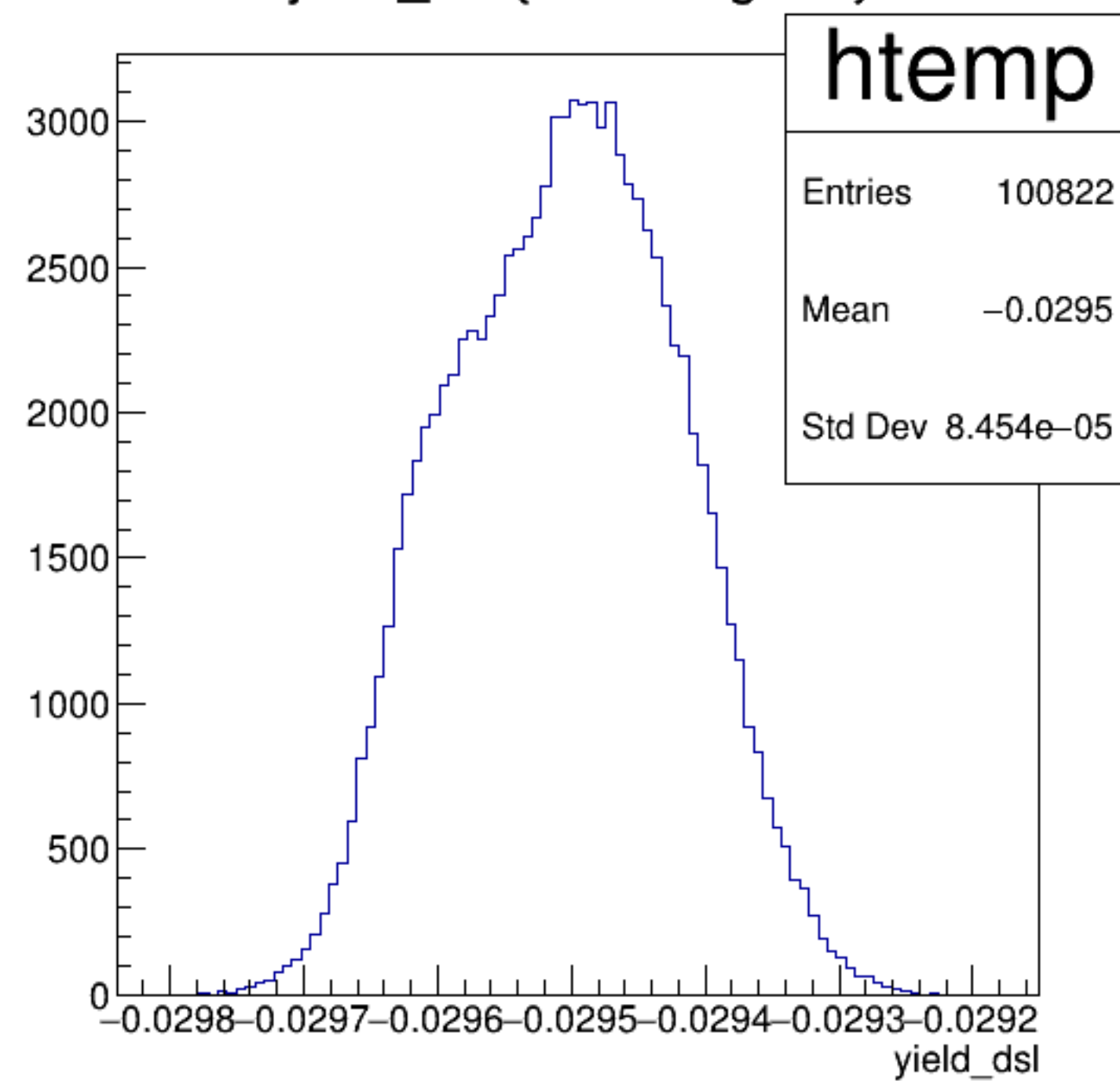


yield_dsl:pattern_number {ErrorFlag==0}

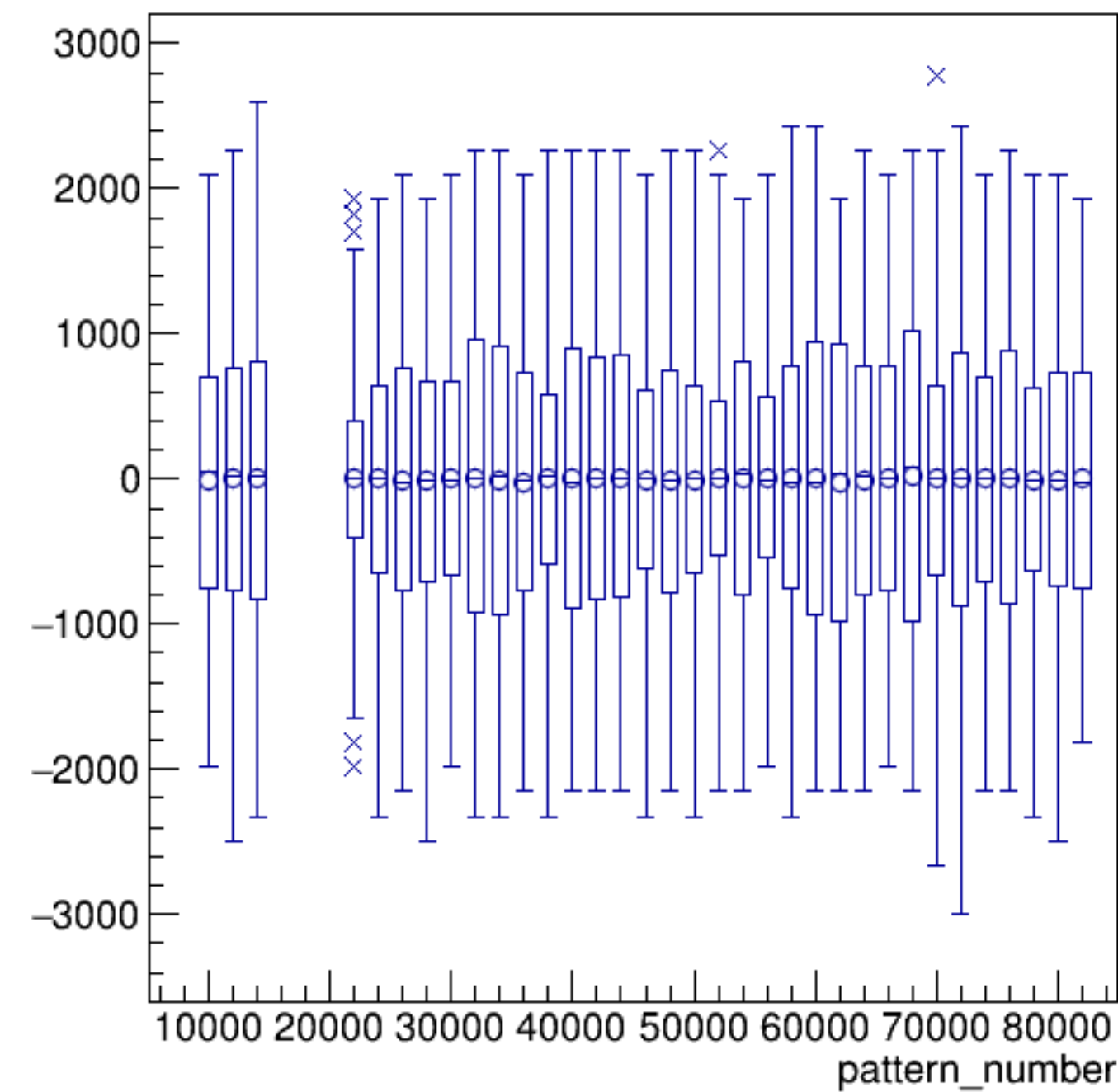


run8526_000_pairTree_dsl.png

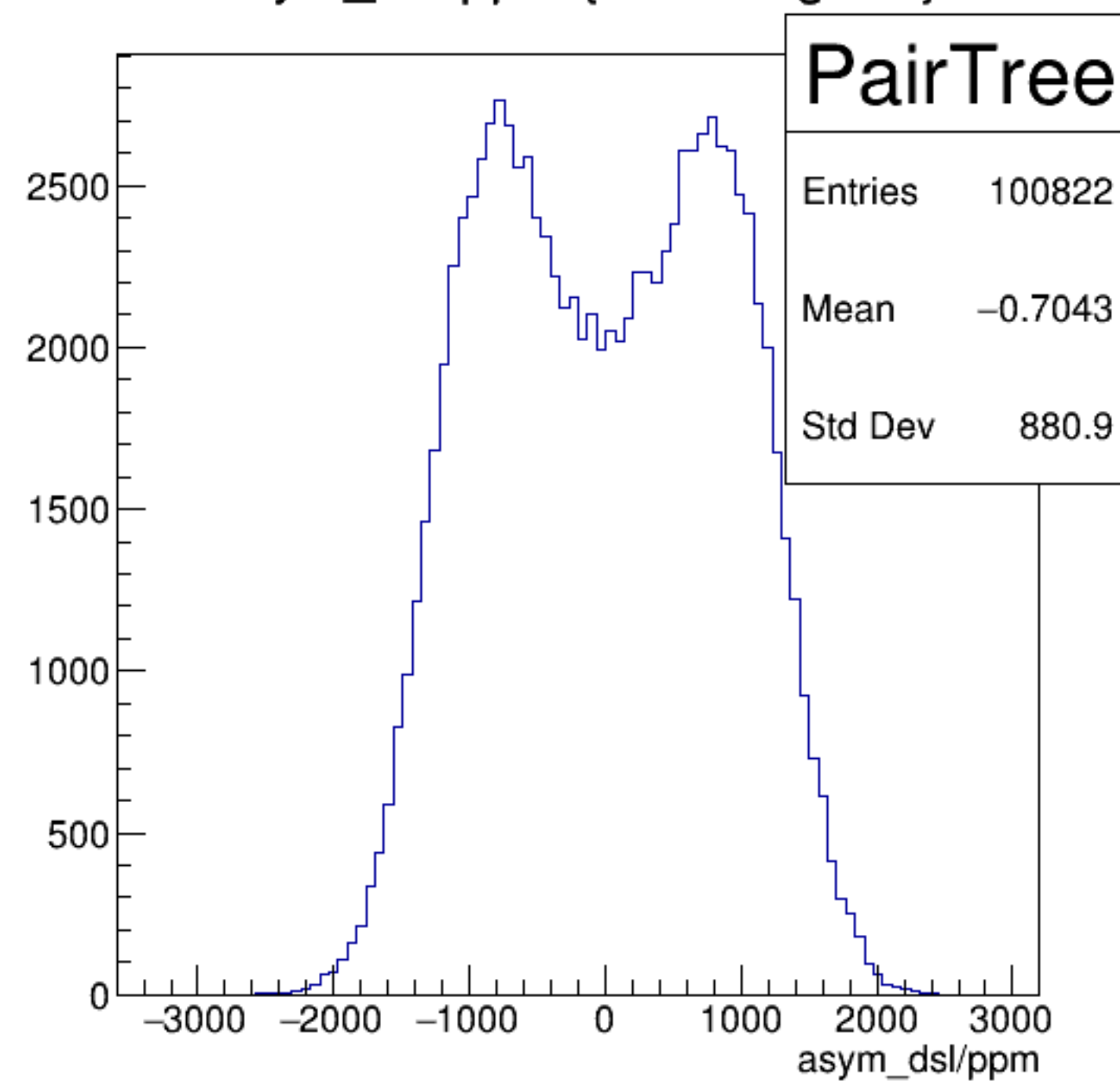
yield_dsl {ErrorFlag==0}



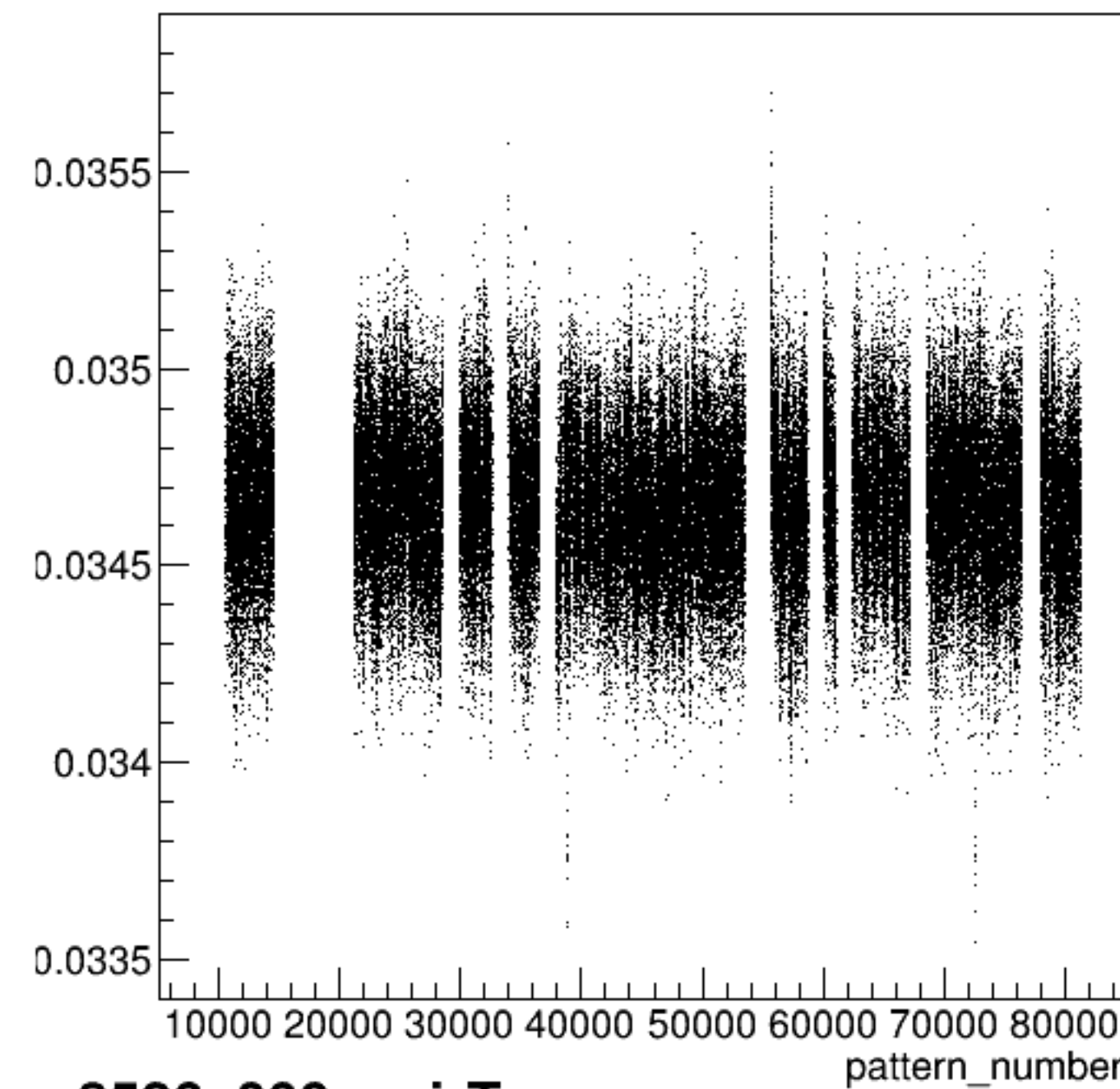
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl/ppm {ErrorFlag==0}

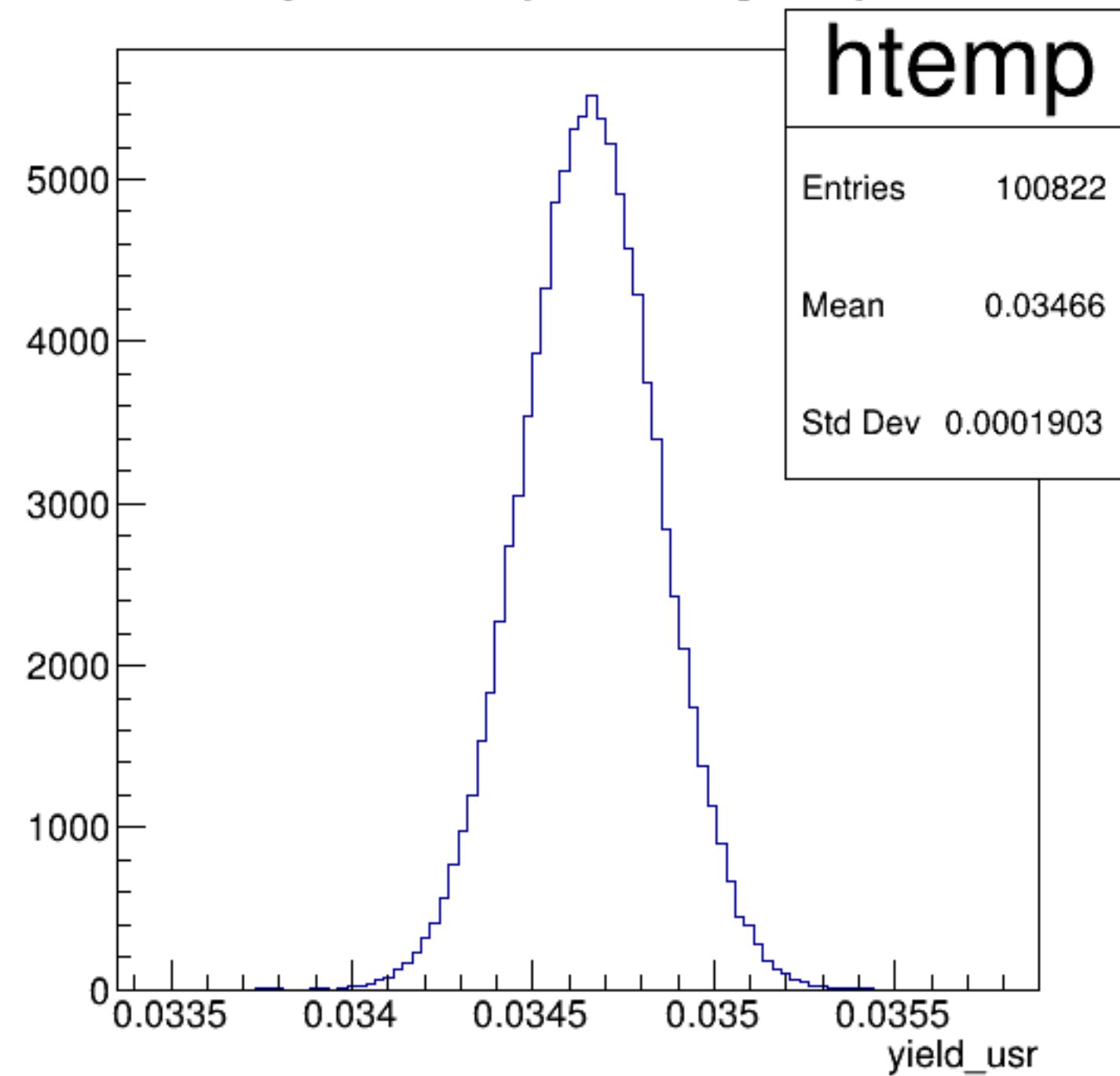


yield_usr:pattern_number {ErrorFlag==0}

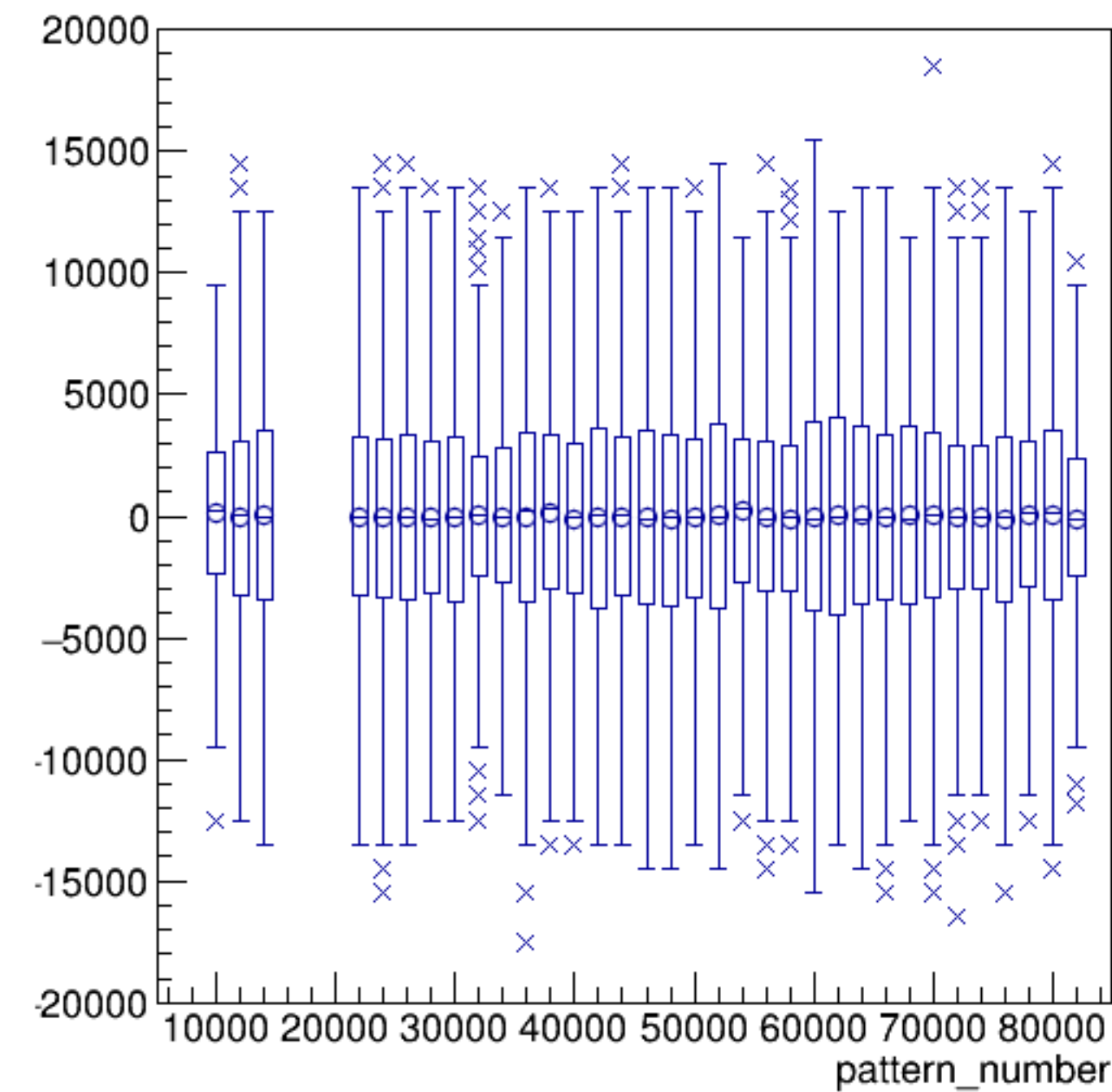


run8526_000_pairTree_usr.png

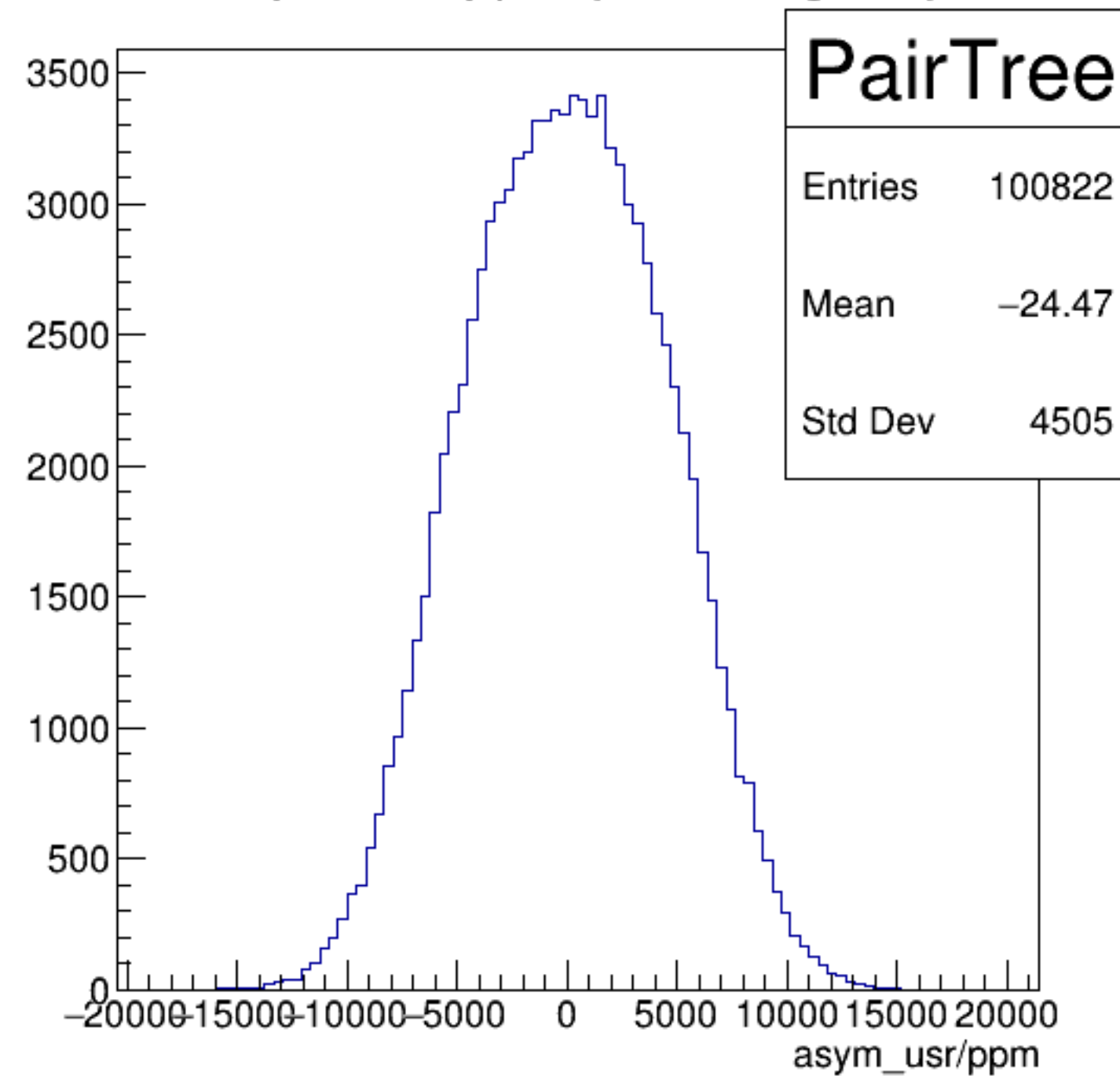
yield_usr {ErrorFlag==0}



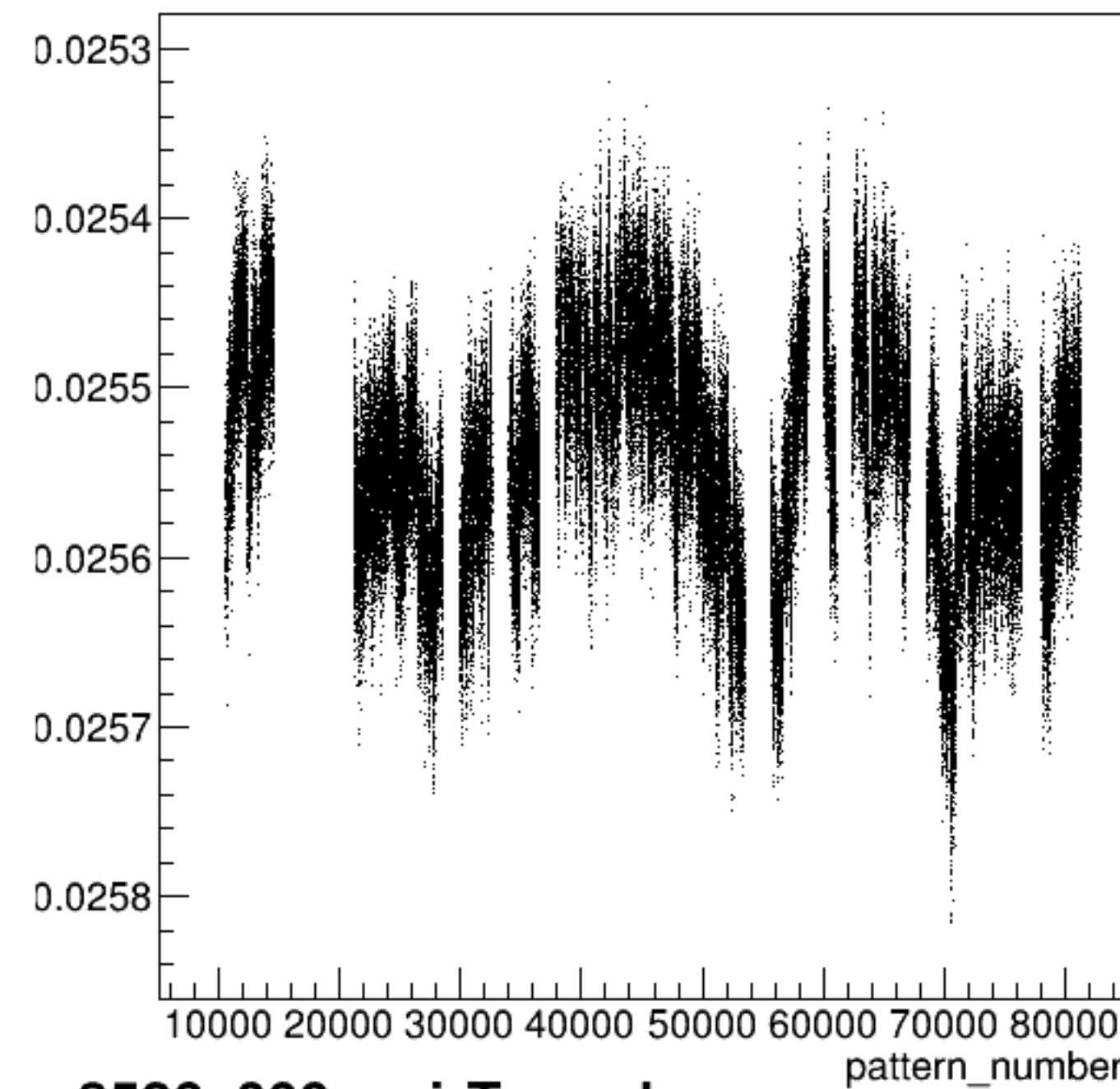
asym_usr/ppm:pattern_number {ErrorFlag==0}



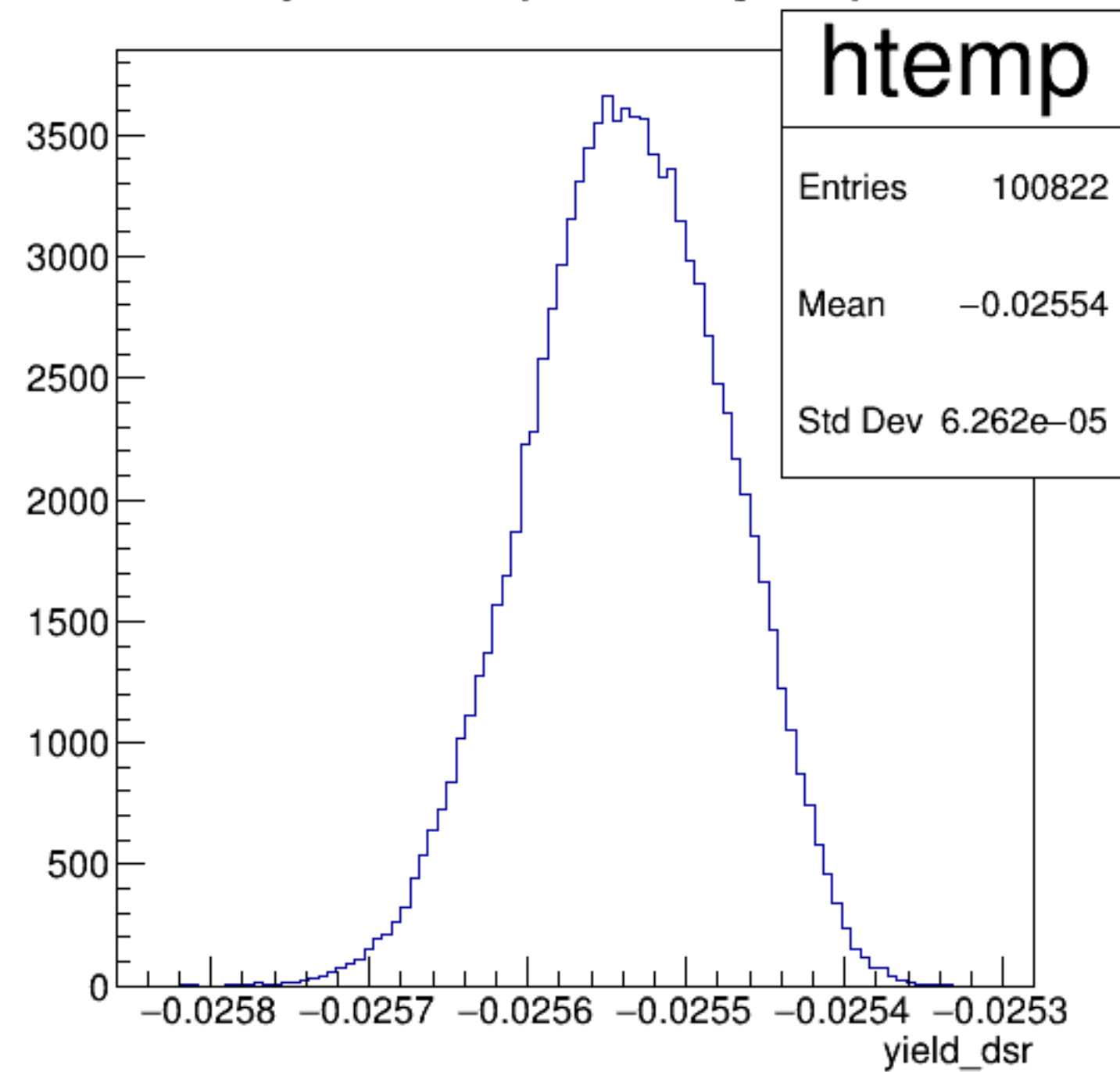
asym_usr/ppm {ErrorFlag==0}



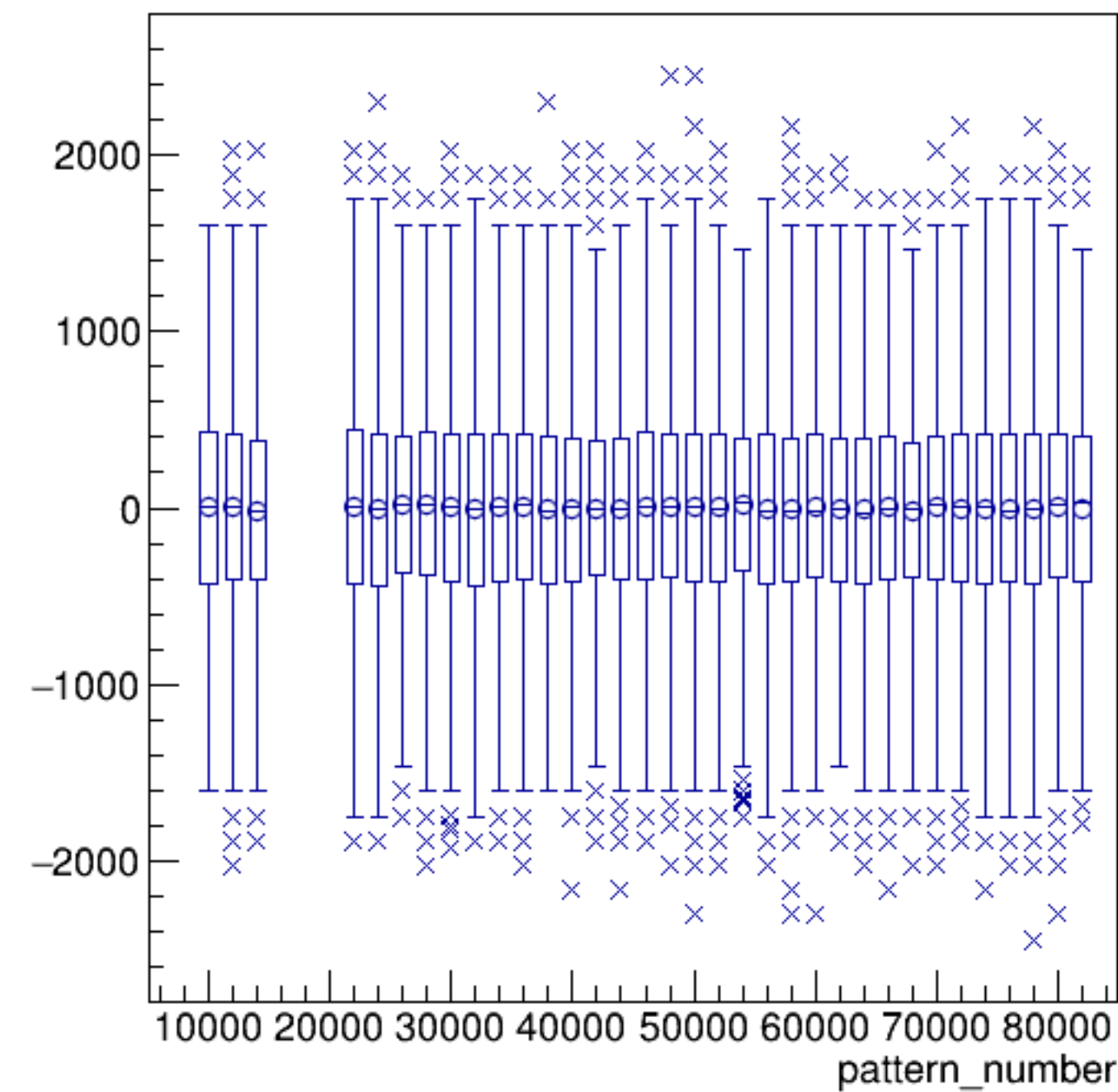
yield_dsr:pattern_number {ErrorFlag==0}



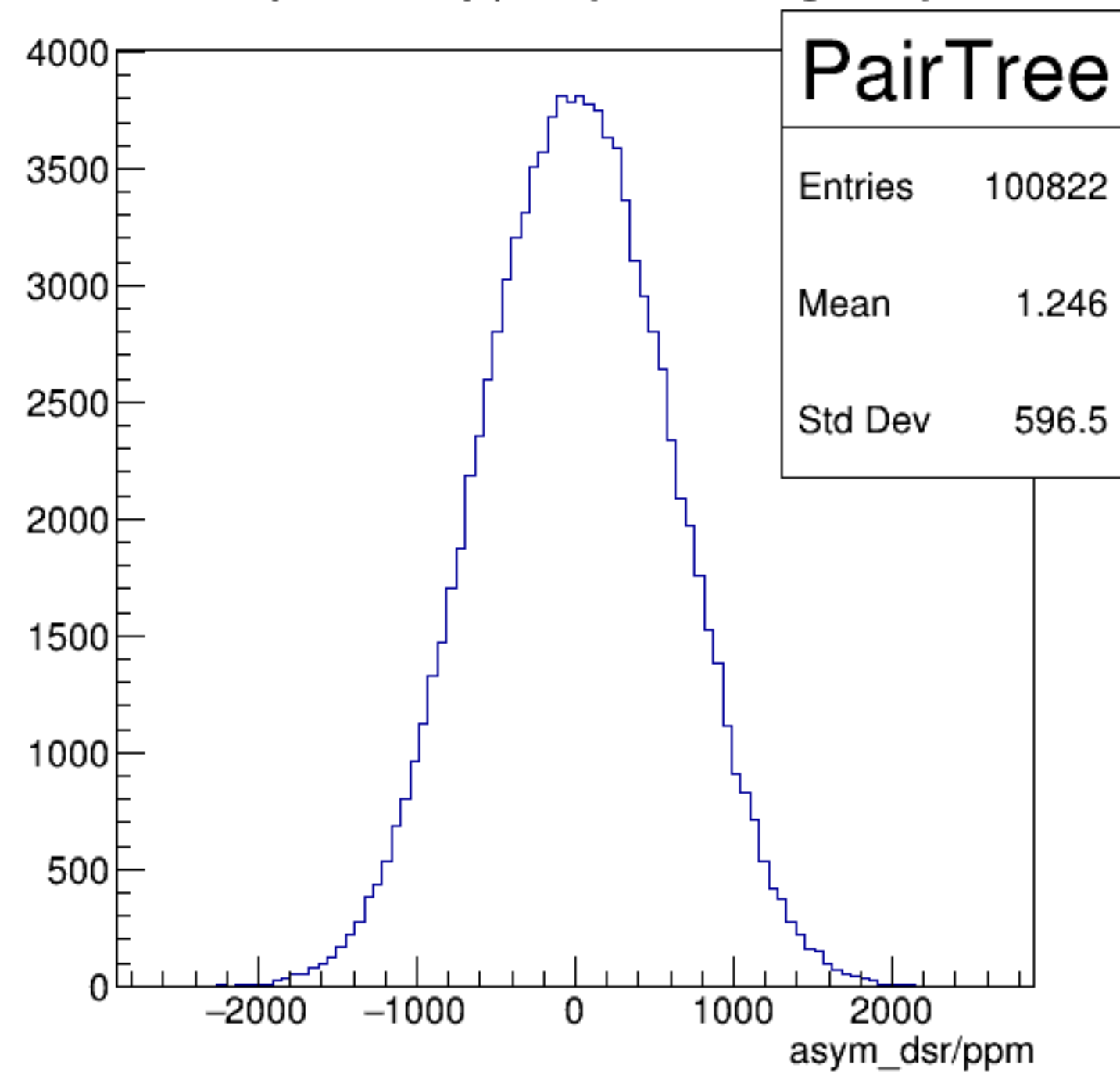
yield_dsr {ErrorFlag==0}



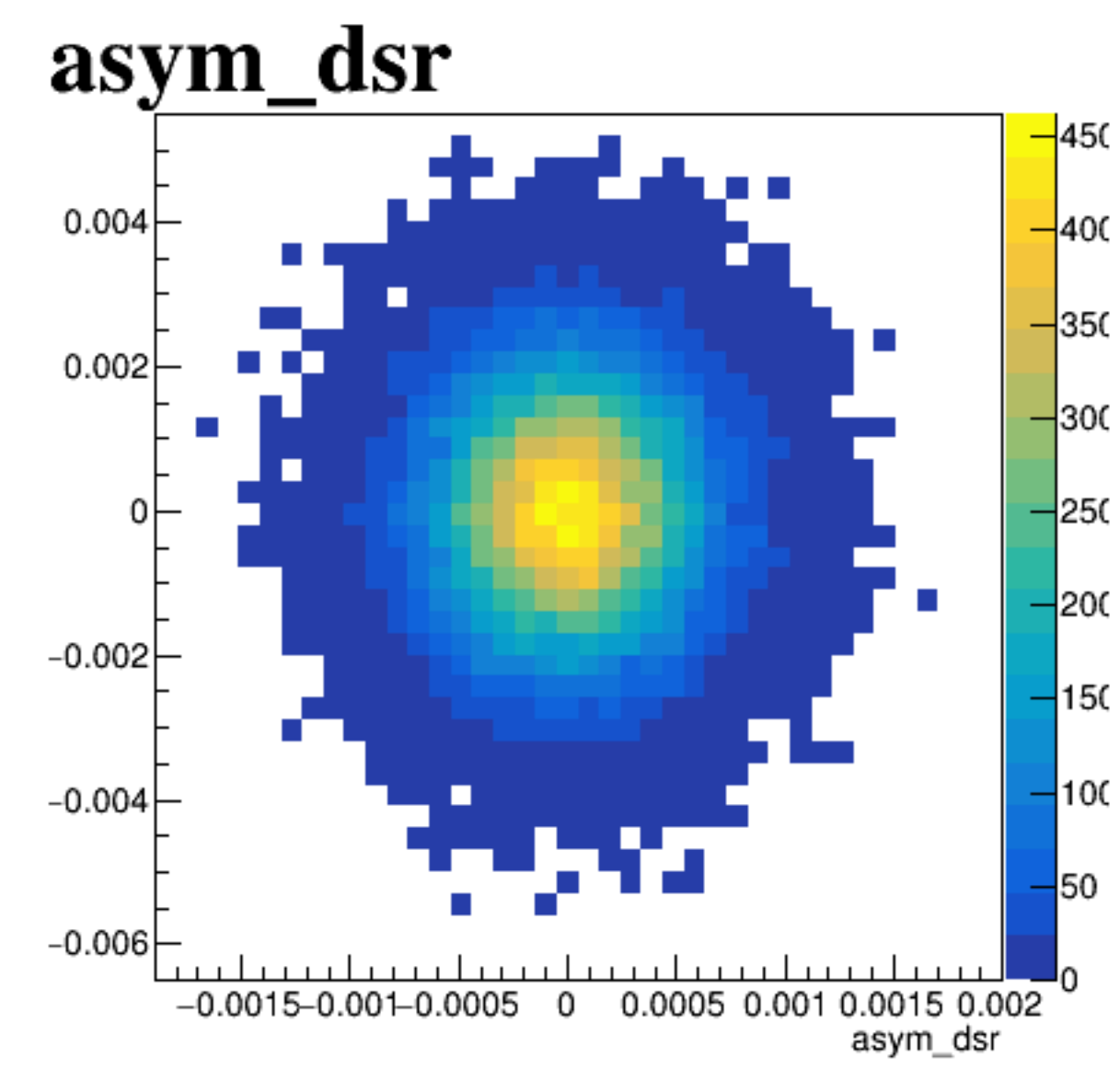
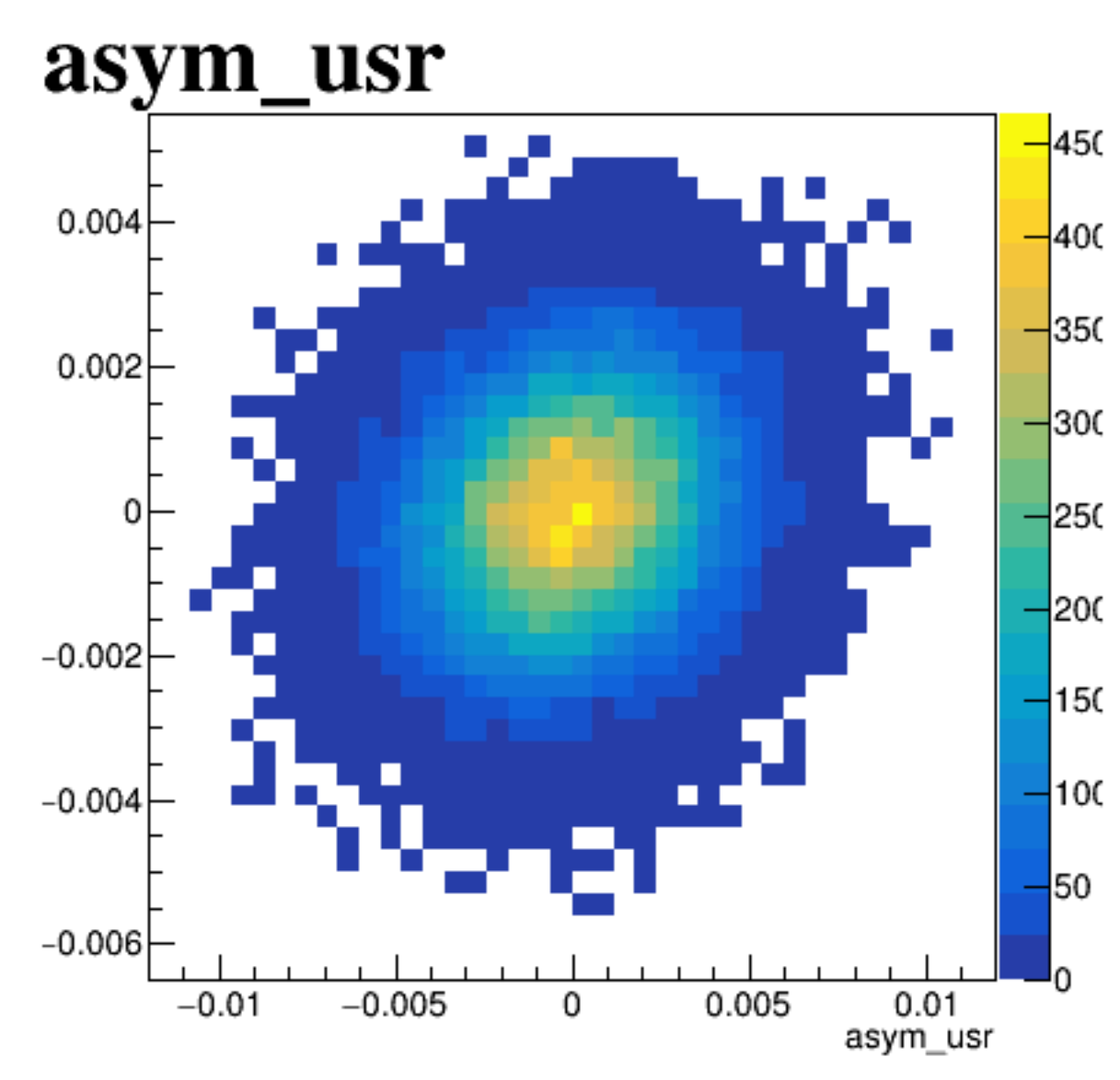
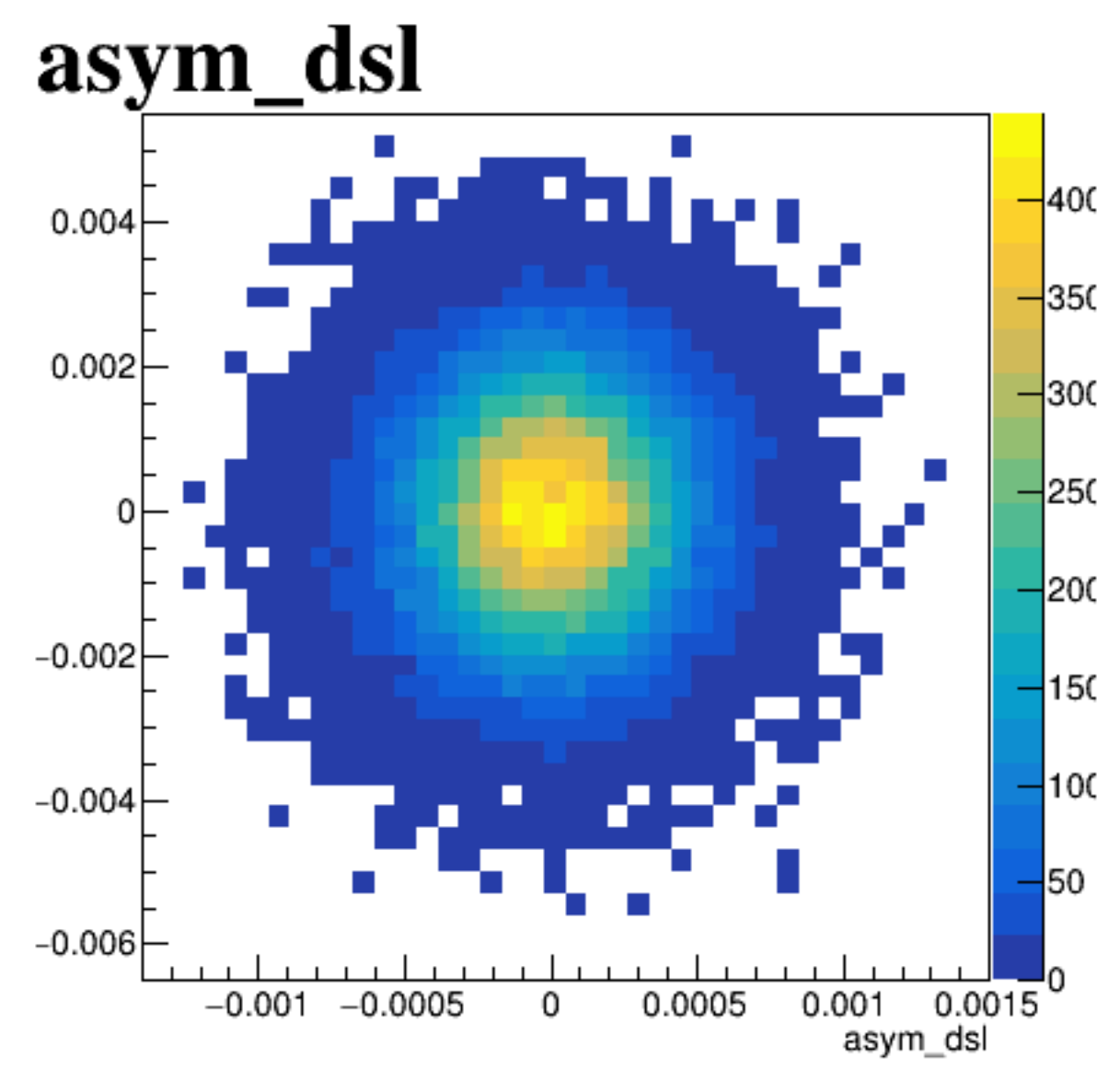
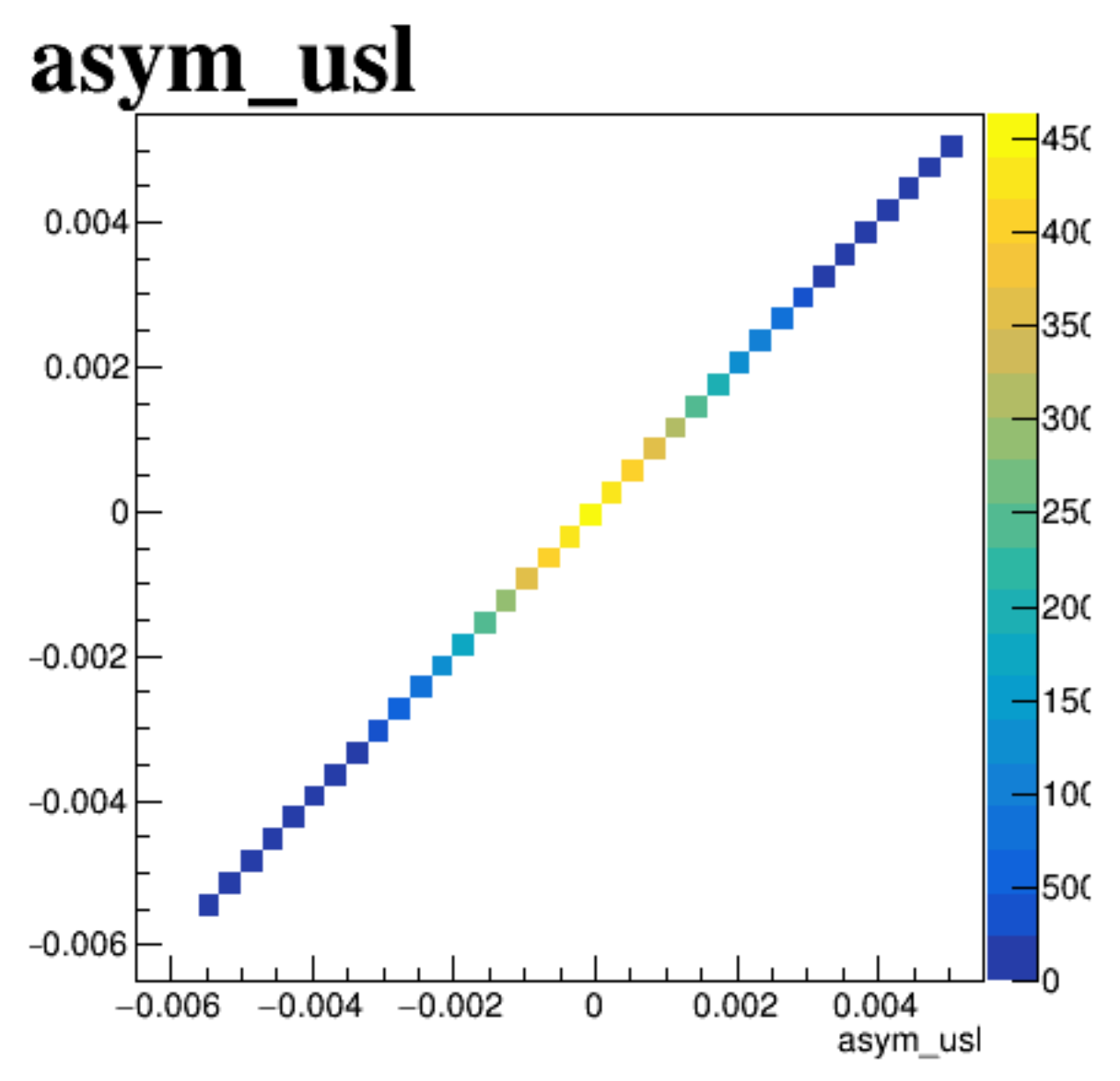
asym_dsr/ppm:pattern_number {ErrorFlag==0}



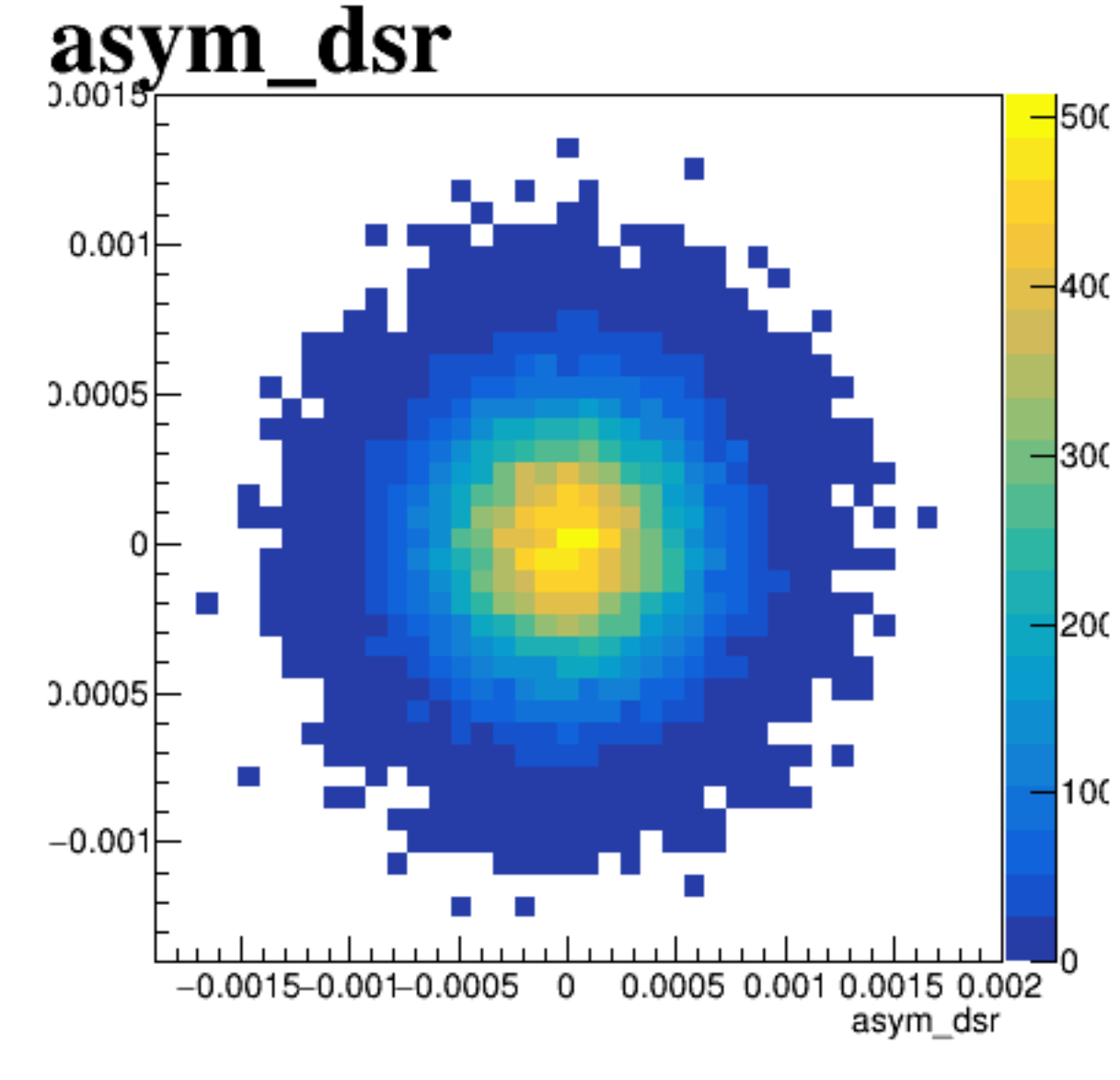
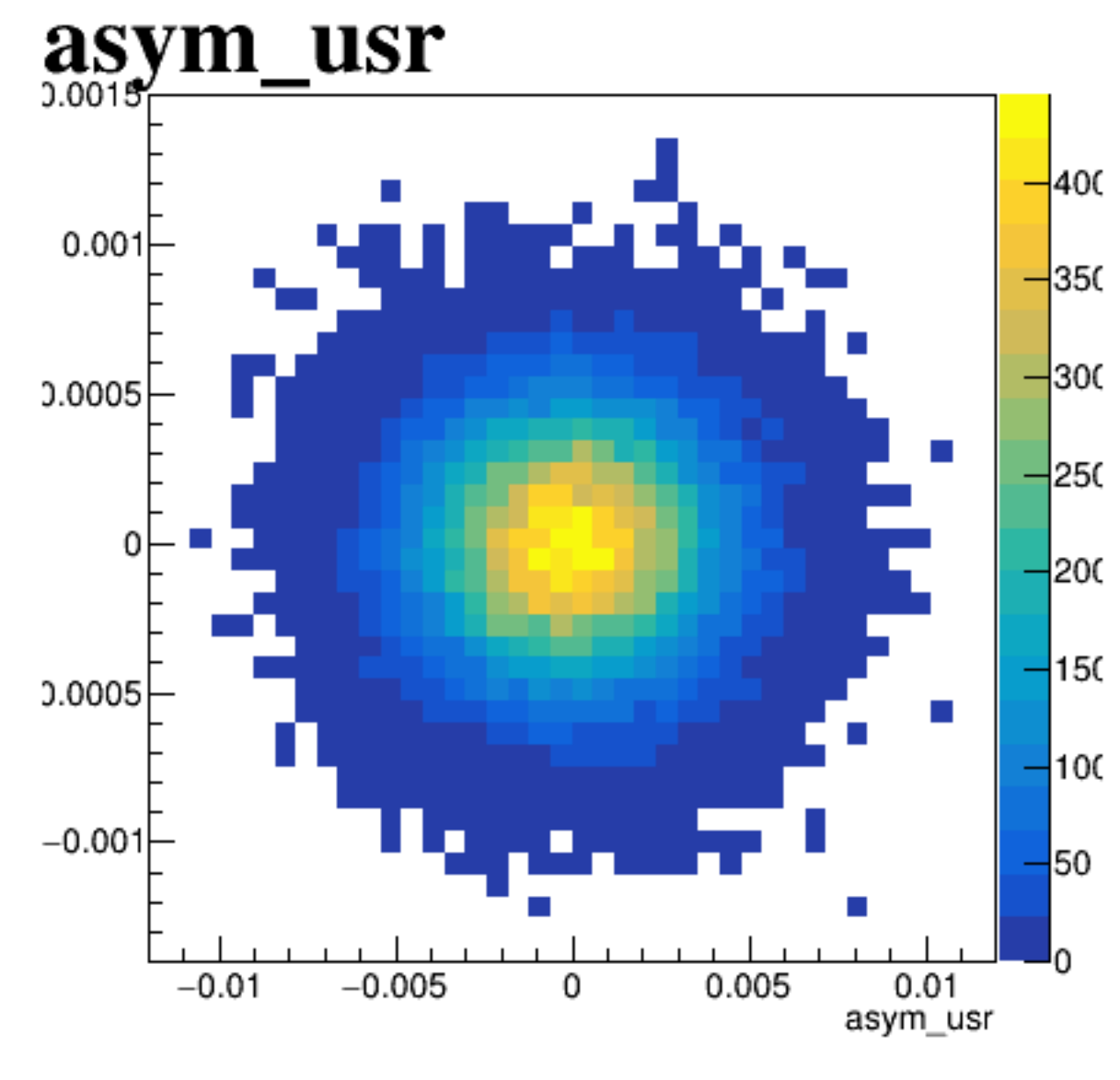
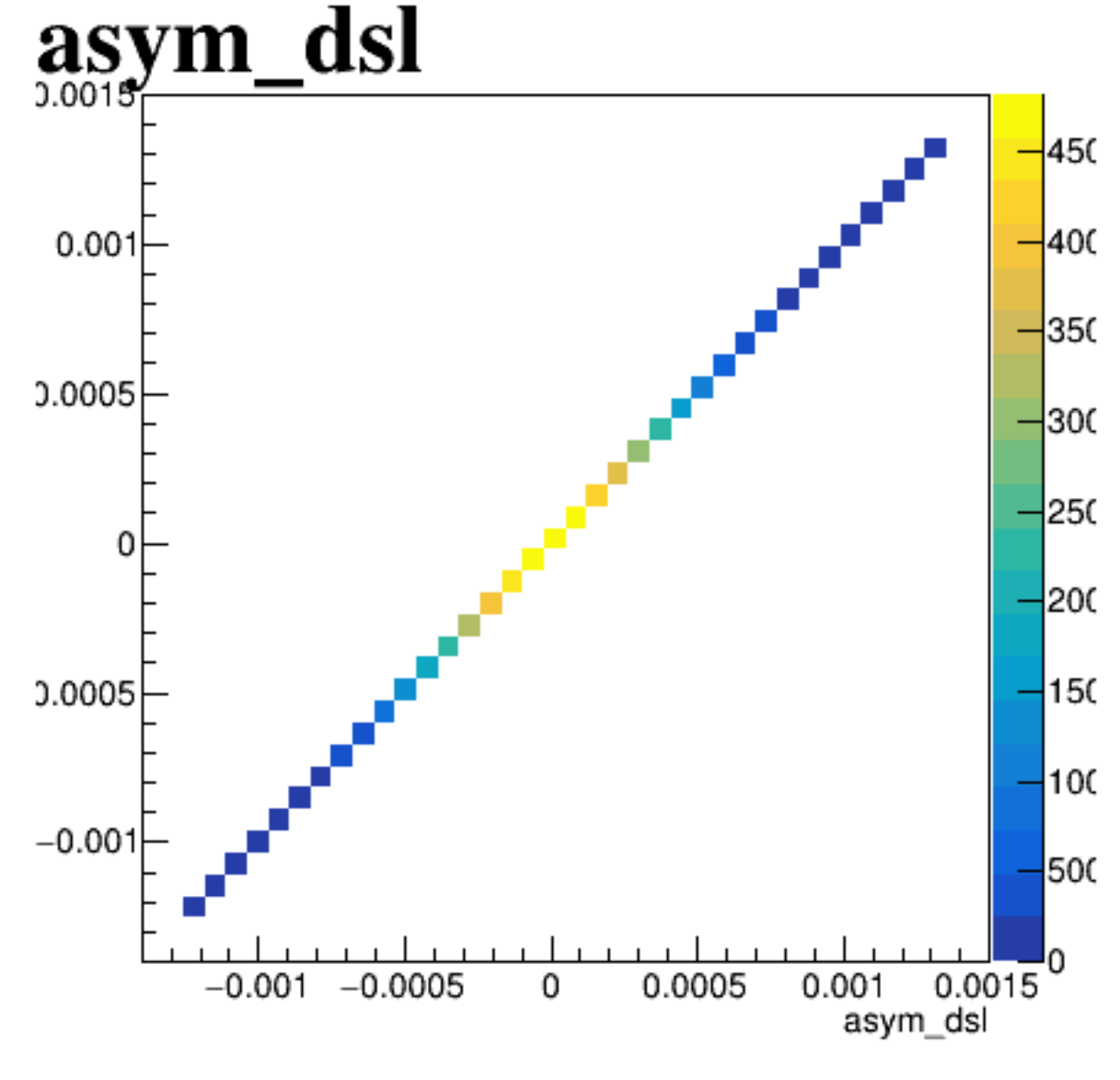
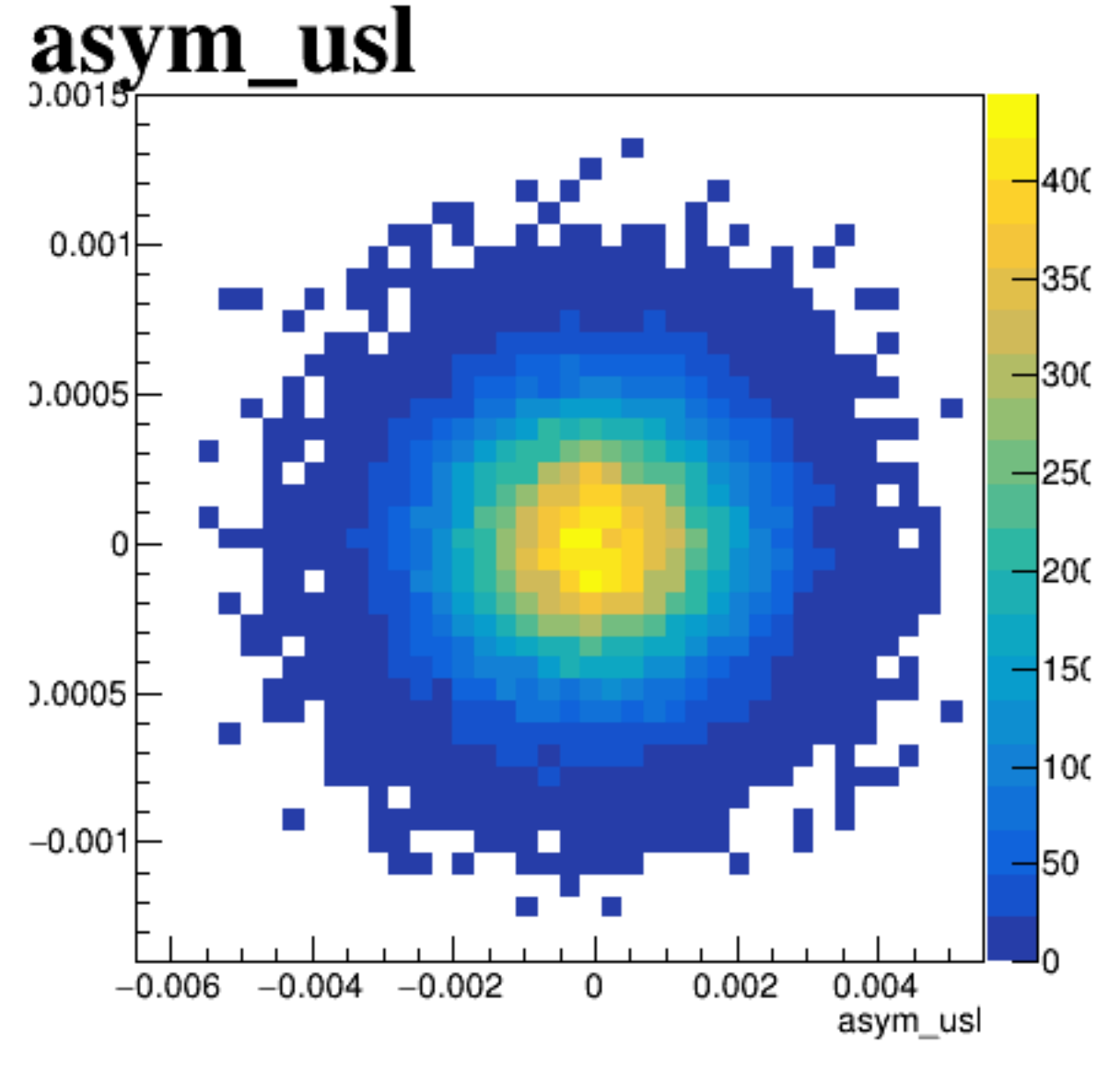
asym_dsr/ppm {ErrorFlag==0}



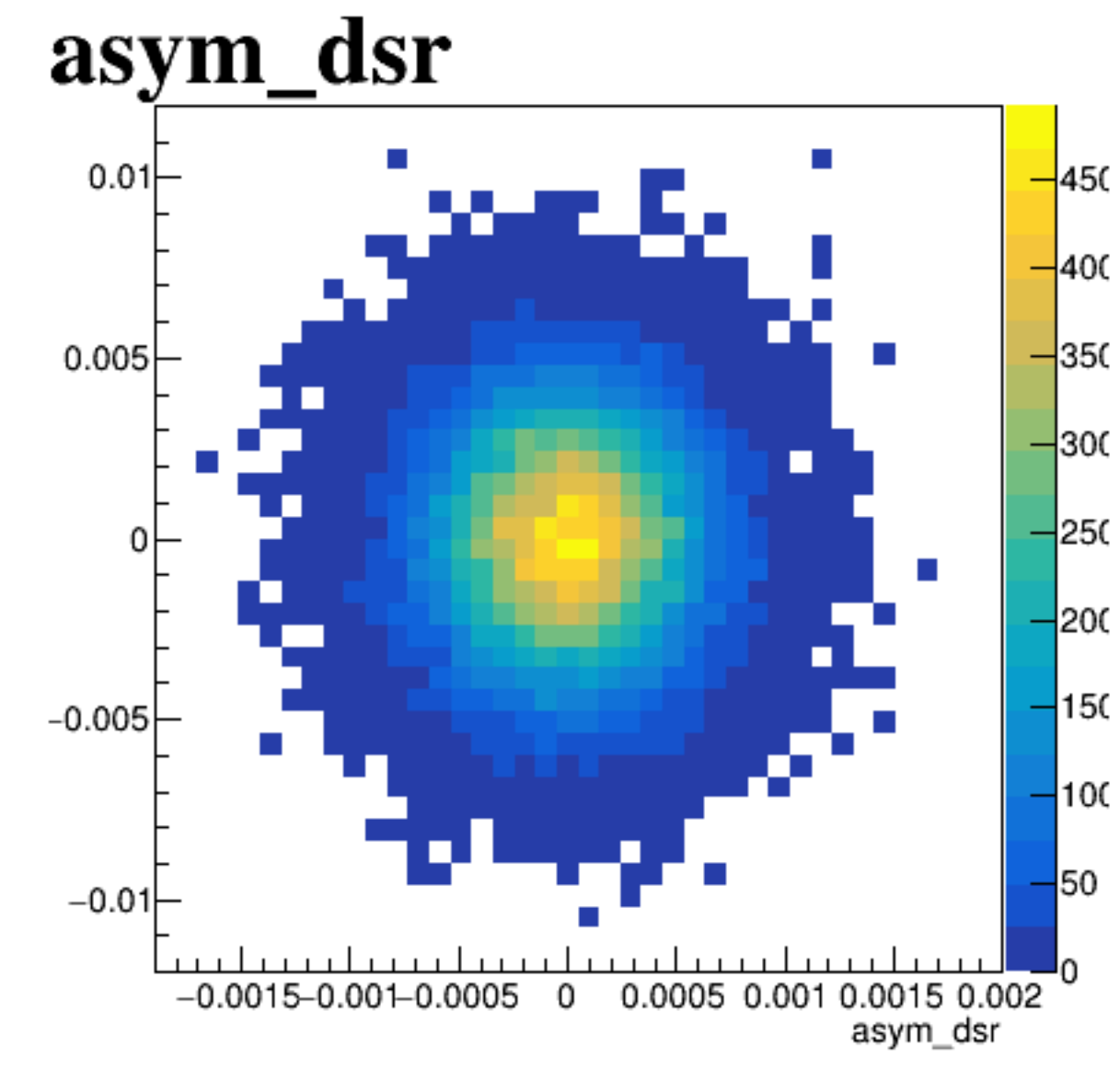
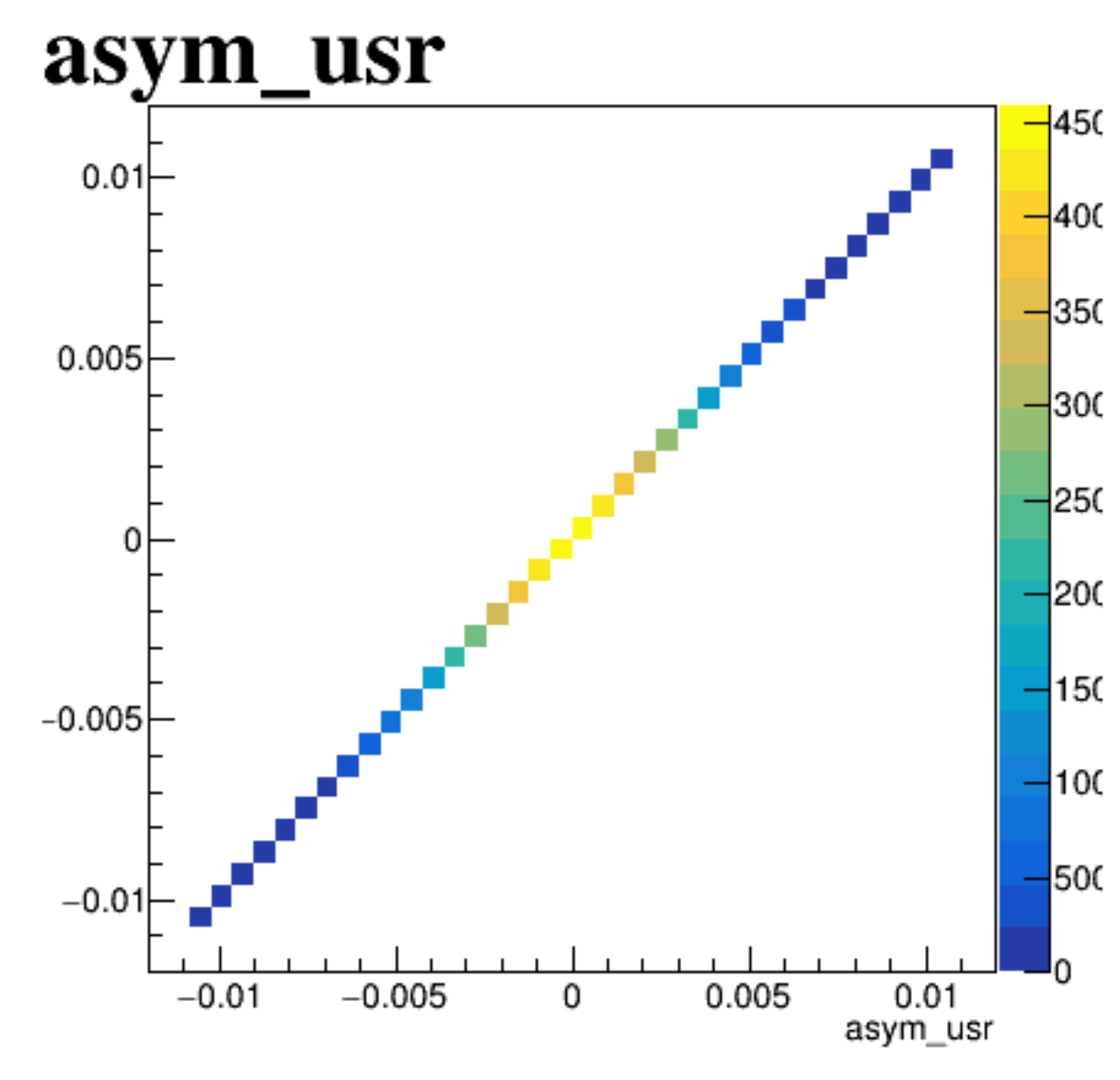
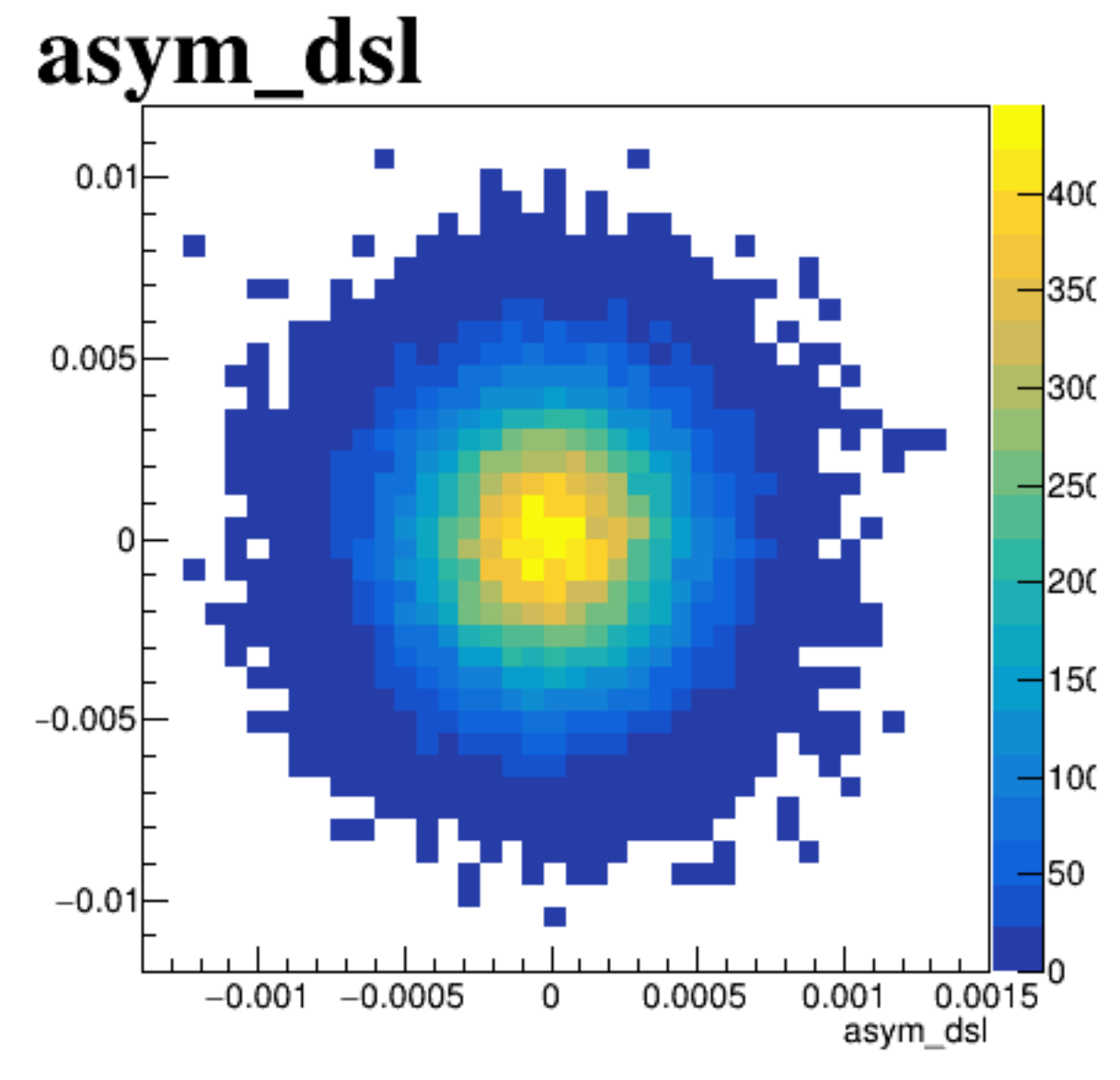
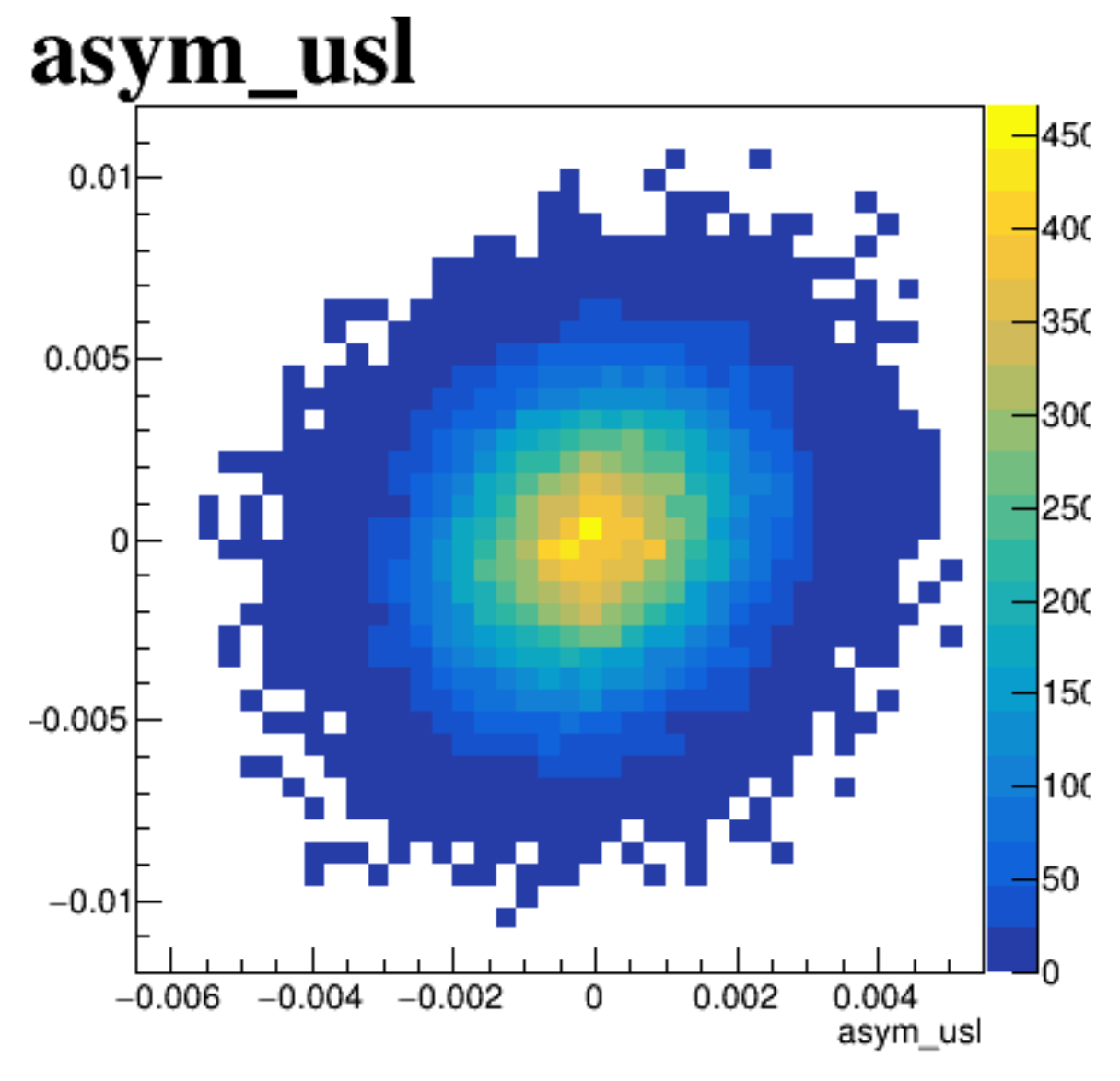
asym_usl



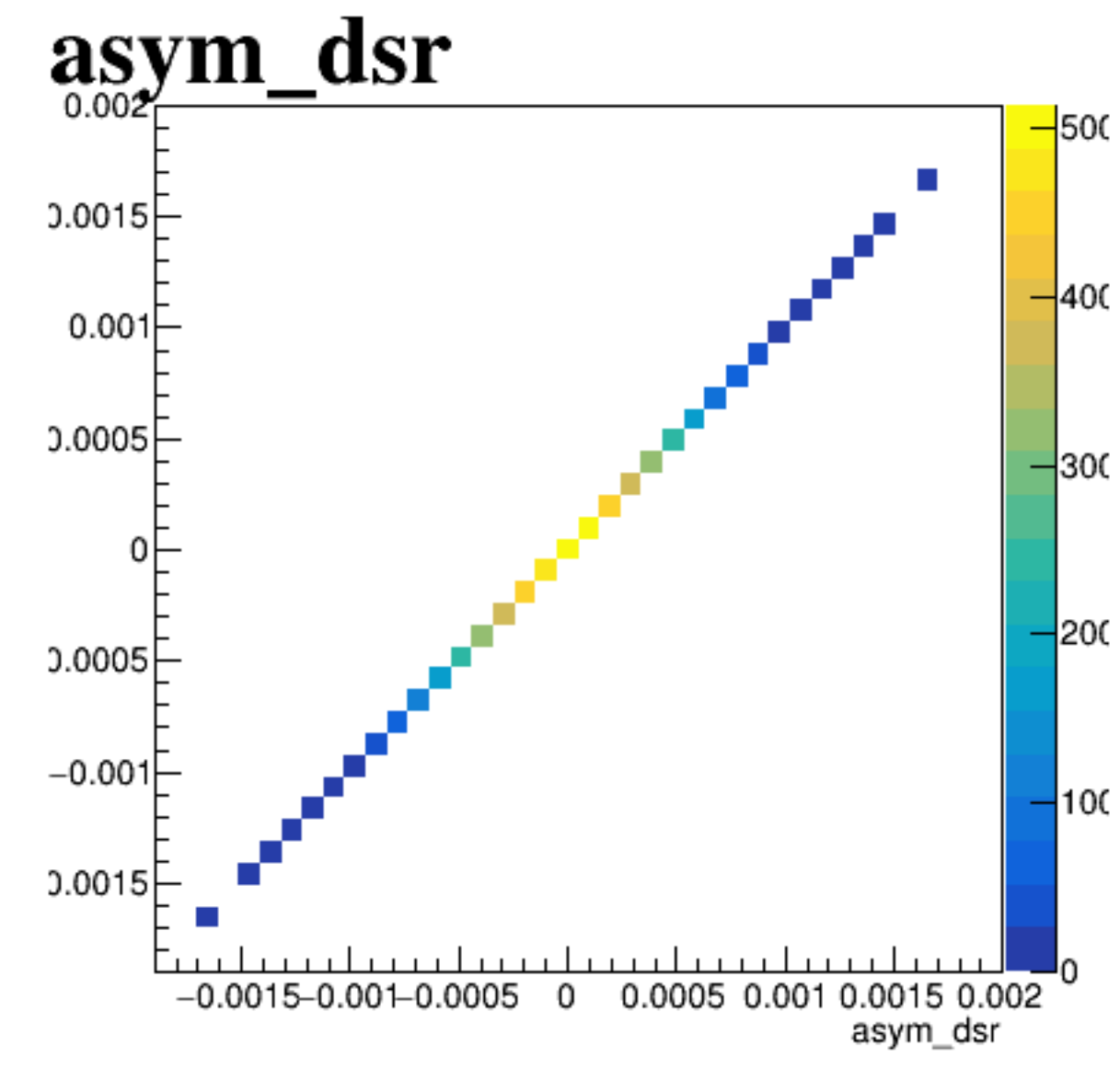
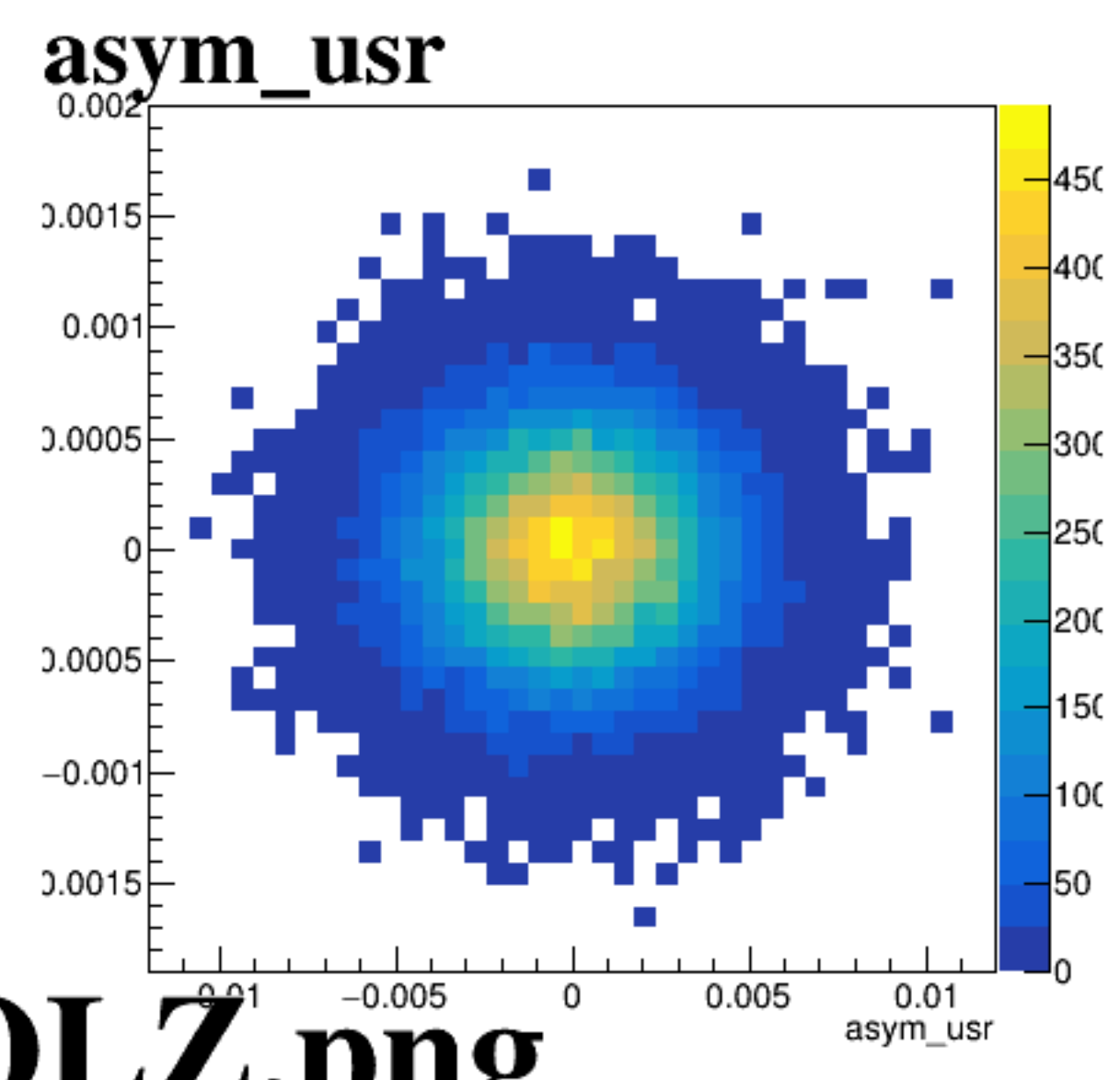
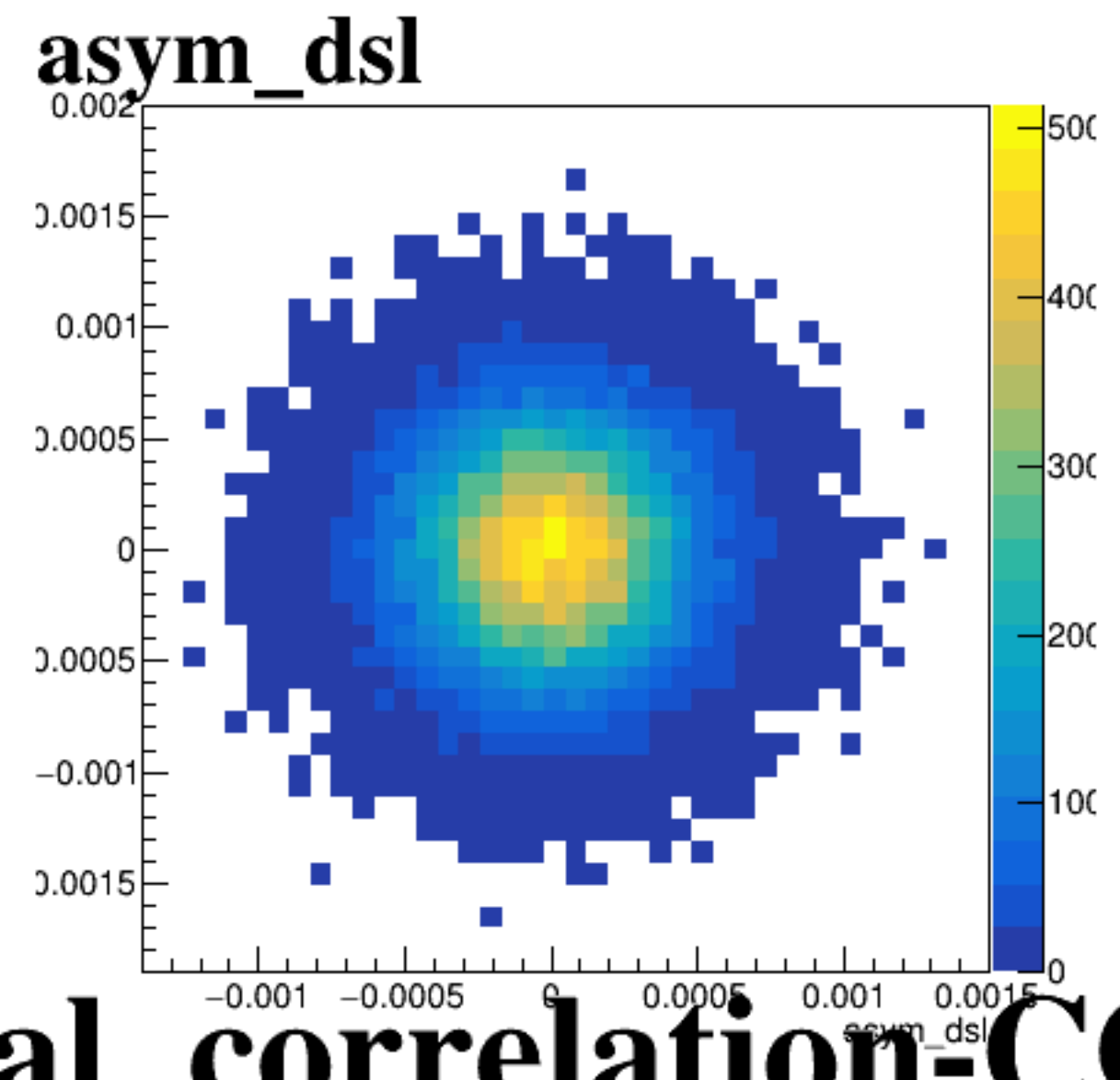
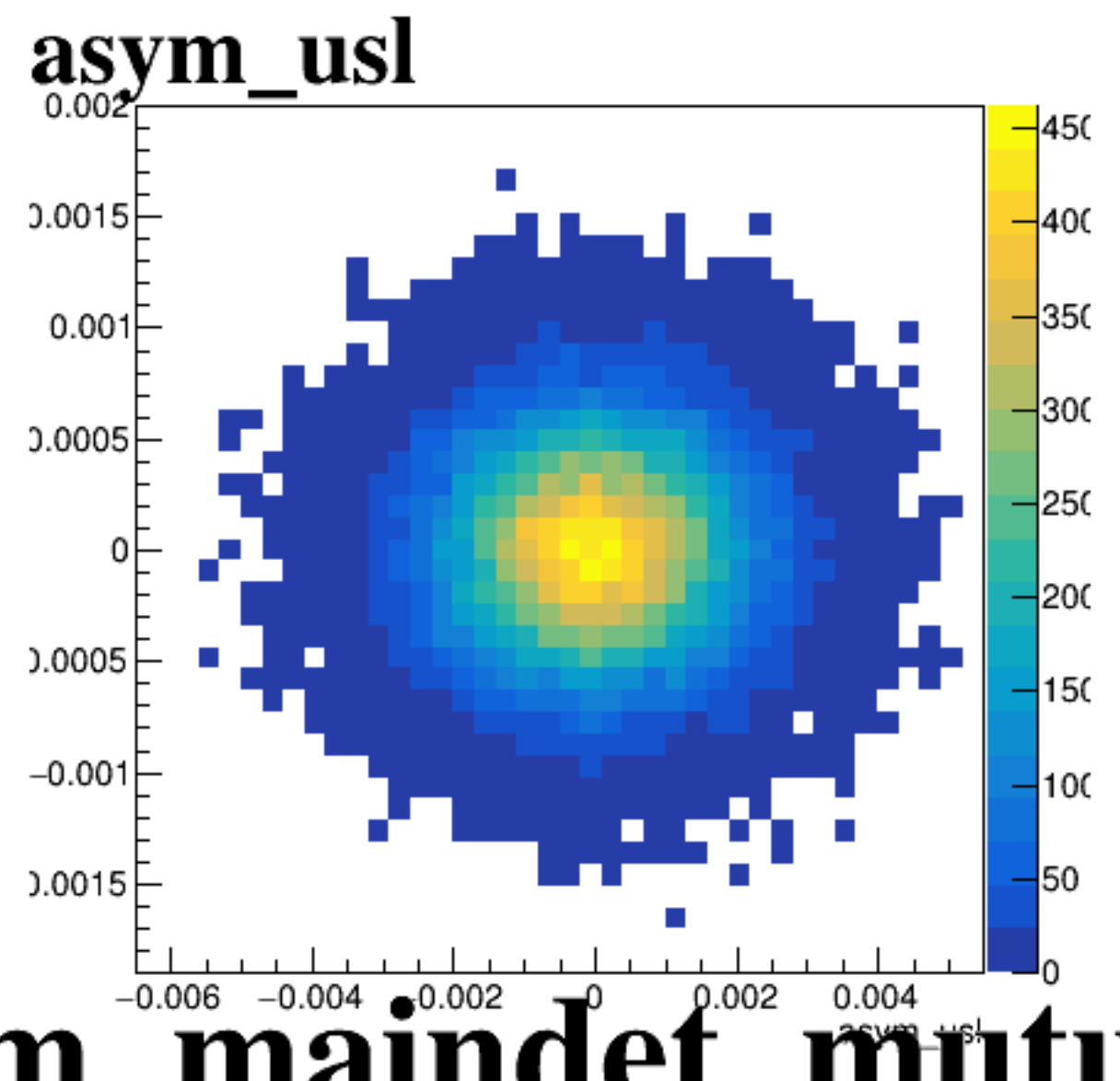
asym_dsl



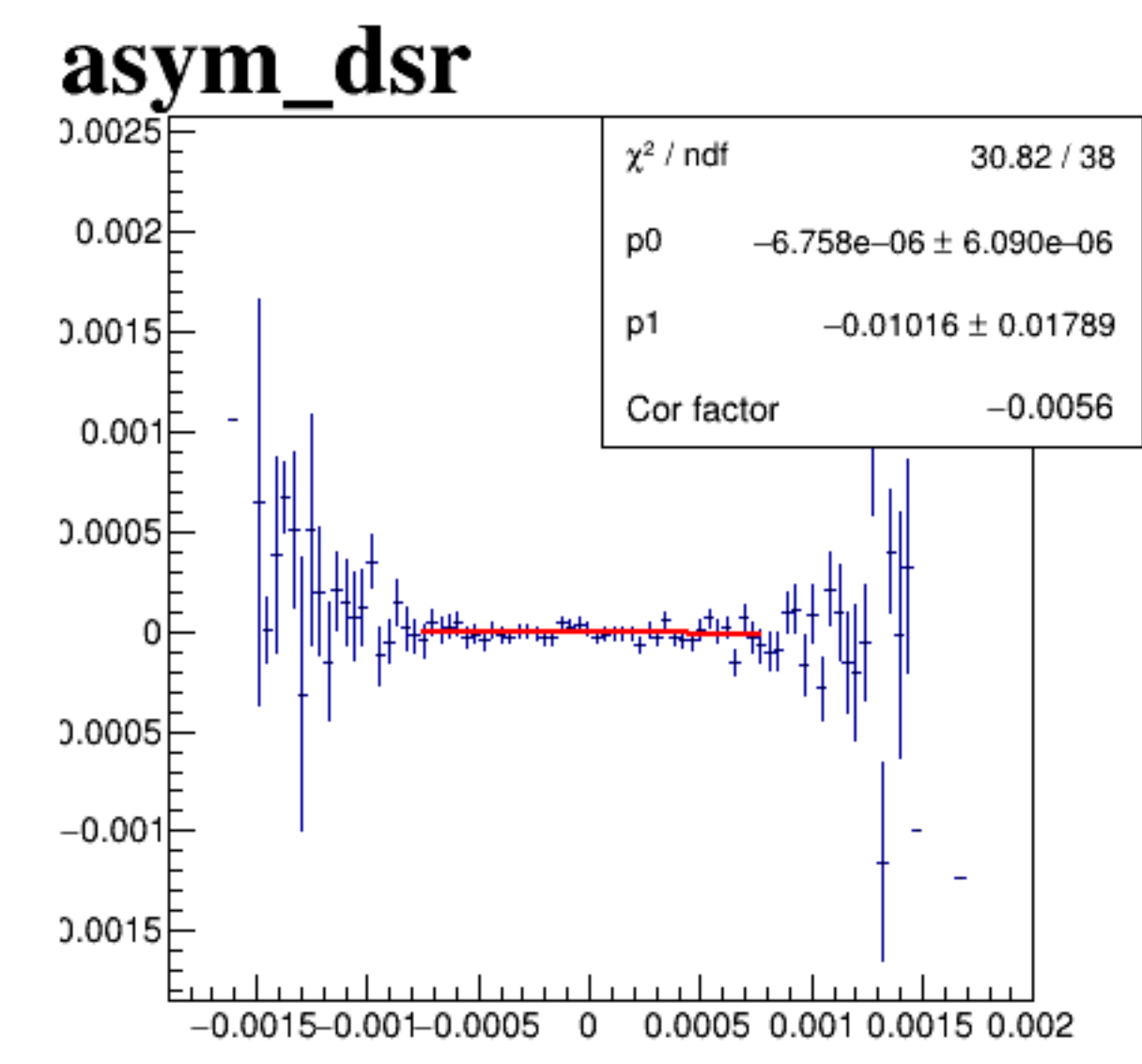
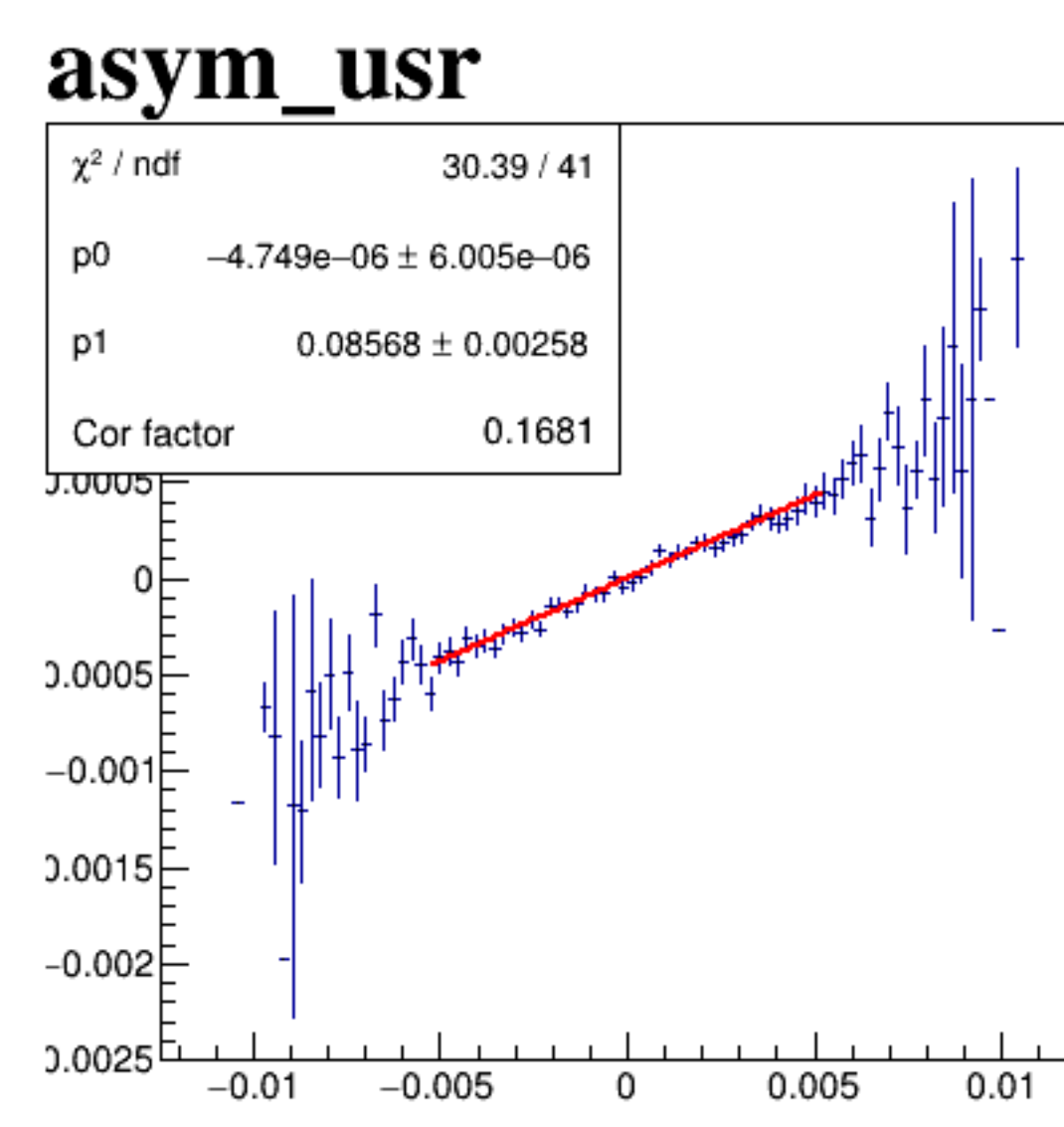
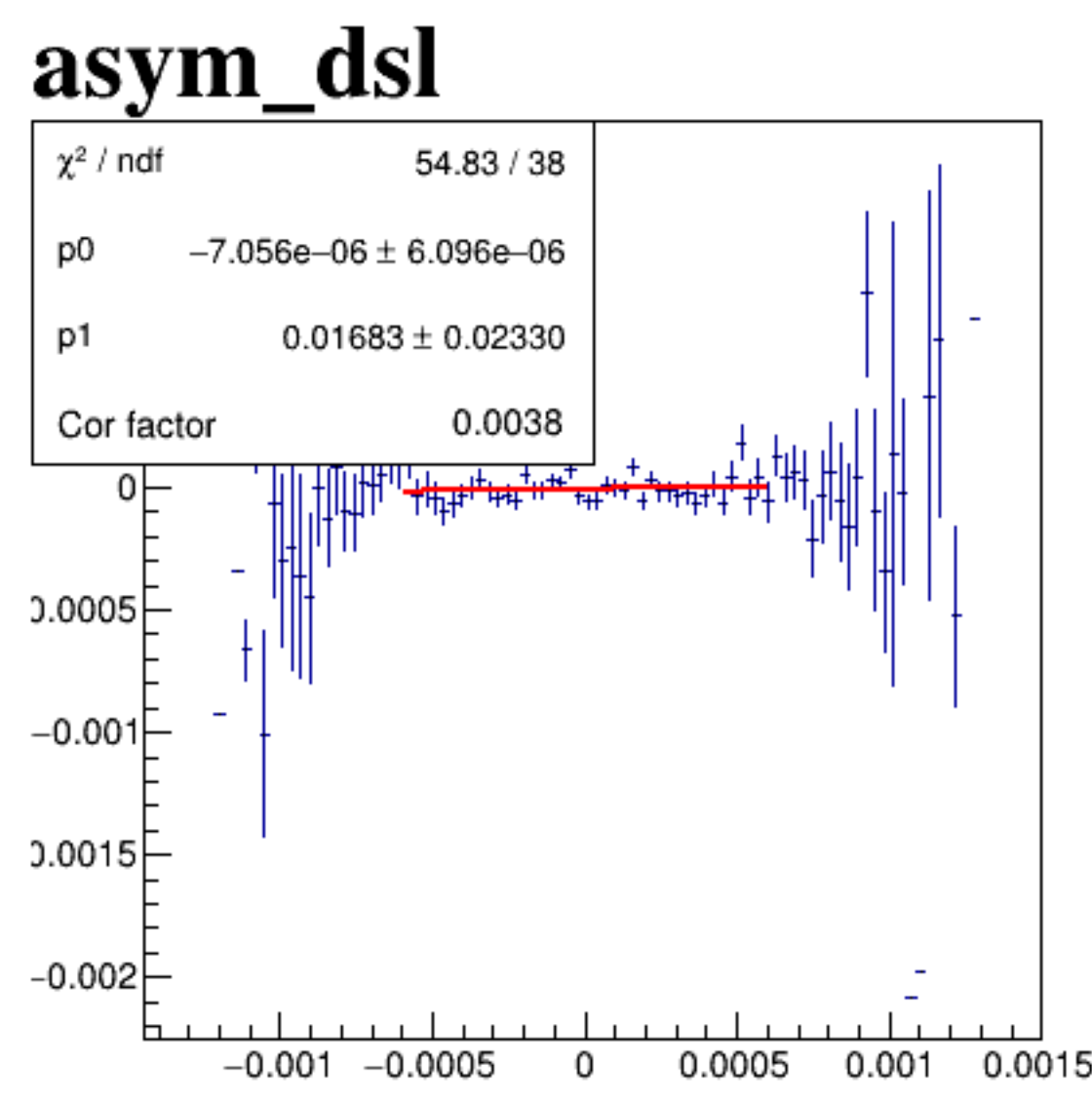
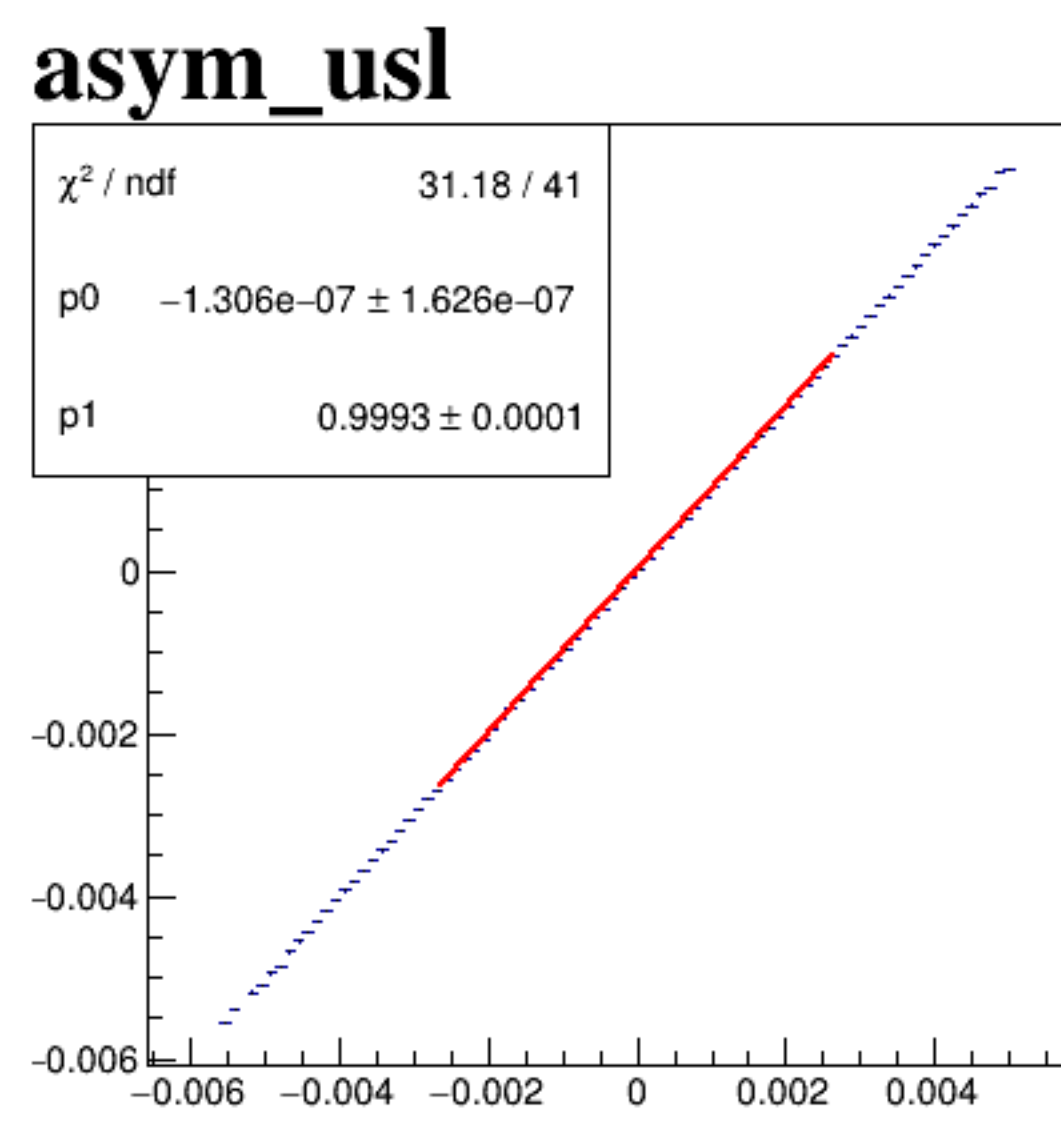
asym_usr



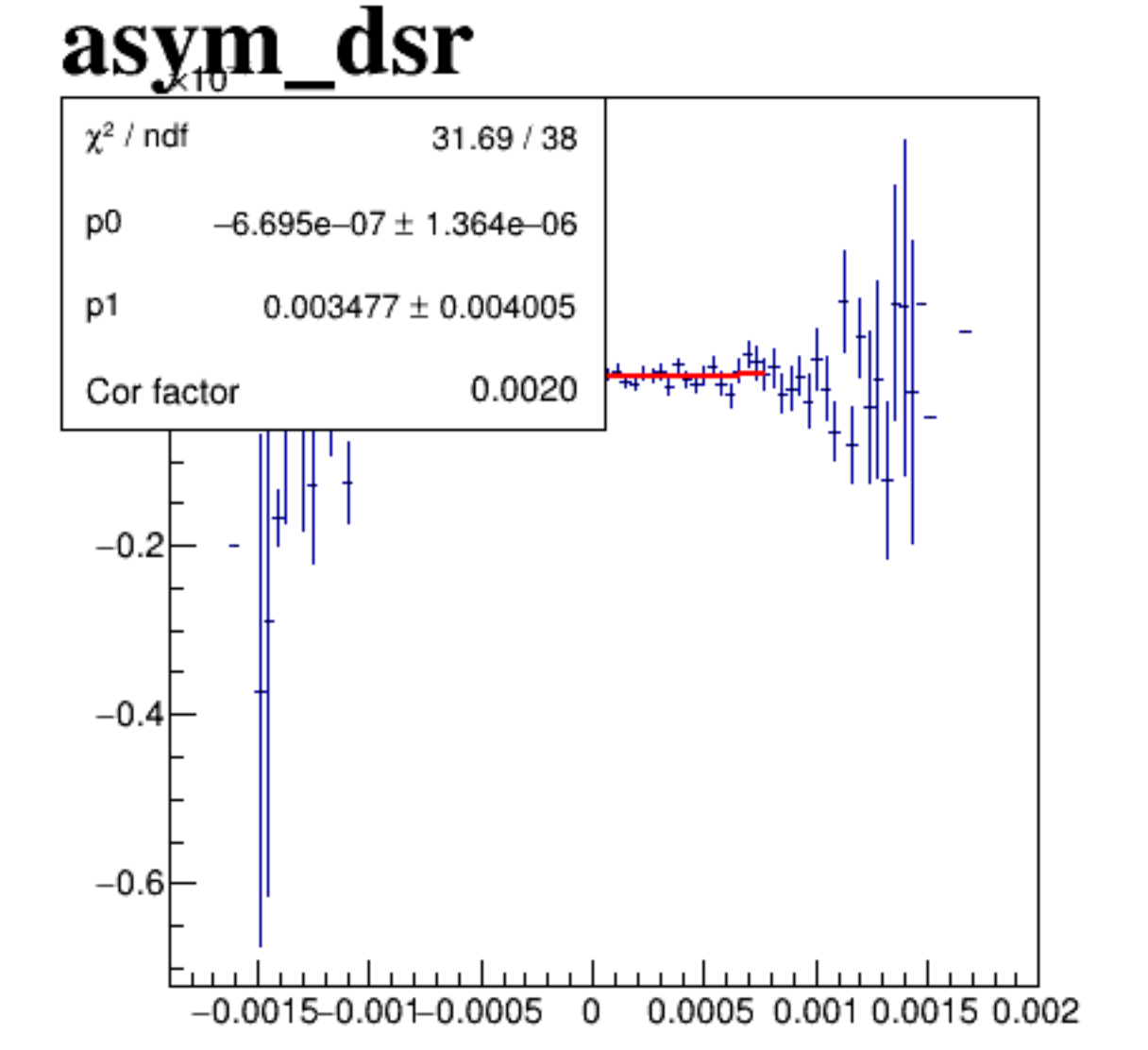
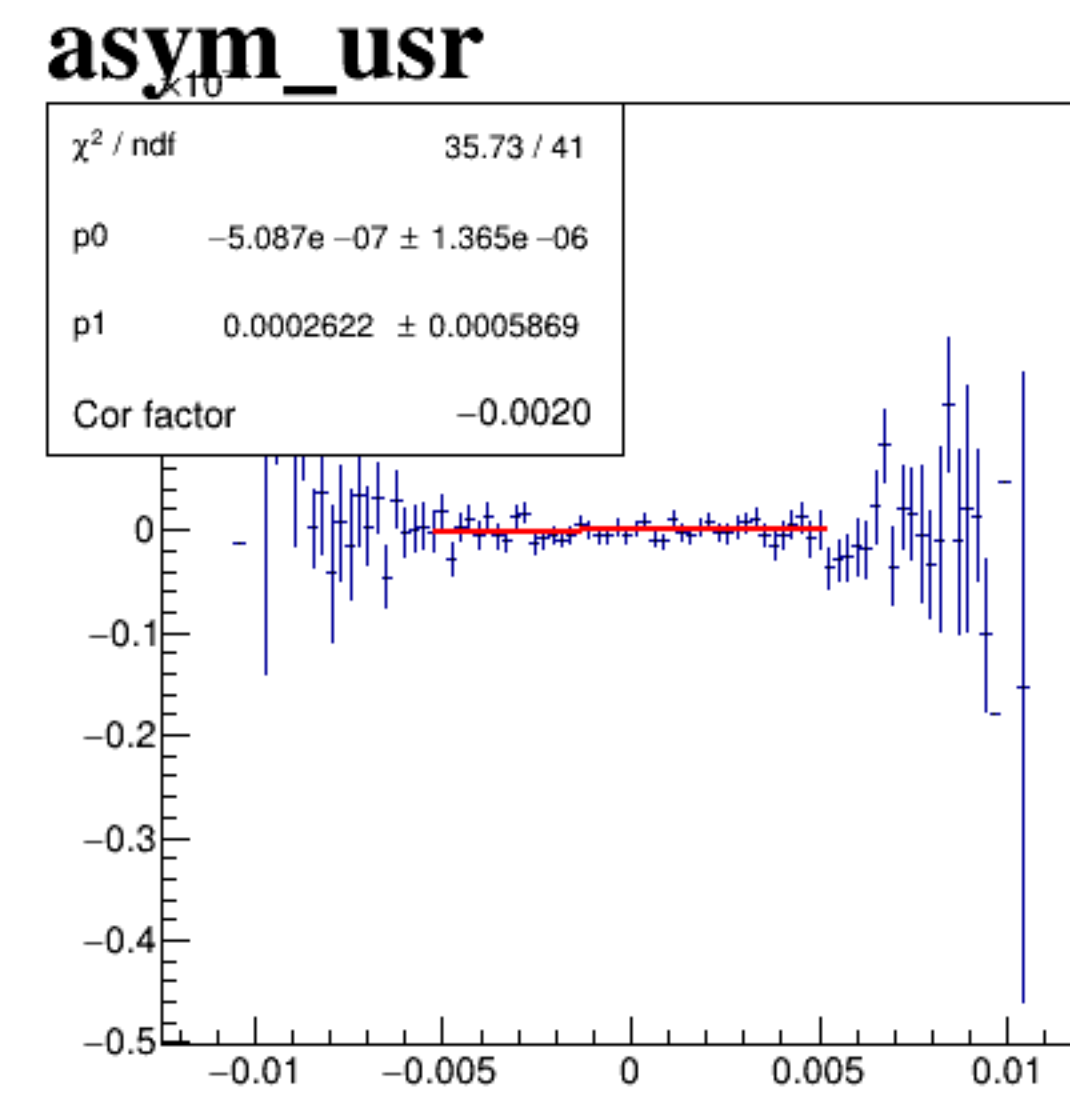
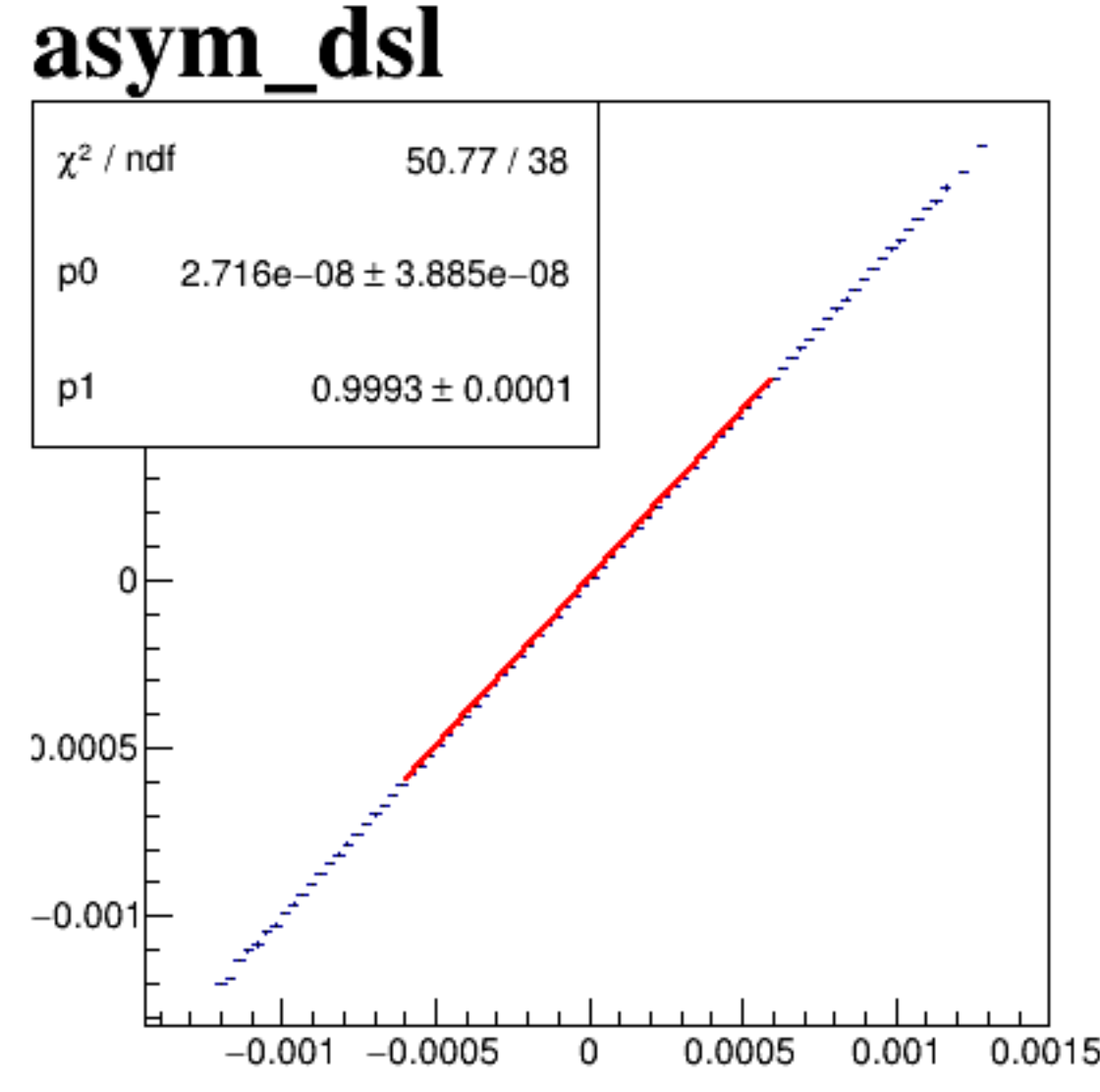
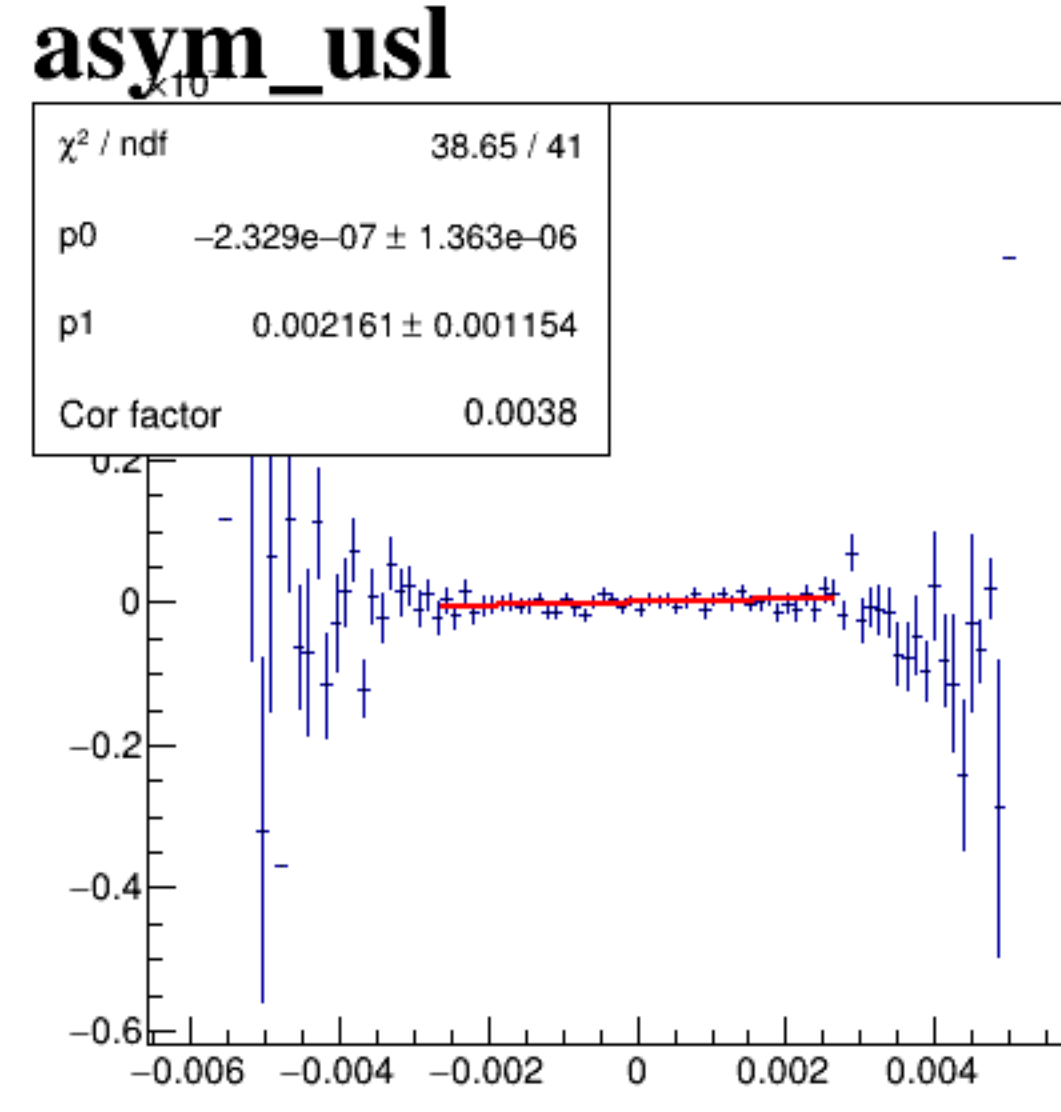
asym_dsr



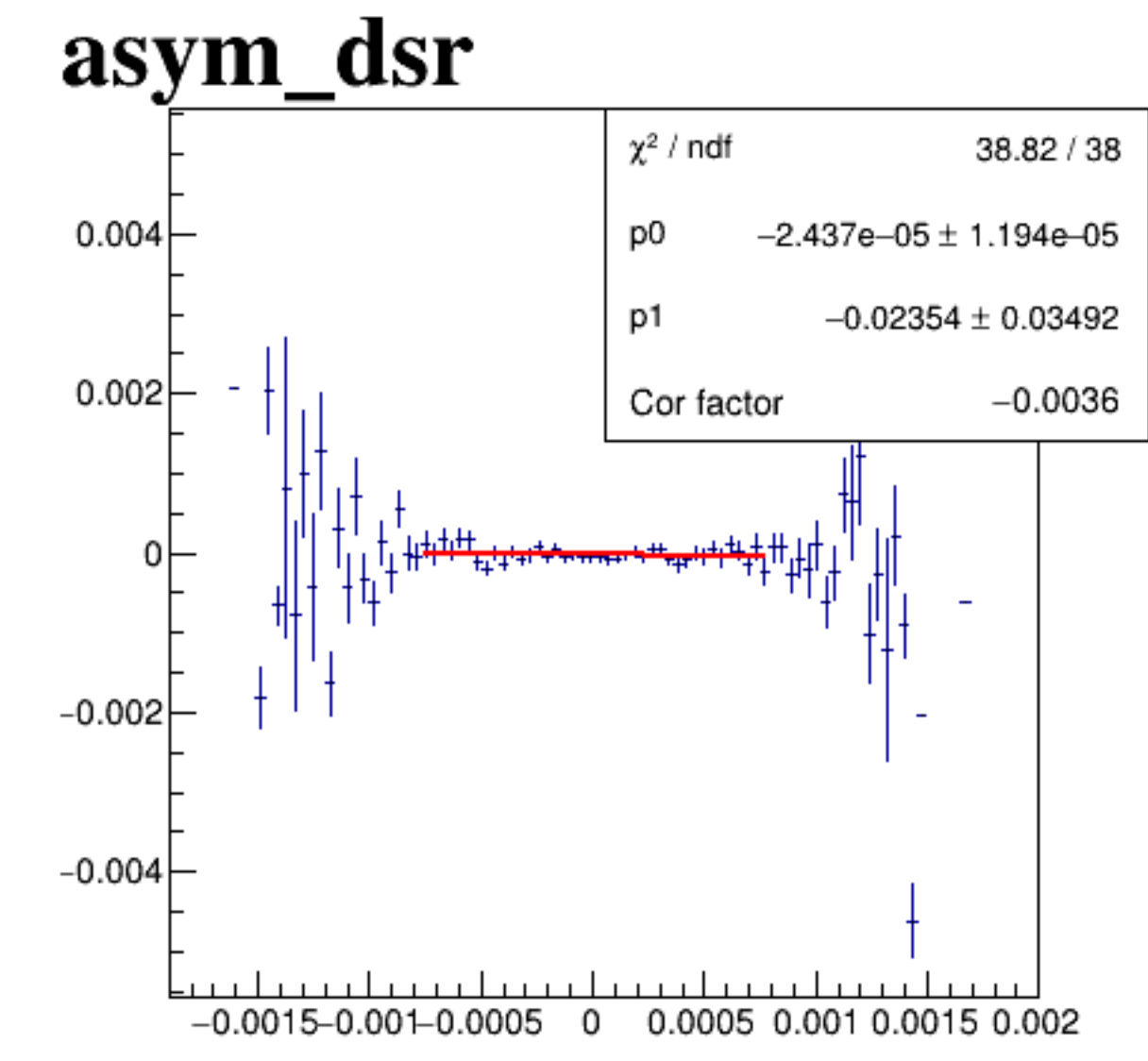
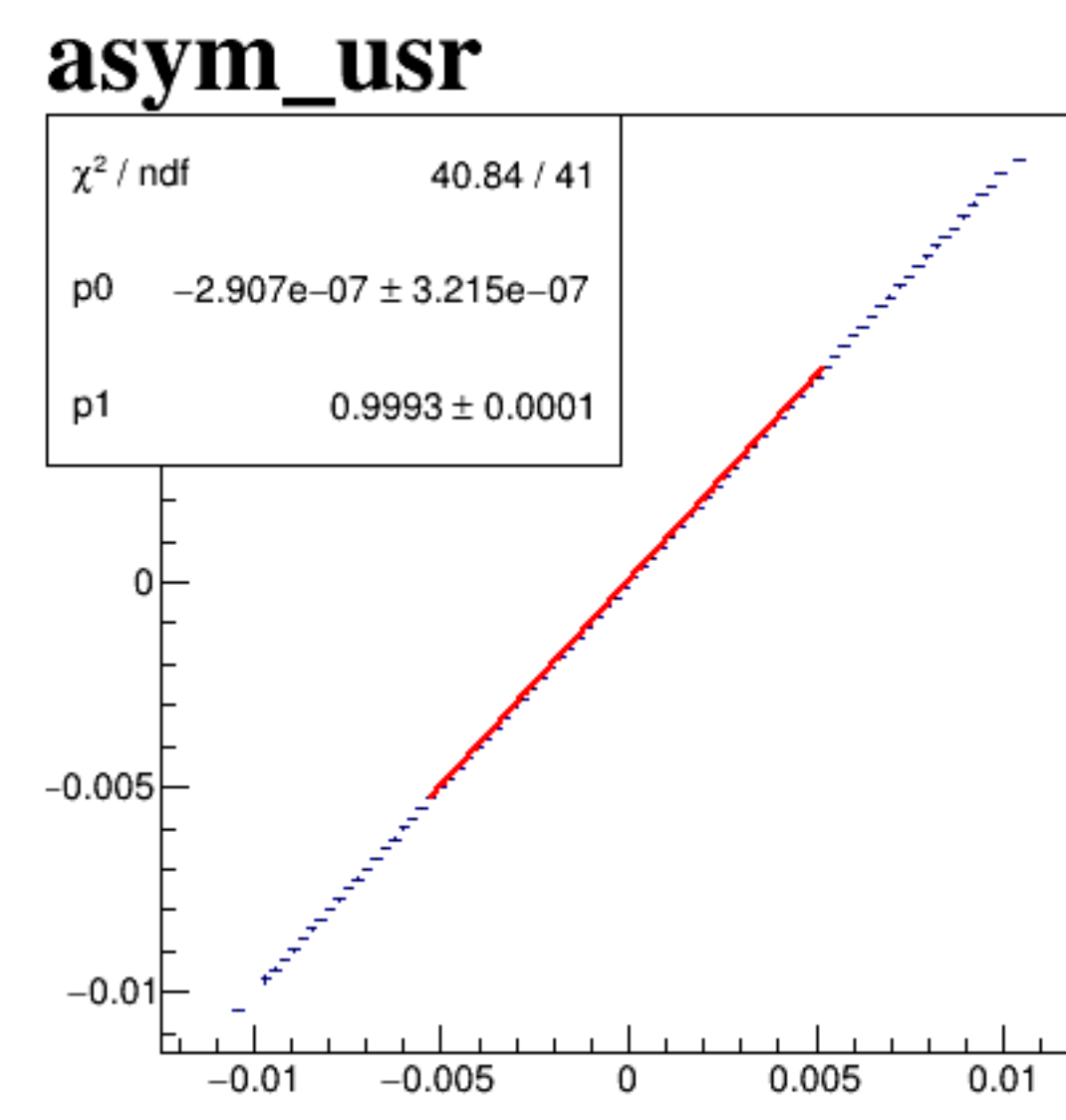
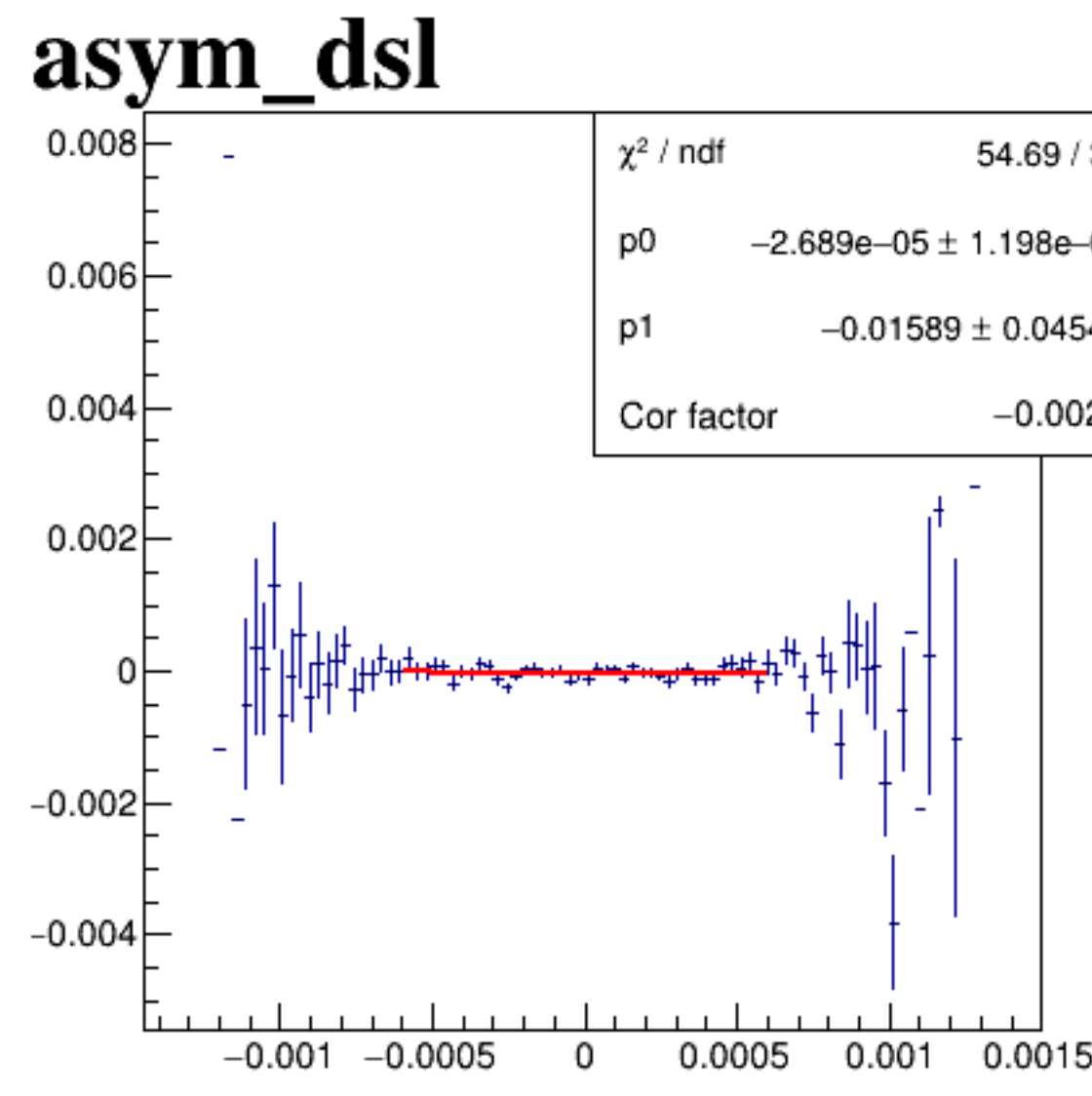
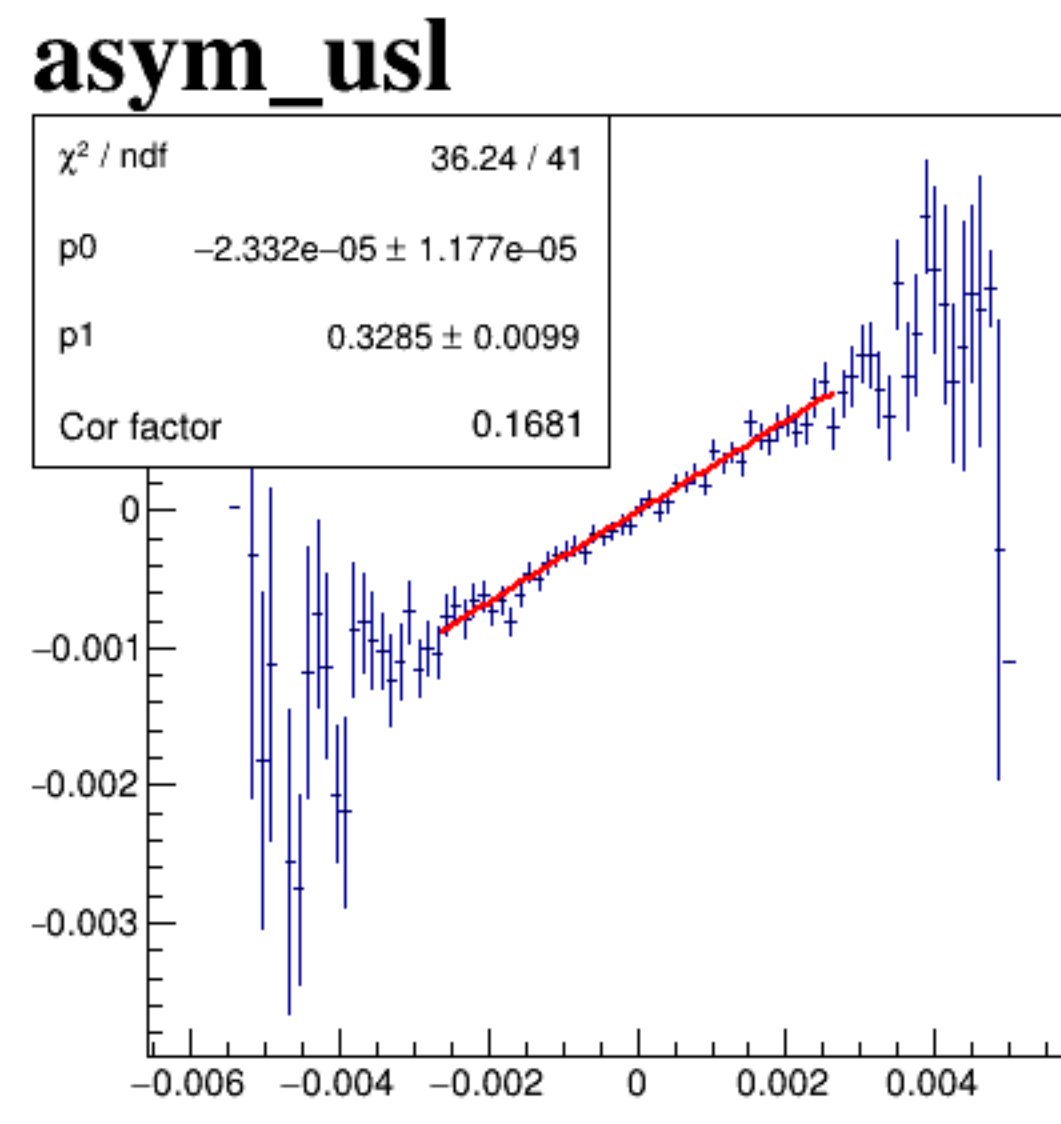
asym_usl



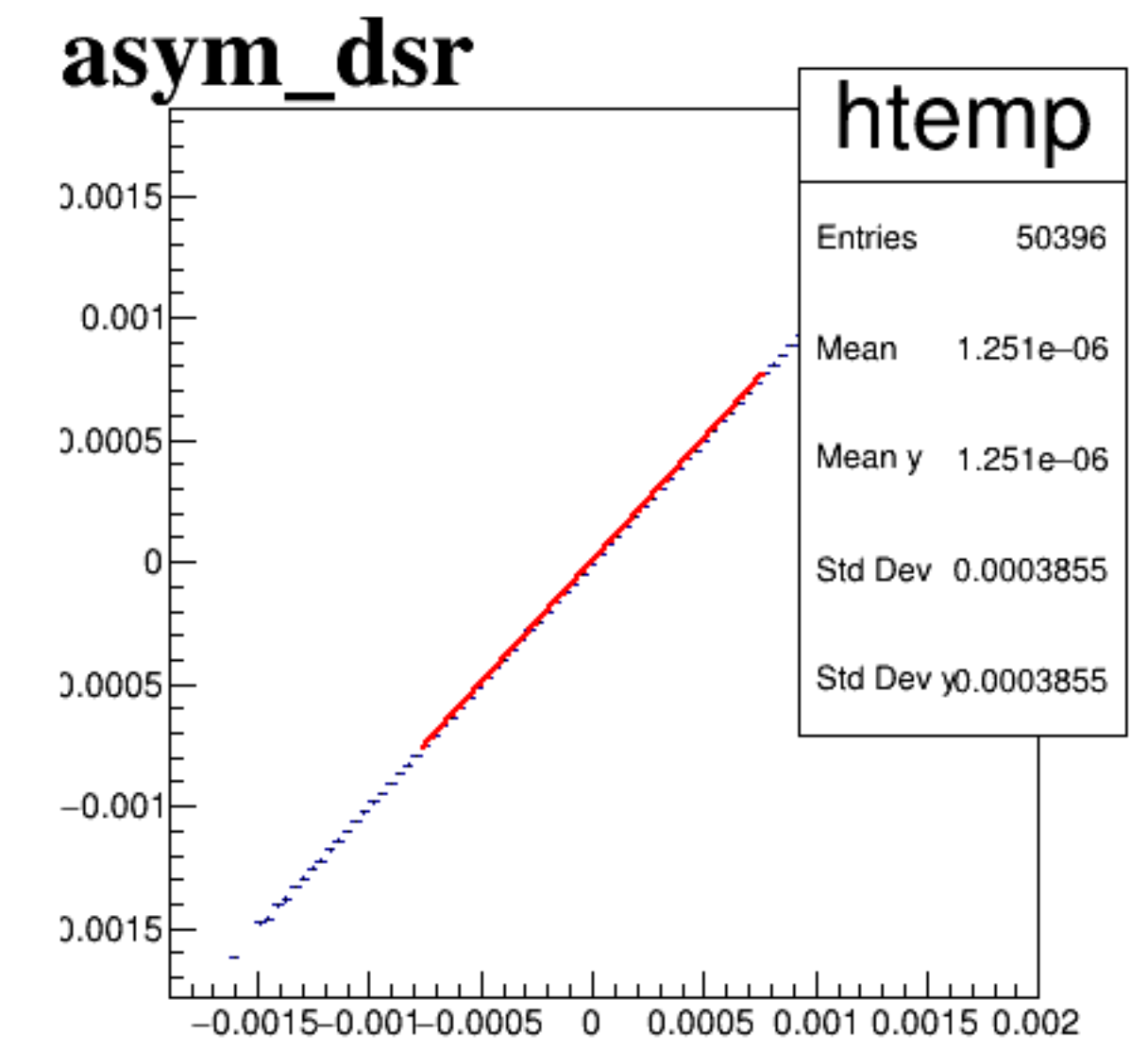
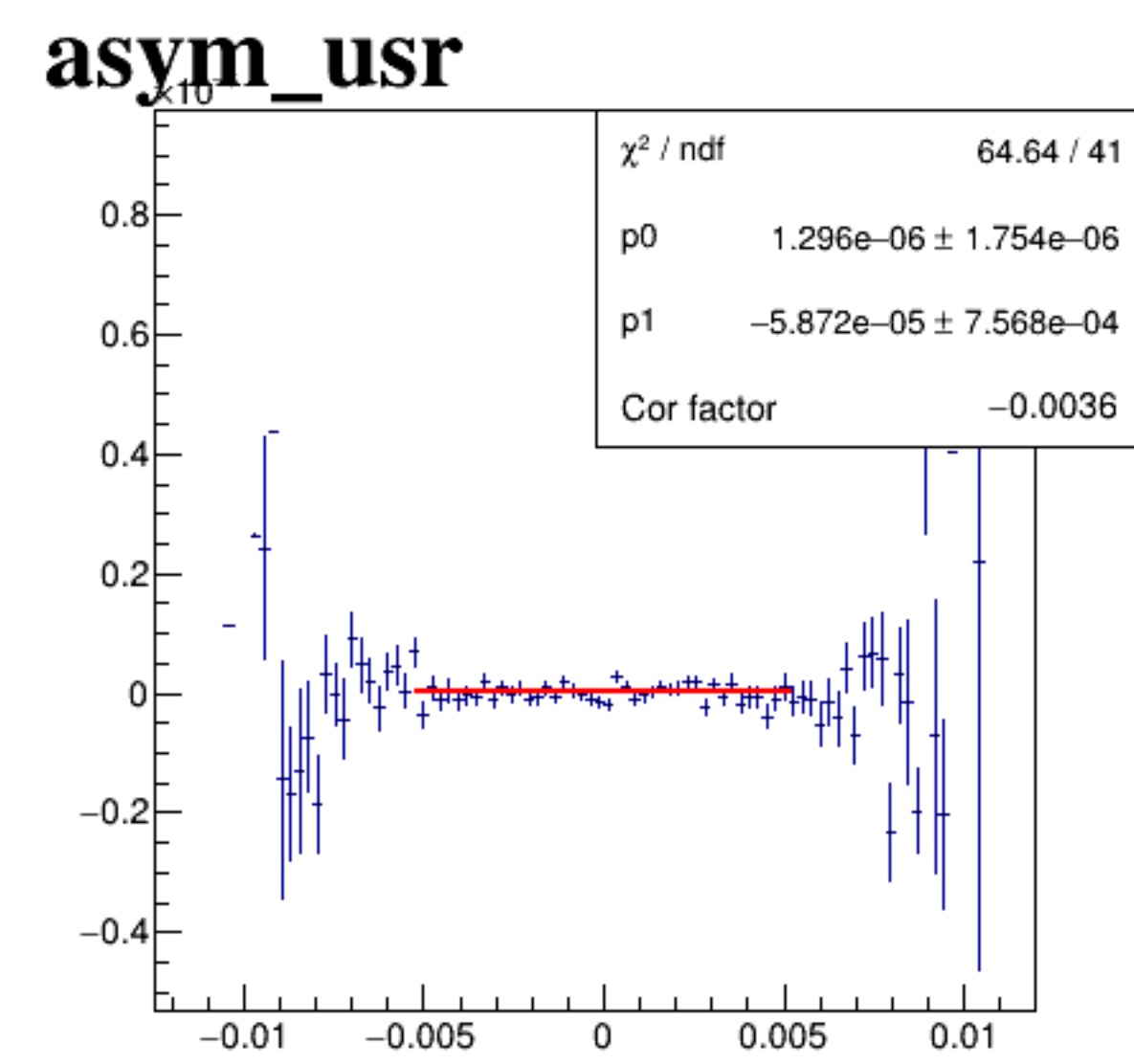
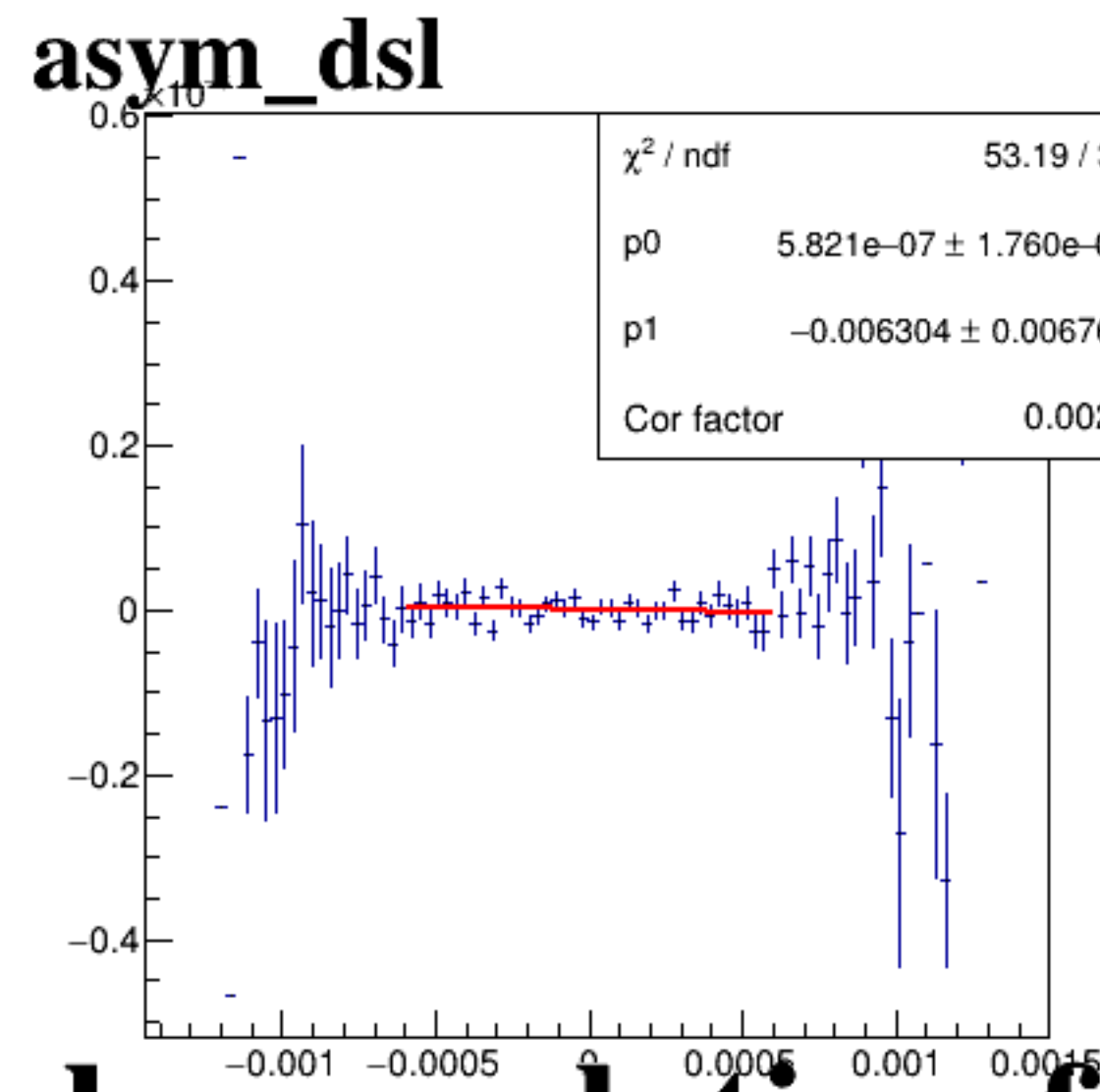
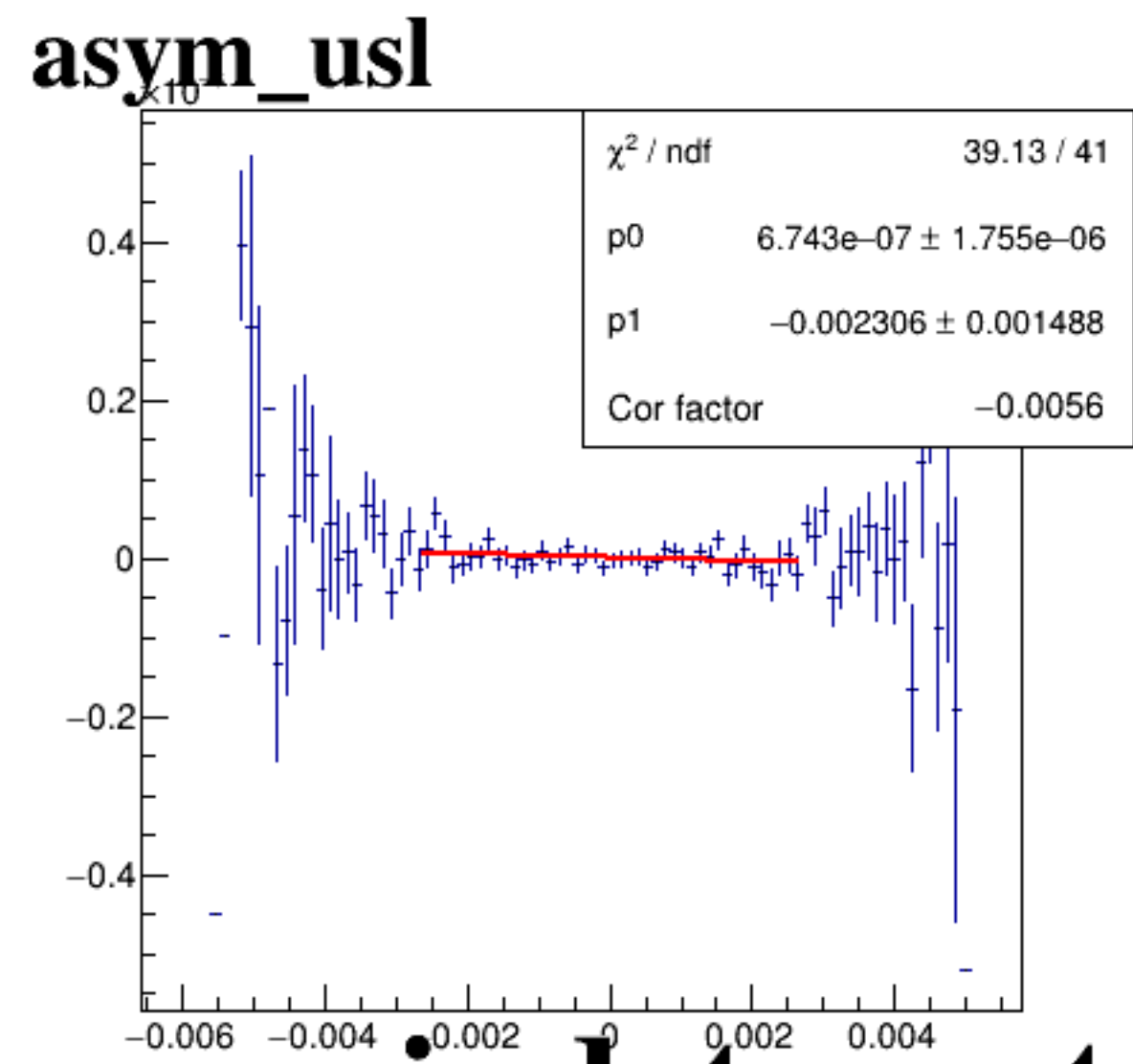
asym_dsl



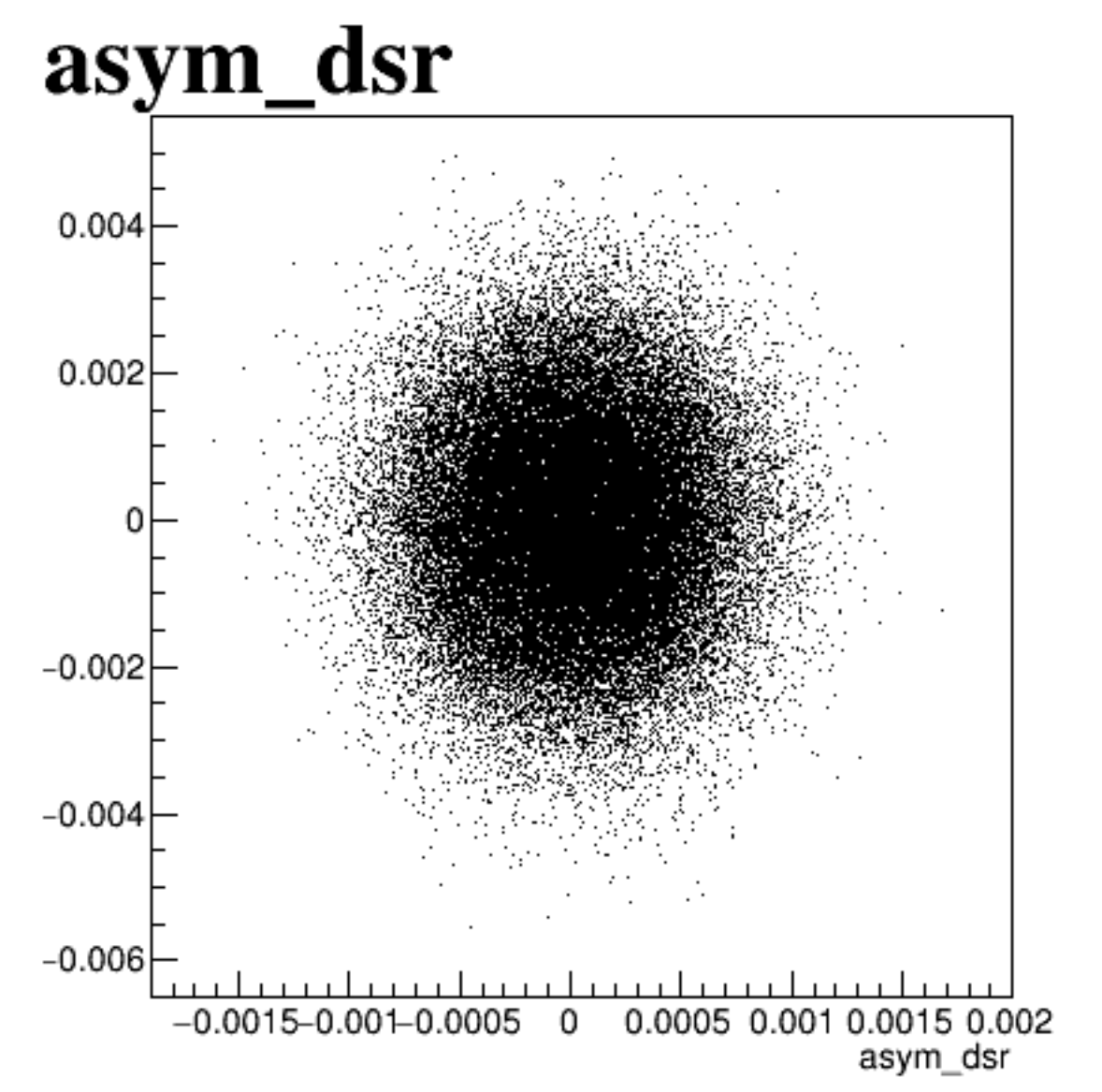
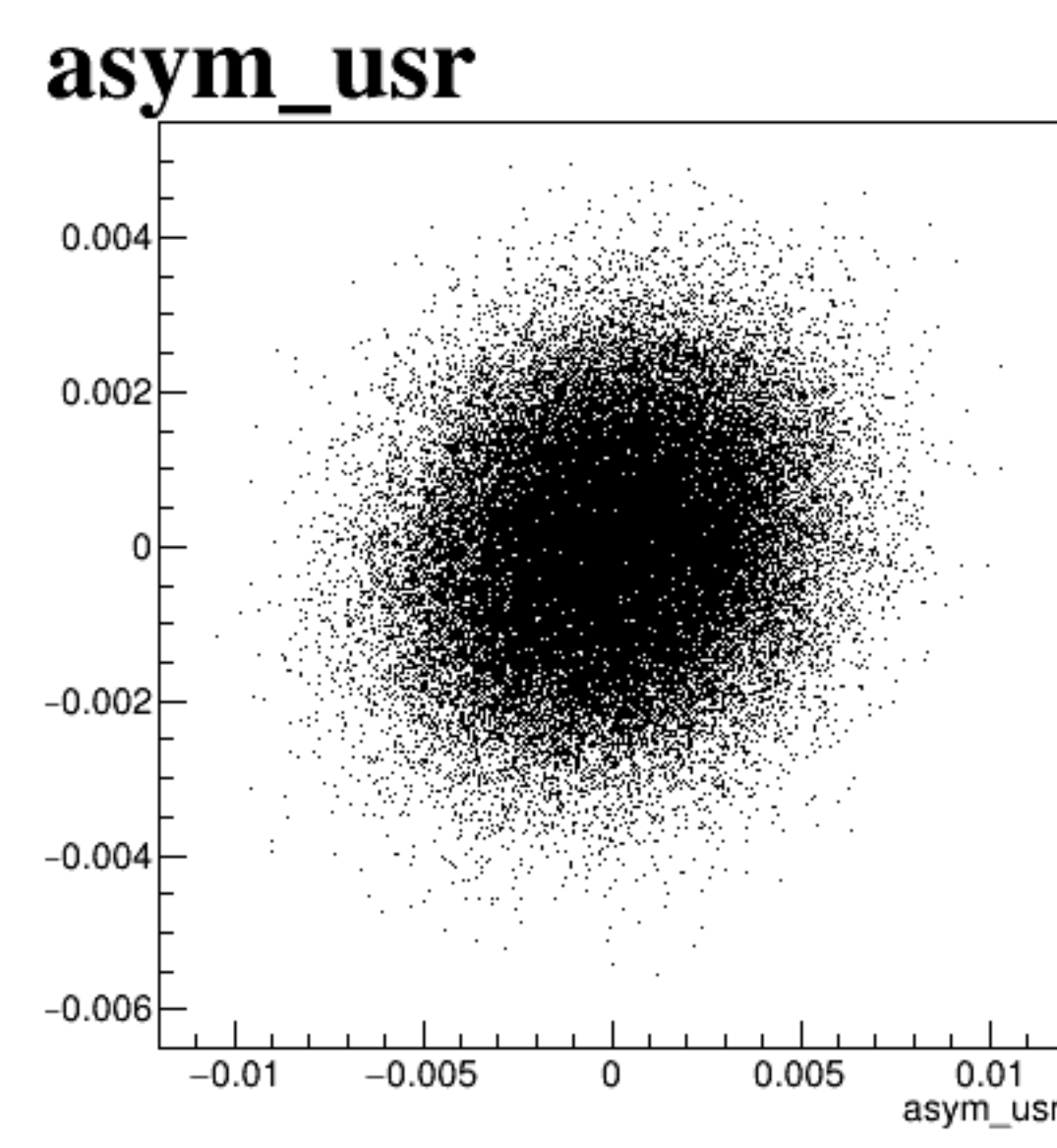
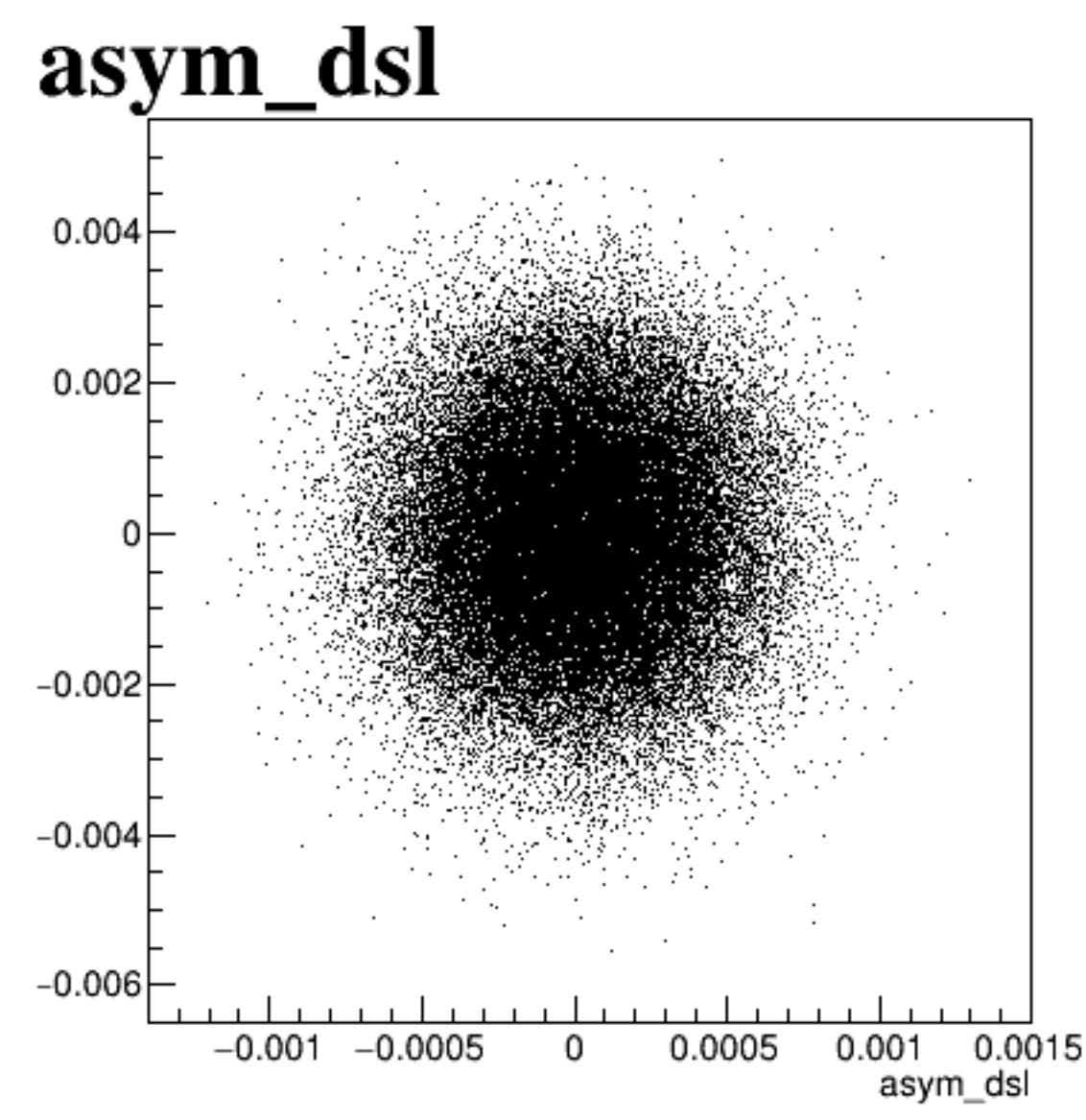
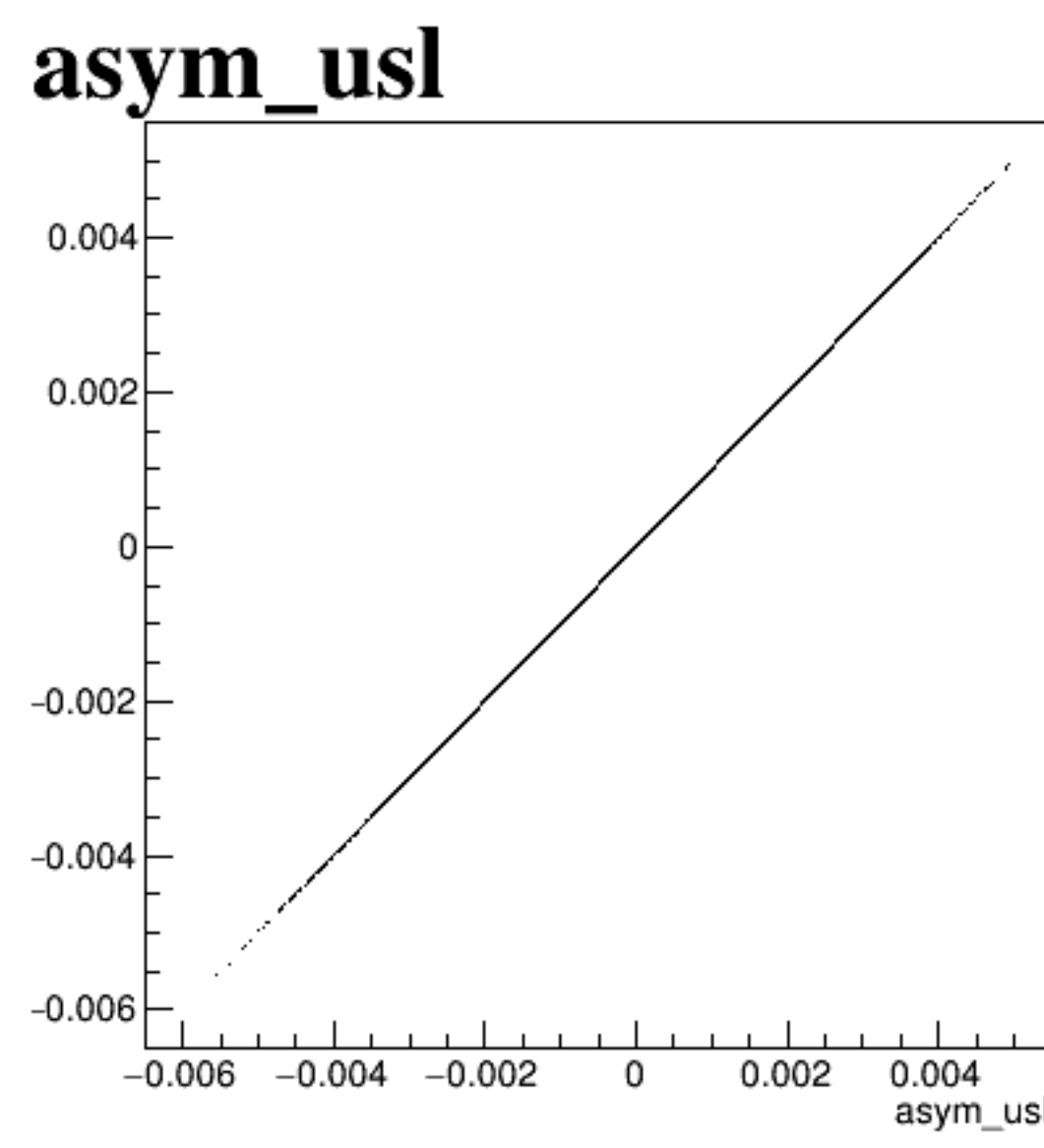
asym_usr



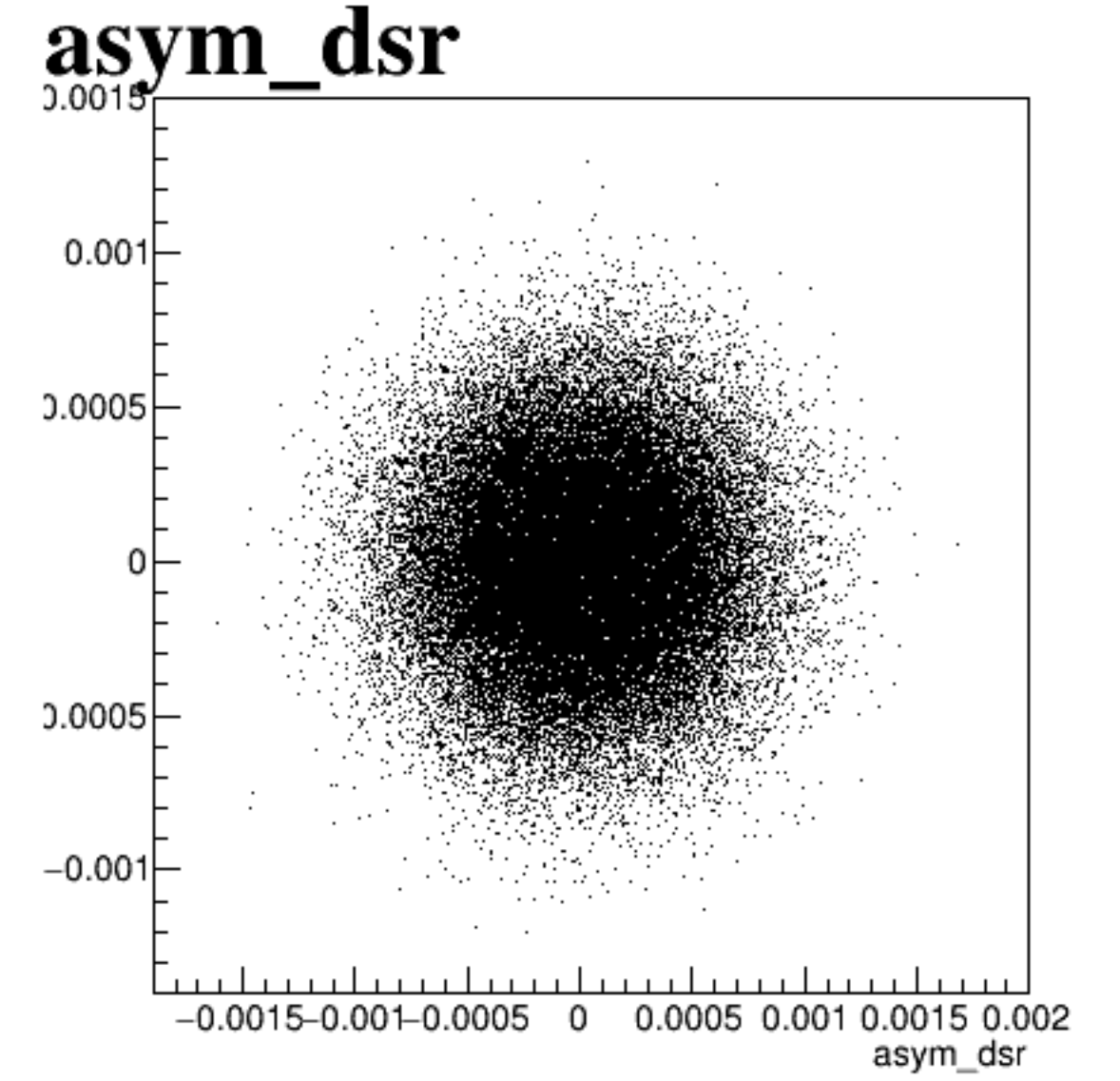
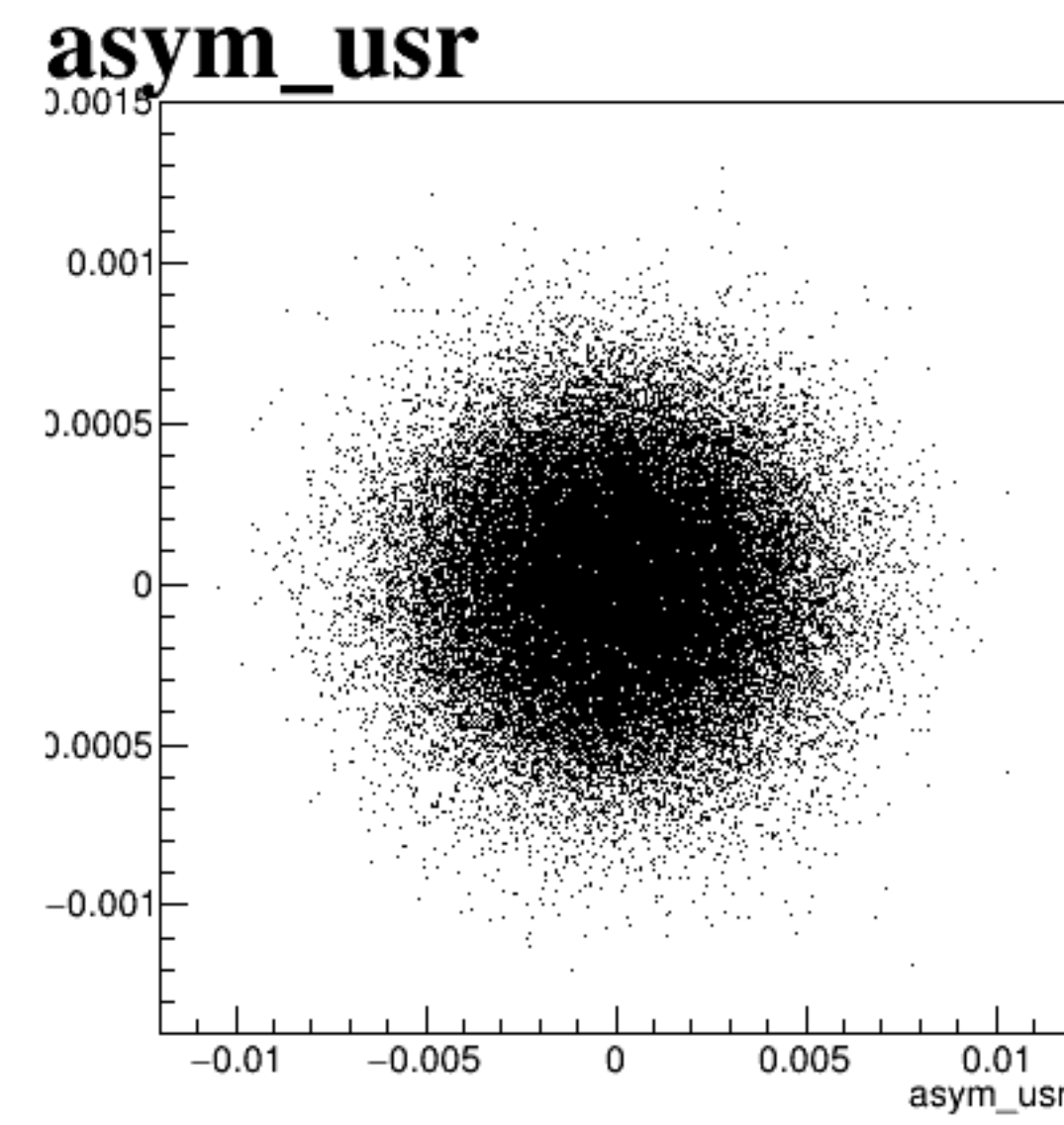
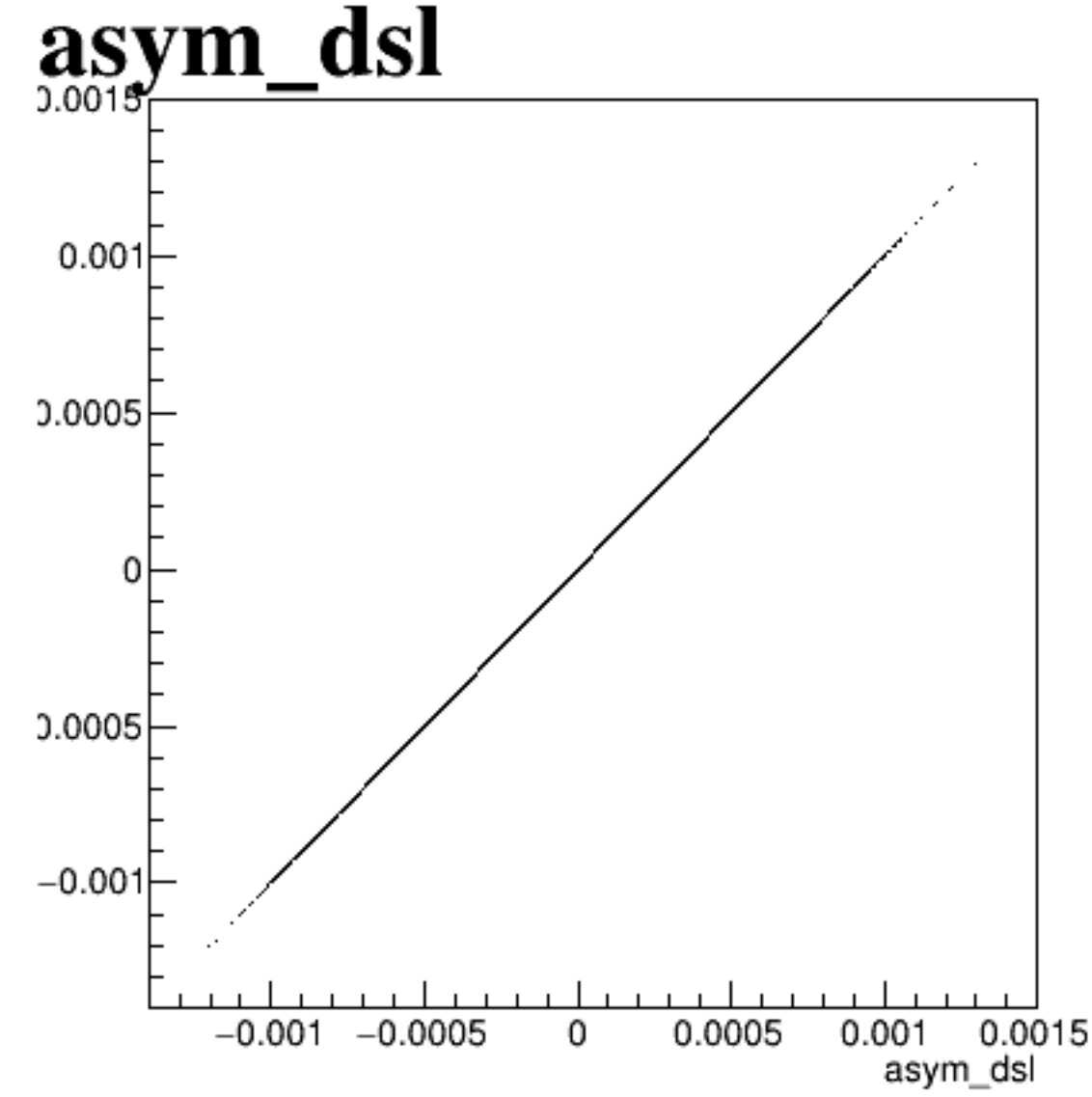
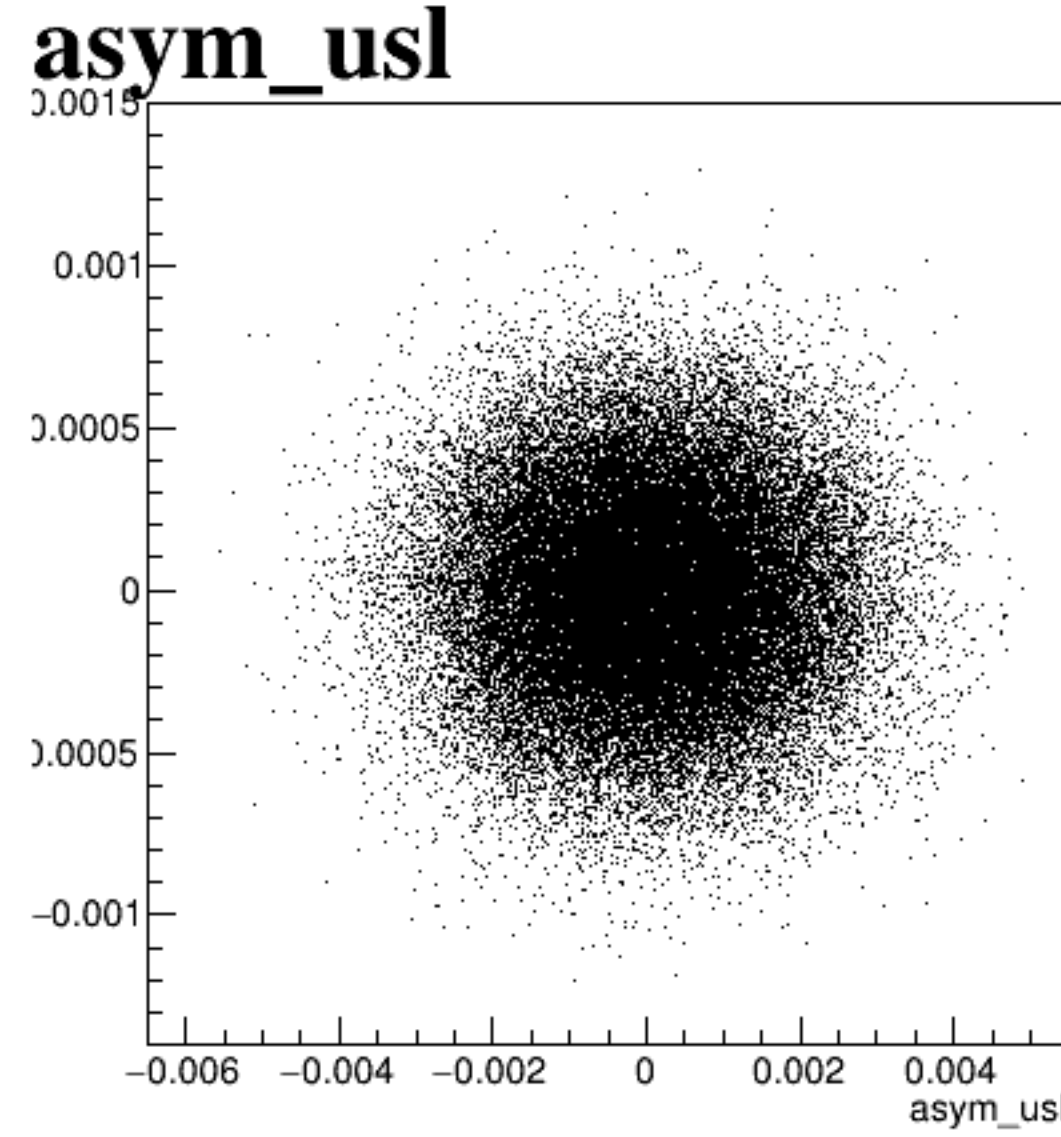
asym_dsr



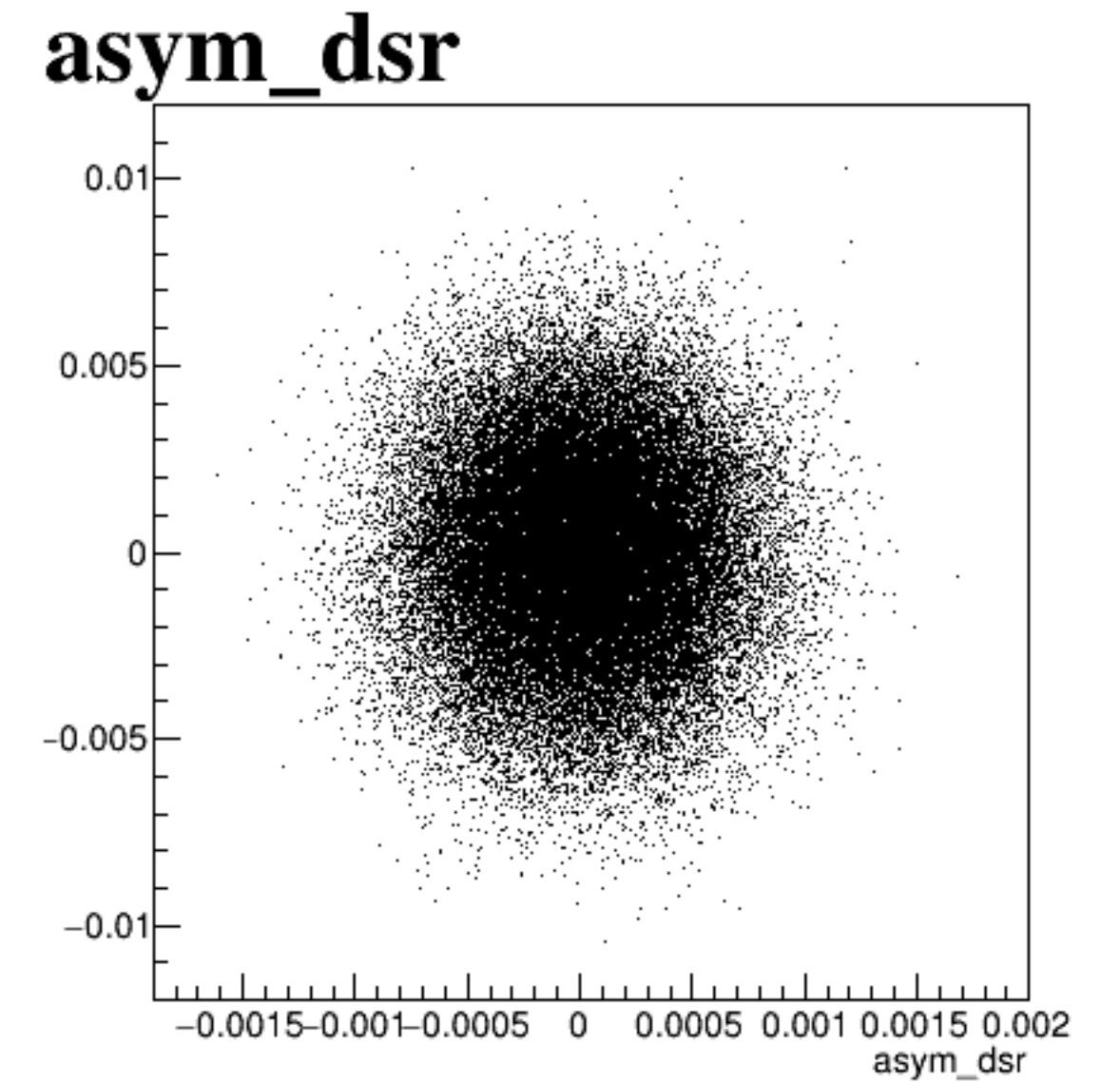
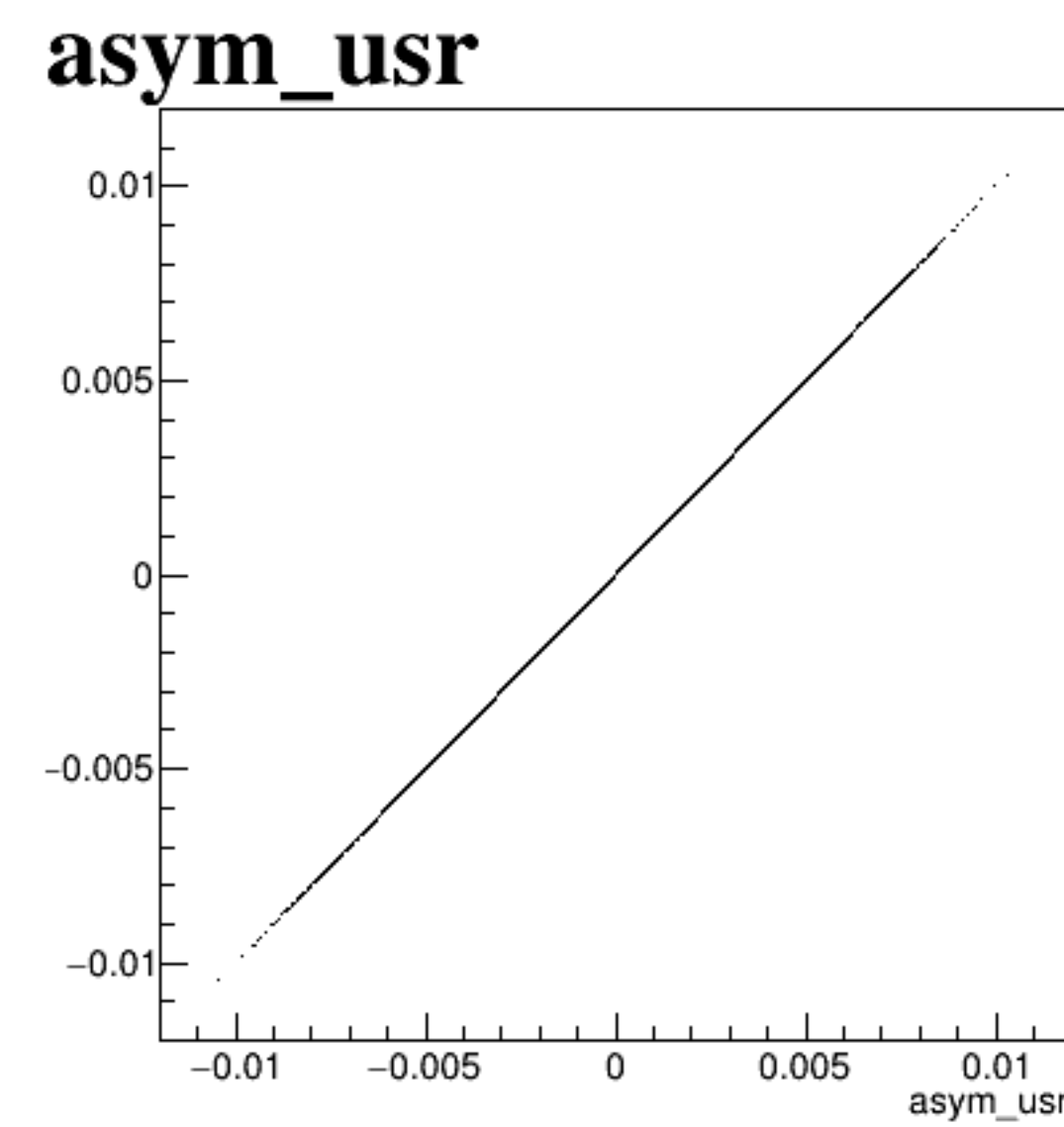
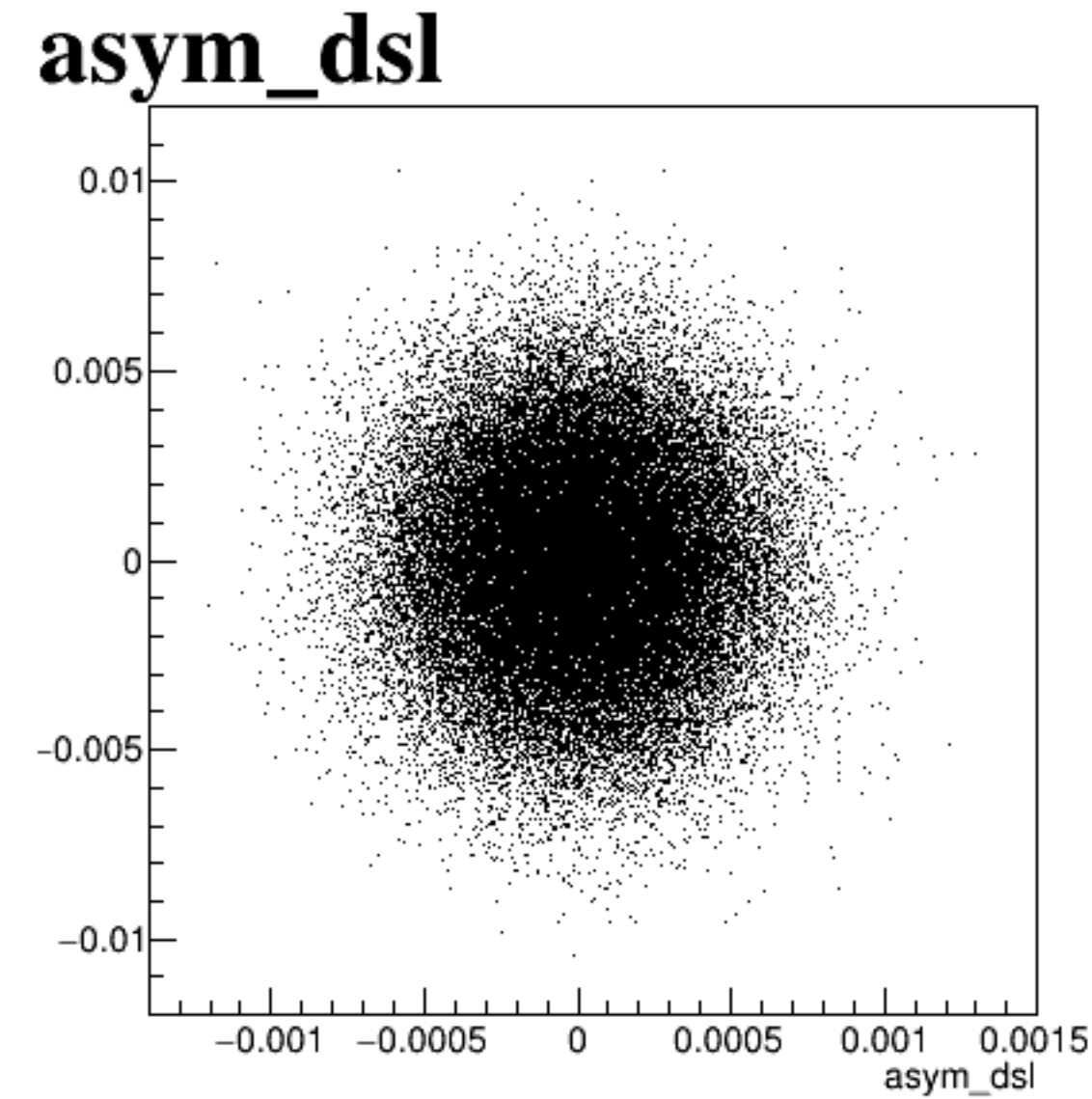
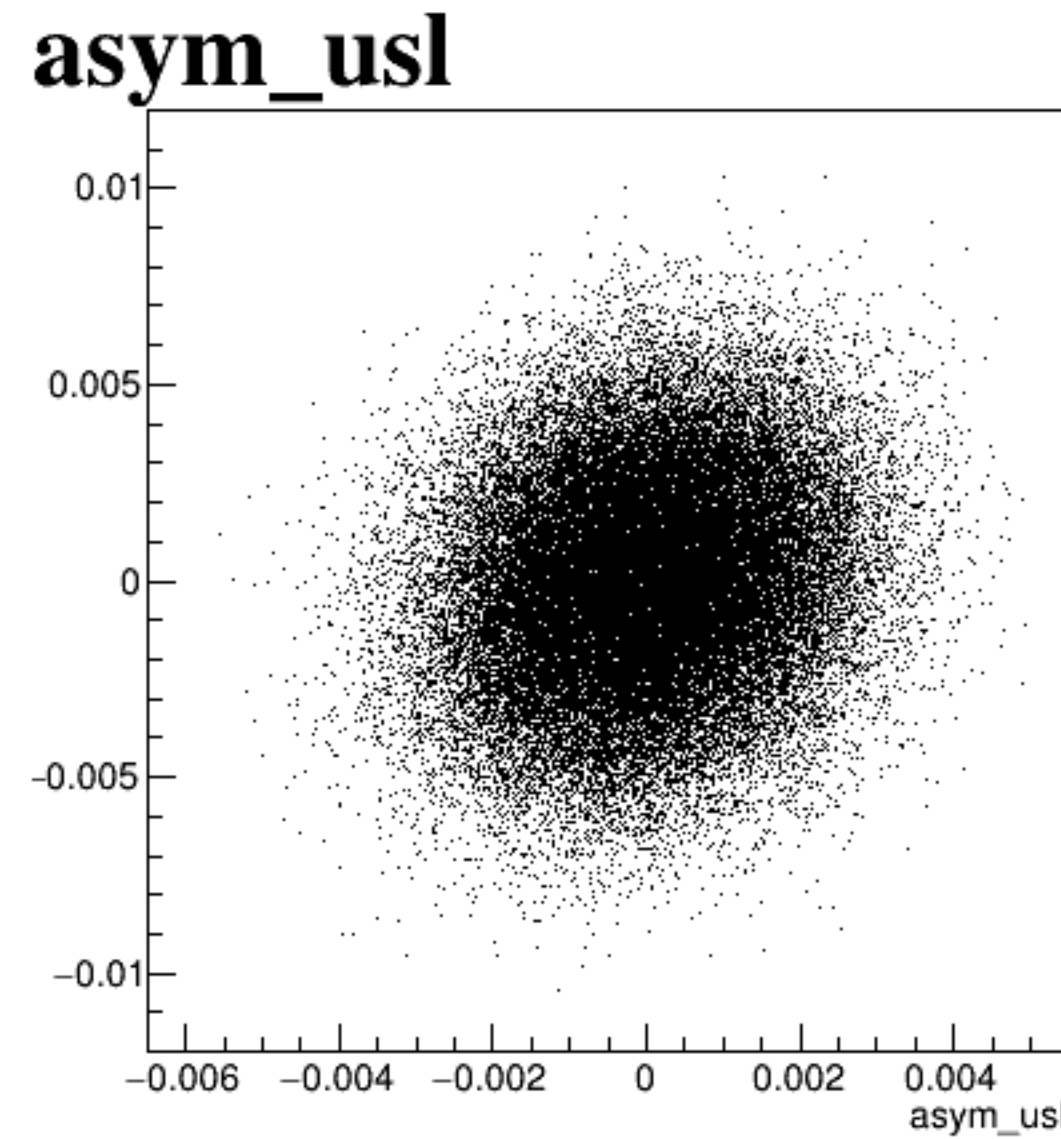
asym_usl



asym_dsl



asym_usr



asym_dsr

