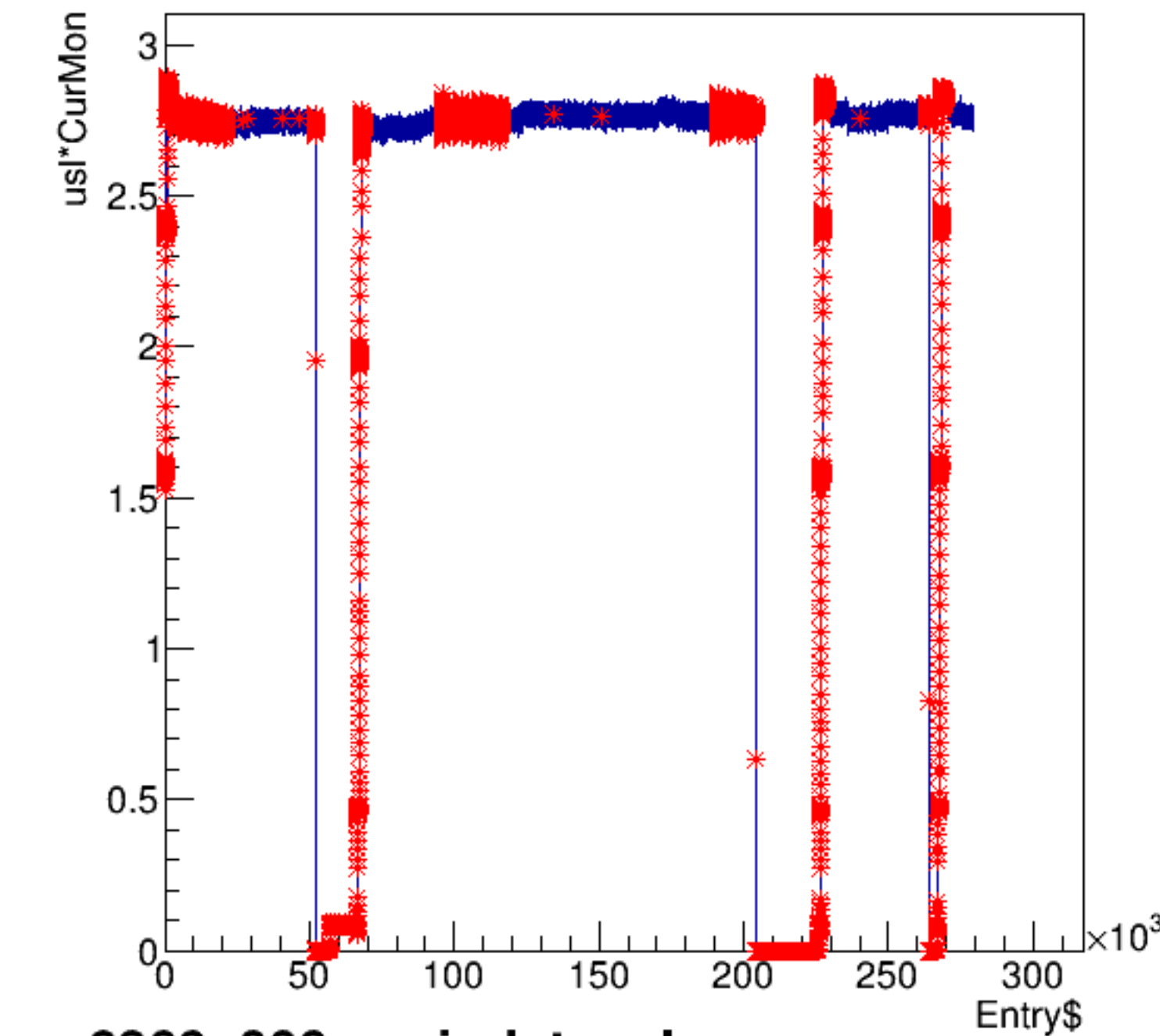
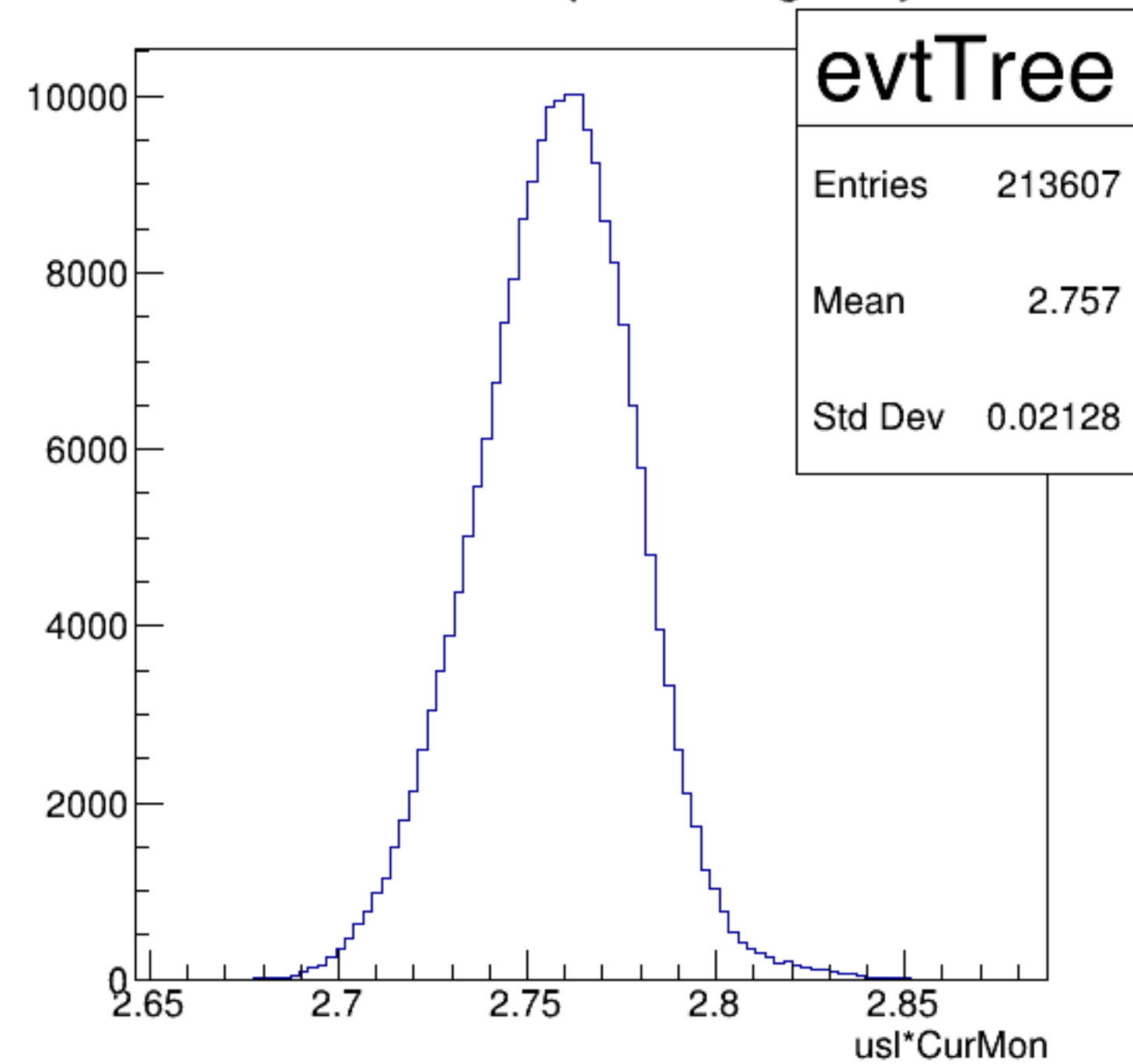


usl*CurMon:Entry\$

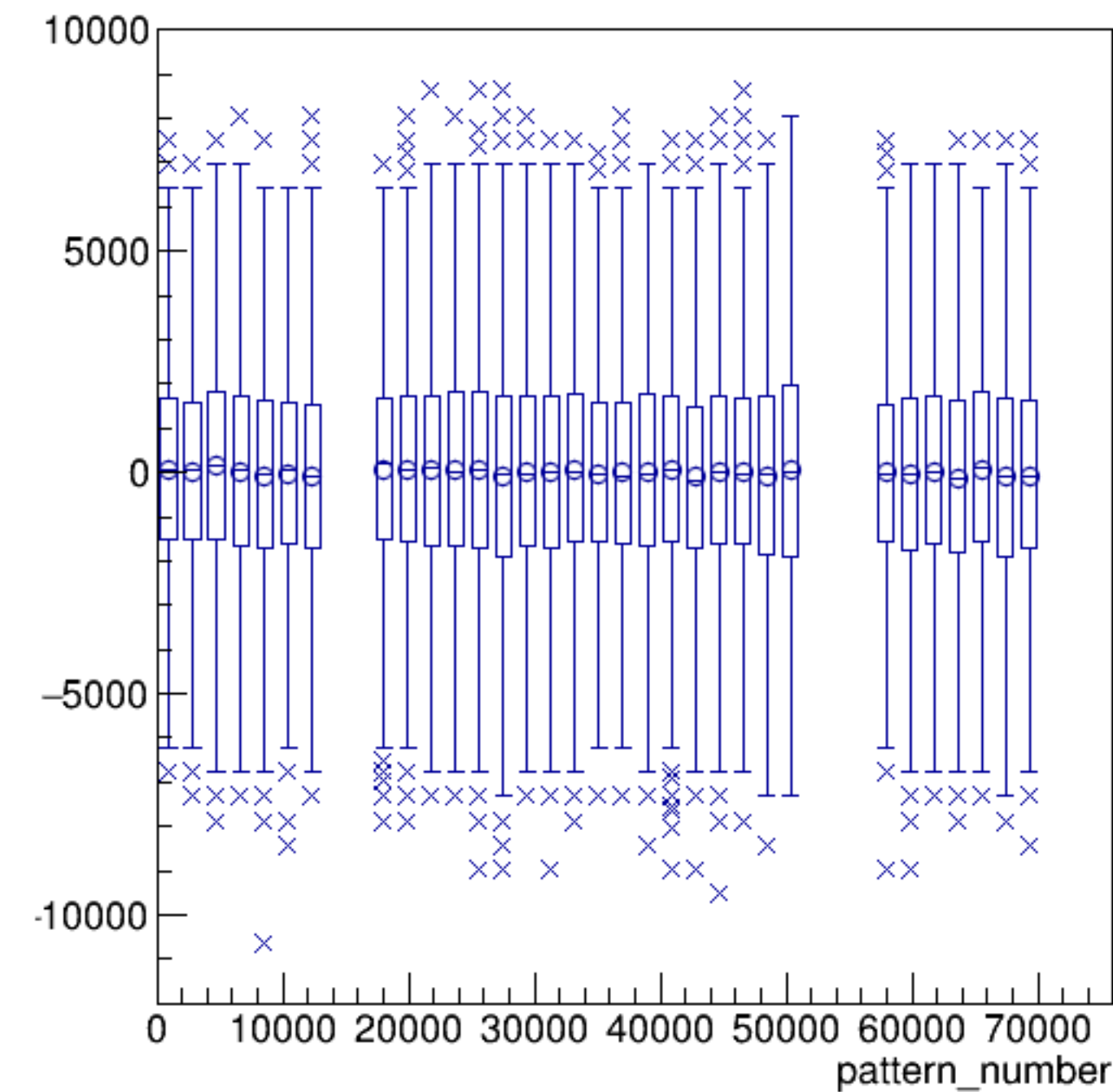


run6369_000_maindet_usl.png

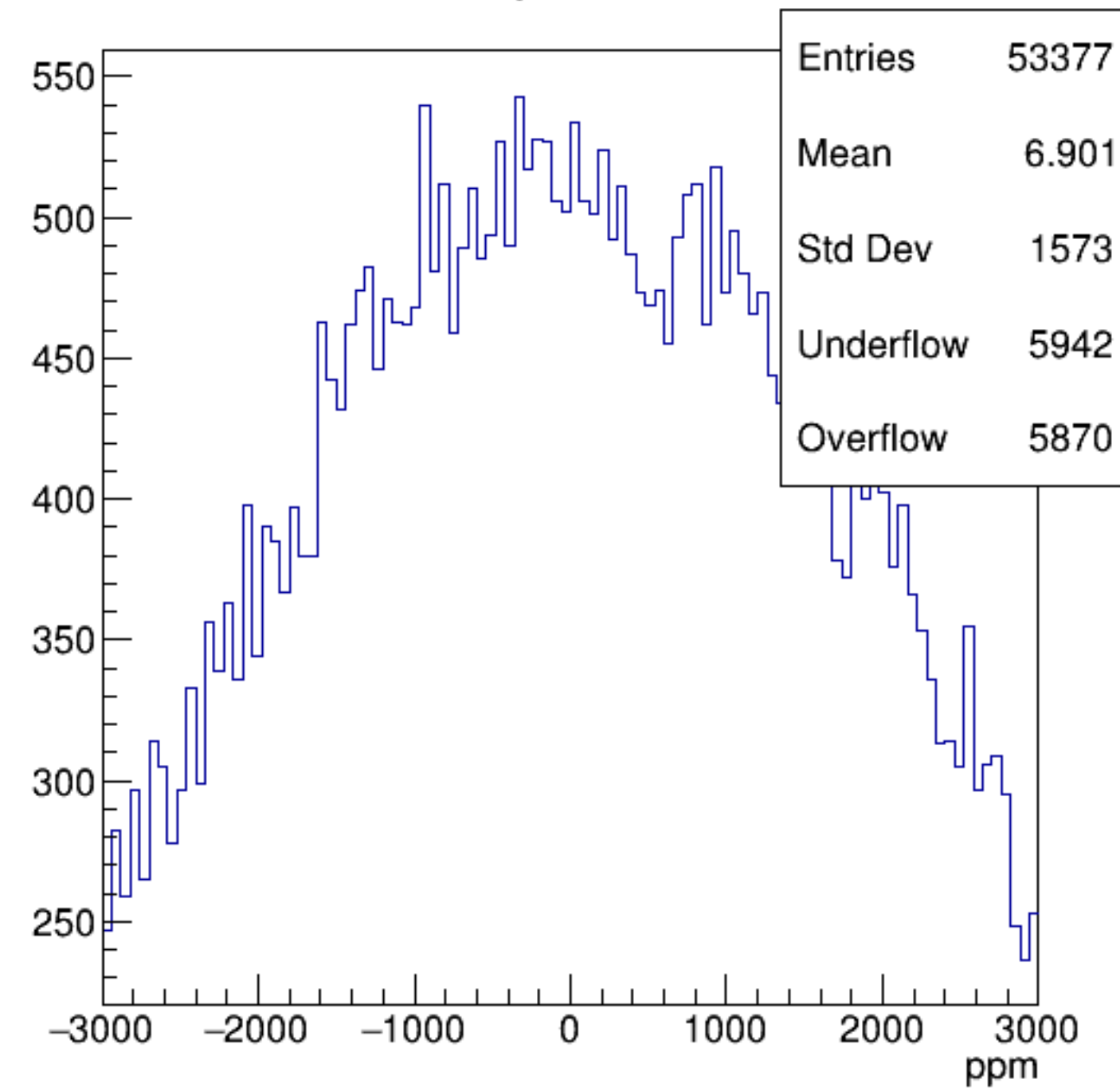
usl*CurMon {ErrorFlag==0}



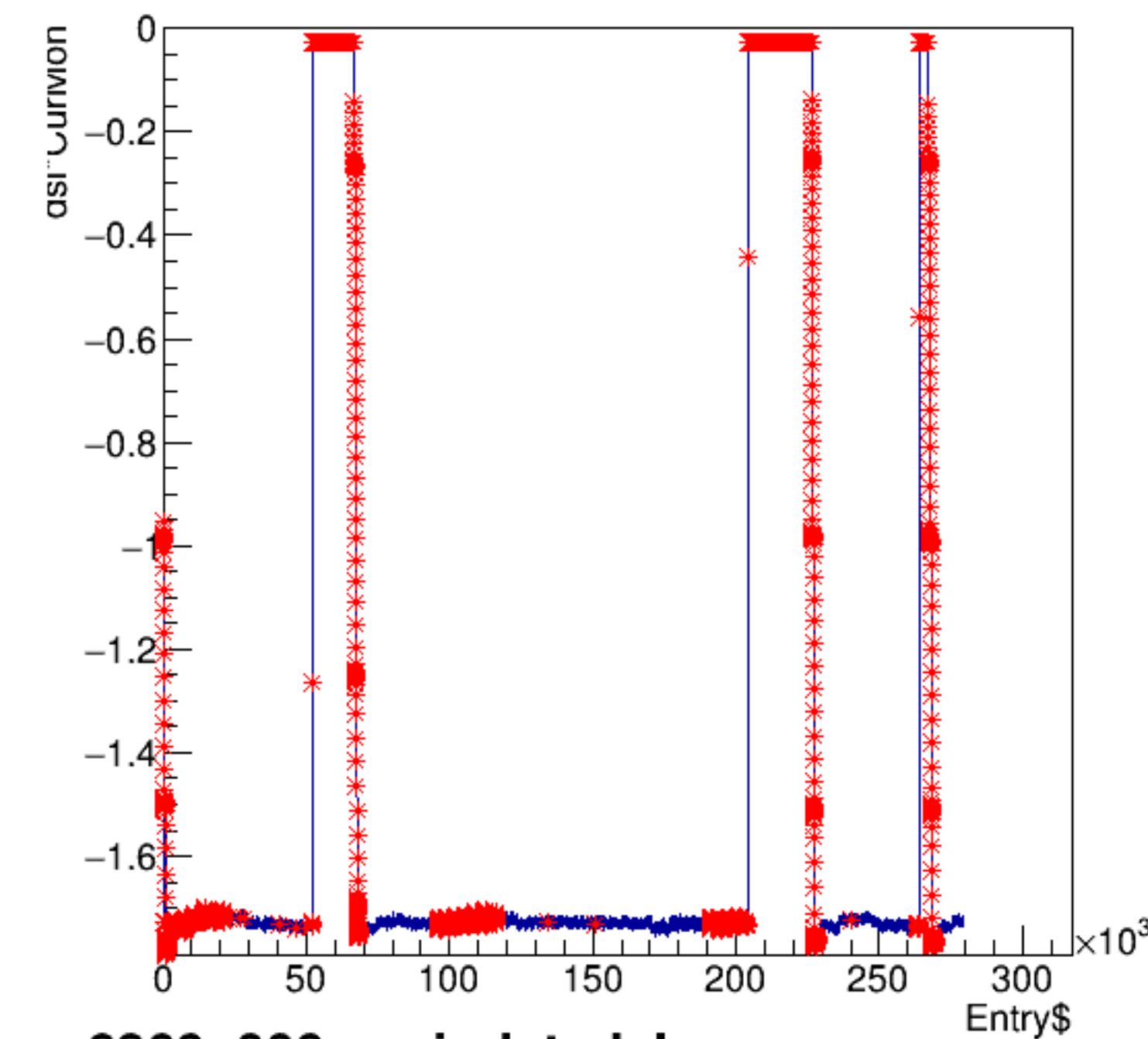
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl

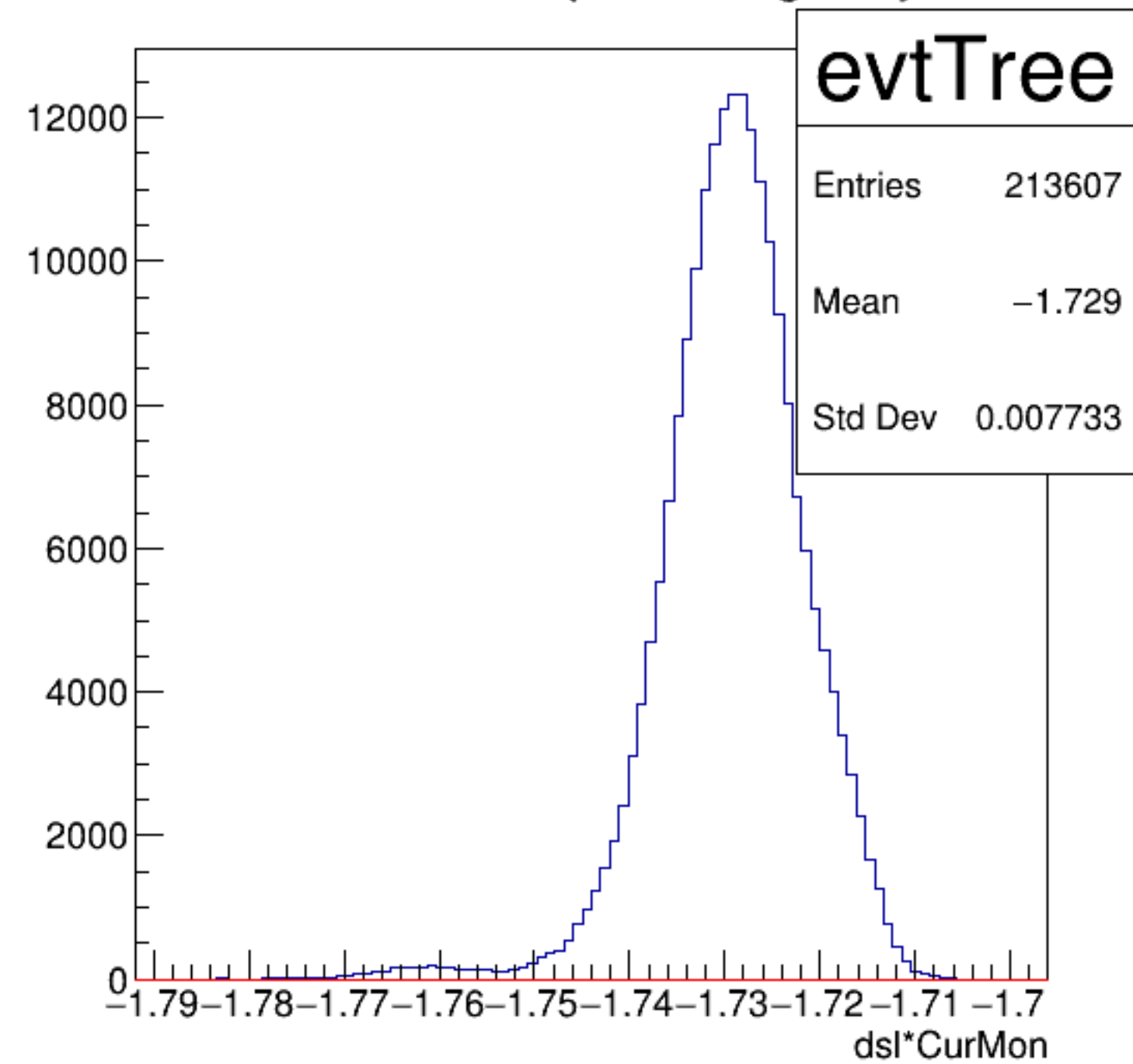


dsl*CurMon:Entry\$

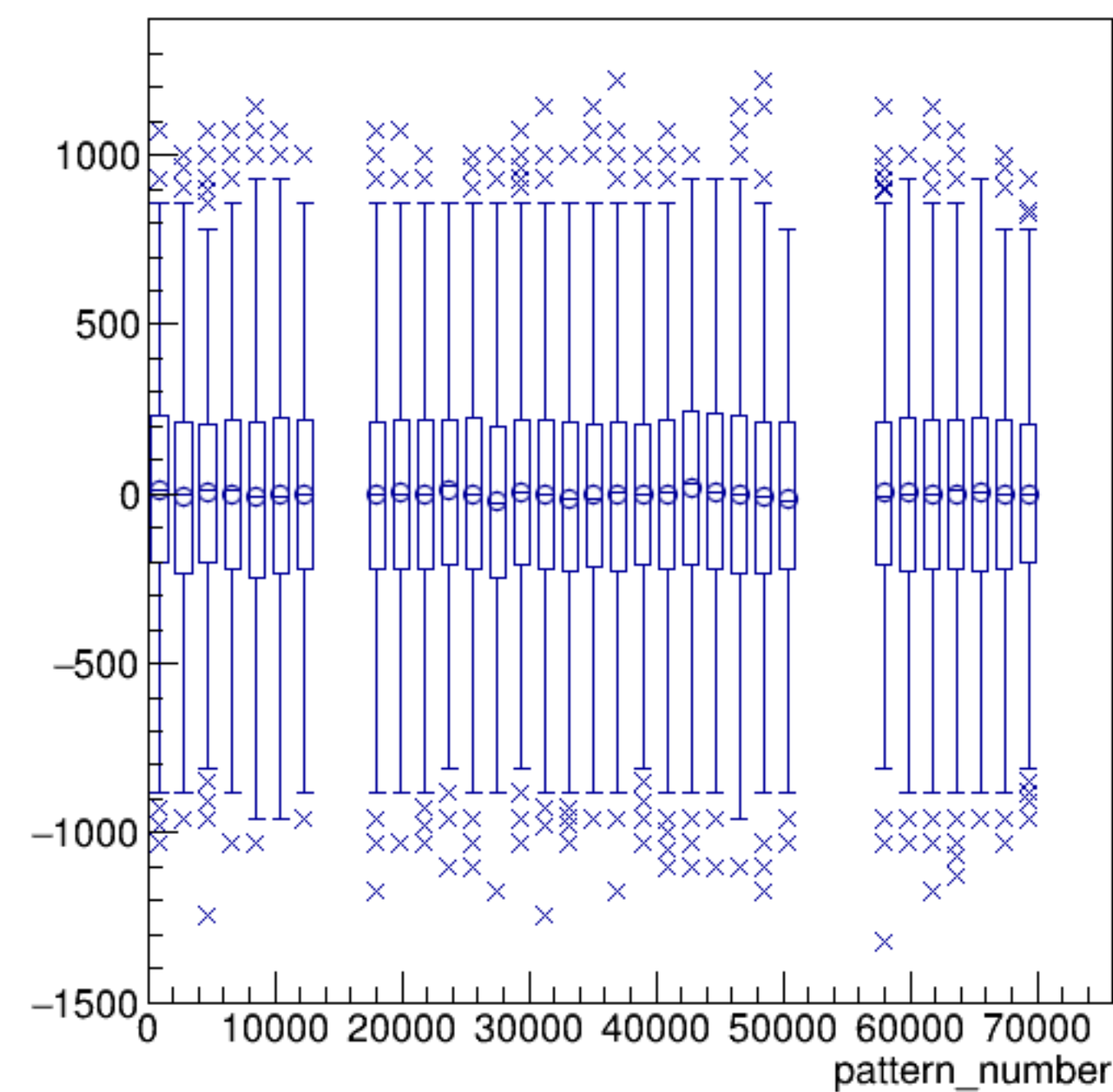


run6369_000_maindet_dsl.png

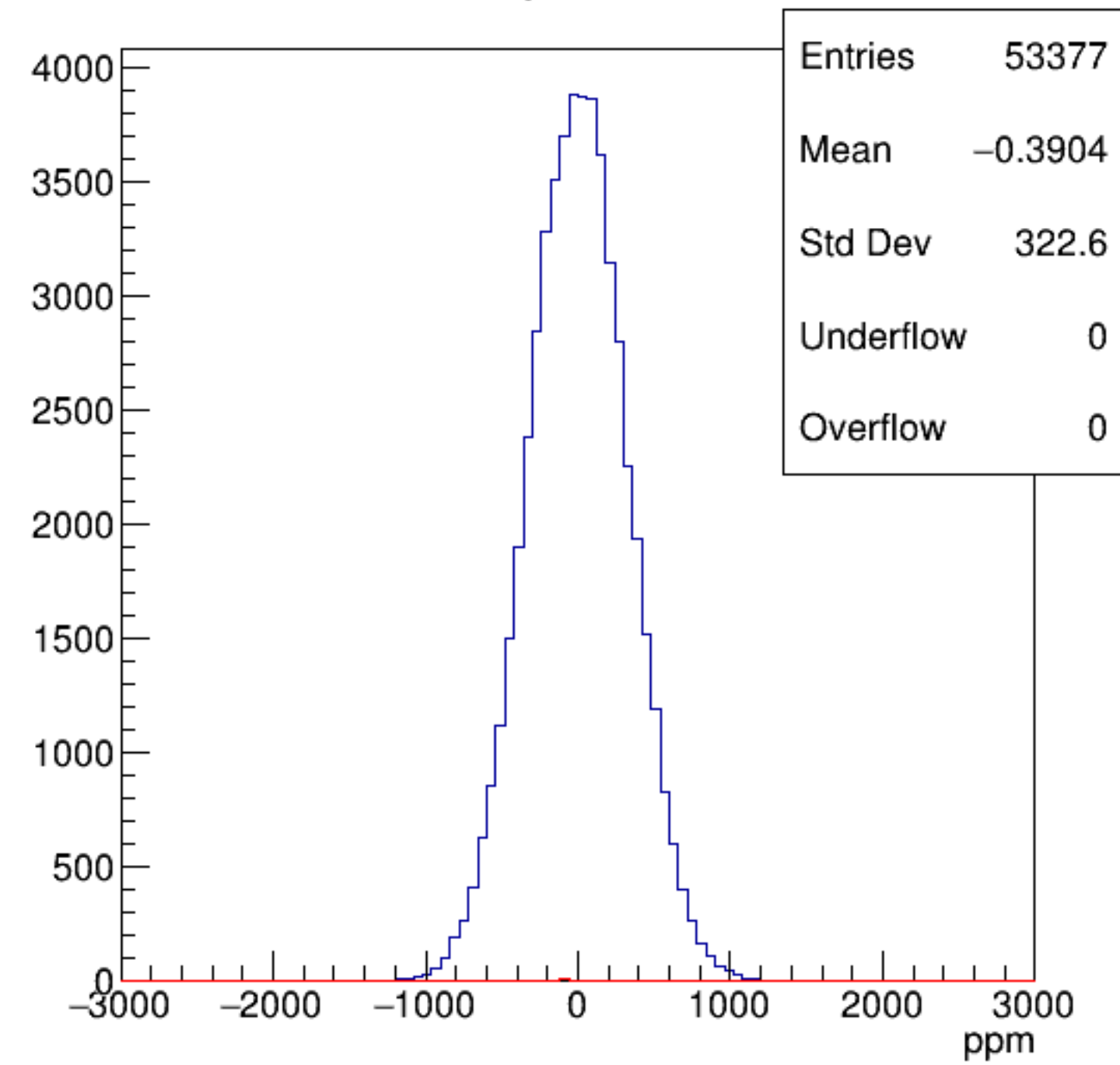
dsl*CurMon {ErrorFlag==0}



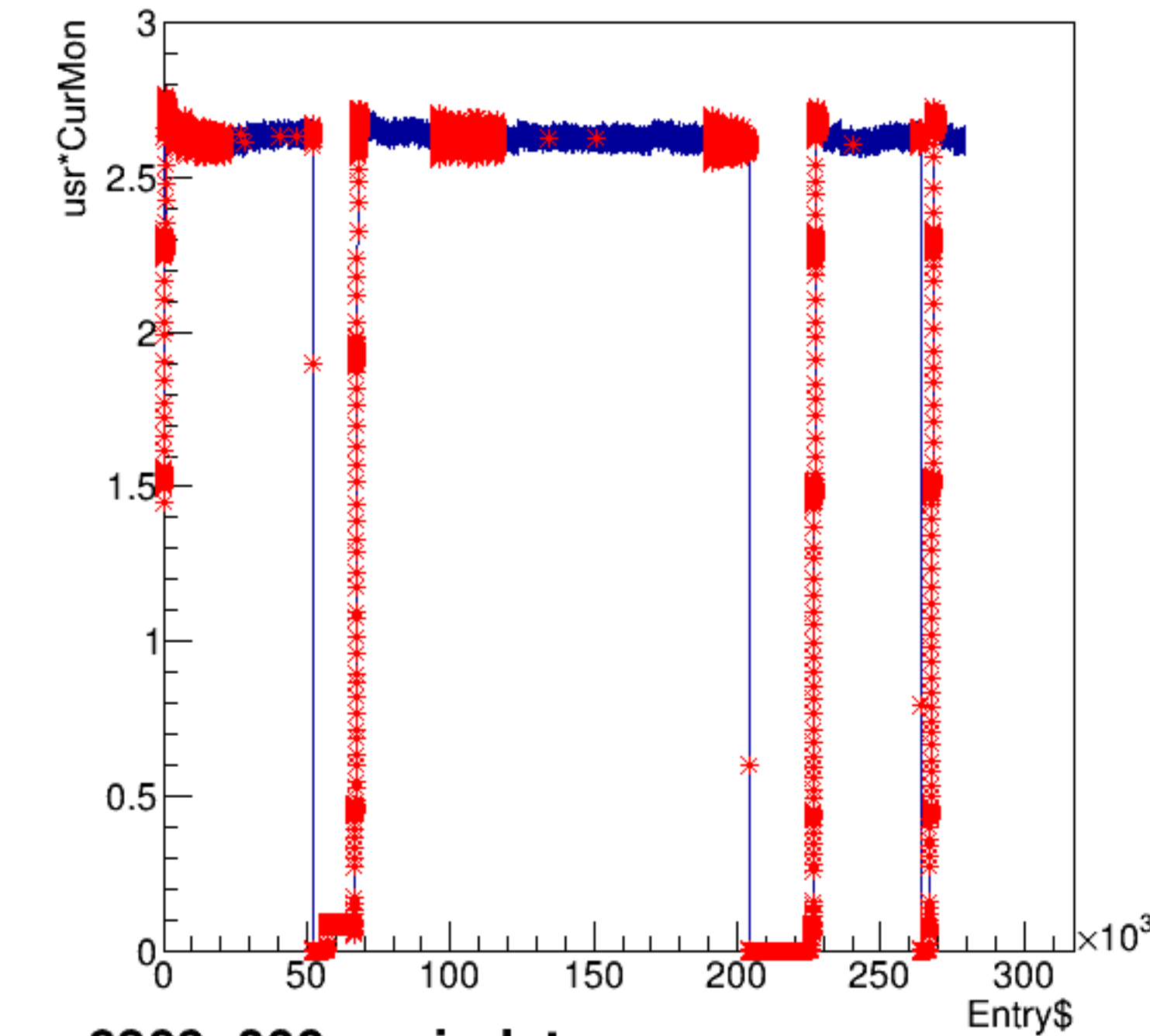
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl

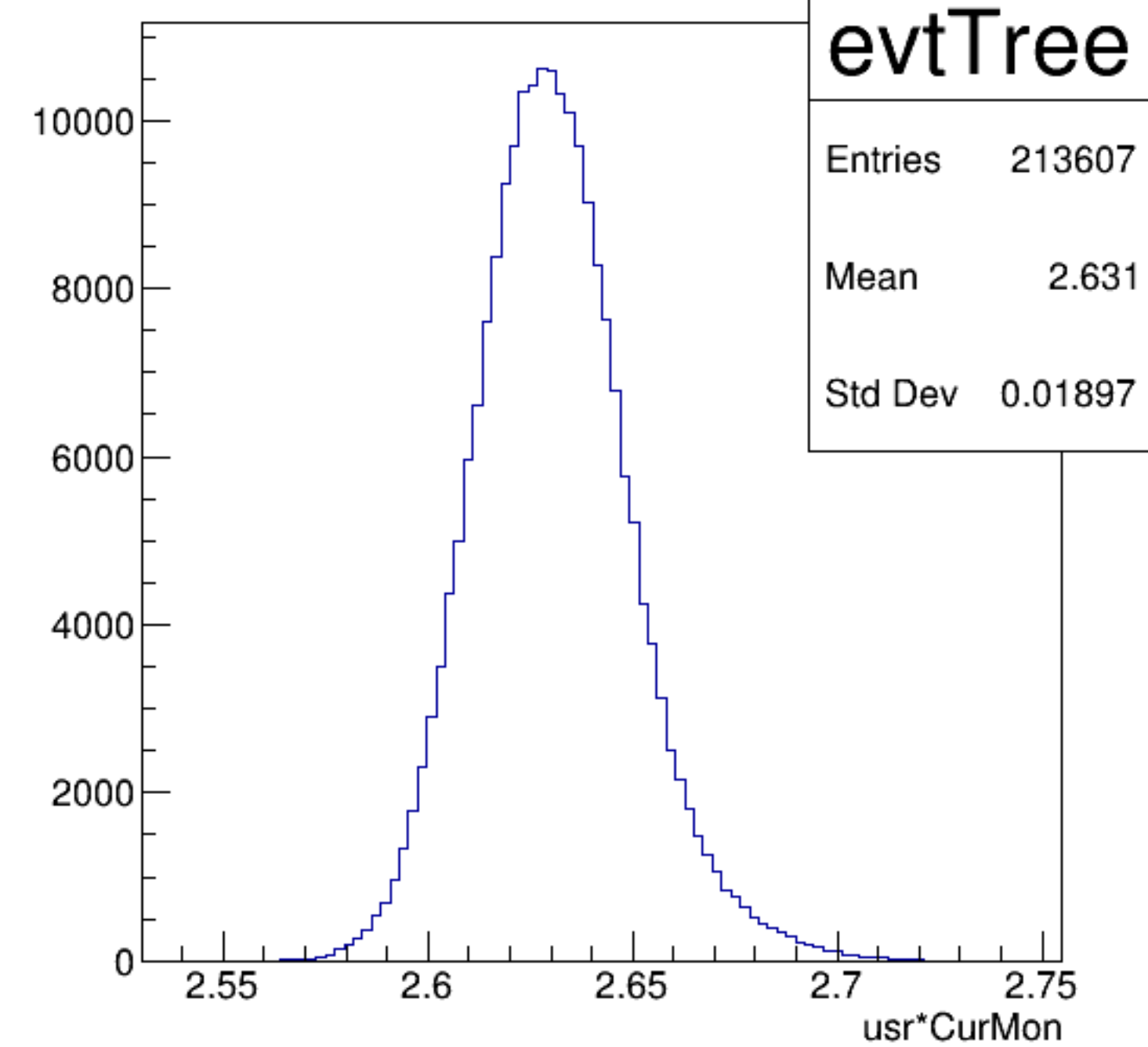


usr*CurMon:Entry\$

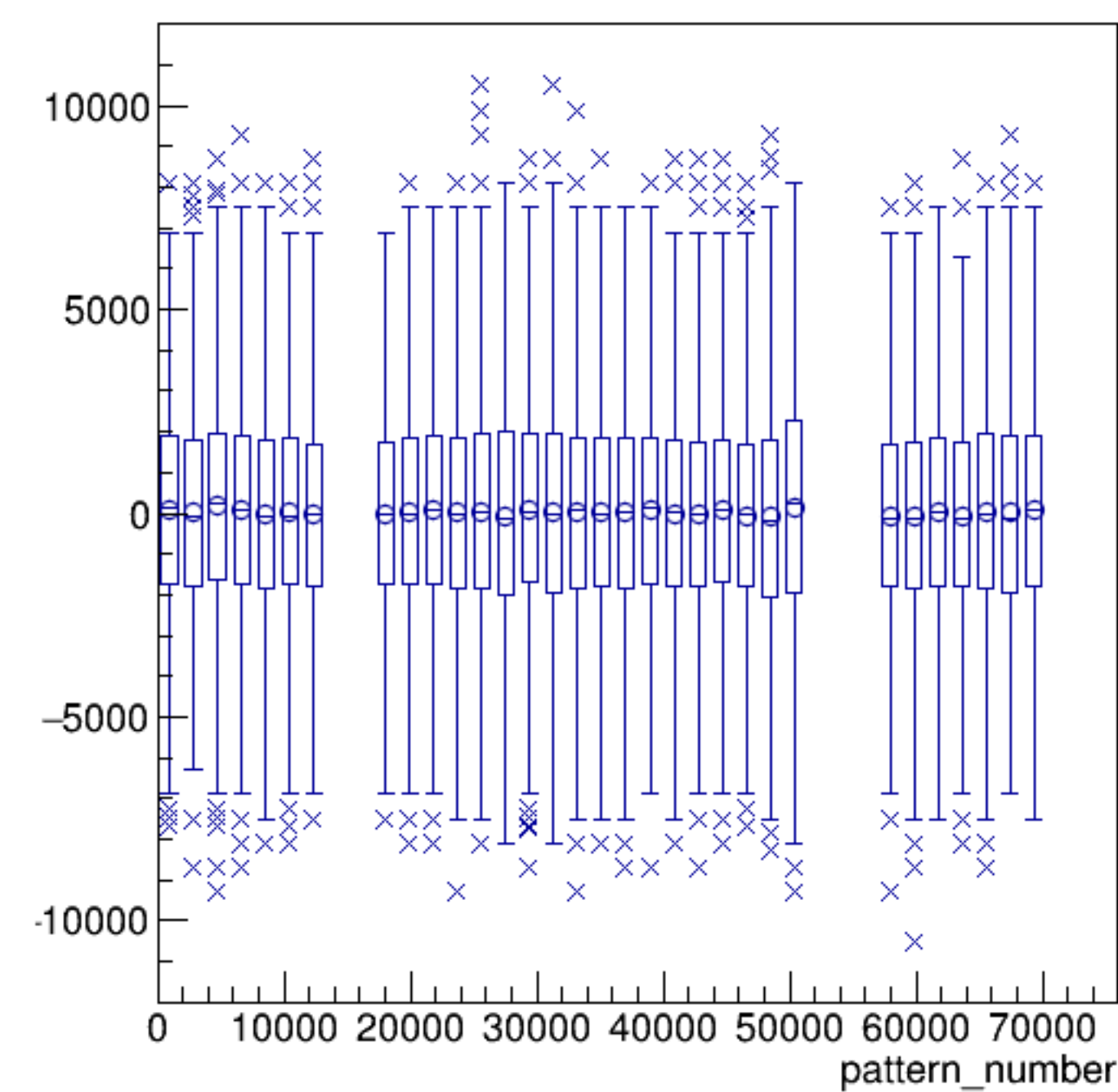


run6369_000_maindet_usr.png

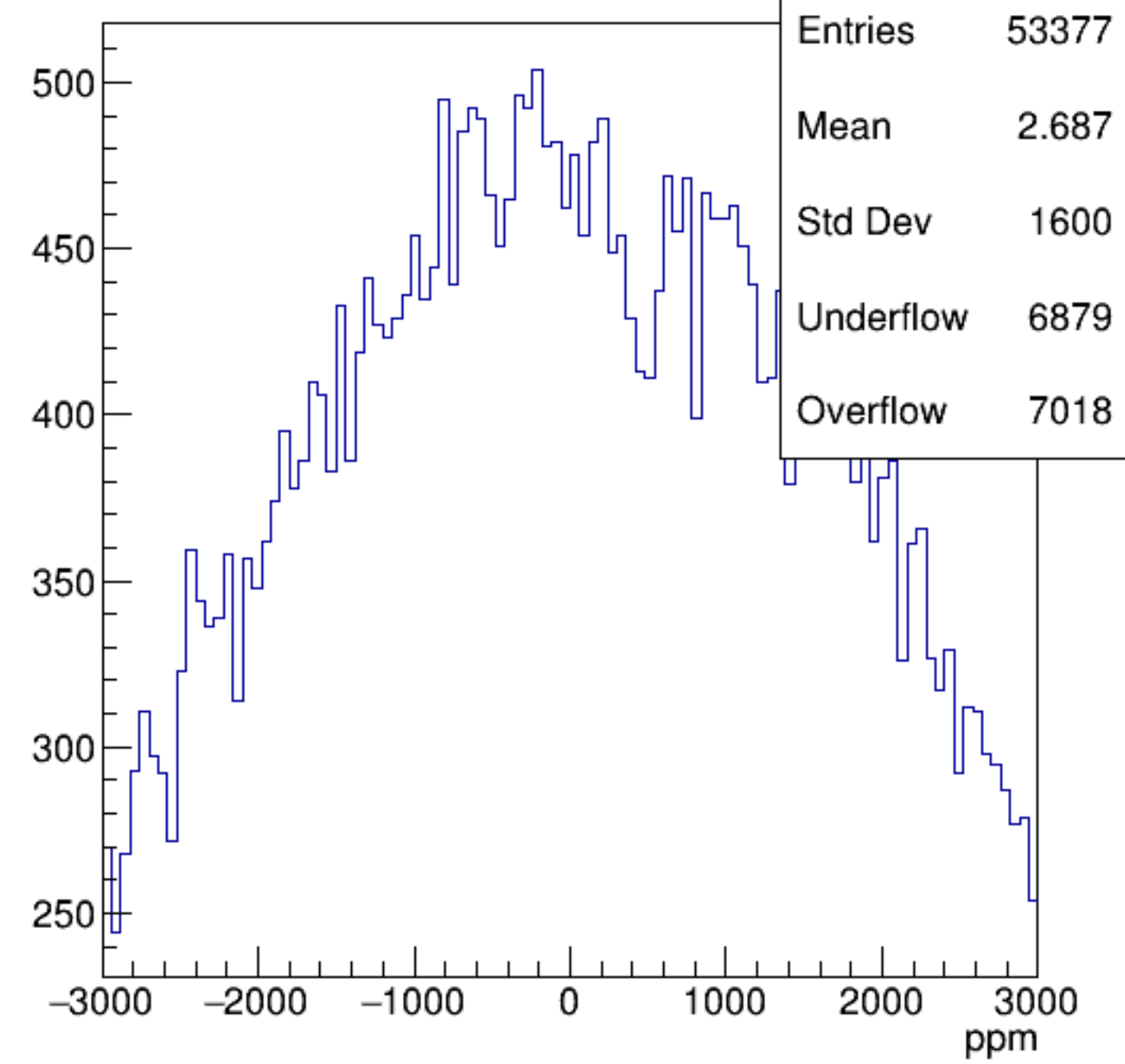
usr*CurMon {ErrorFlag==0}



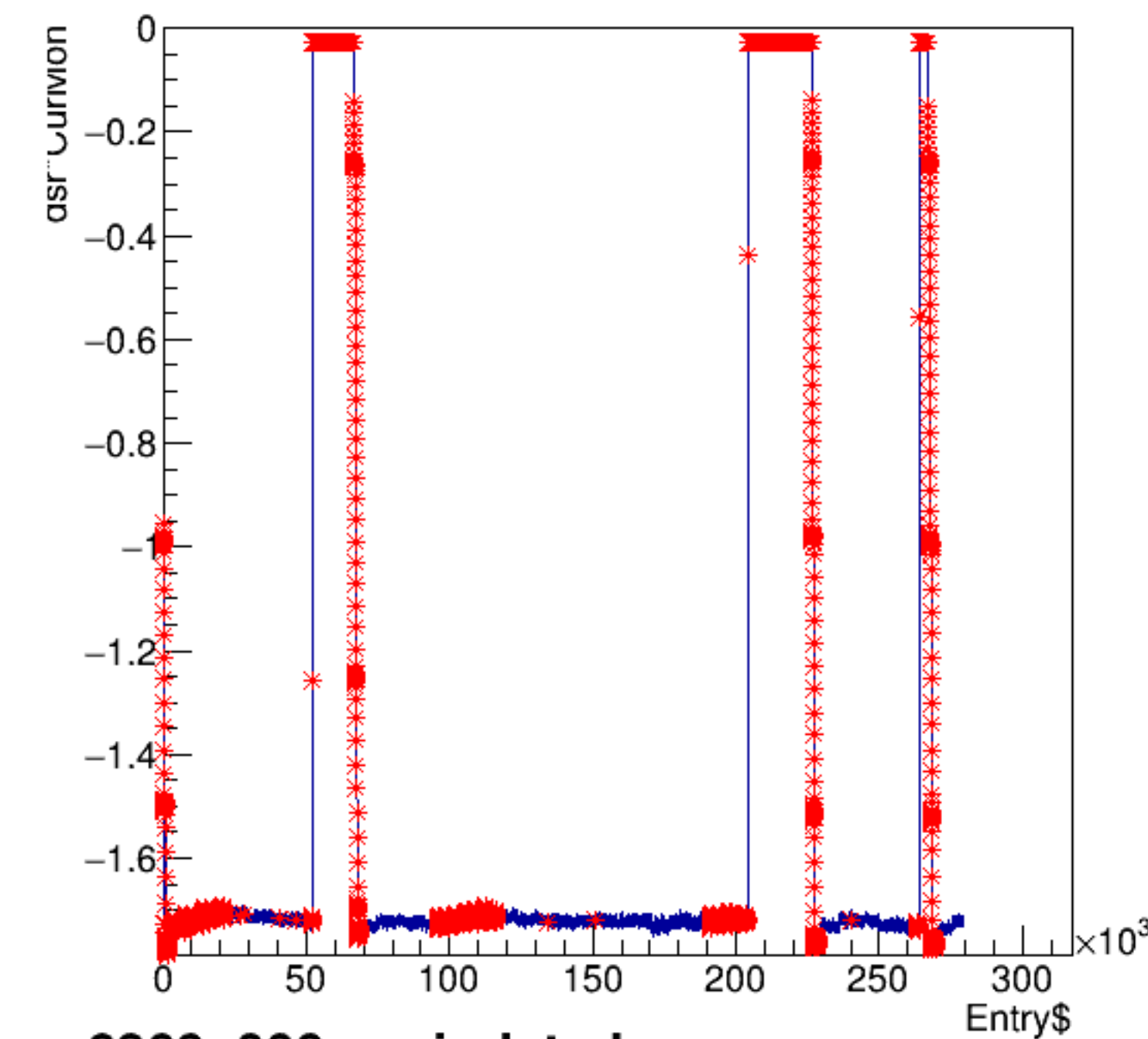
asym_usr/ppm:pattern_number {ErrorFlag==0}



asym_usr

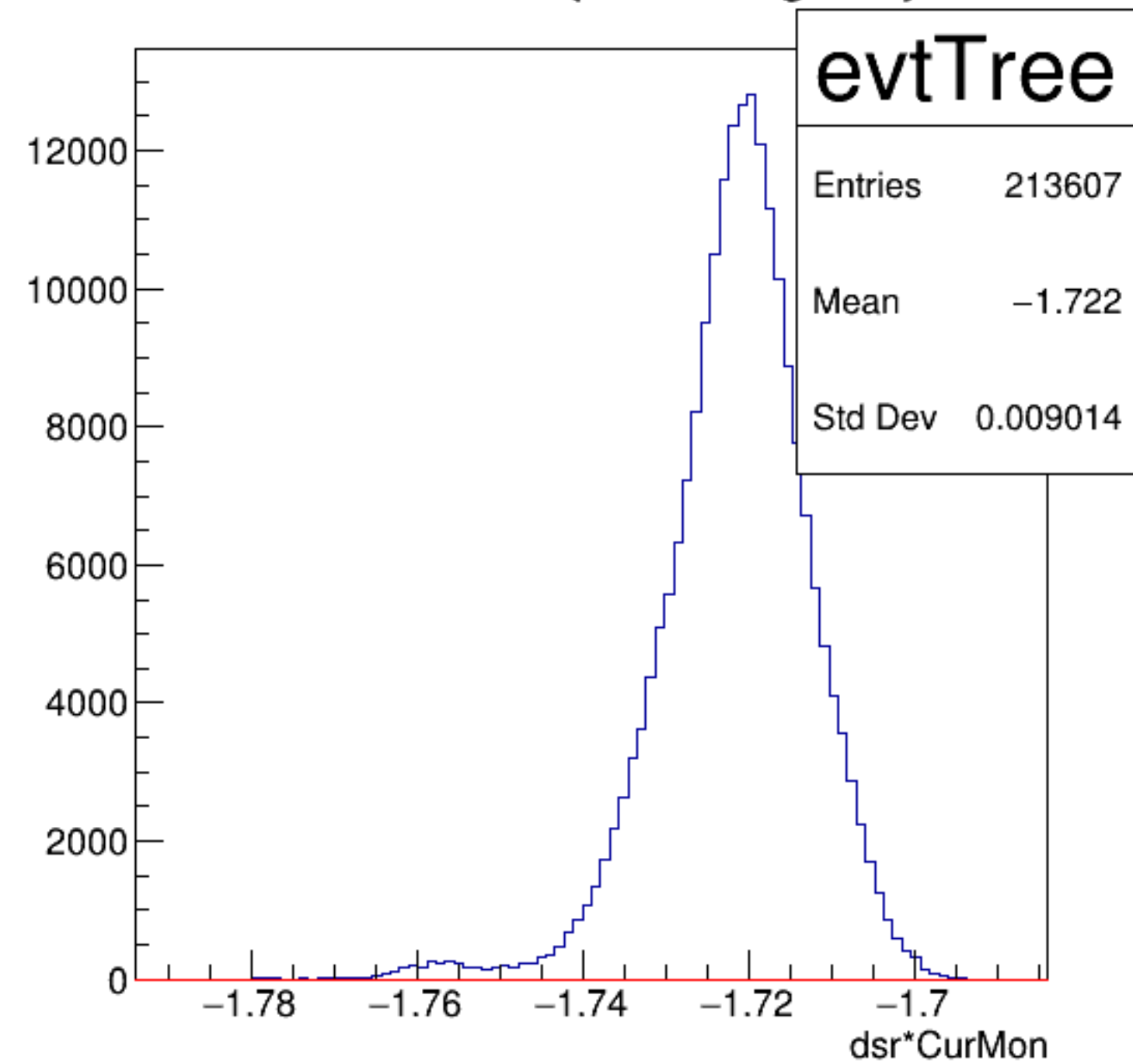


dsr*CurMon:Entry\$

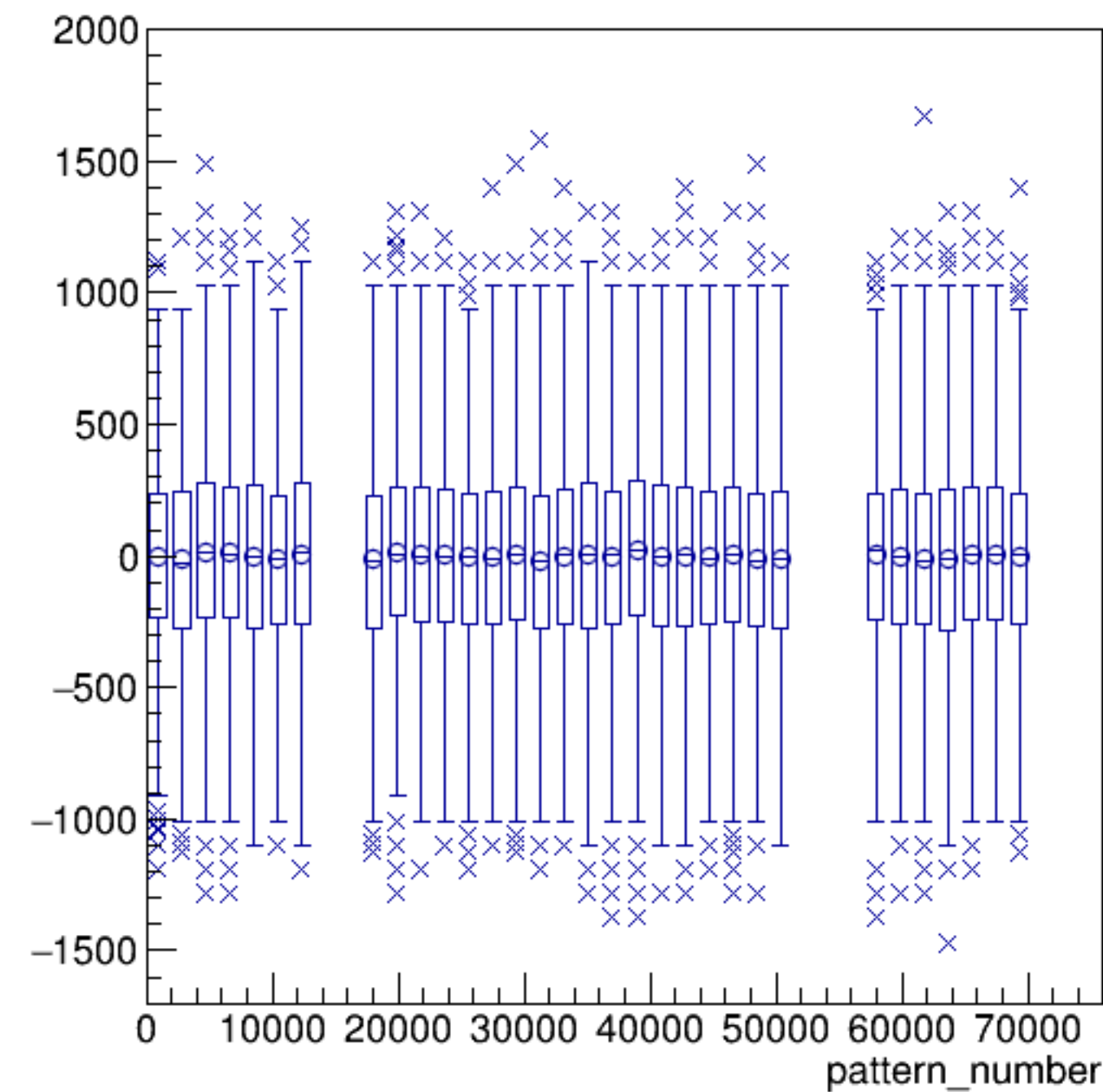


run6369_000_maindet_dsr.png

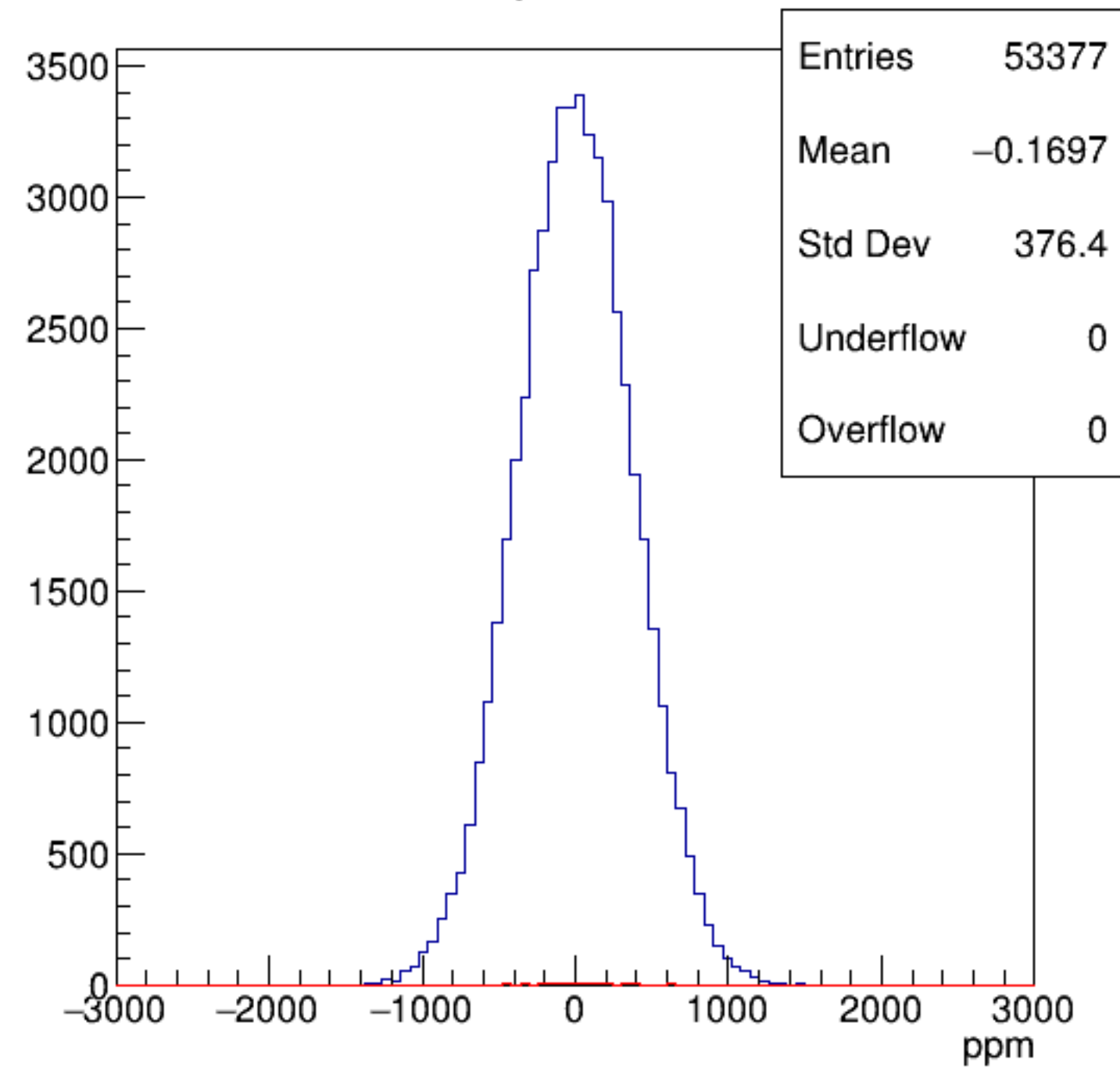
dsr*CurMon {ErrorFlag==0}



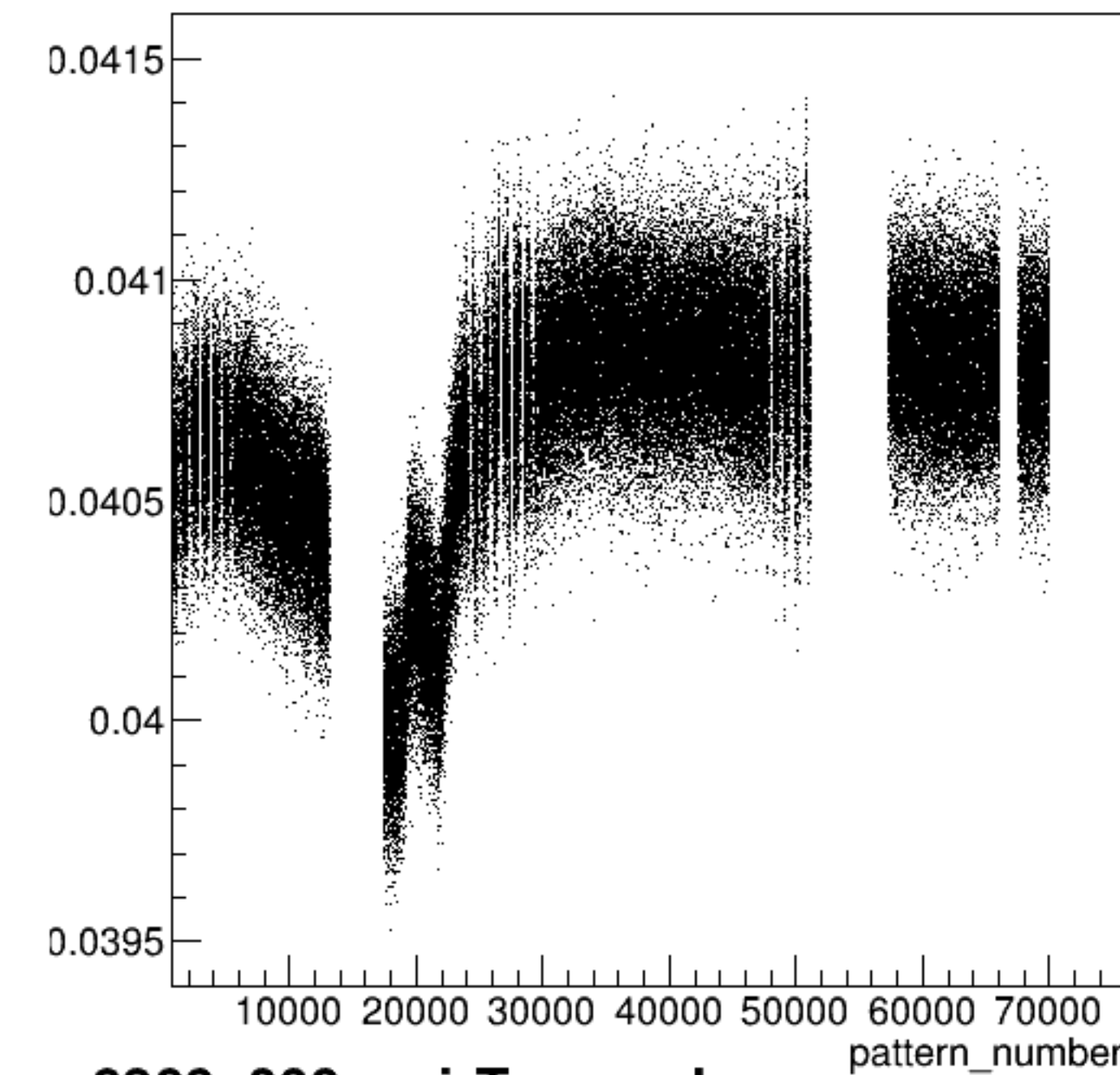
asym_dsr/ppm:pattern_number {ErrorFlag==0}



asym_dsr

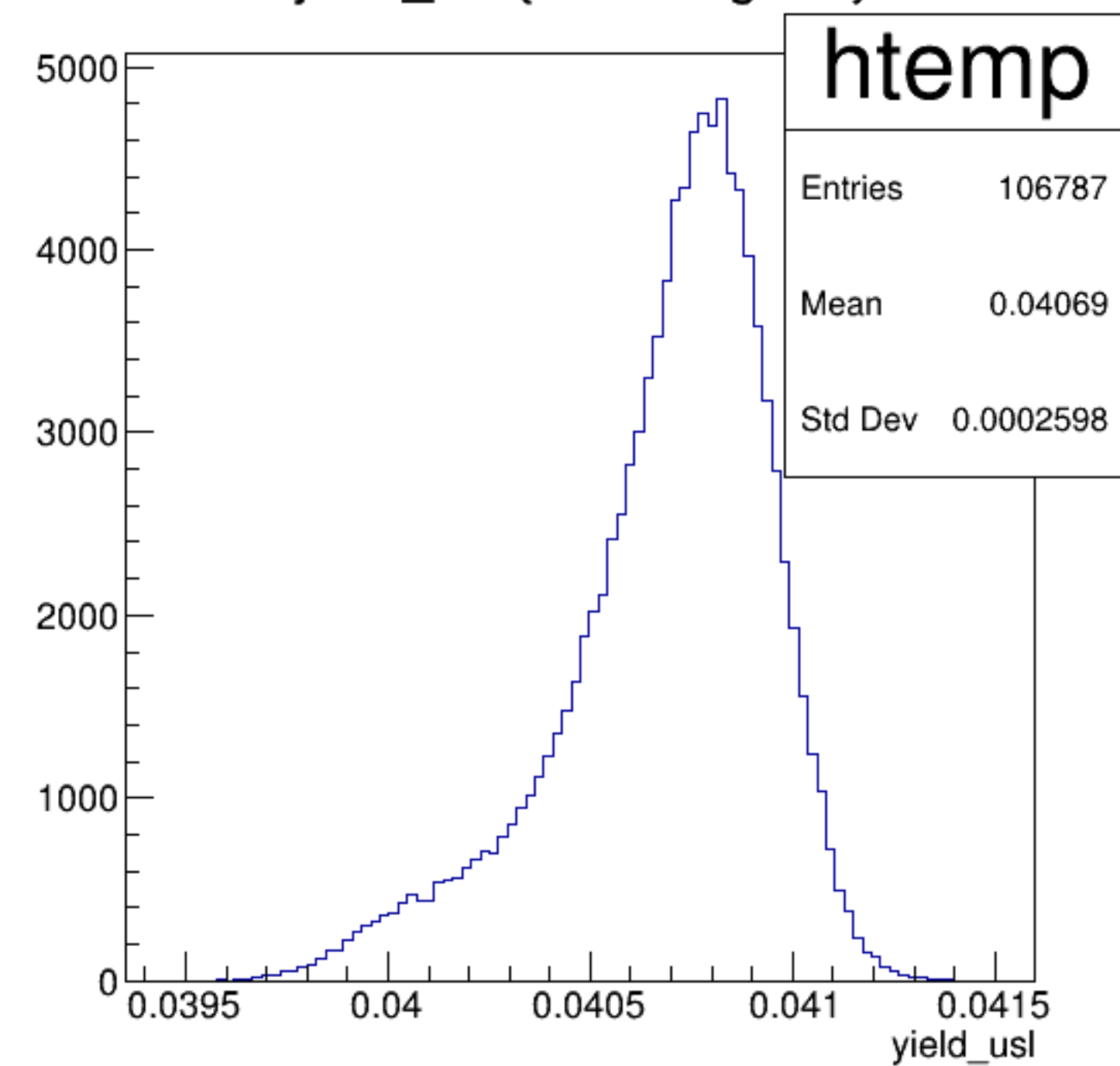


yield_usl:pattern_number {ErrorFlag==0}

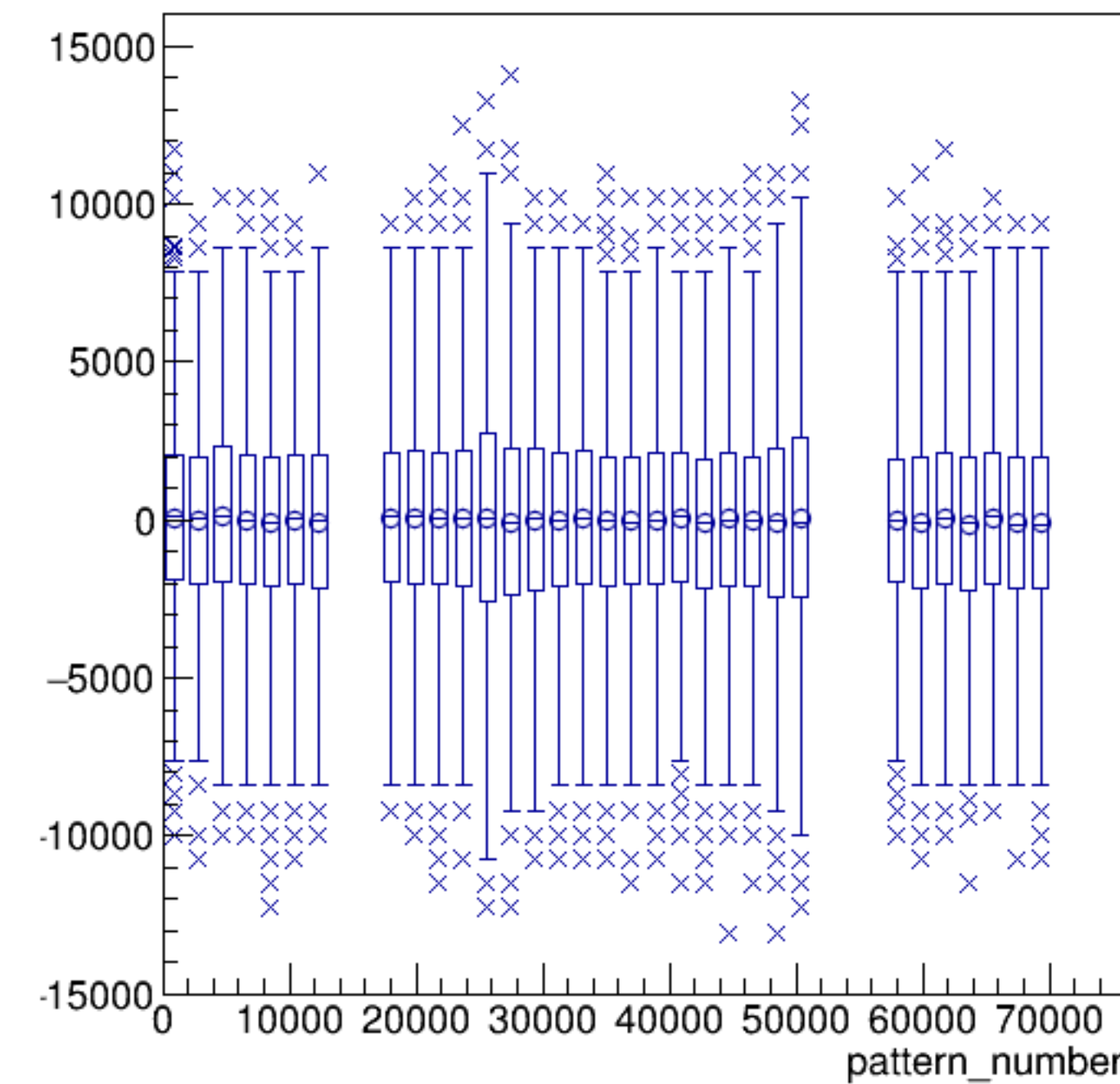


run6369_000_pairTree_usl.png

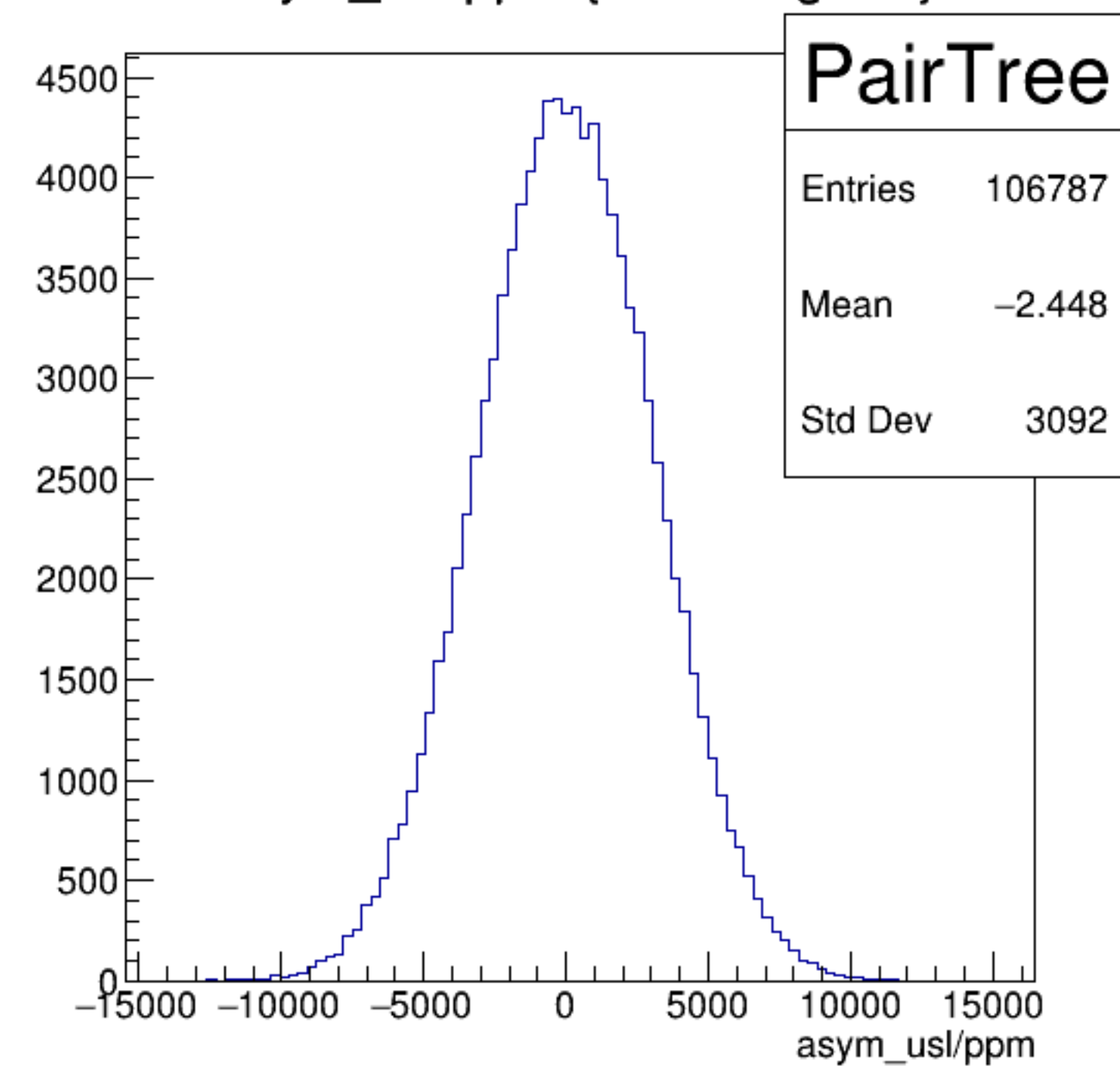
yield_usl {ErrorFlag==0}



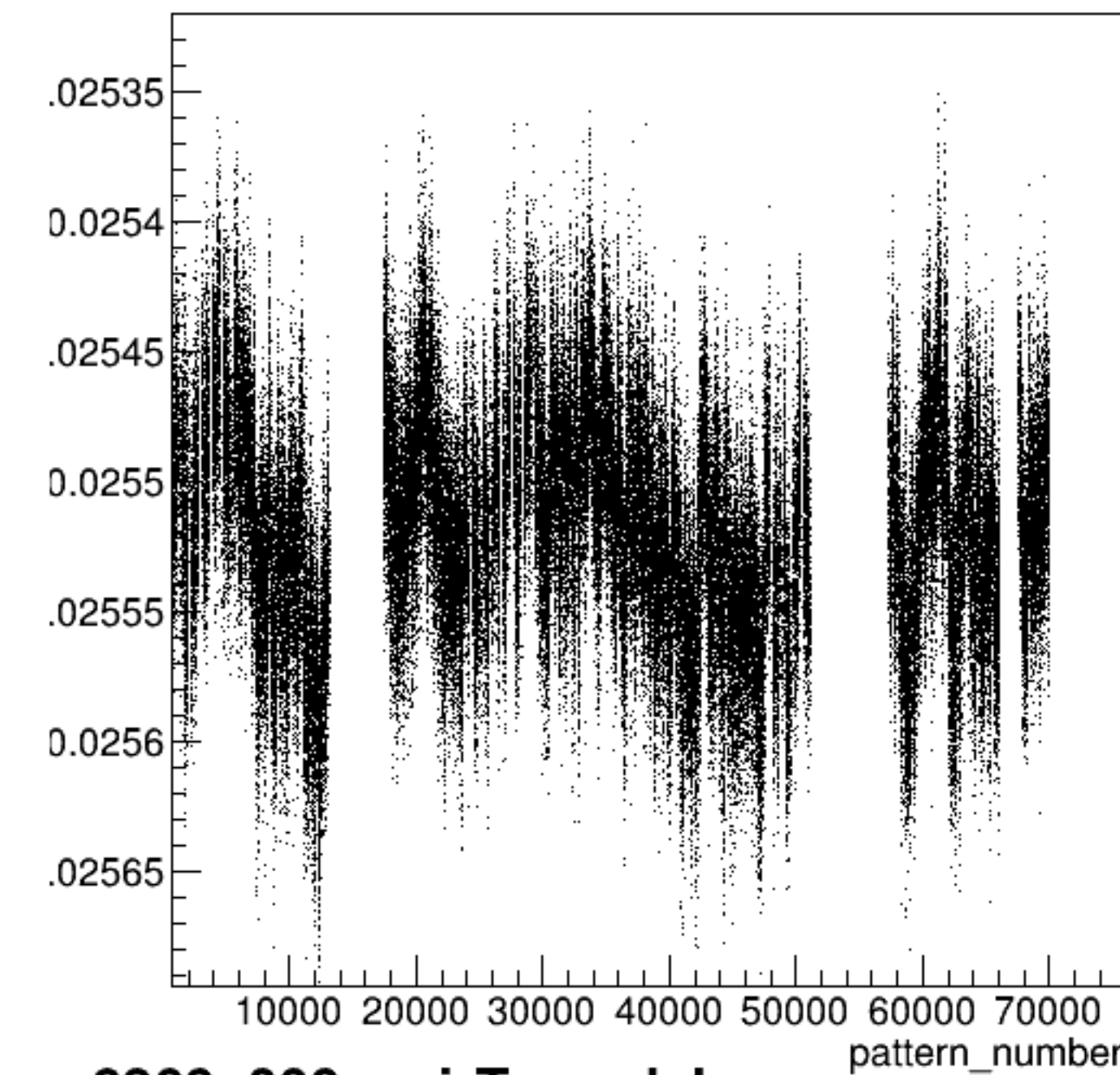
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asym_usl/ppm {ErrorFlag==0}

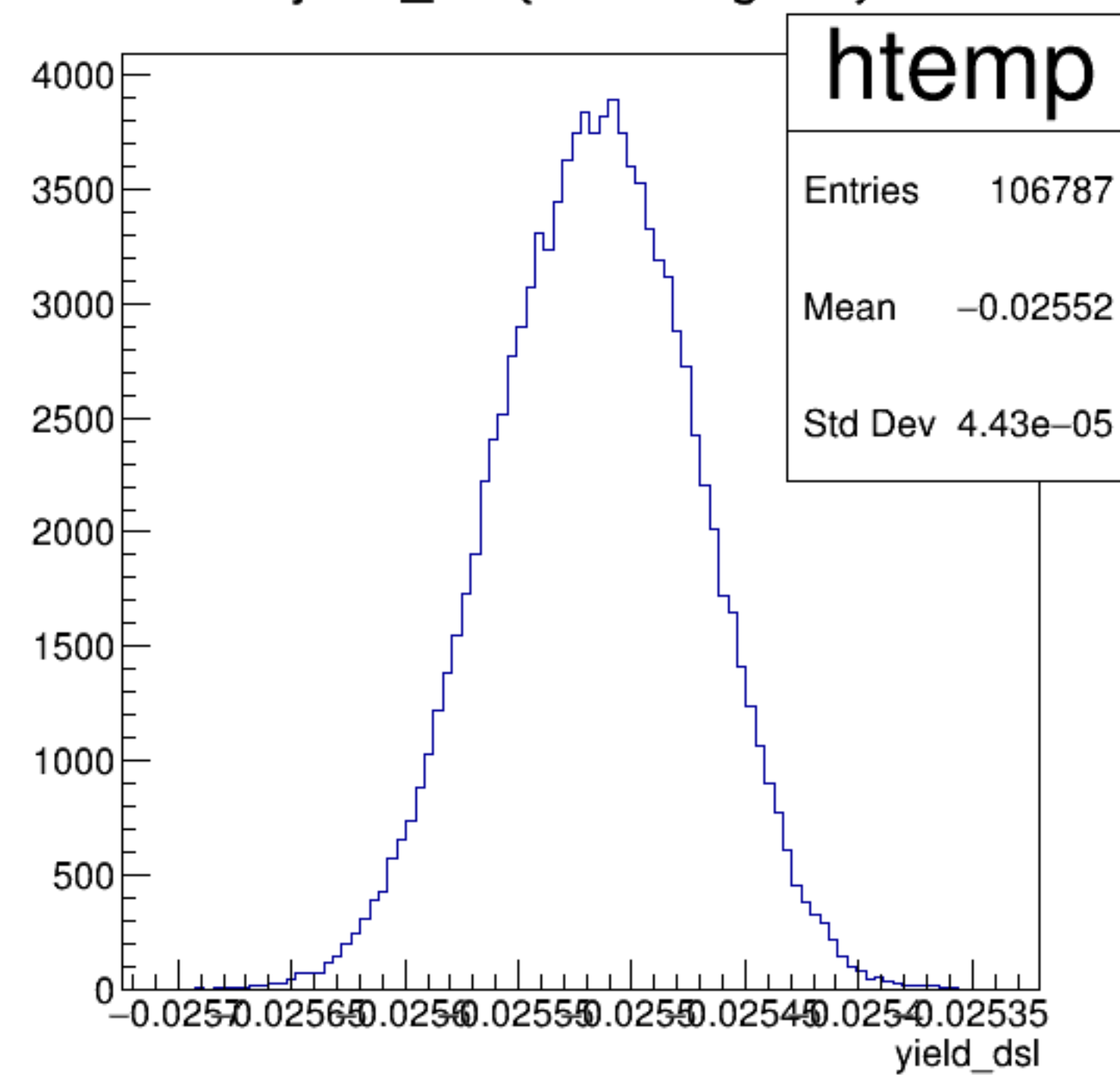


yield_dsl:pattern_number {ErrorFlag==0}

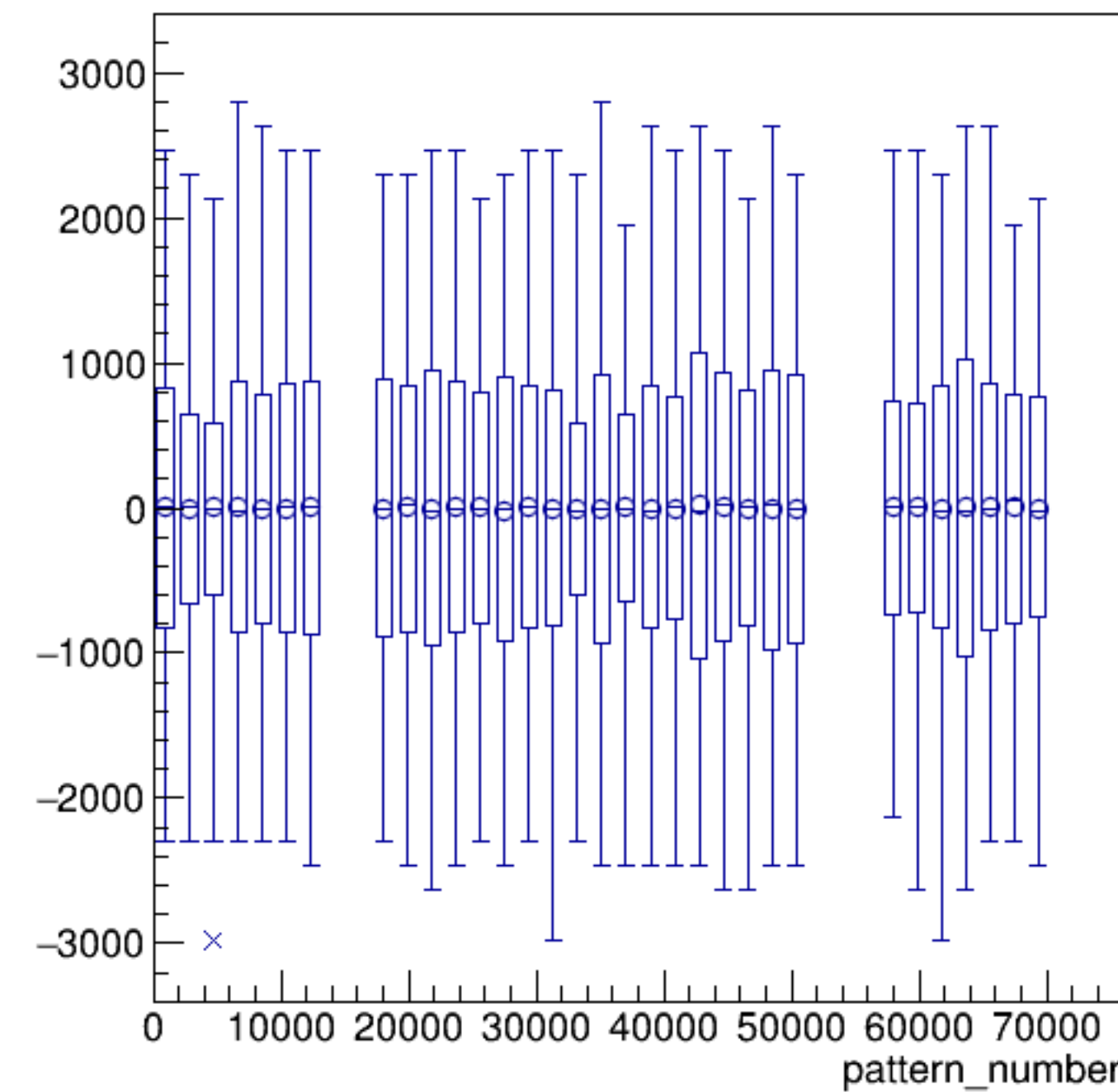


run6369_000_pairTree_dsl.png

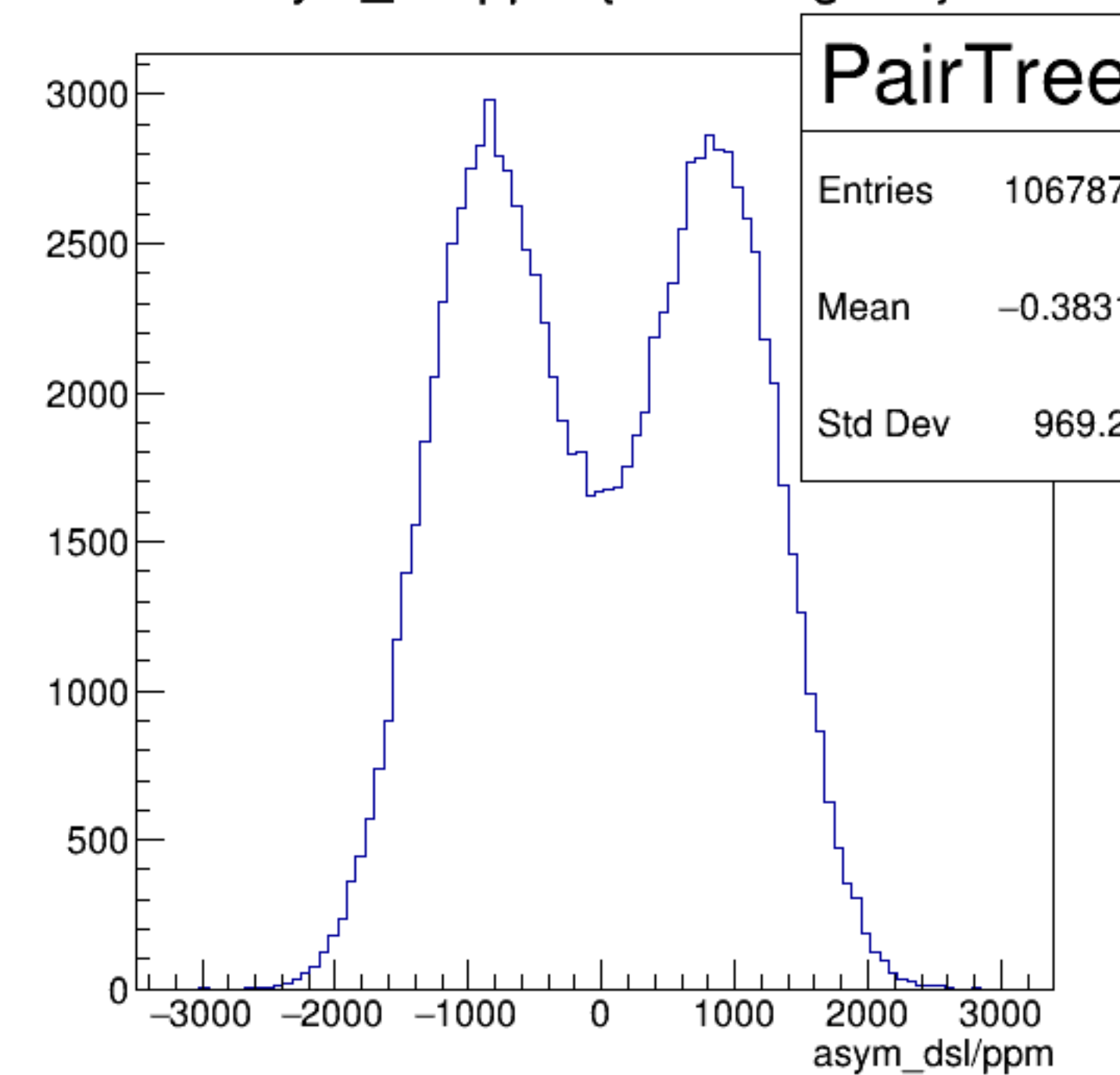
yield_dsl {ErrorFlag==0}



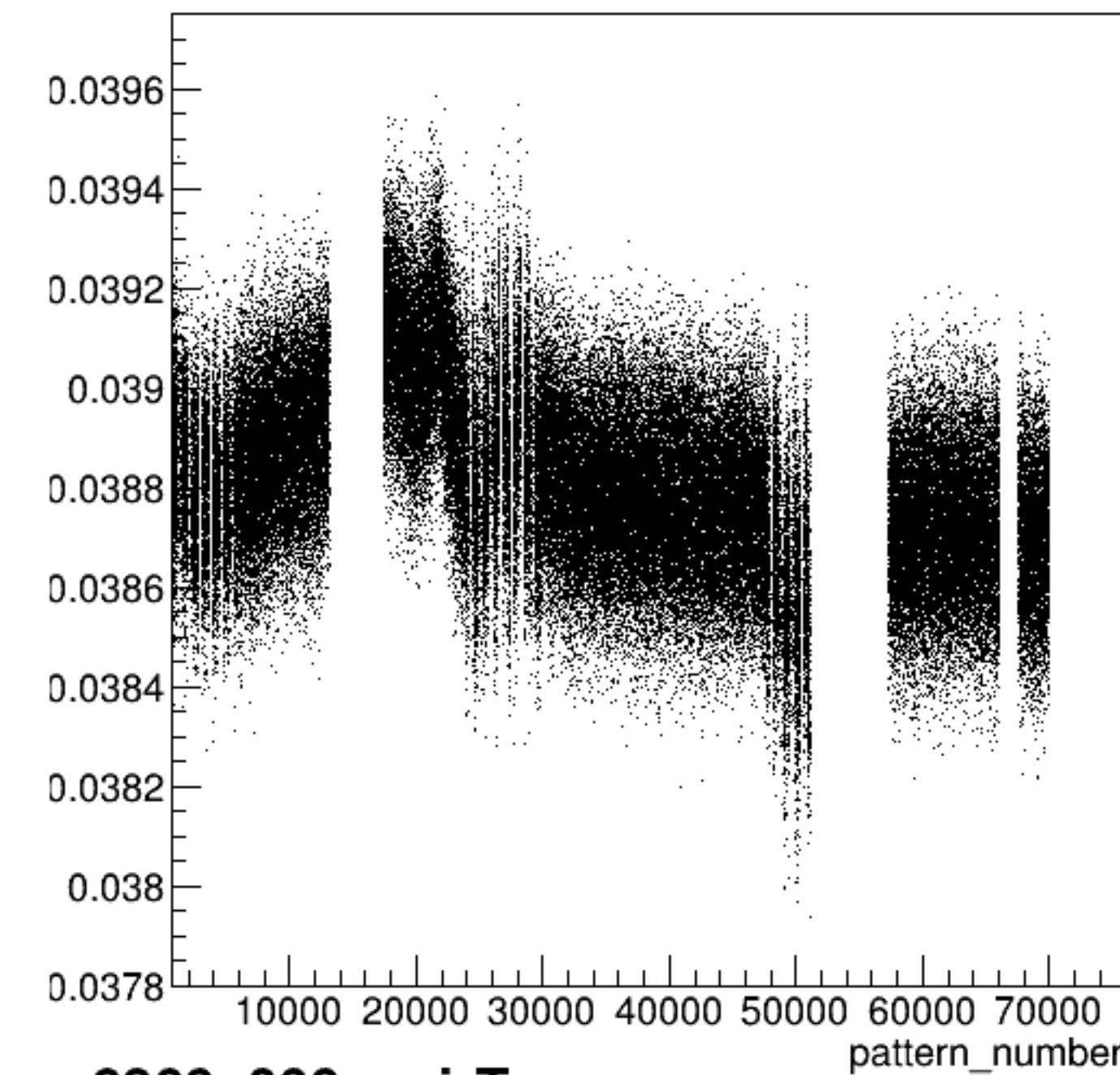
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl/ppm {ErrorFlag==0}

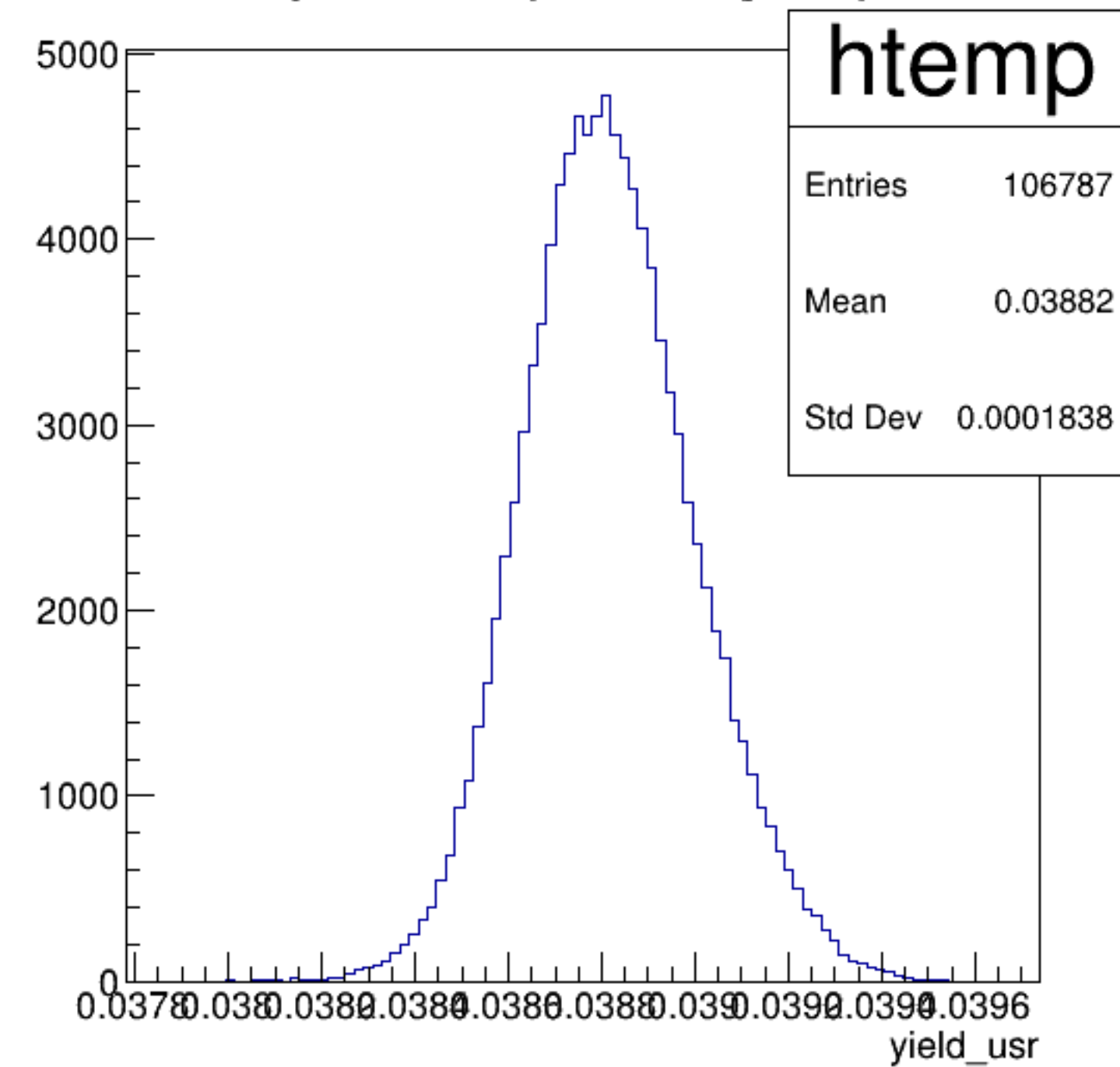


yield_usr:pattern_number {ErrorFlag==0}

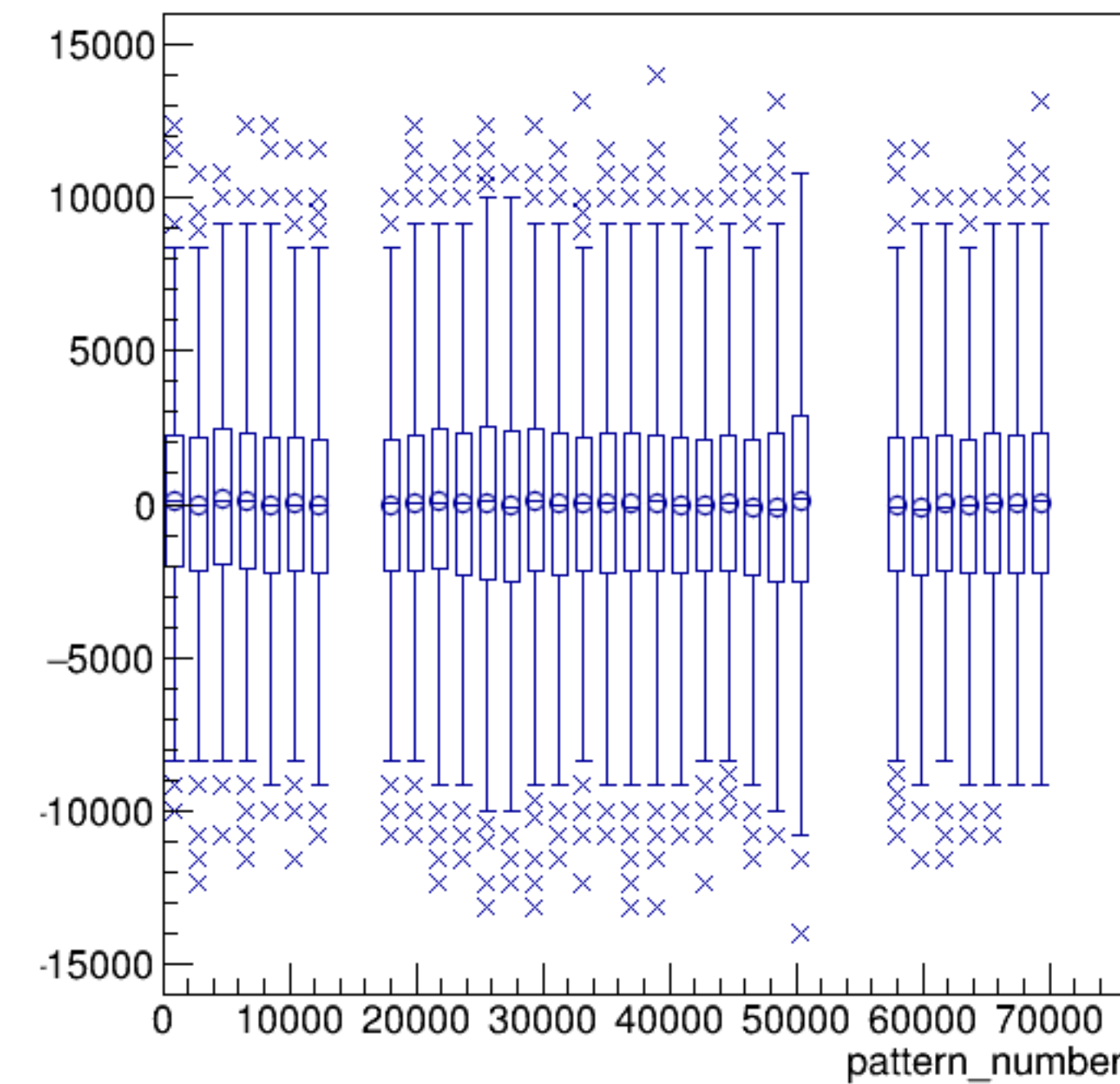


run6369_000_pairTree_usr.png

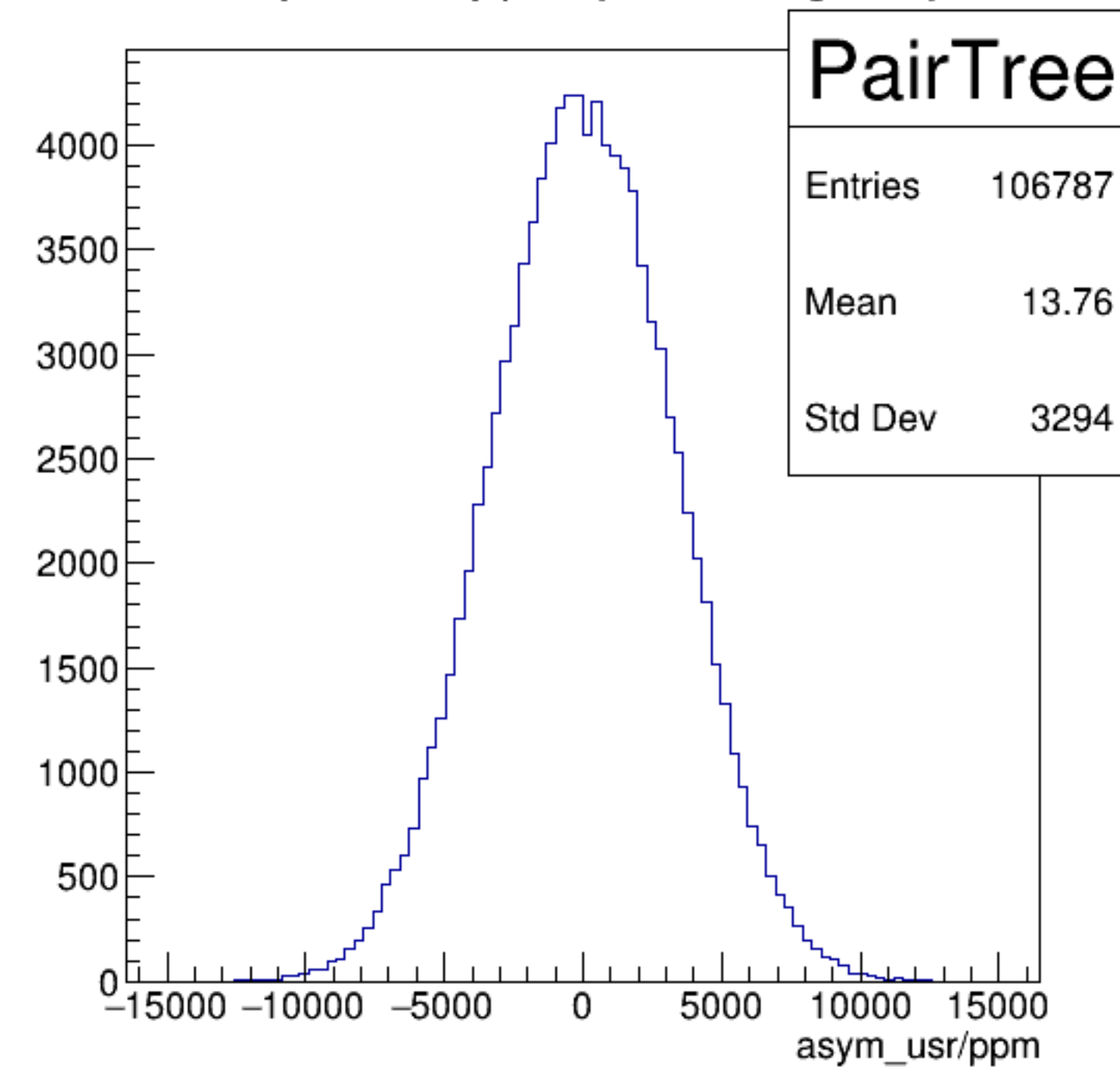
yield_usr {ErrorFlag==0}



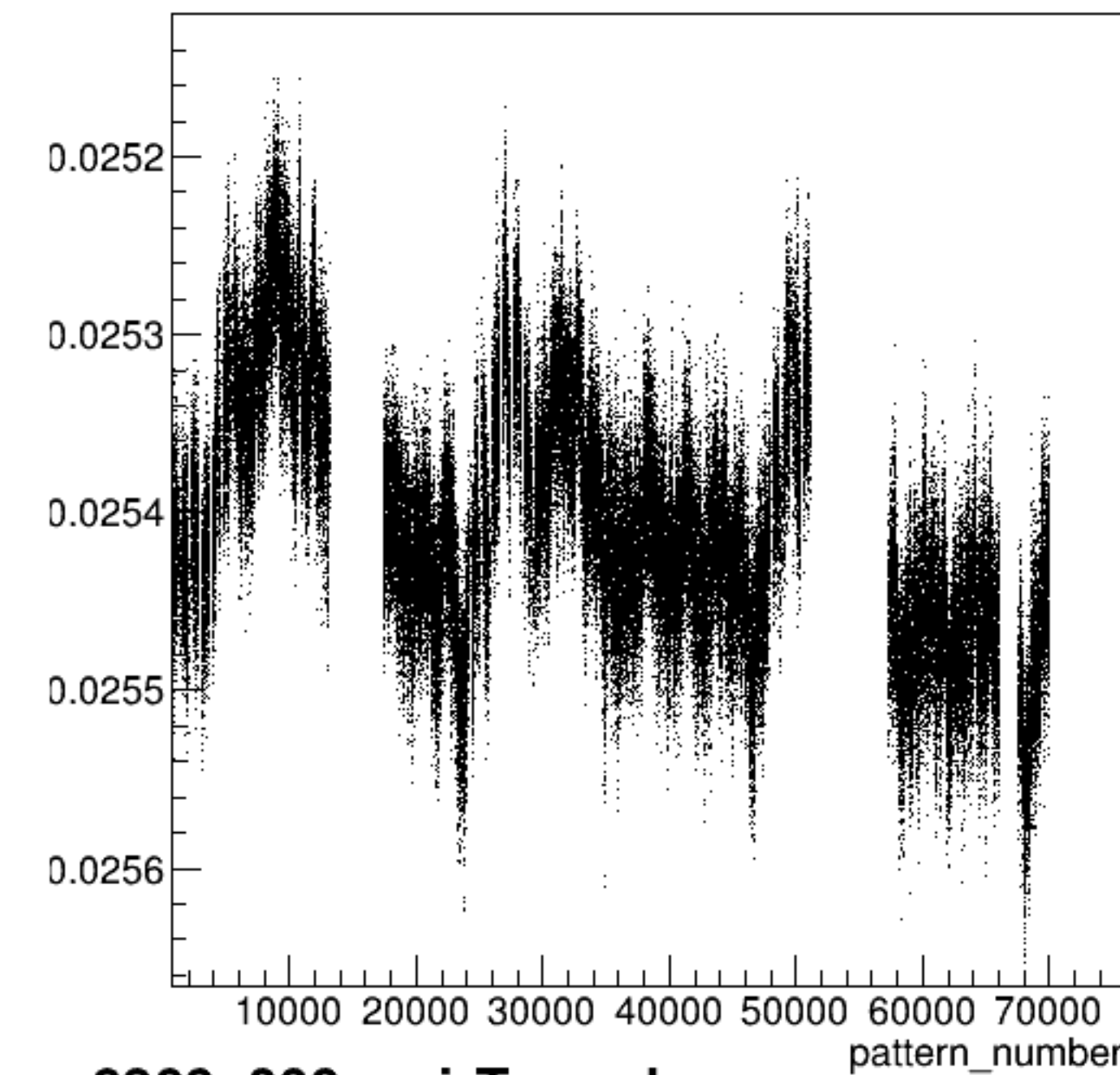
asym_usr/ppm:pattern_number {ErrorFlag==0}



asym_usr/ppm {ErrorFlag==0}

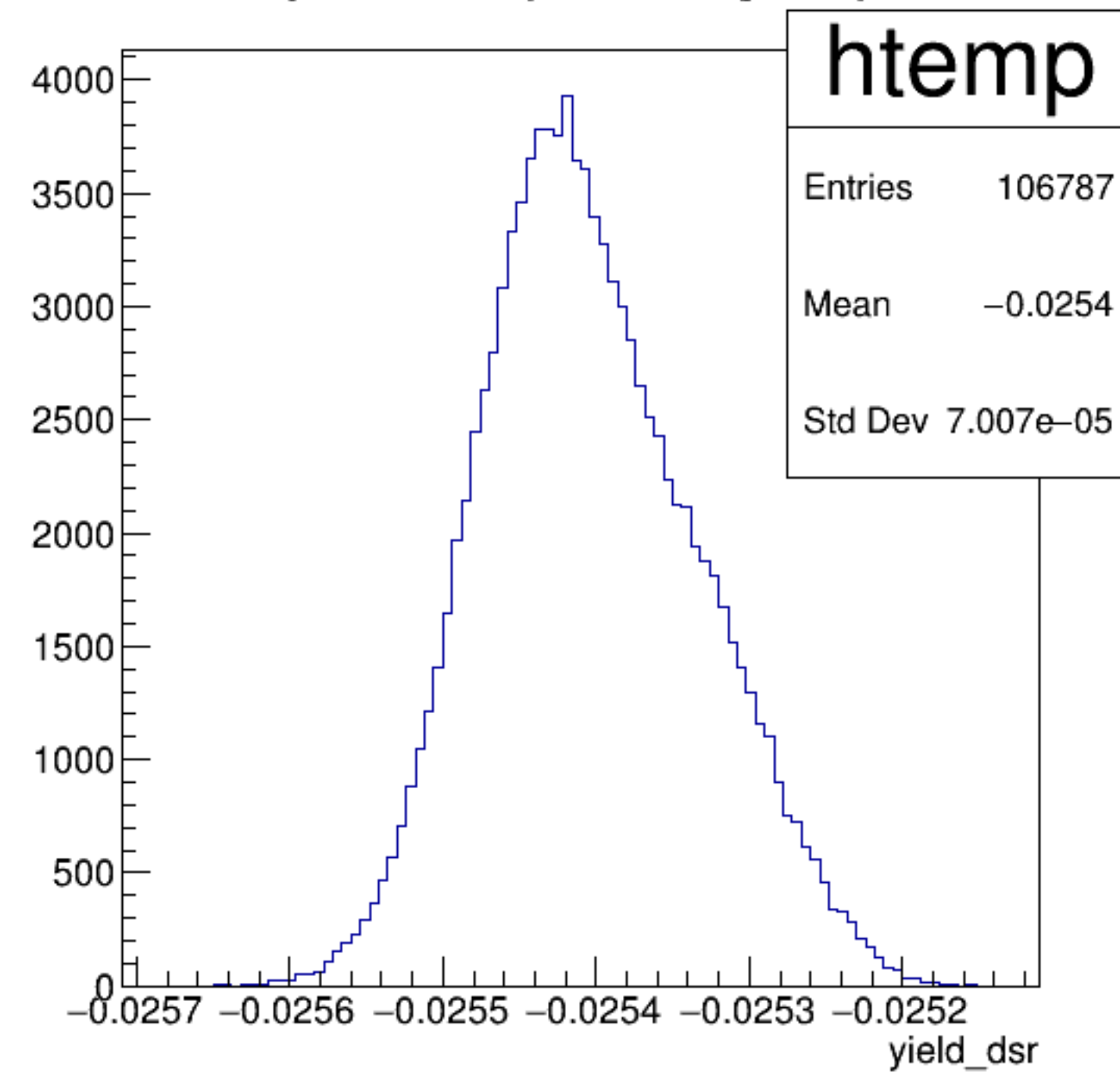


yield_dsr:pattern_number {ErrorFlag==0}

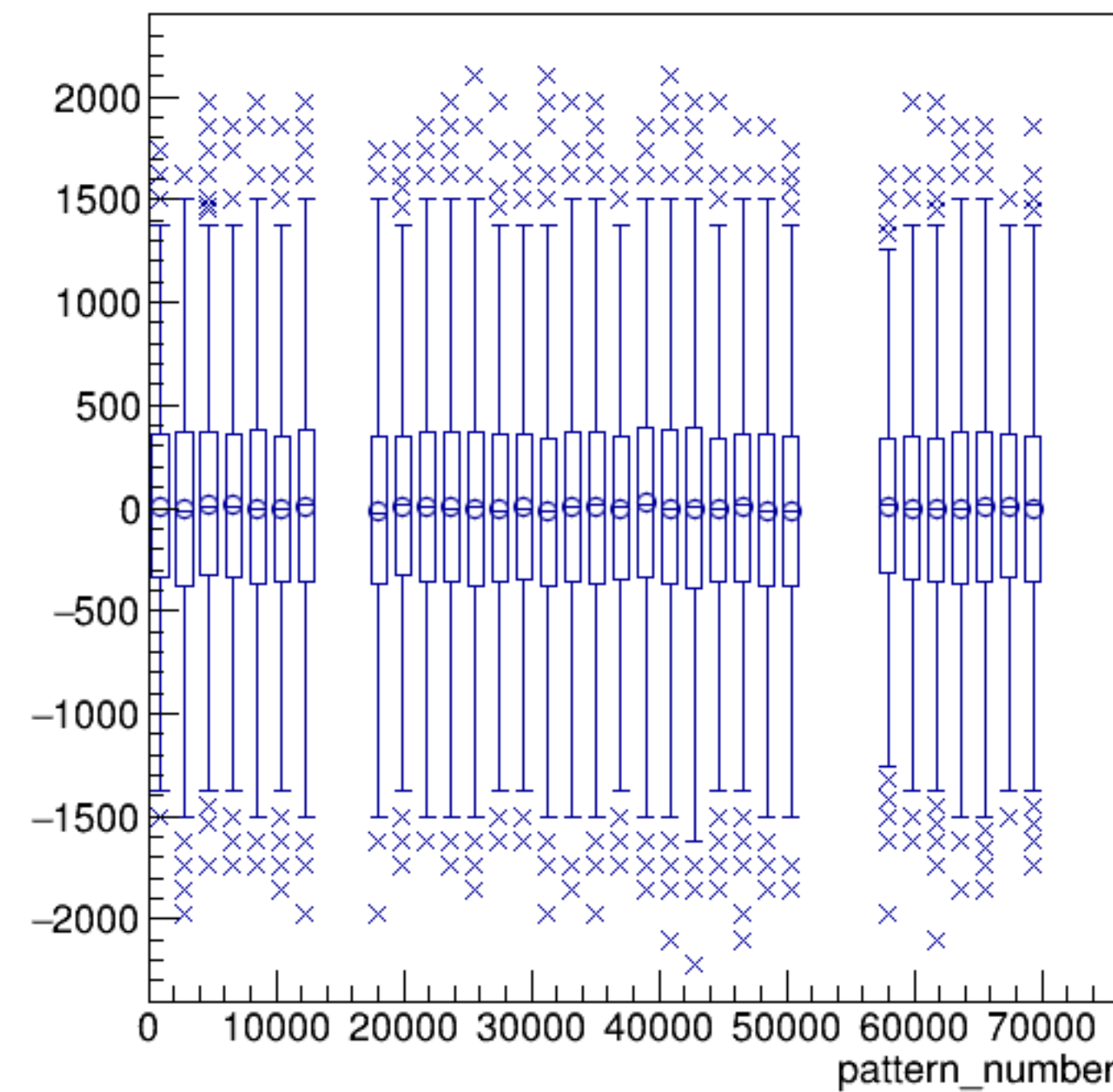


run6369_000_pairTree_dsr.png

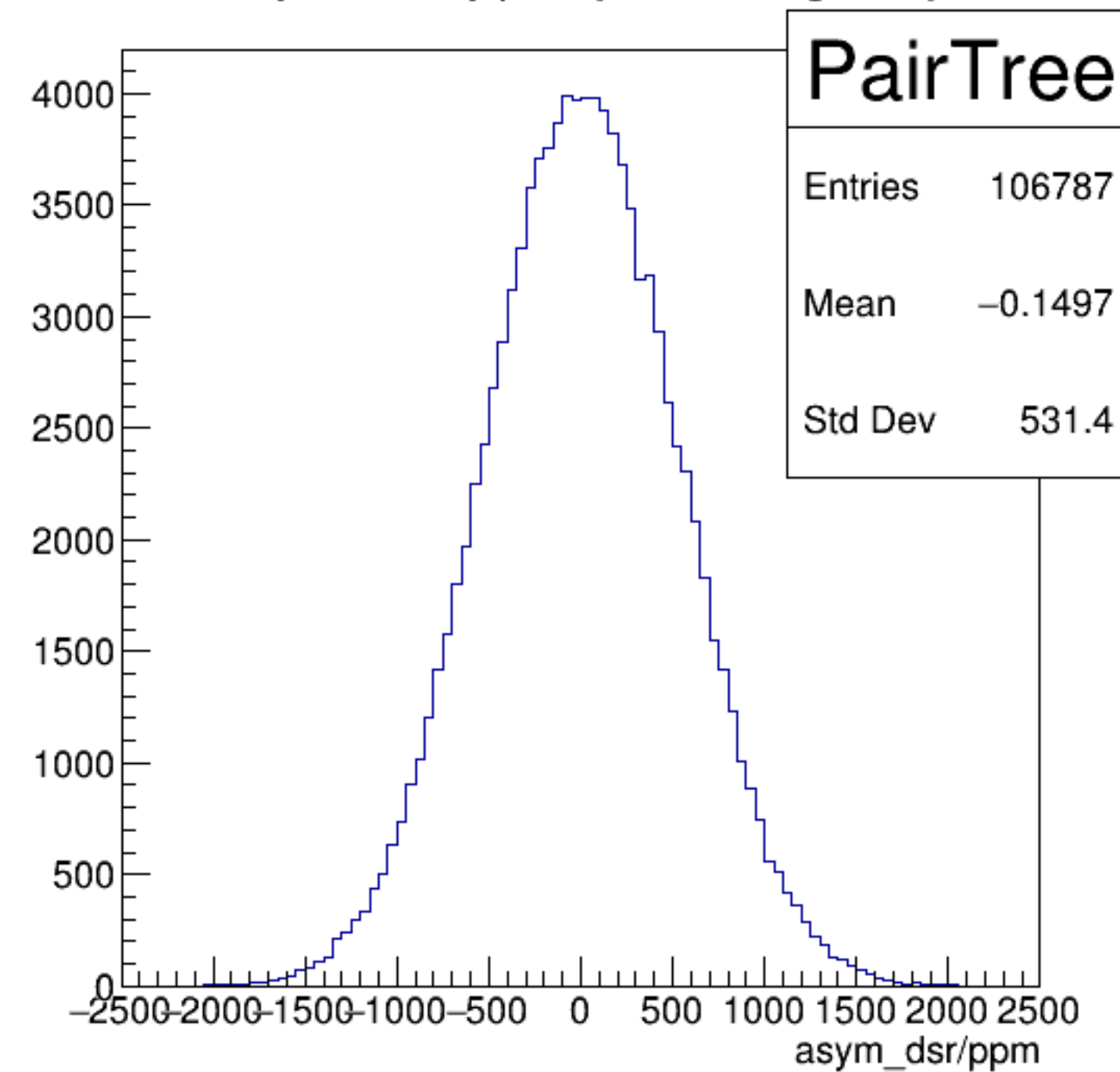
yield_dsr {ErrorFlag==0}



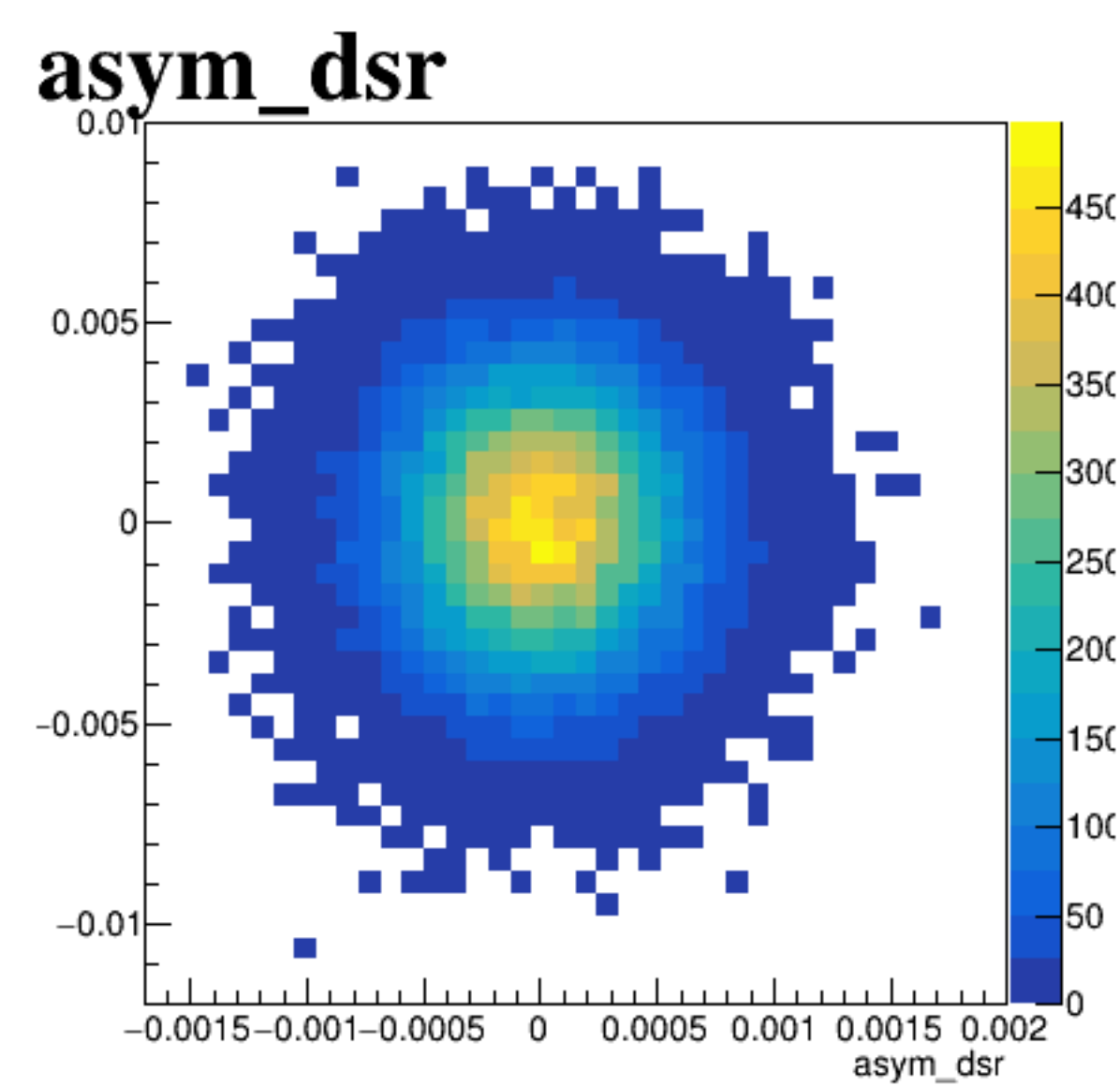
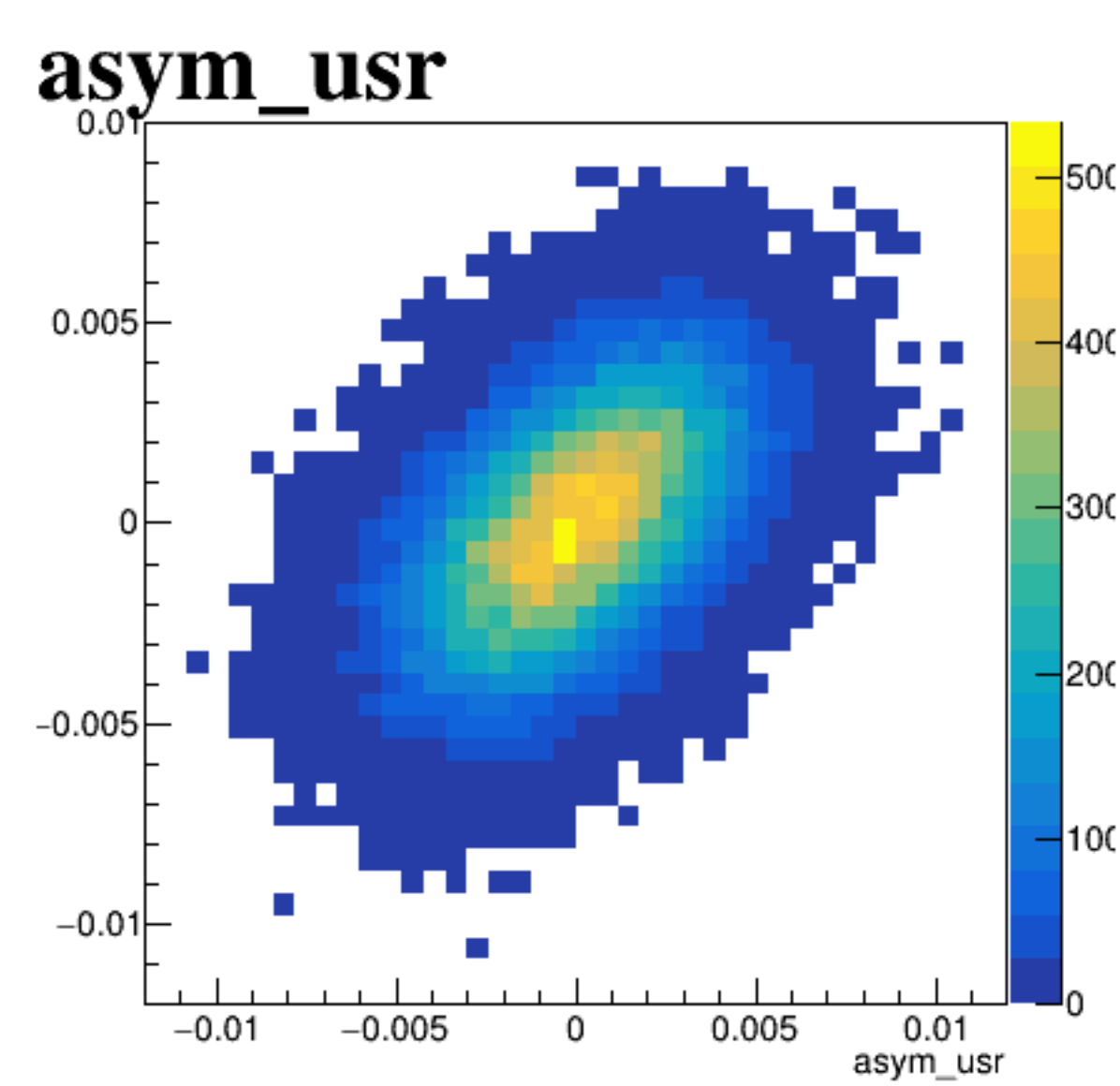
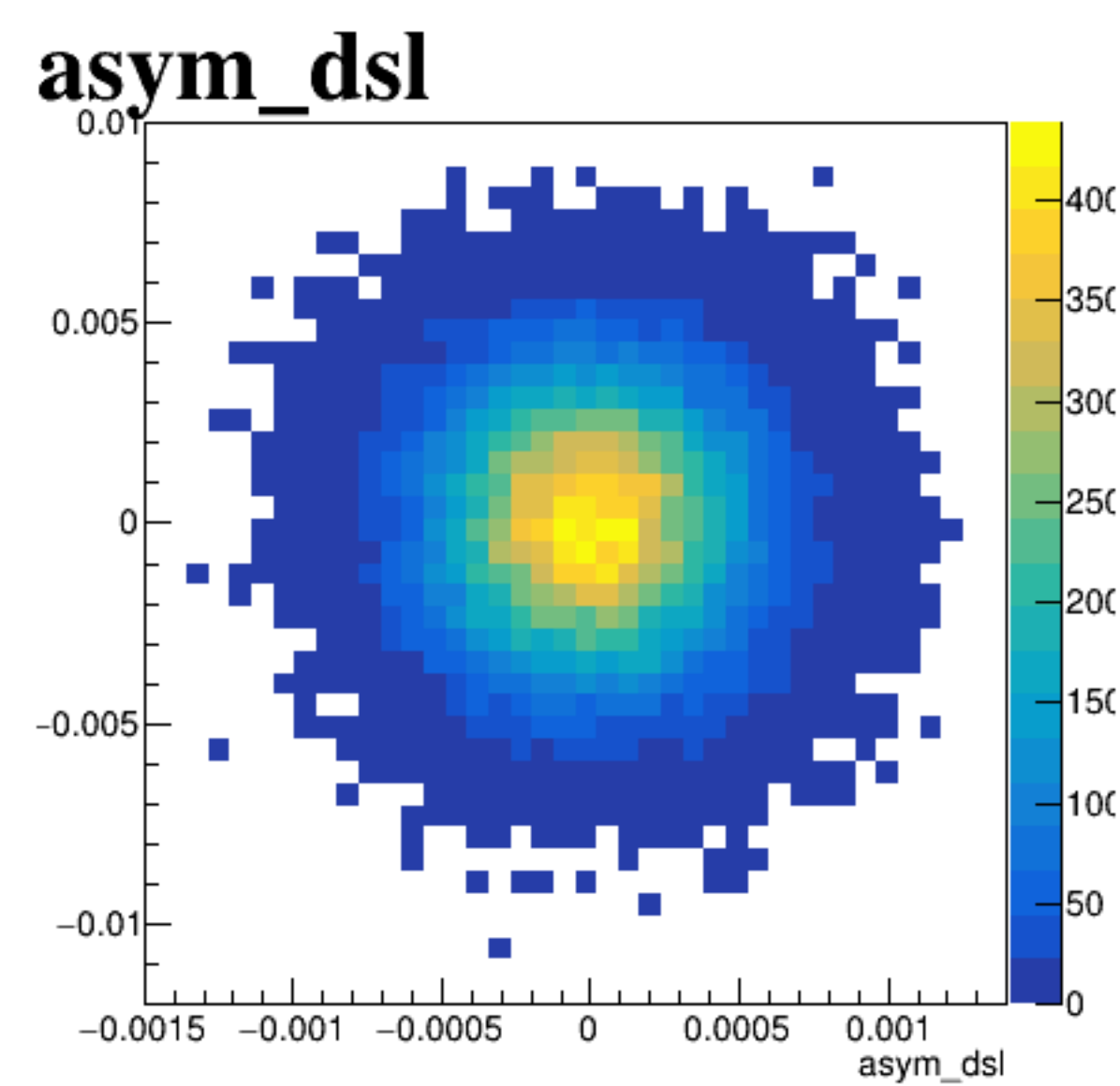
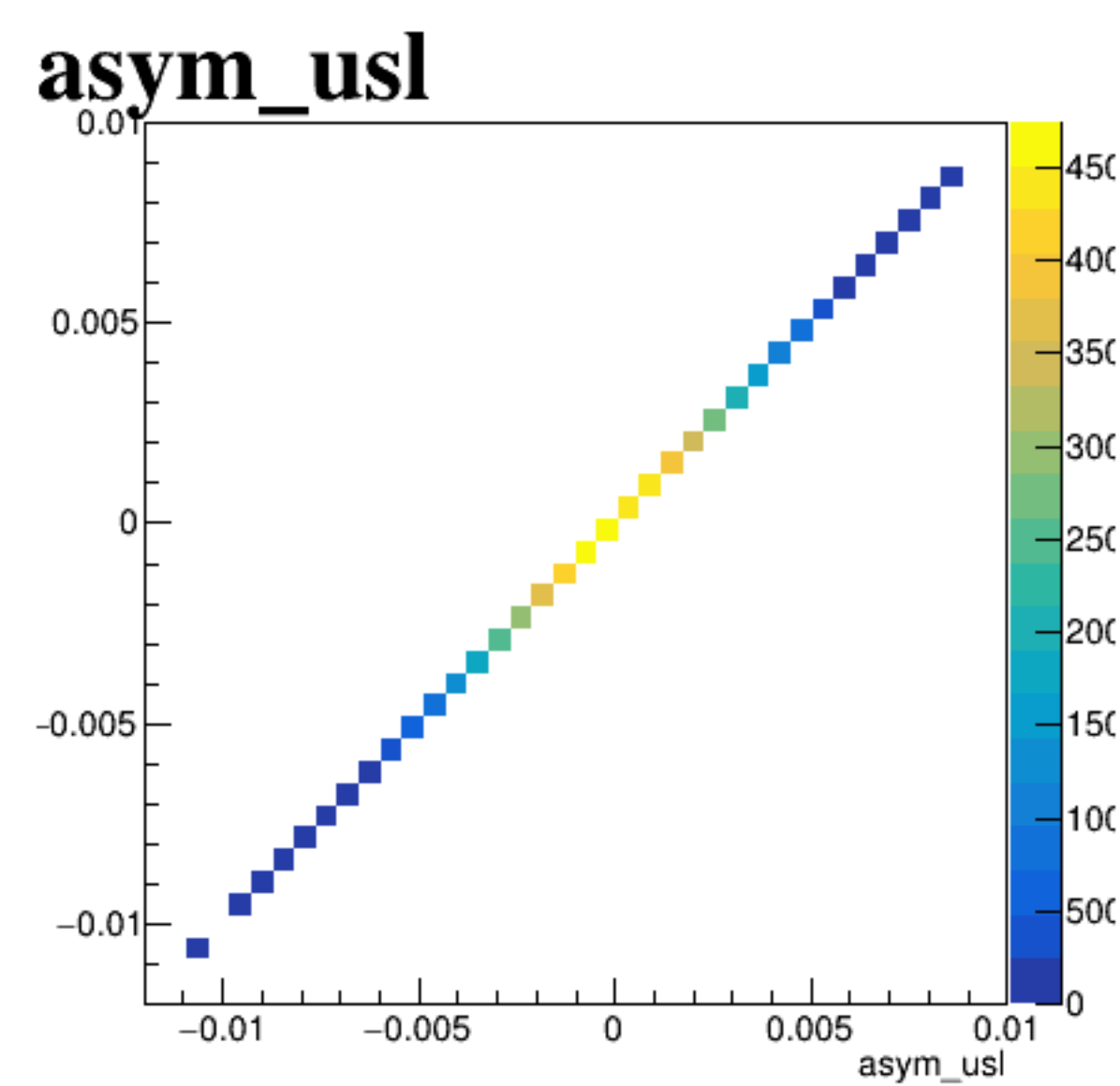
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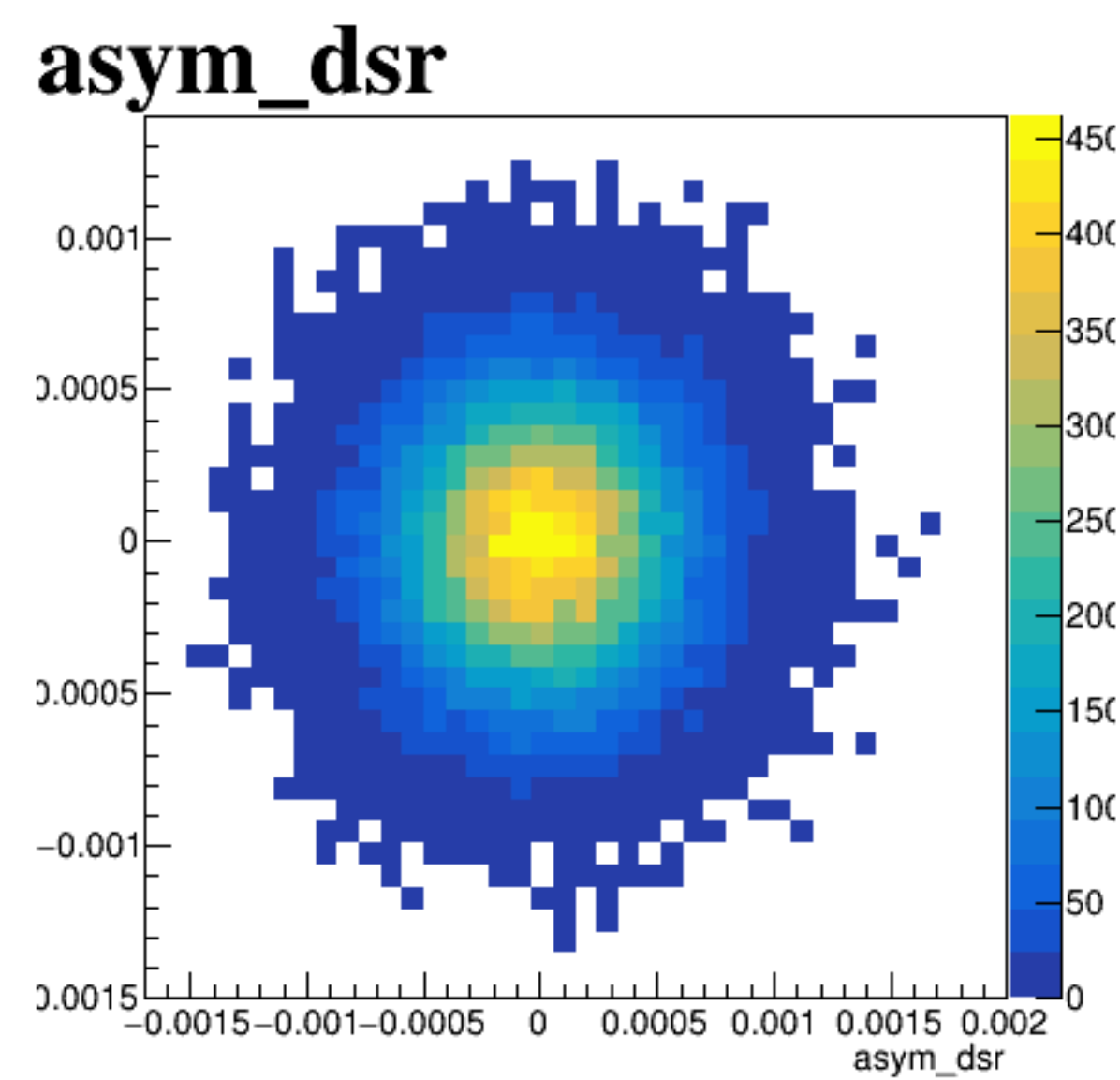
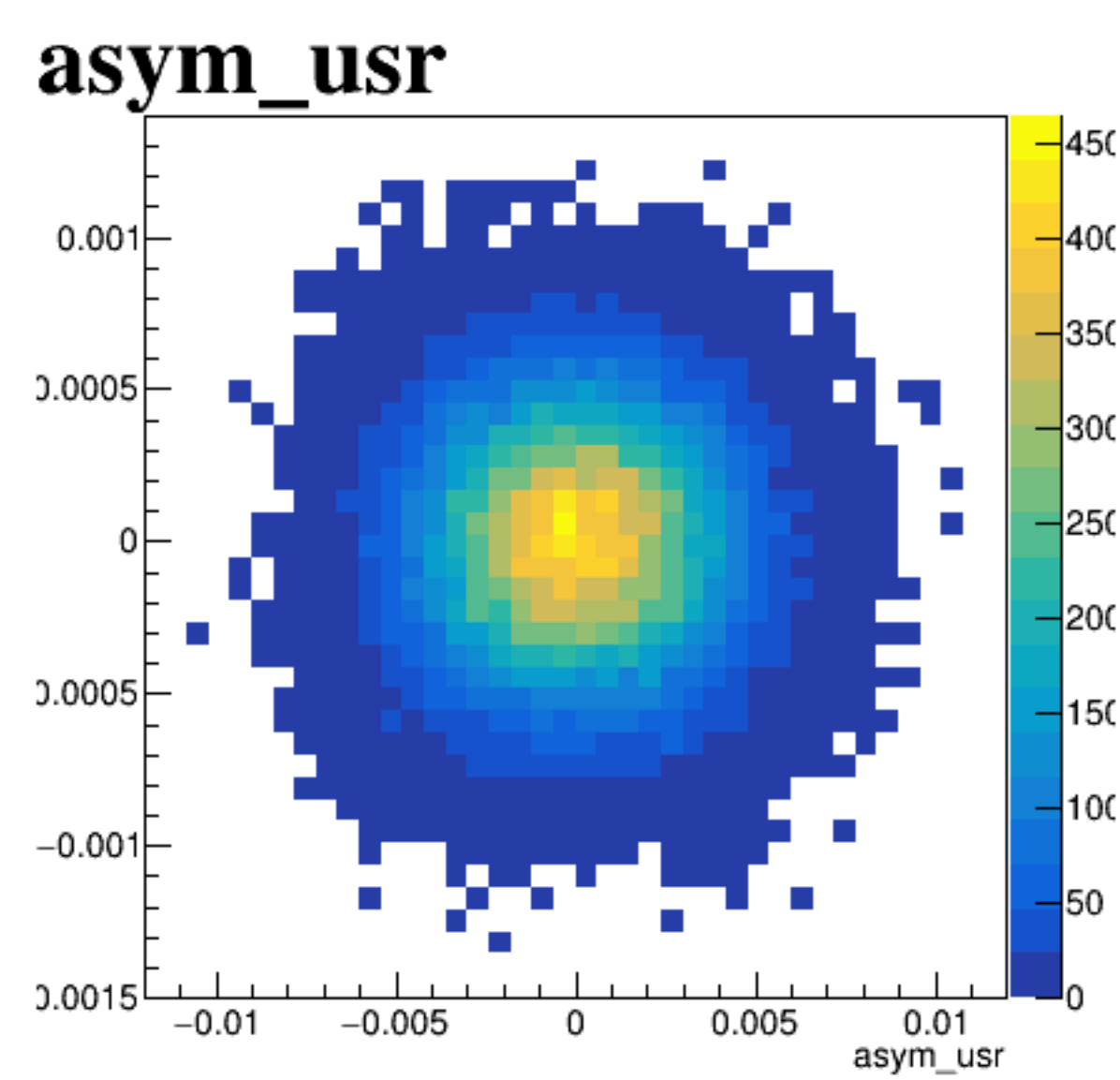
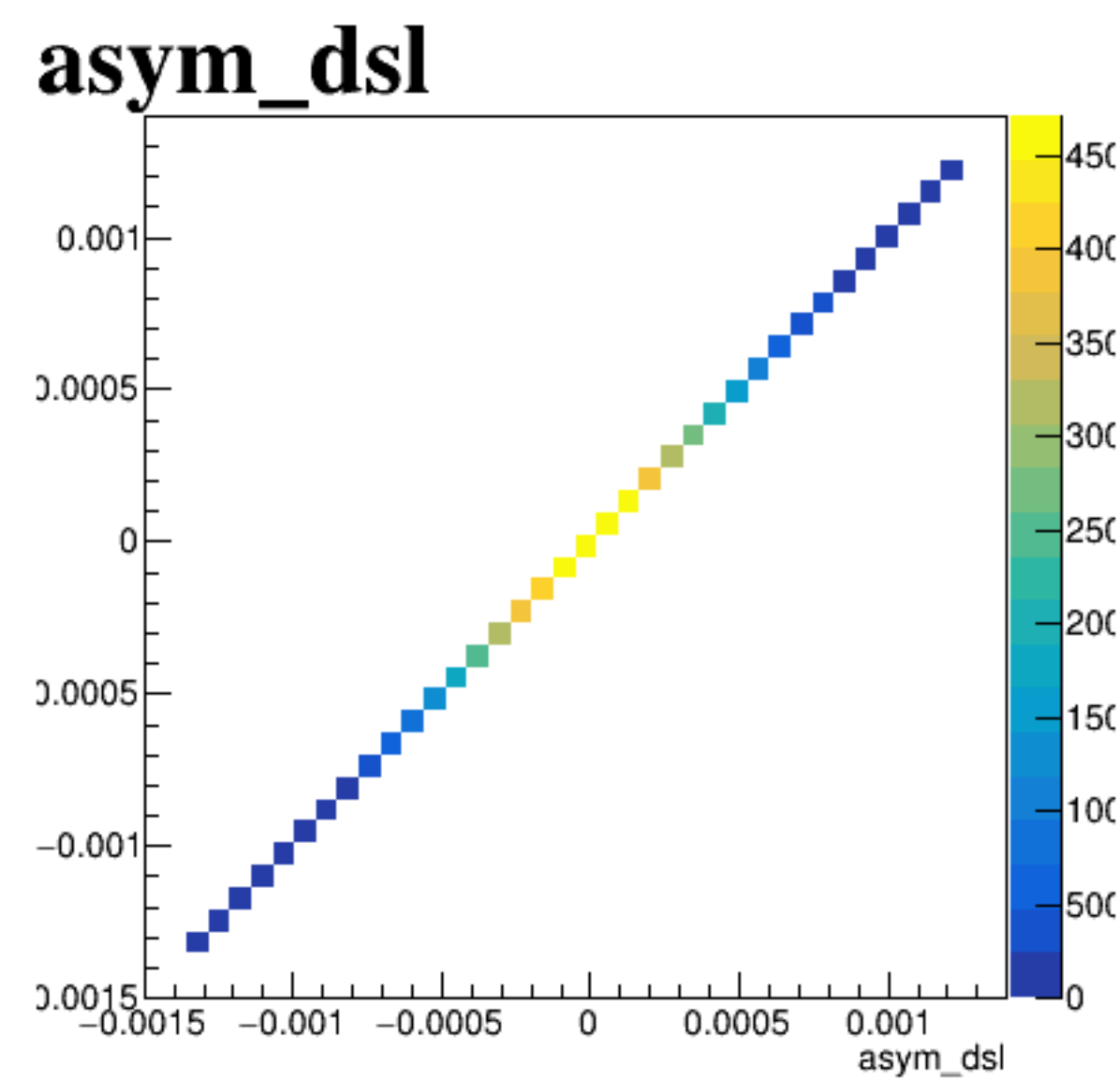
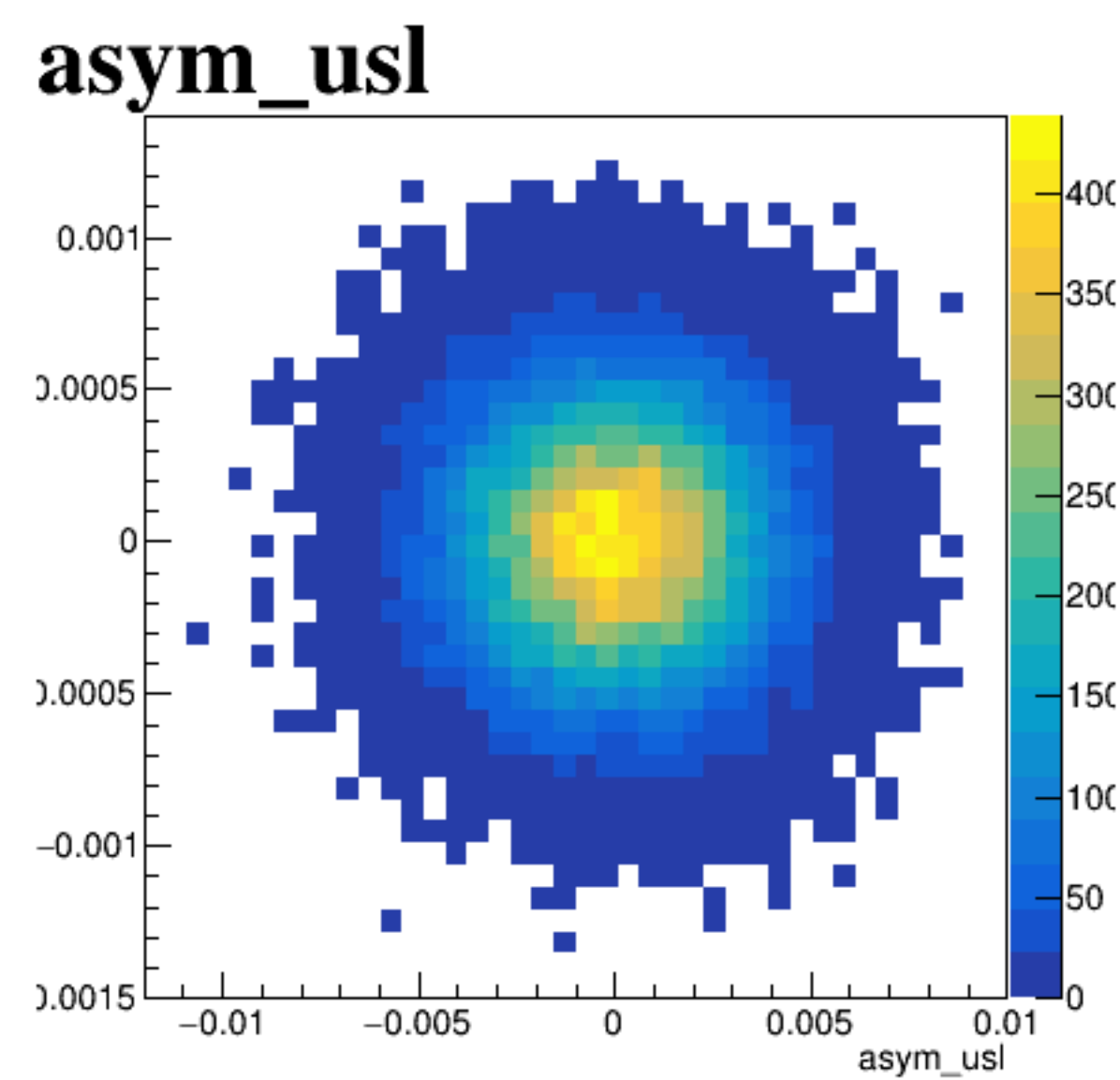
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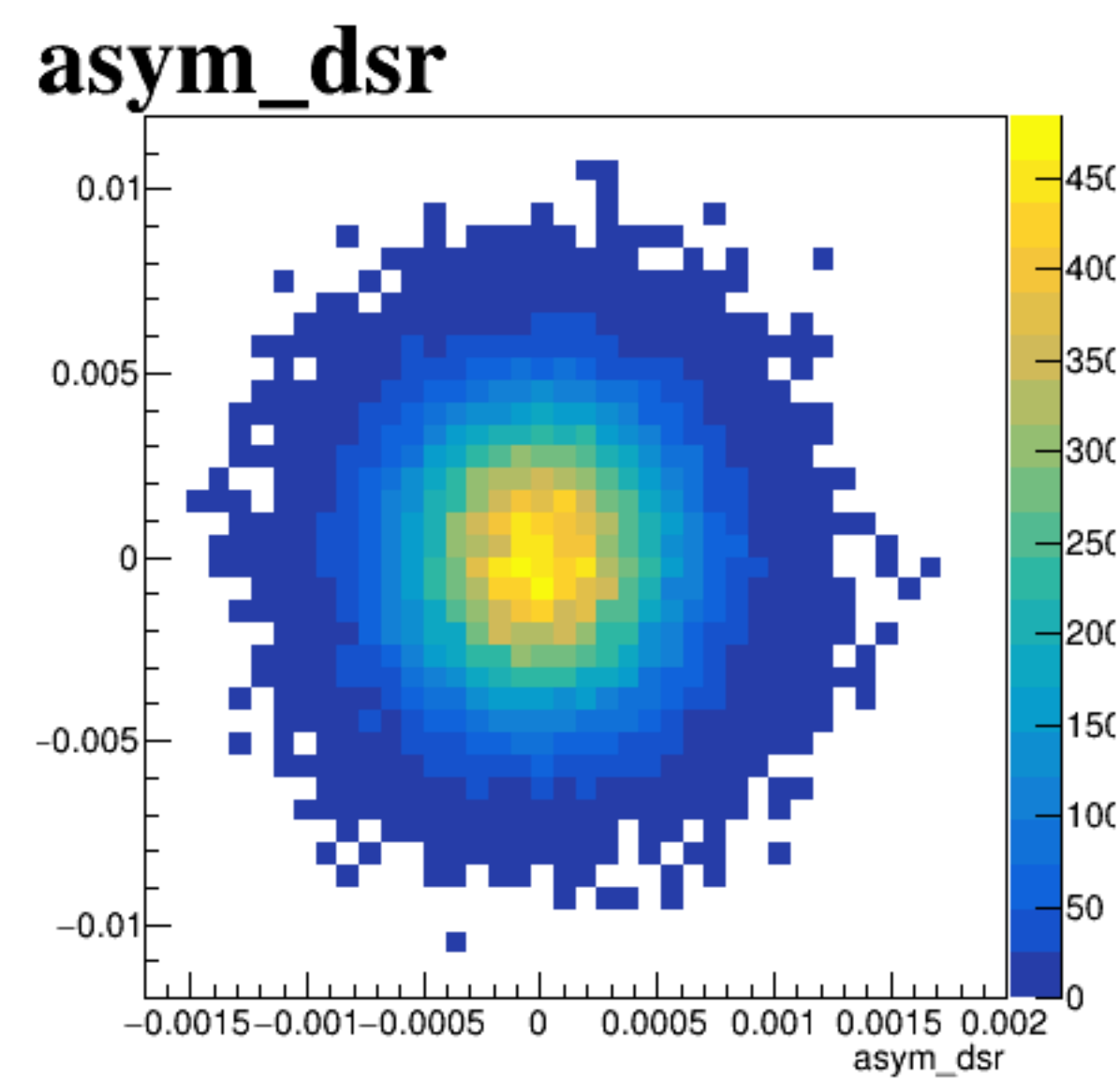
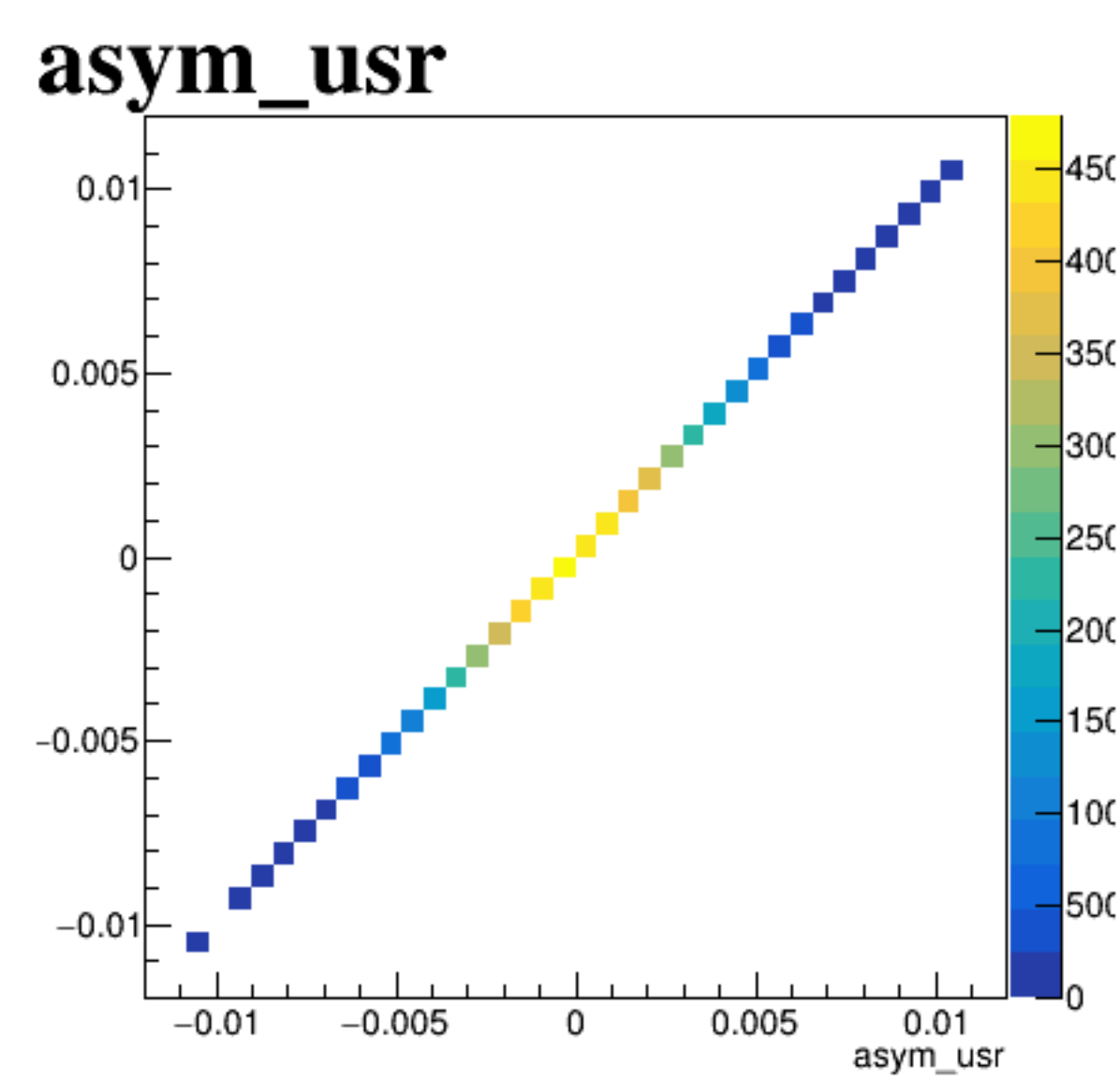
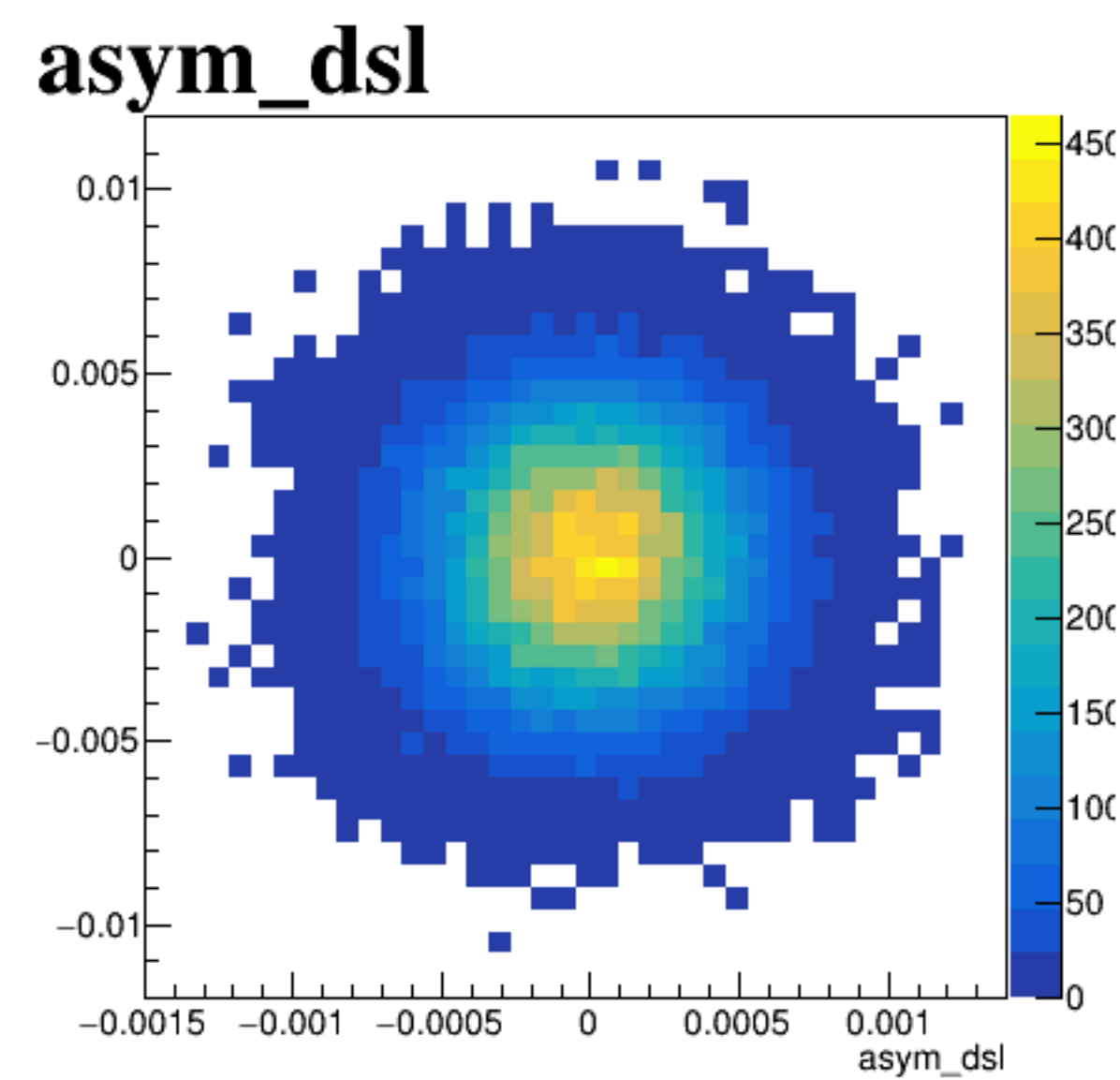
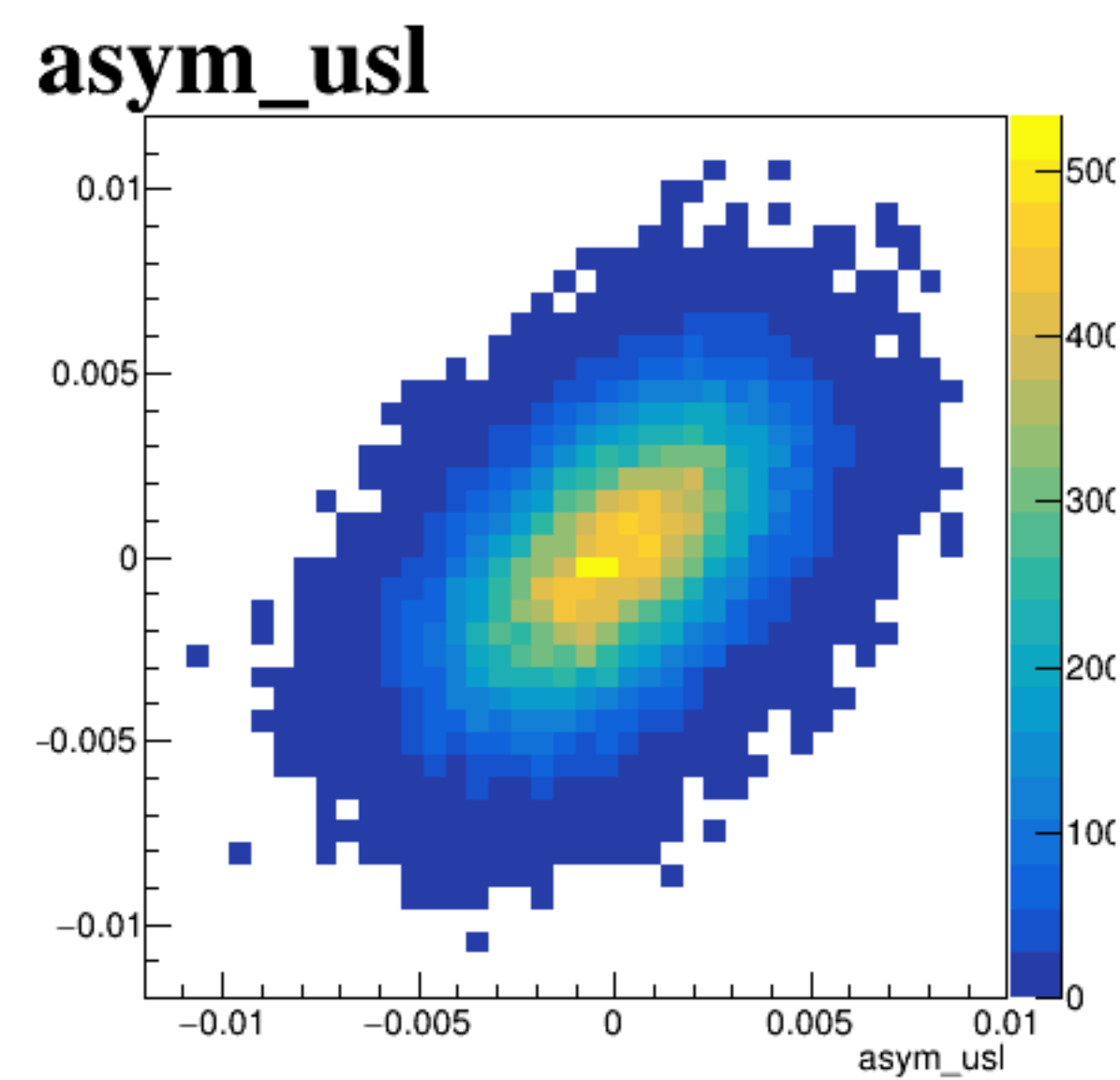
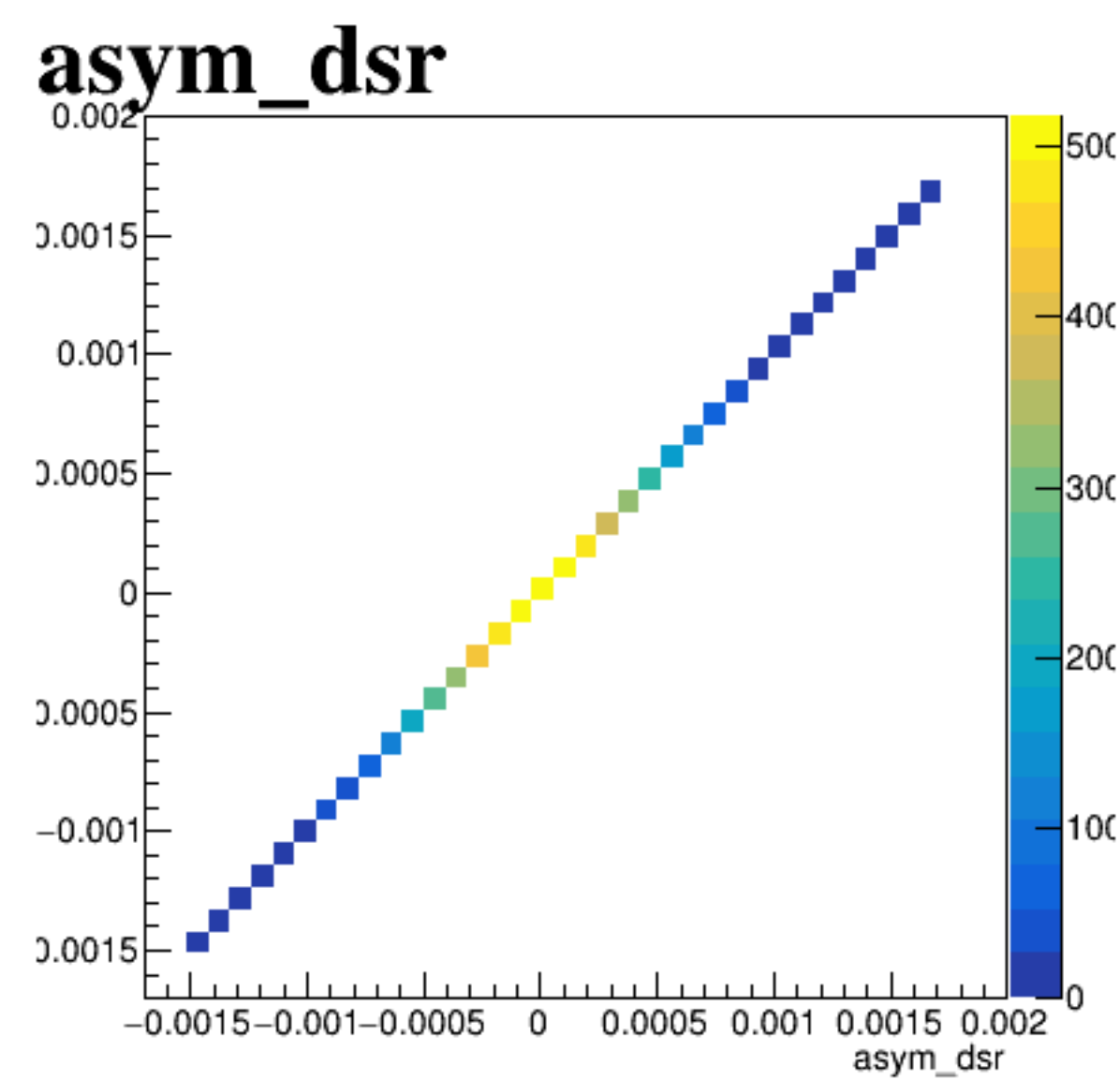
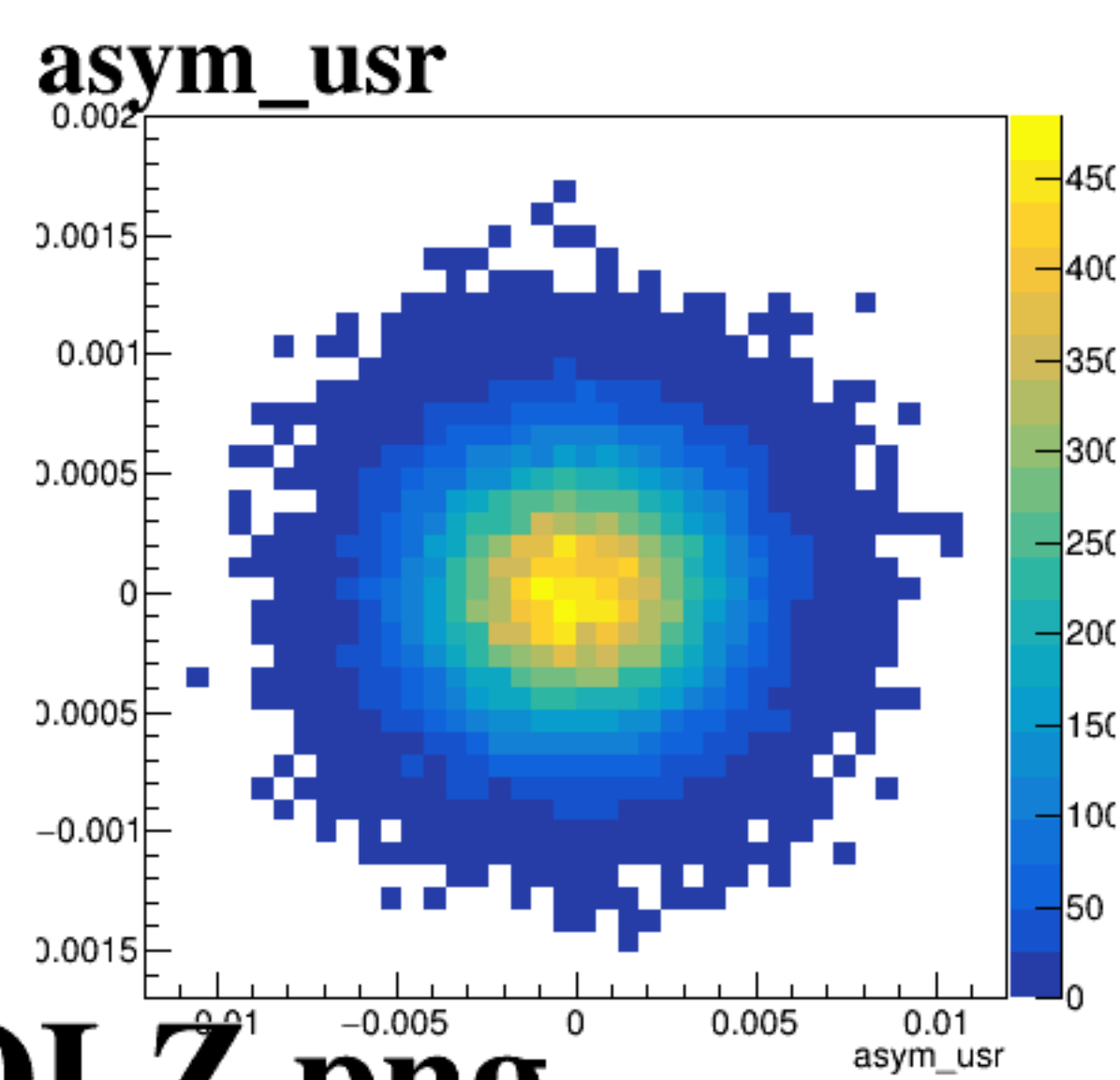
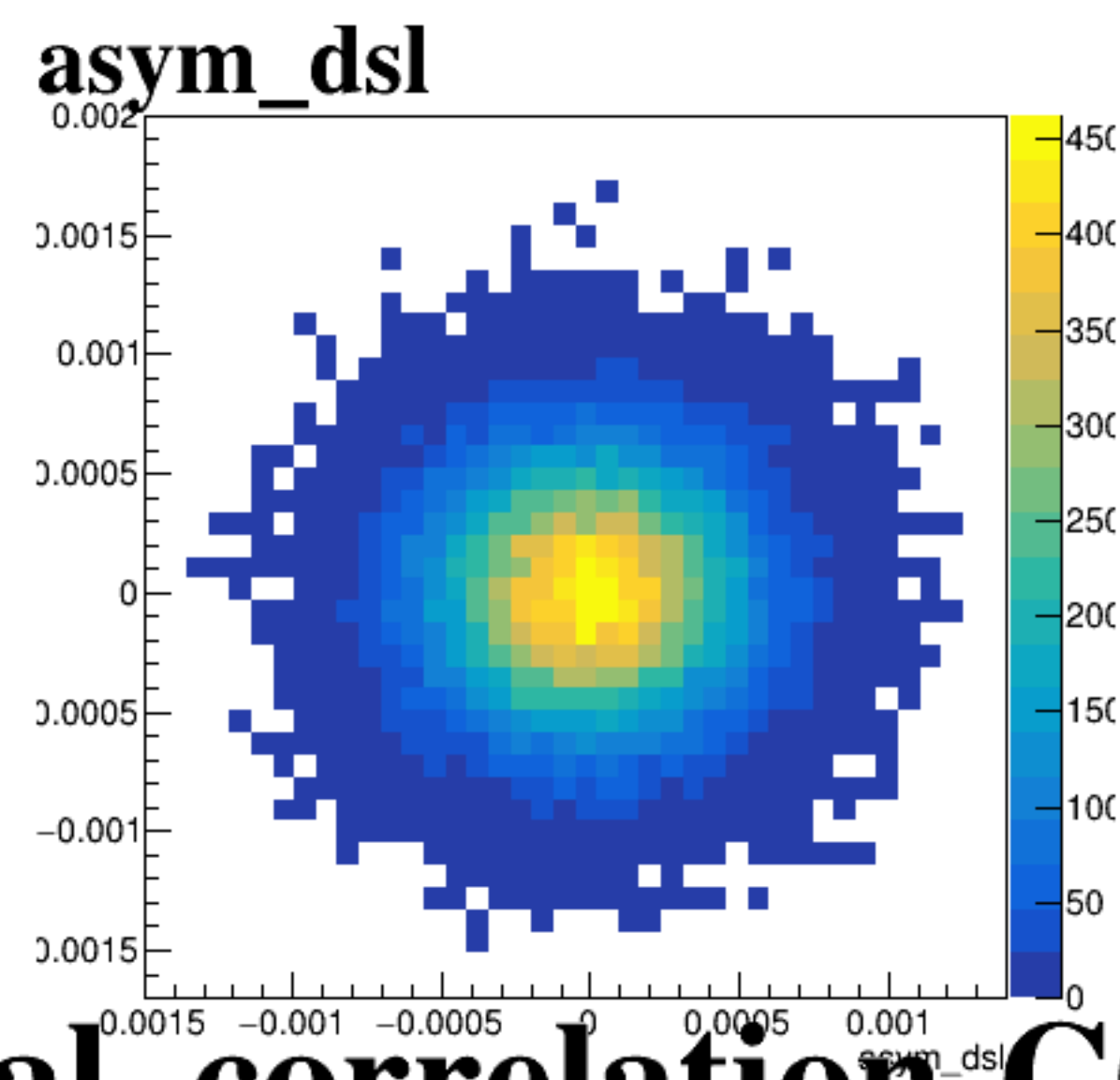
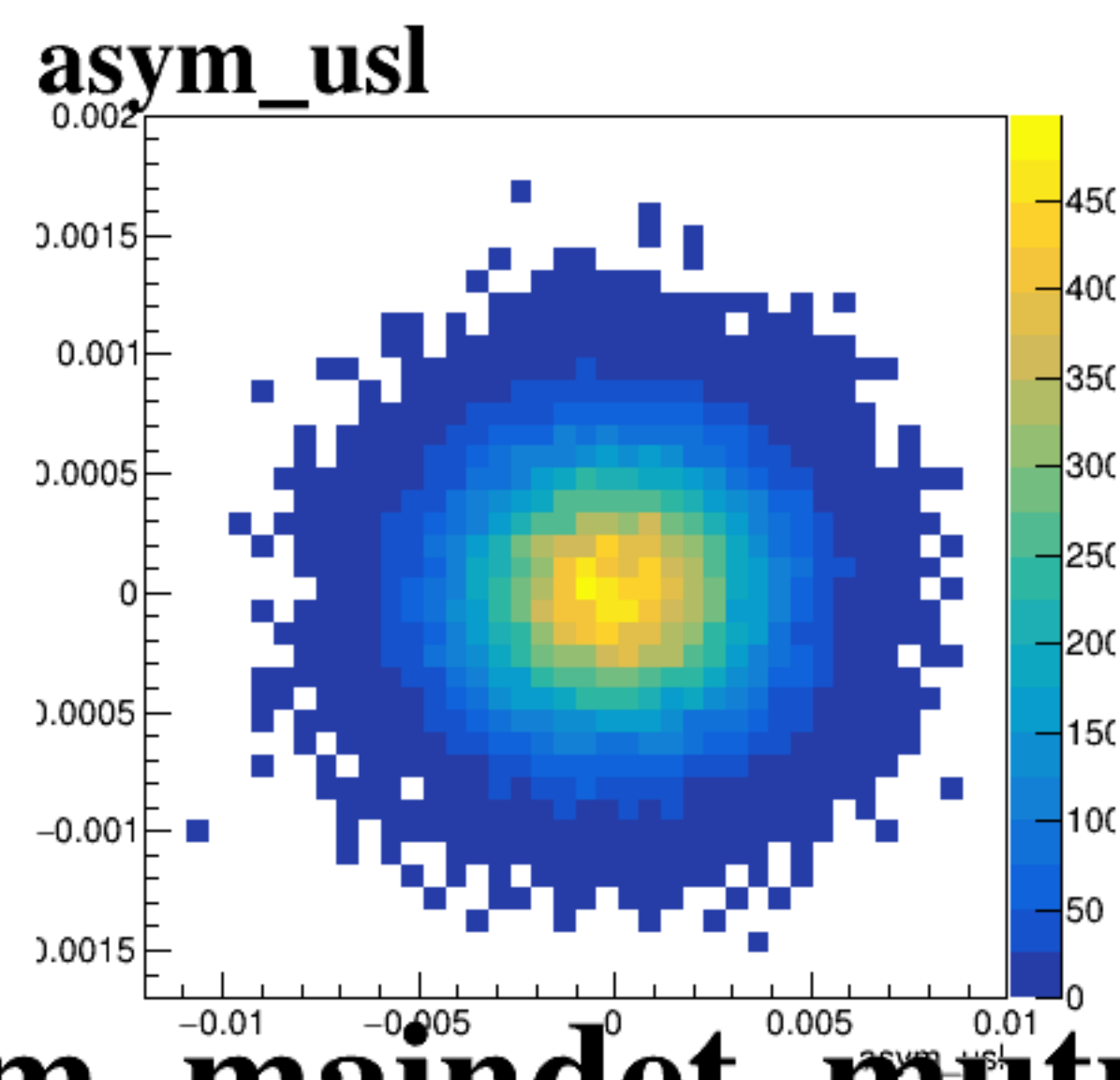
asym_usl



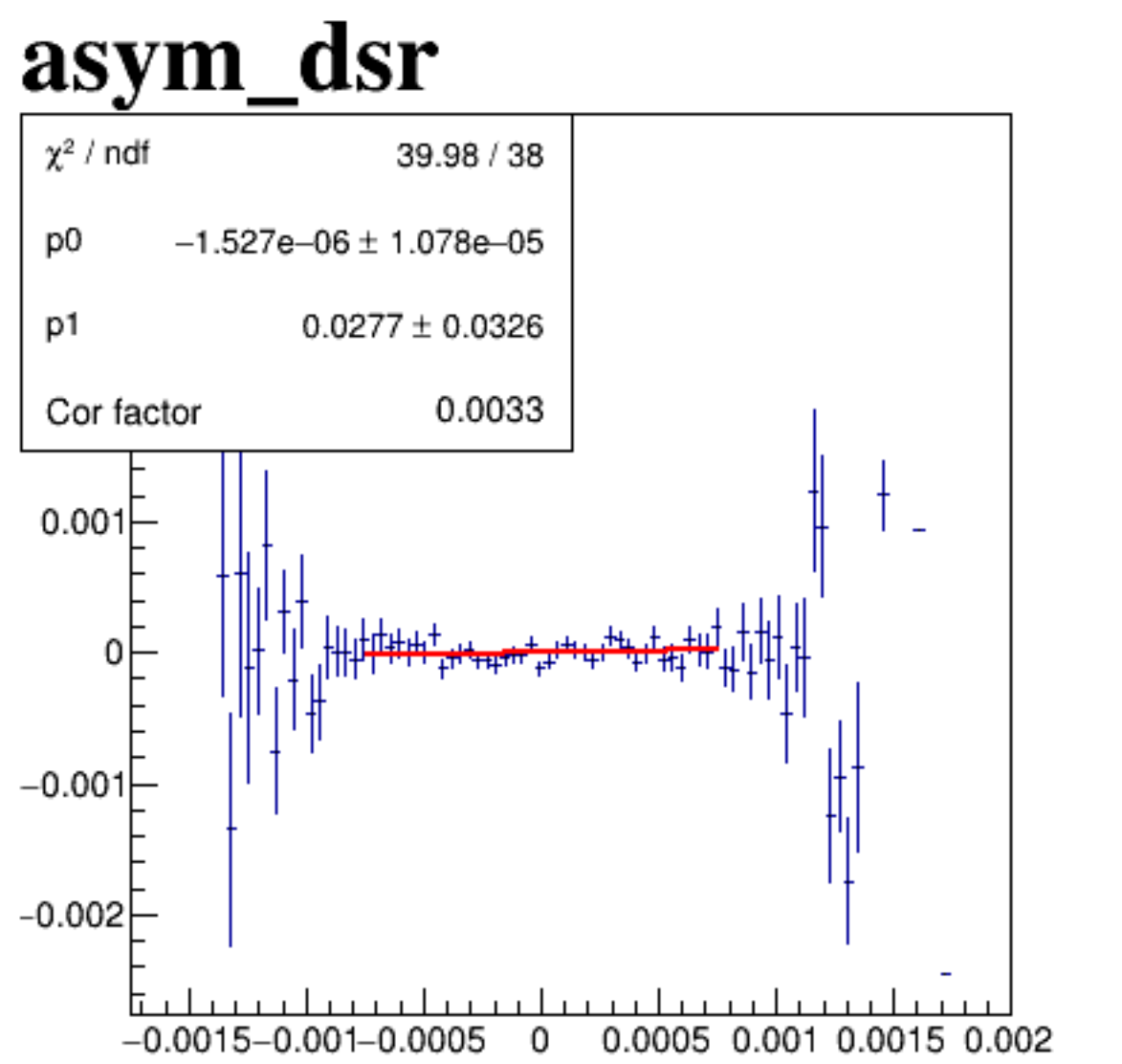
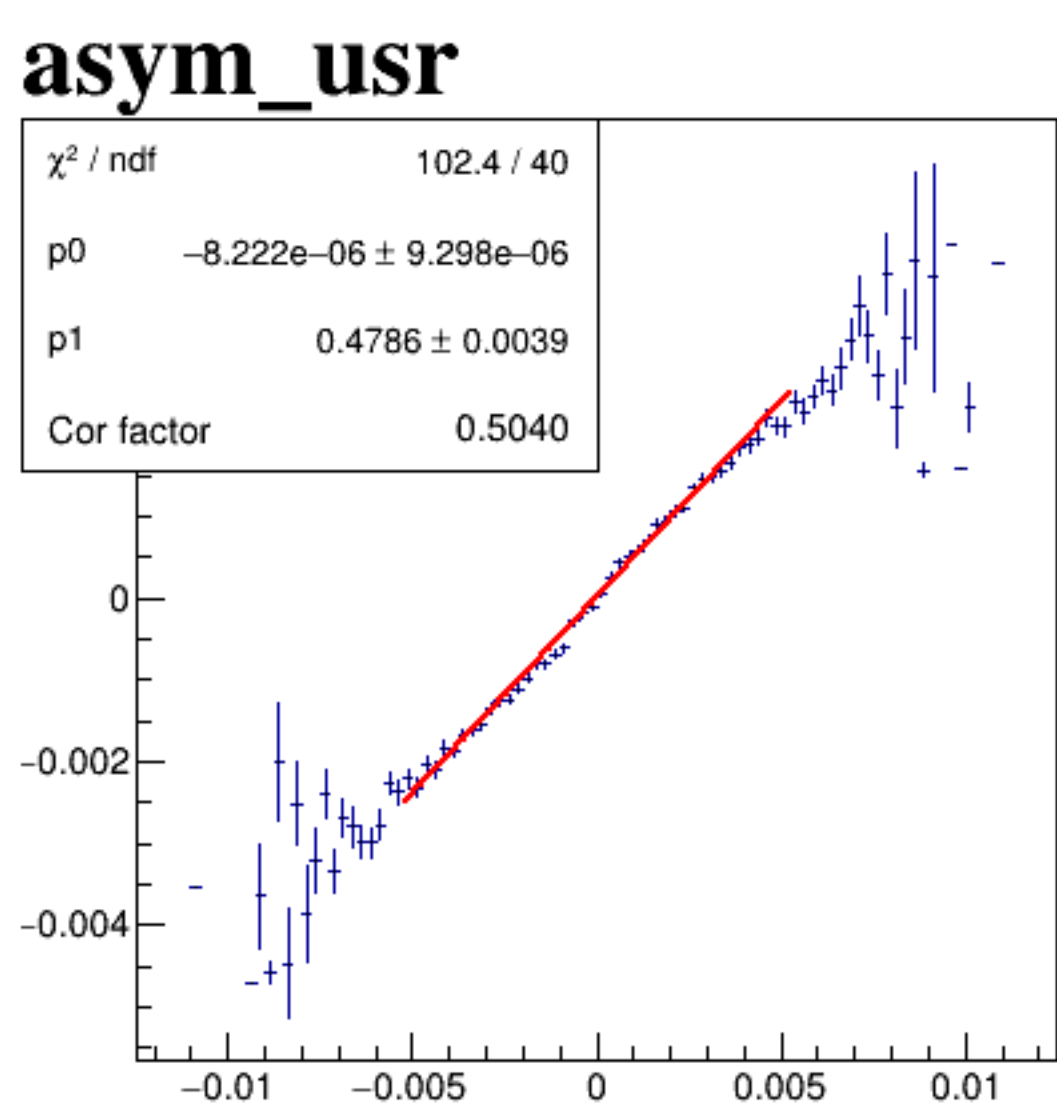
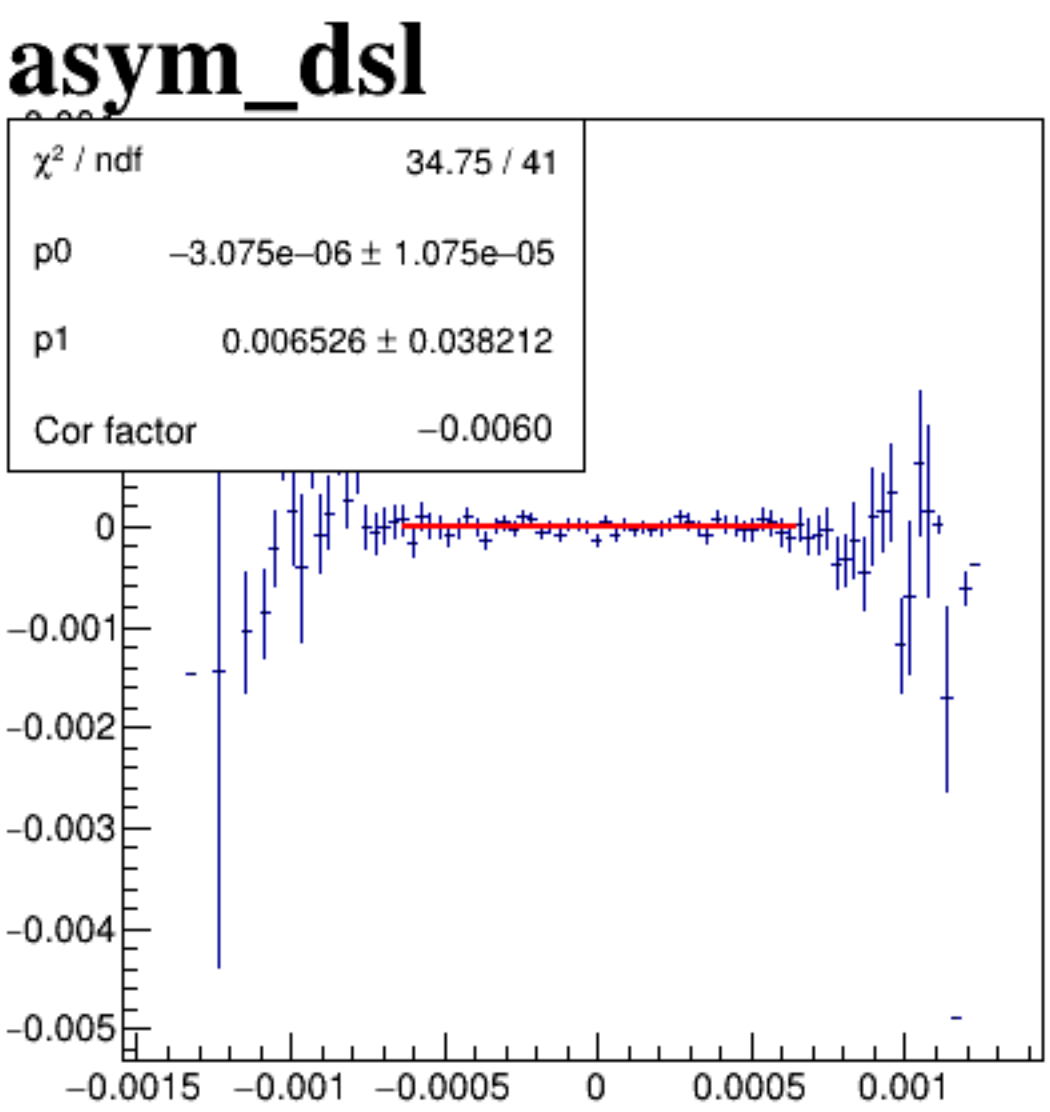
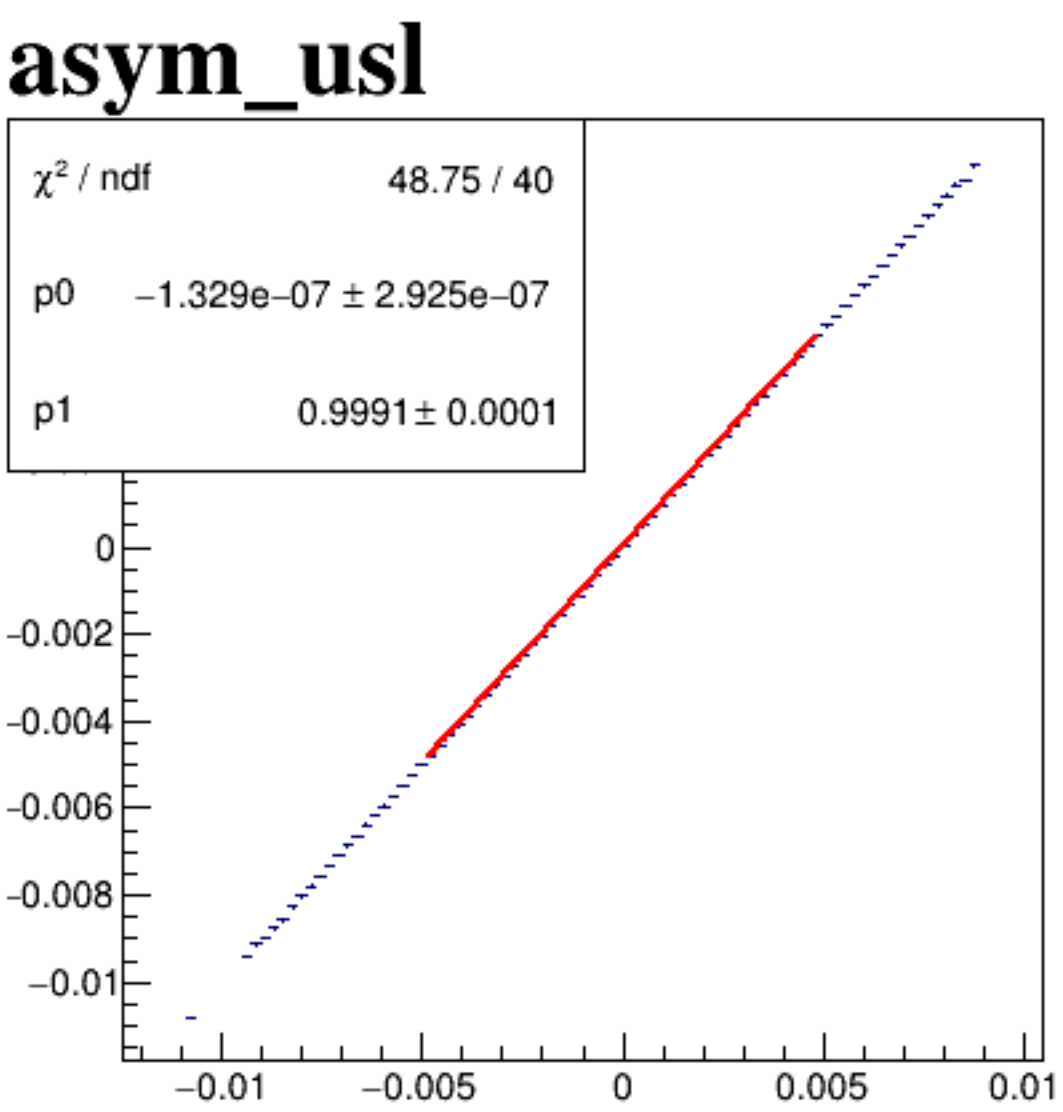
asym_dsl



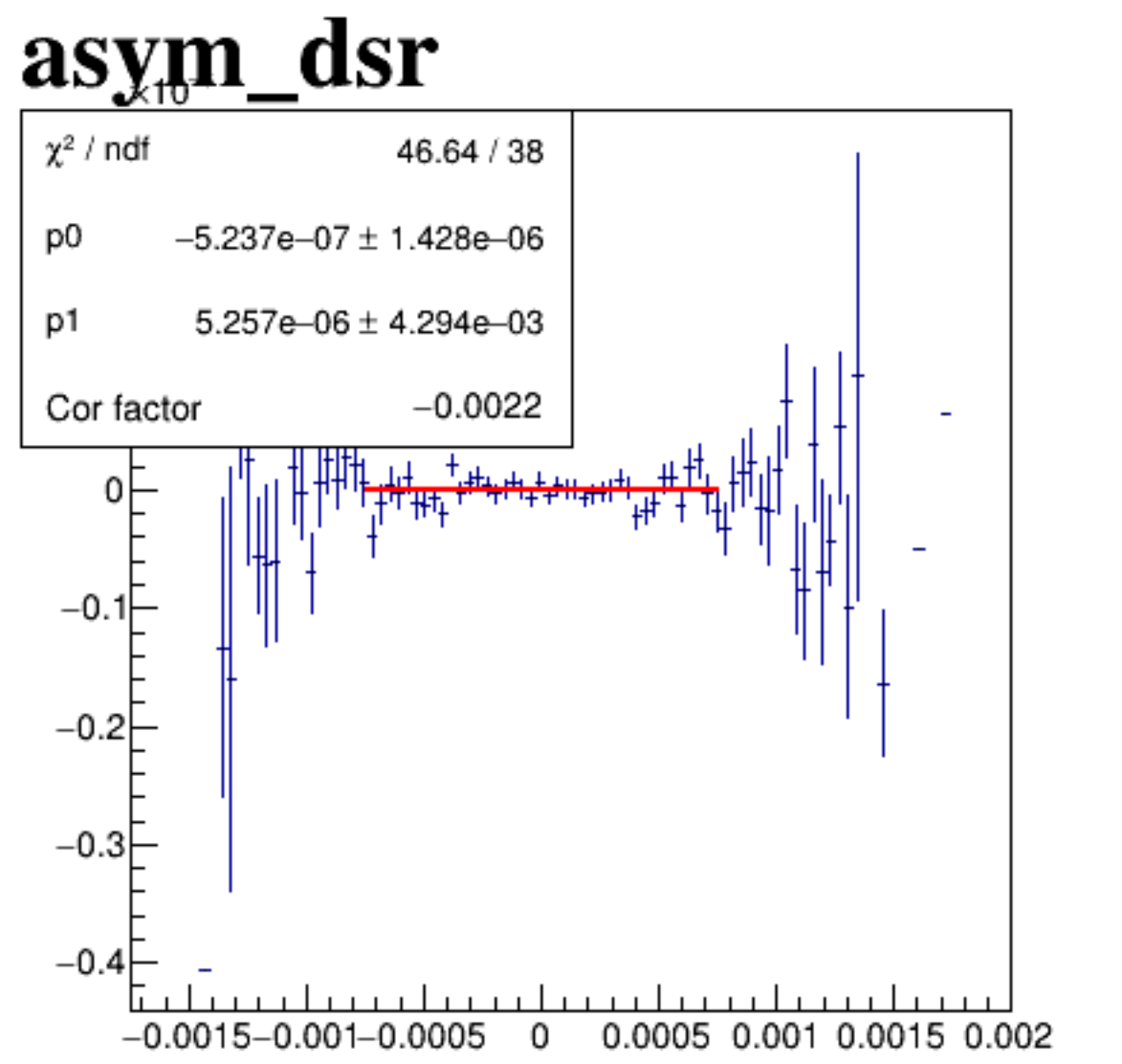
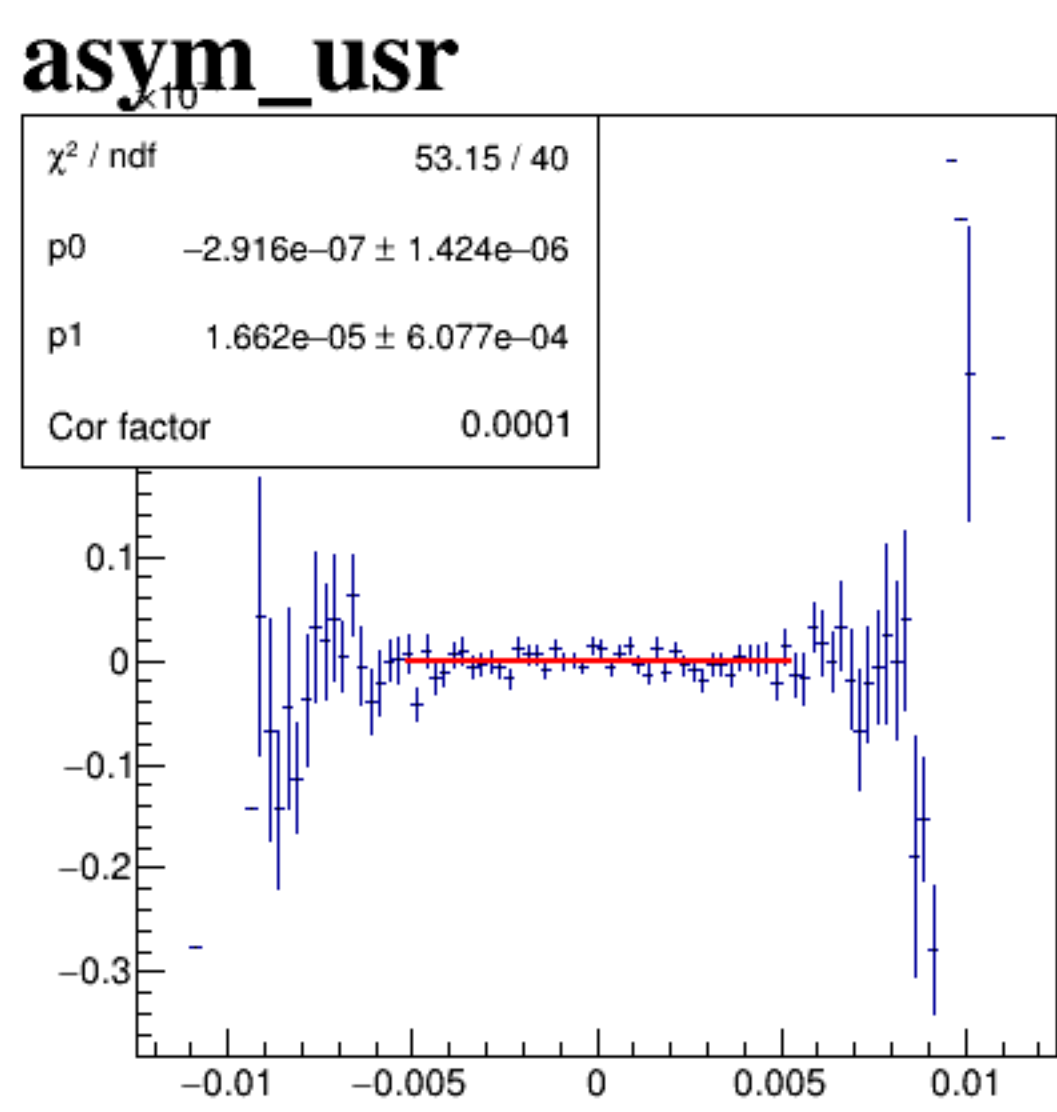
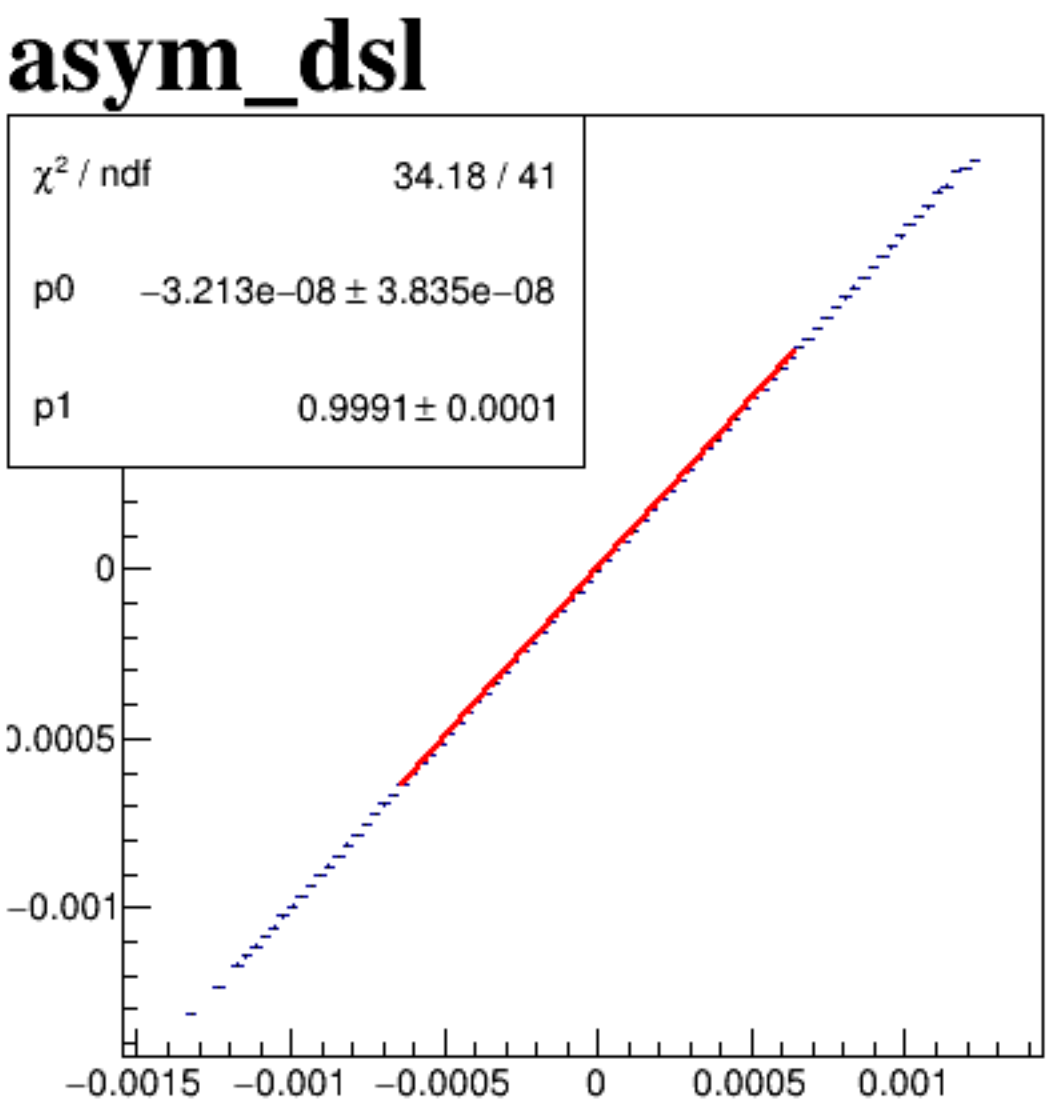
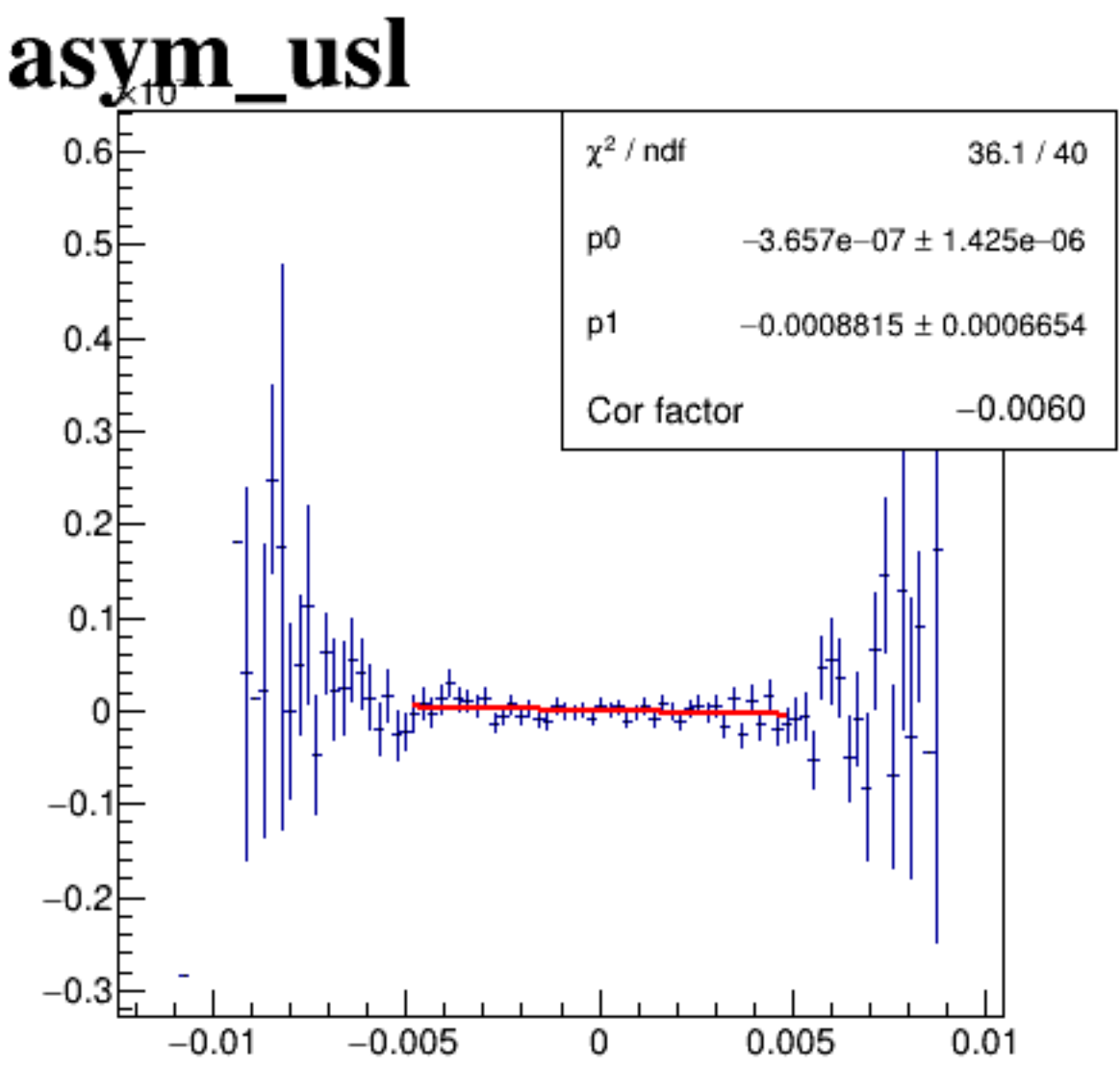
asym_usr

**asym_dsr**

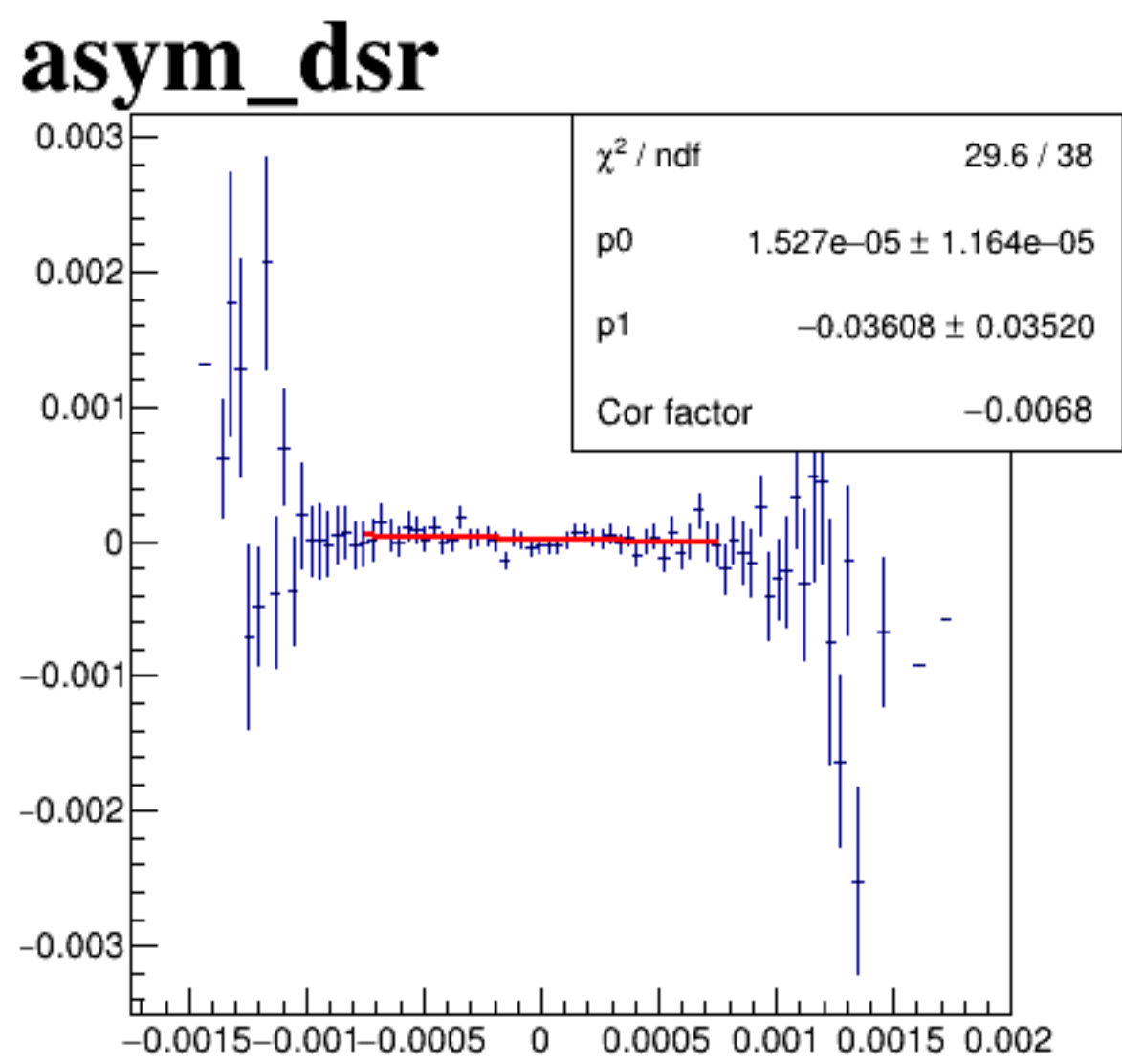
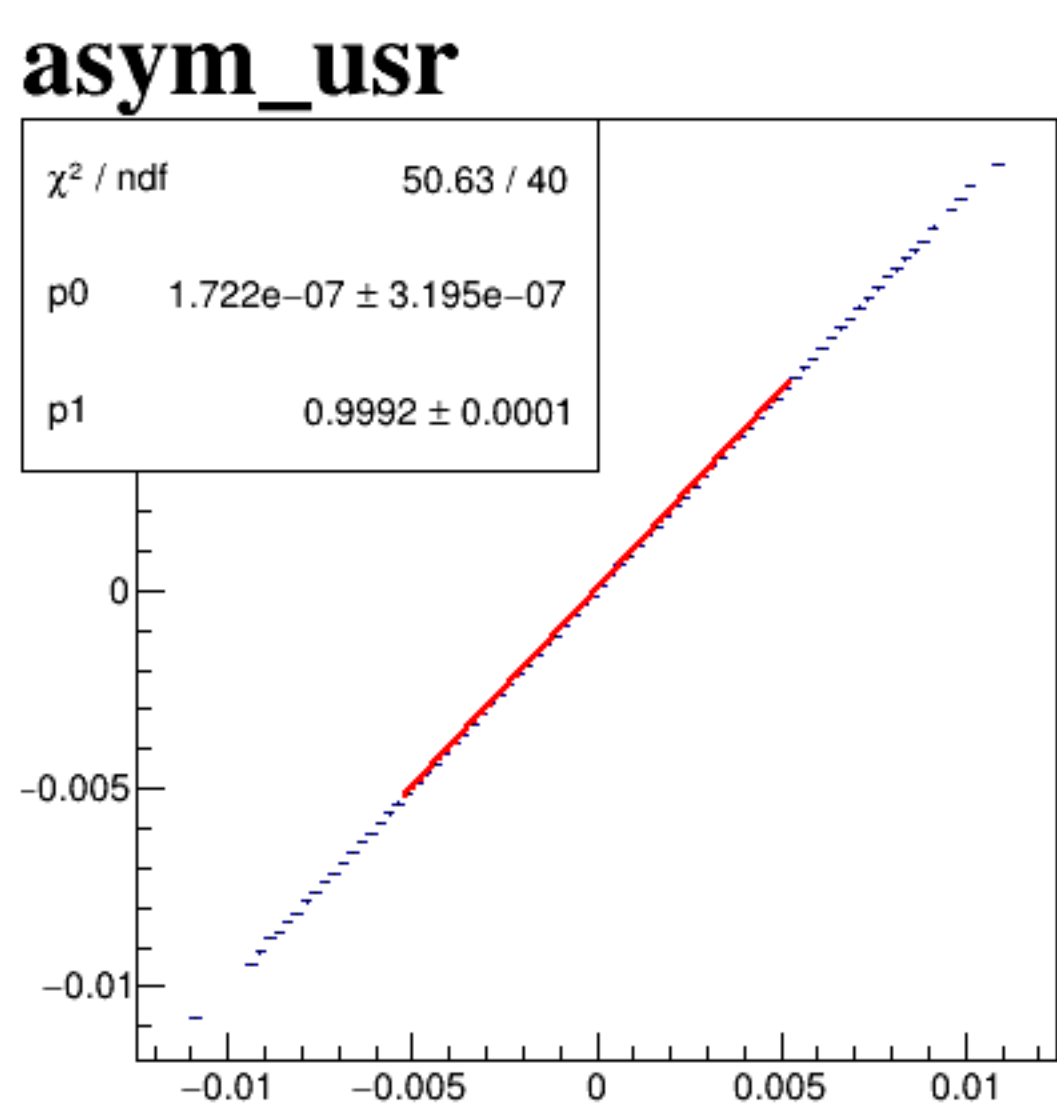
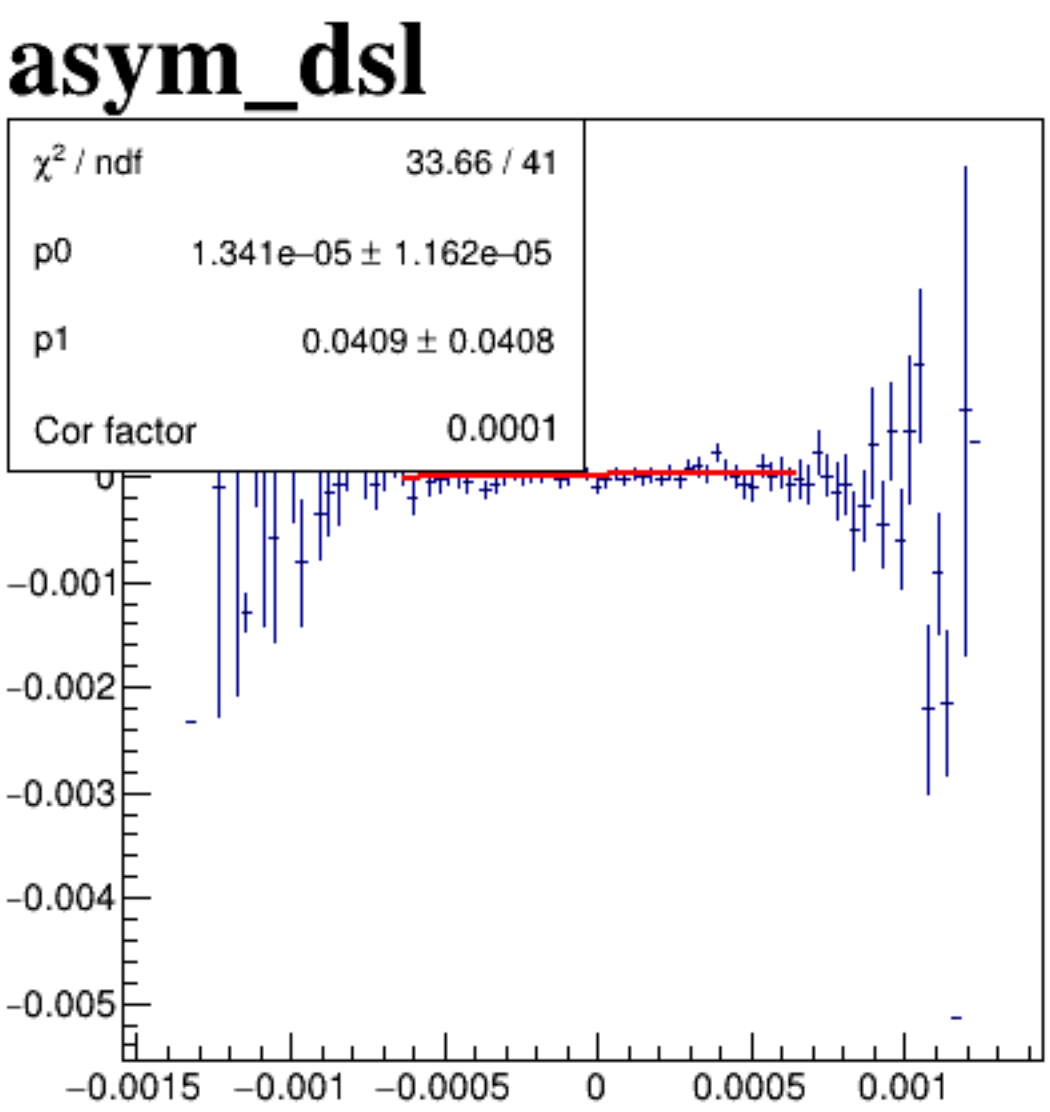
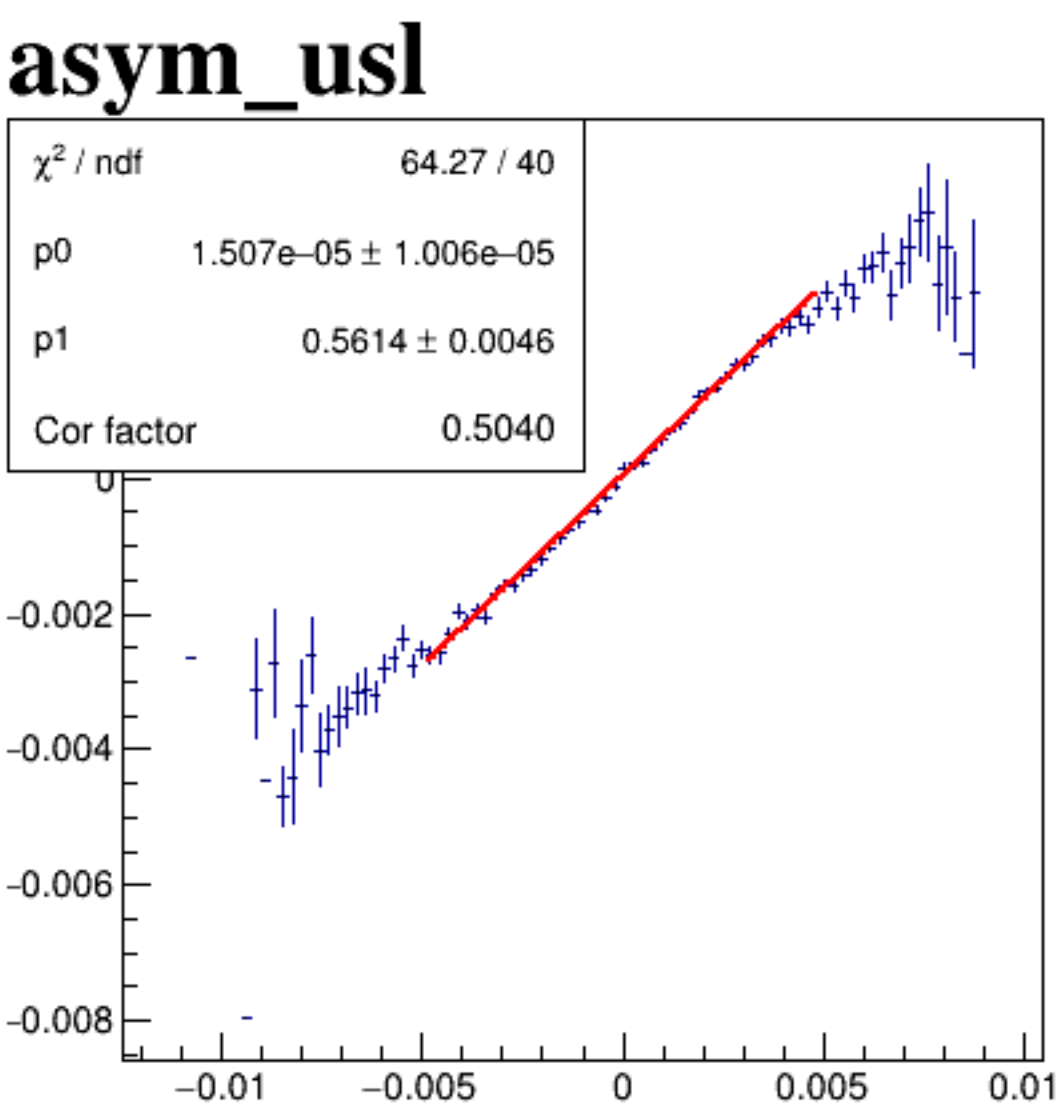
asym_usl



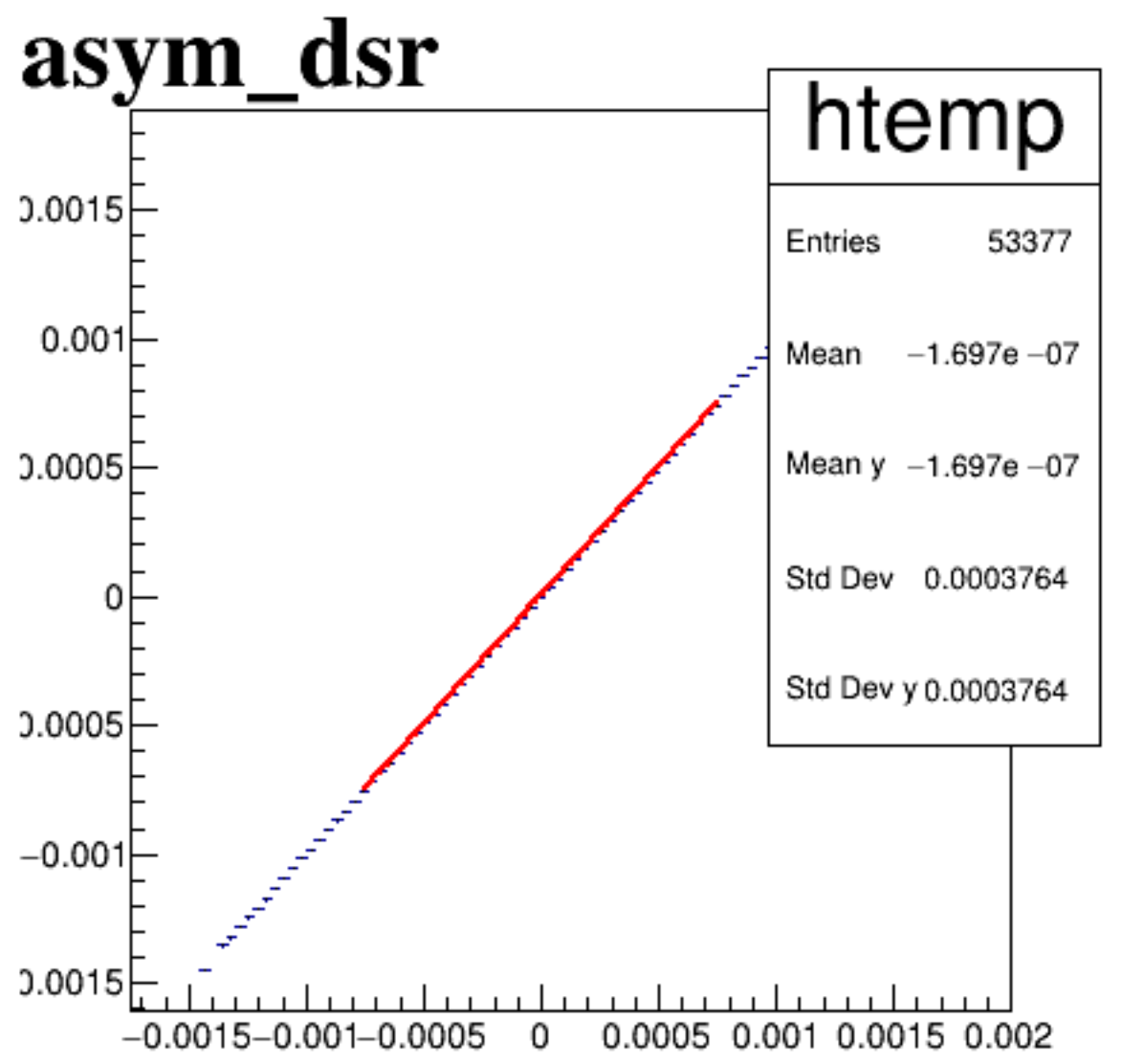
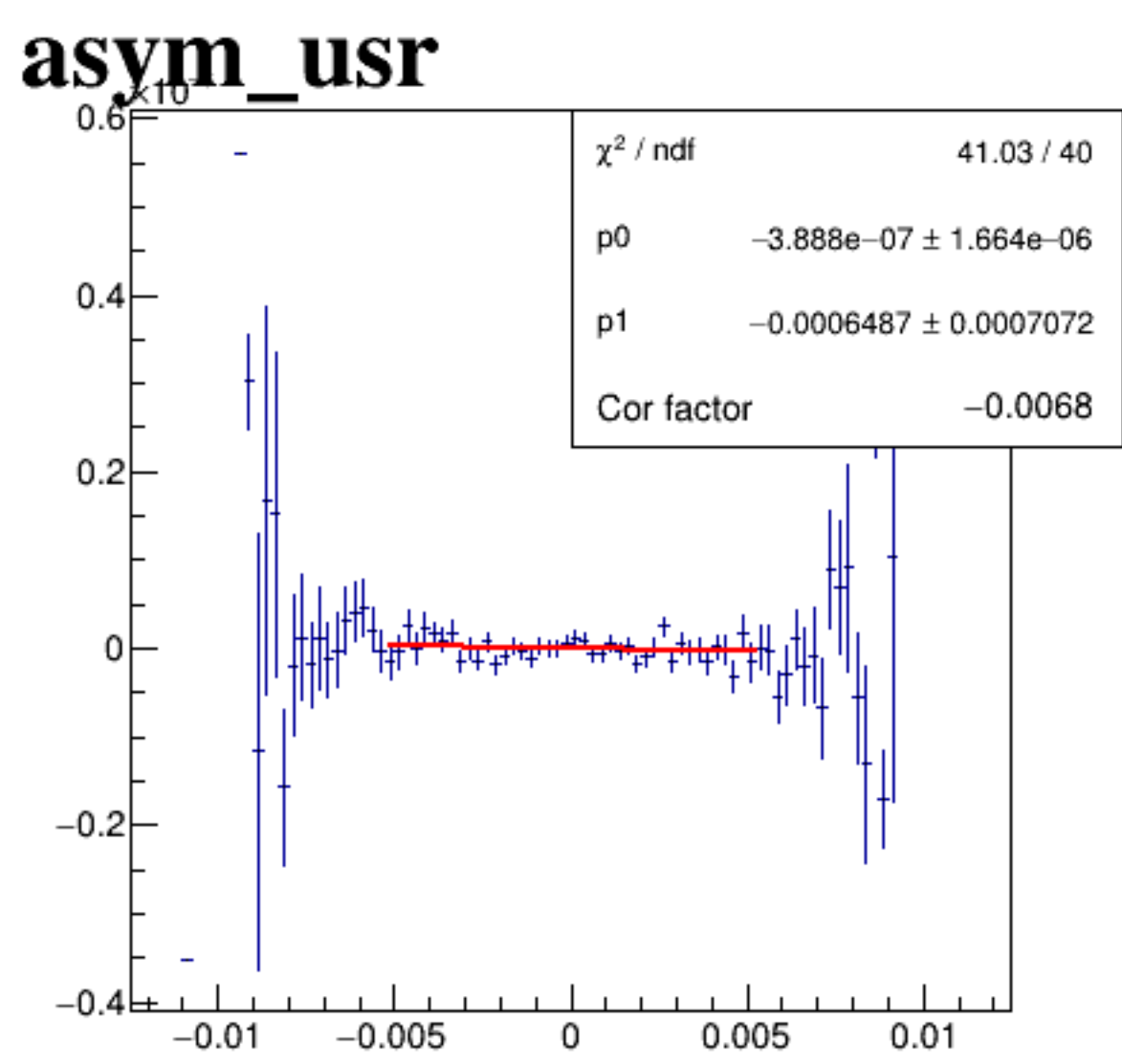
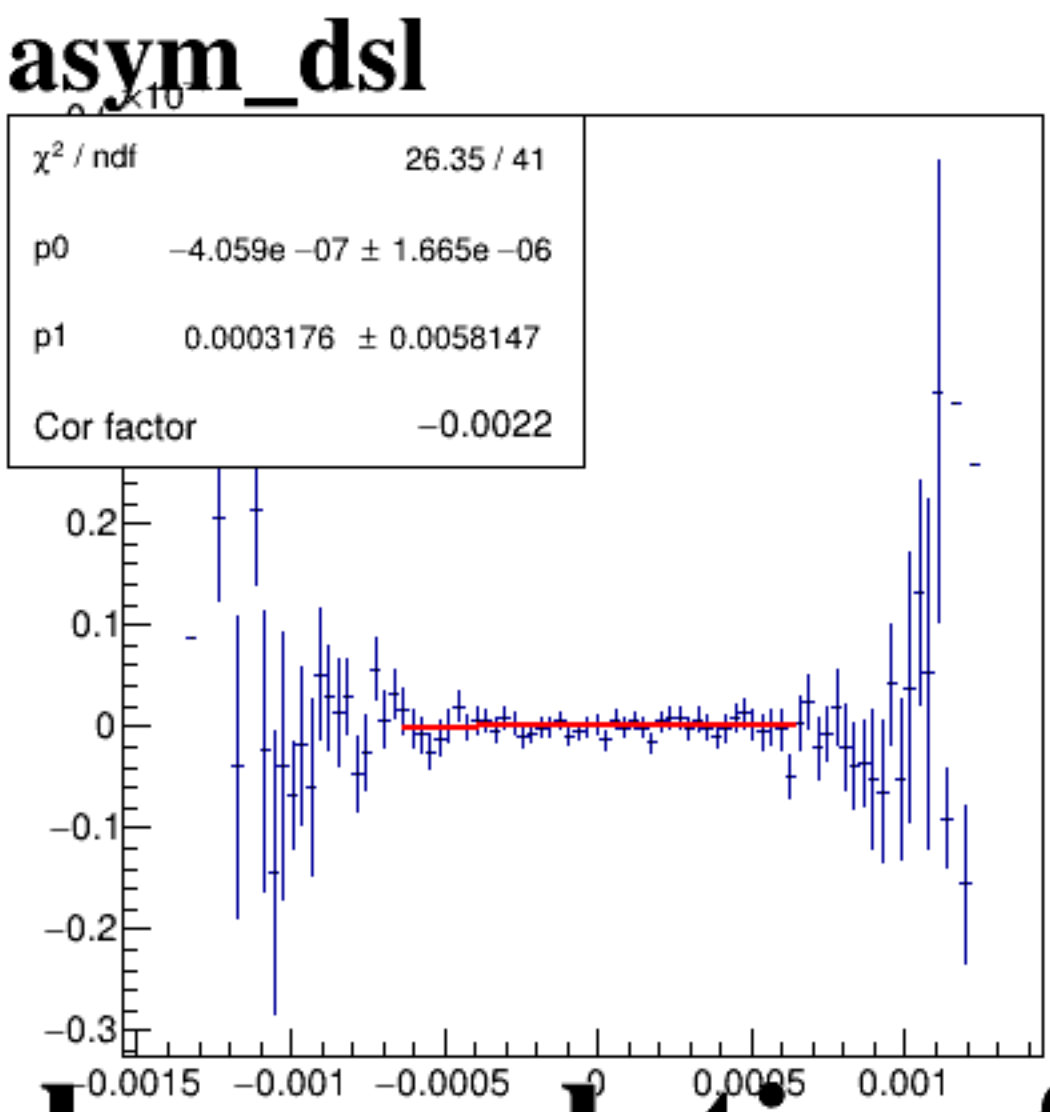
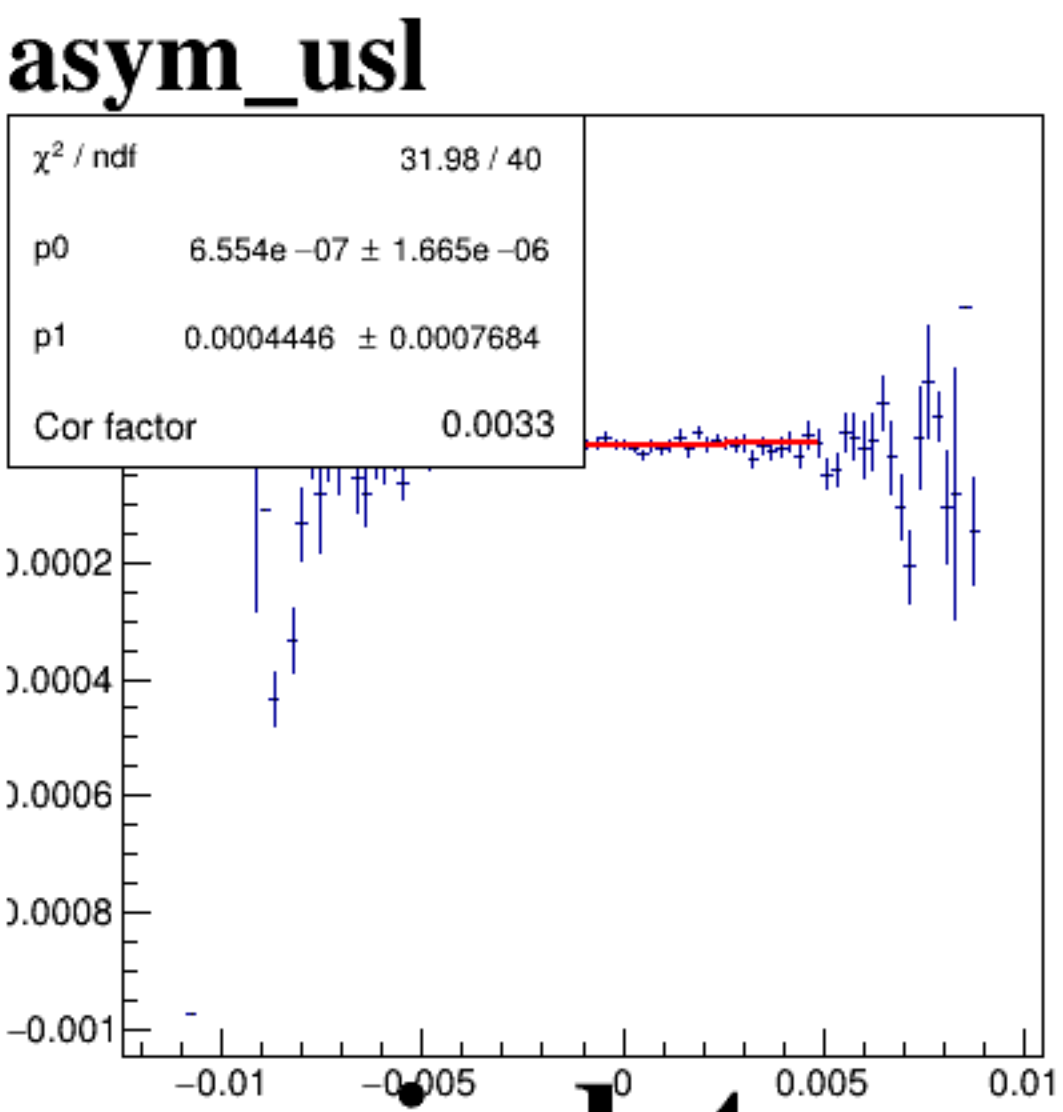
asym_dsl



asym_usr



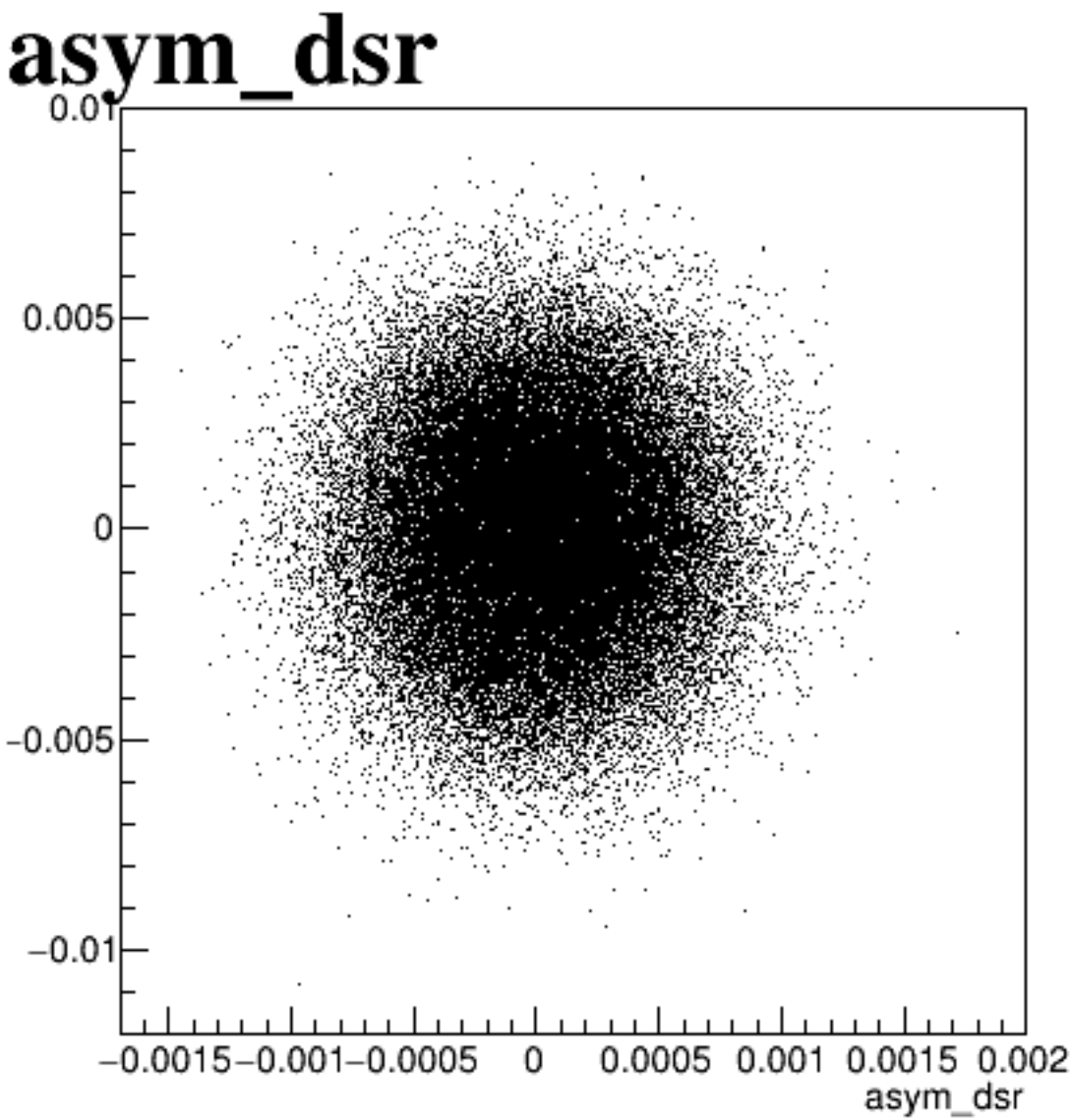
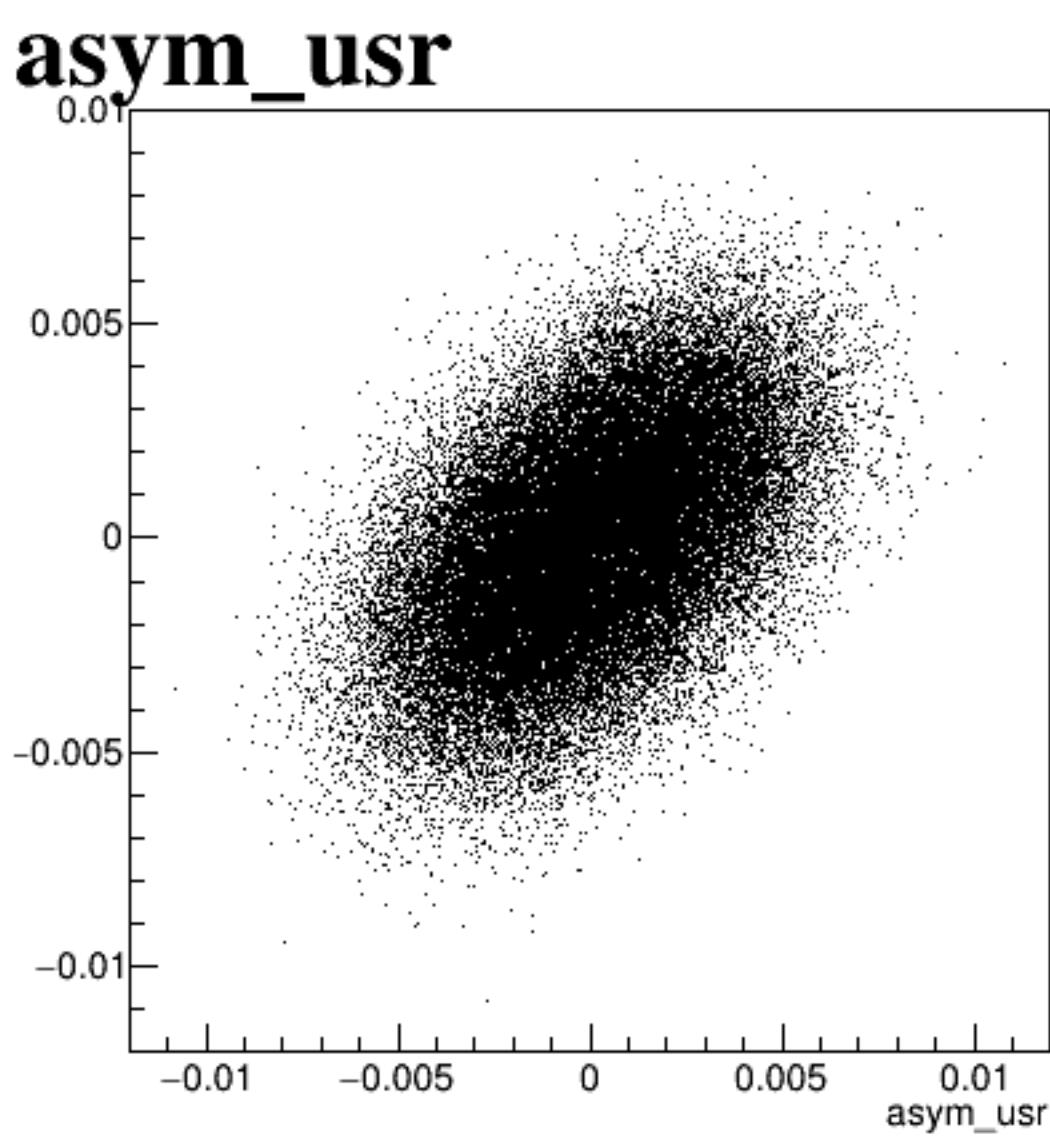
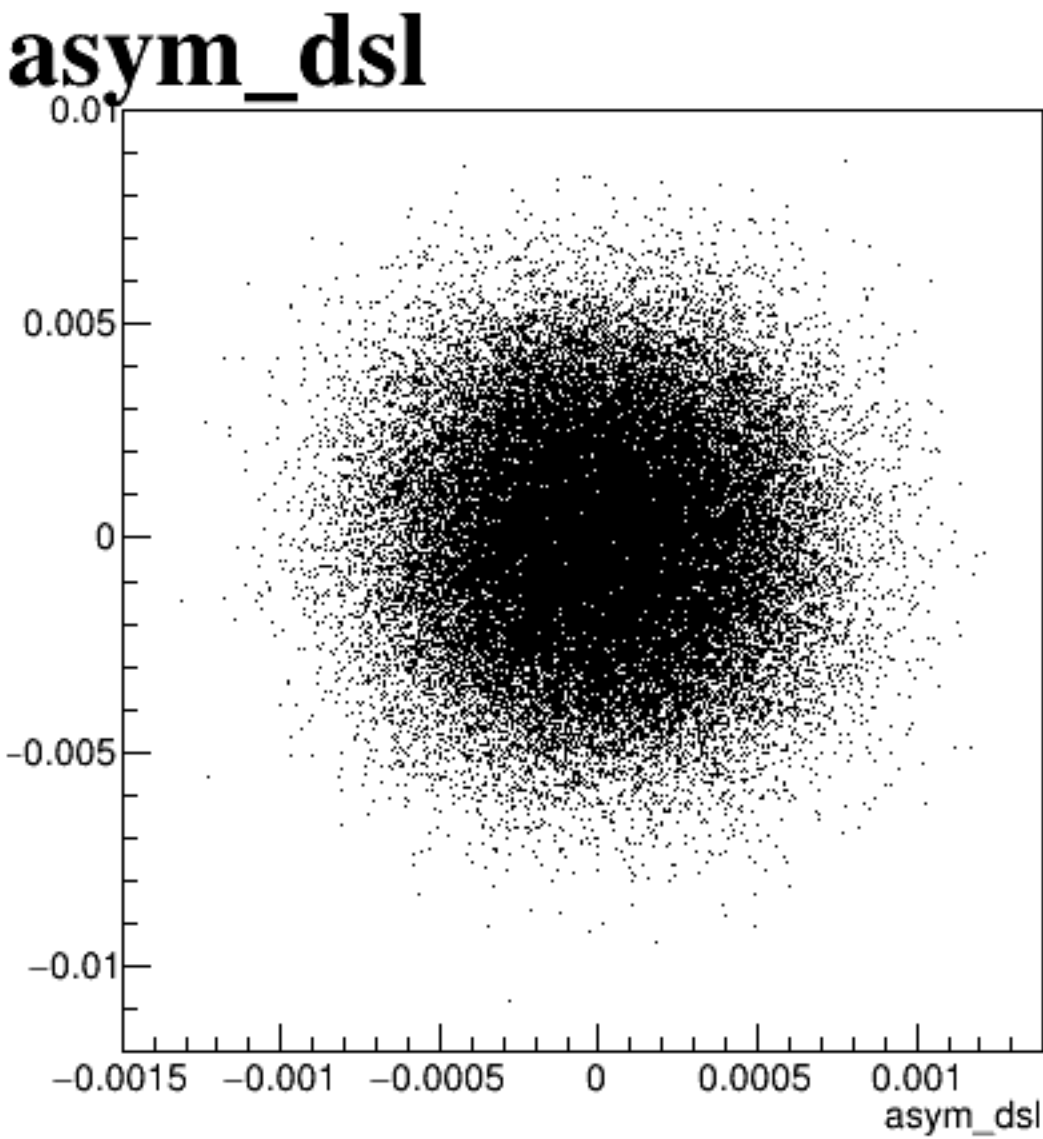
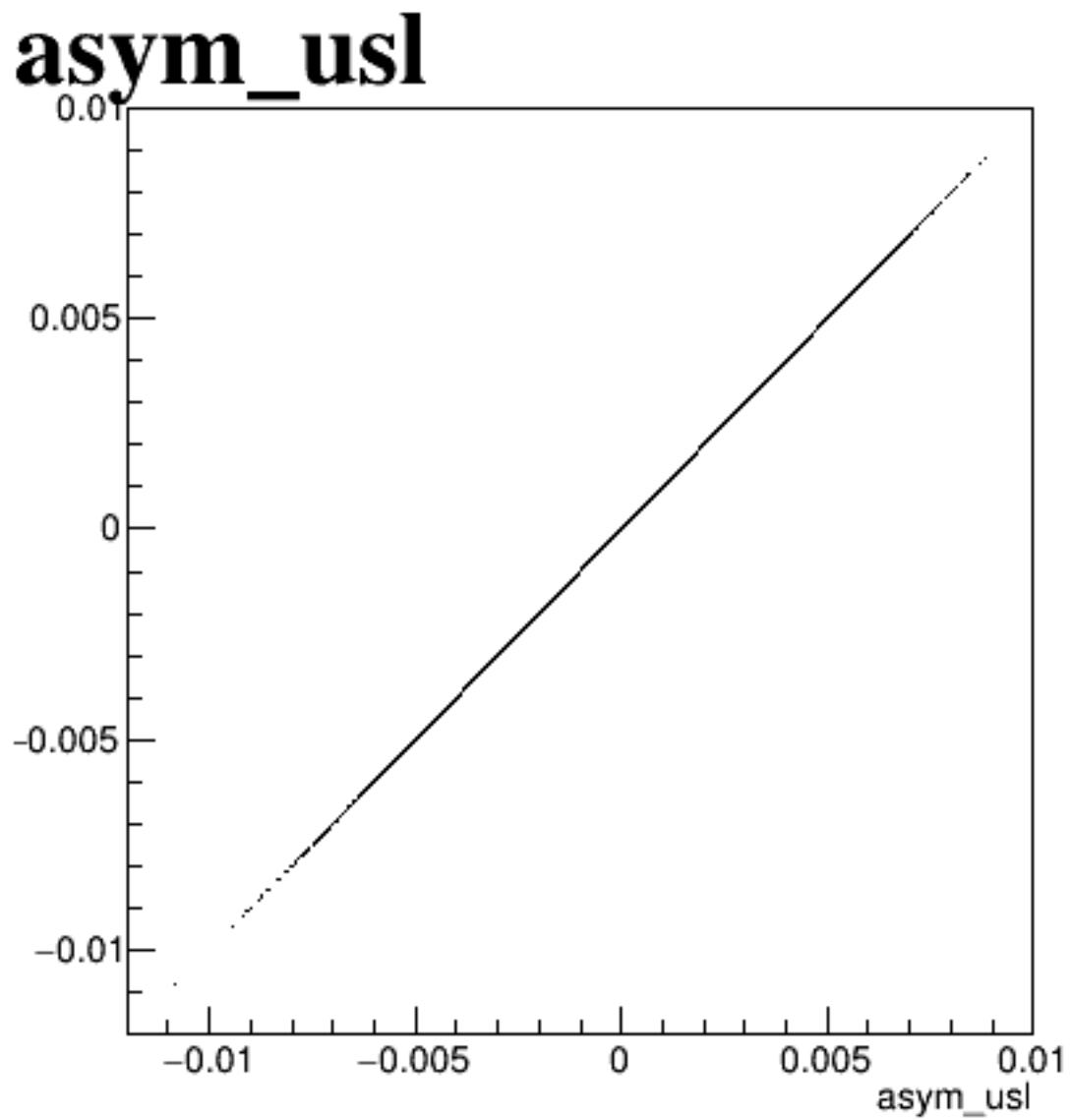
asym_dsr



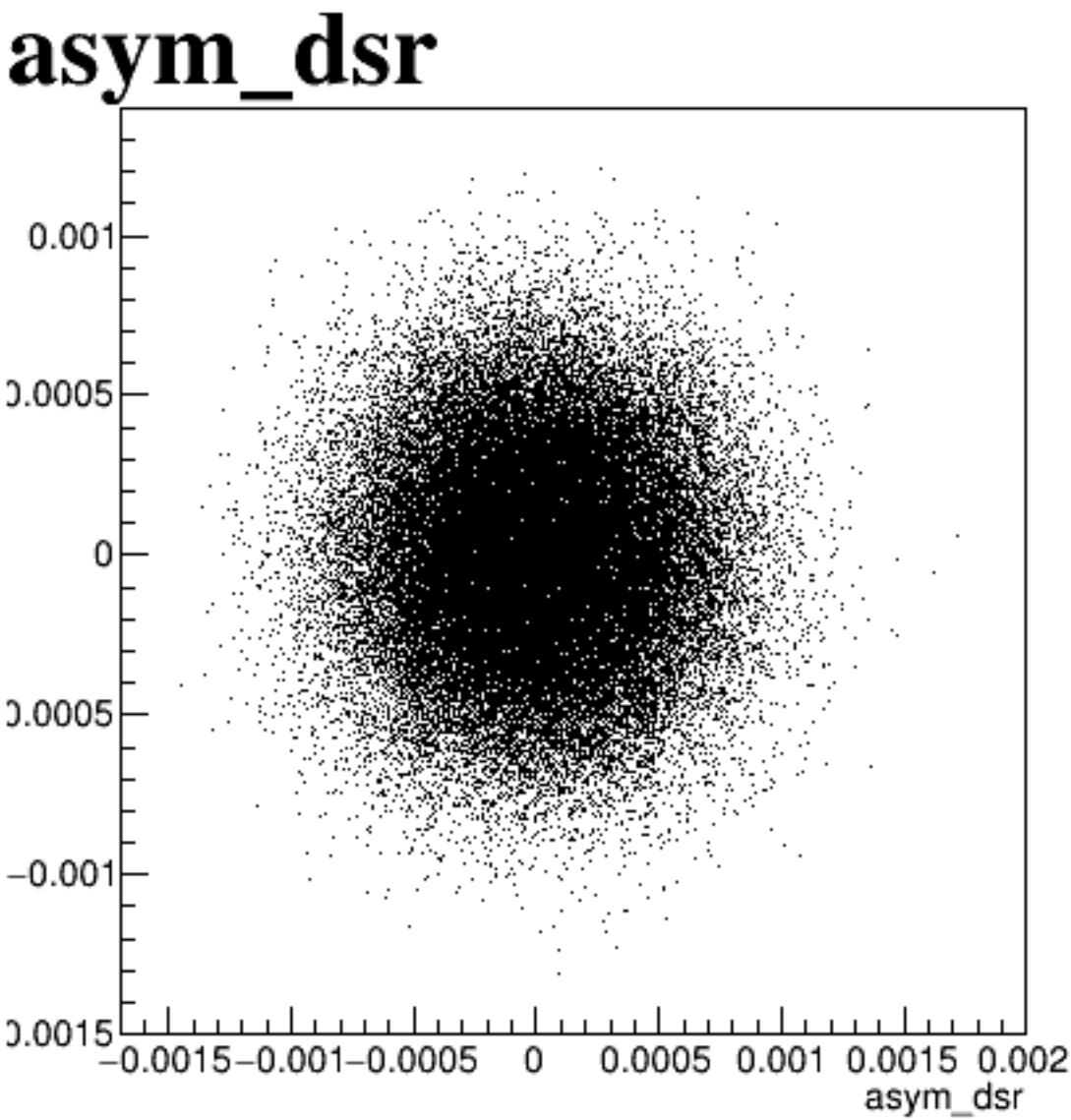
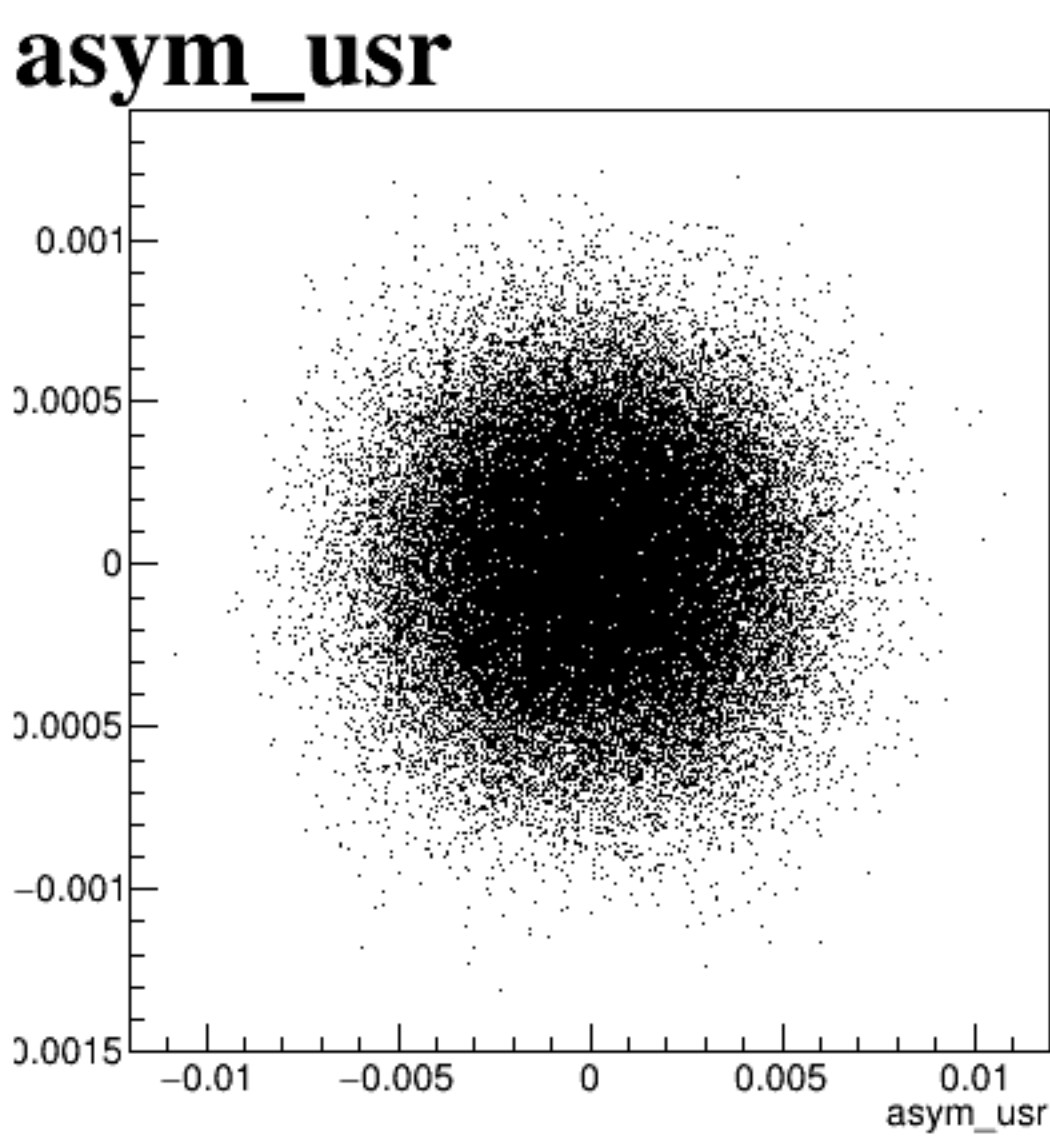
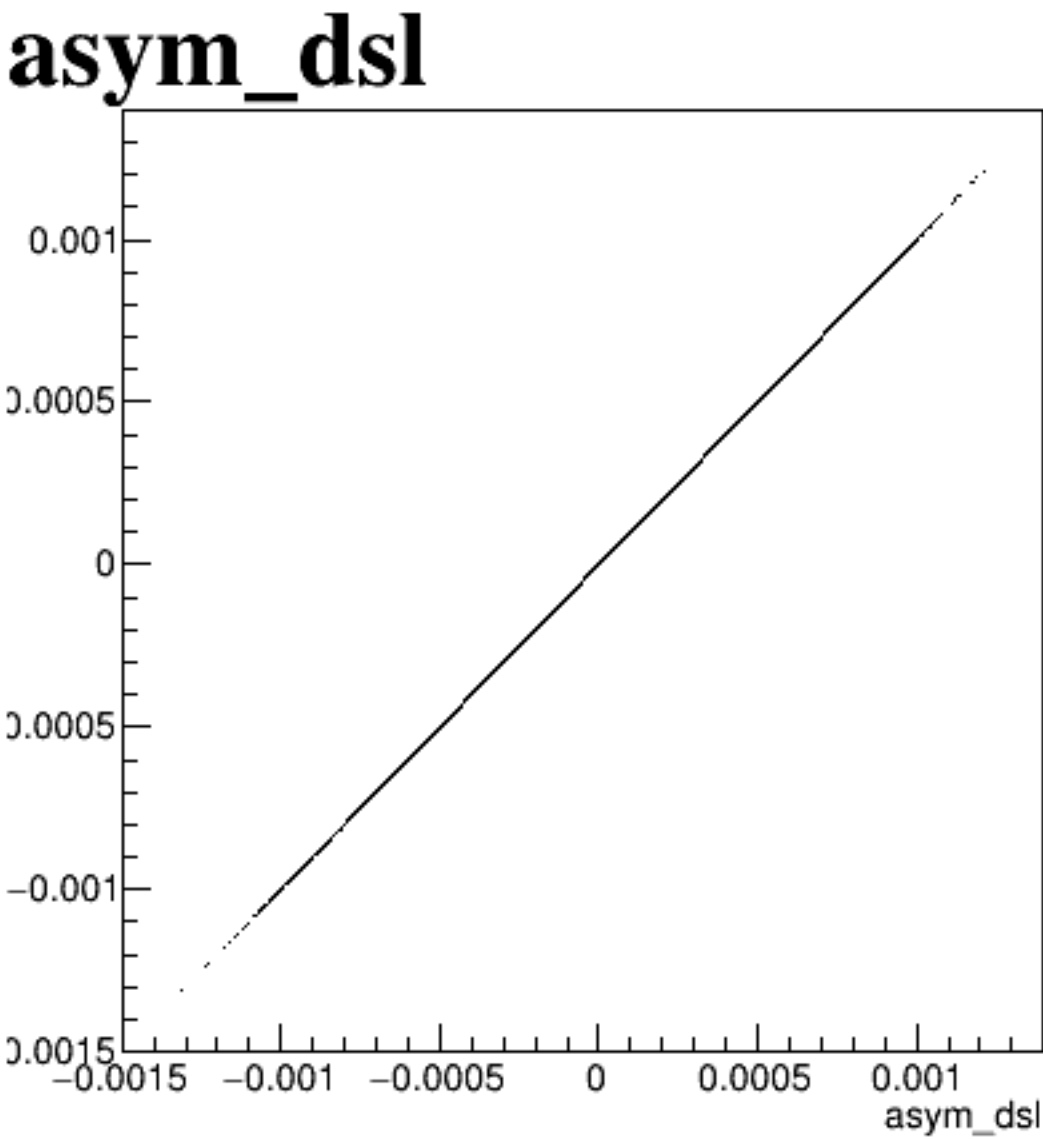
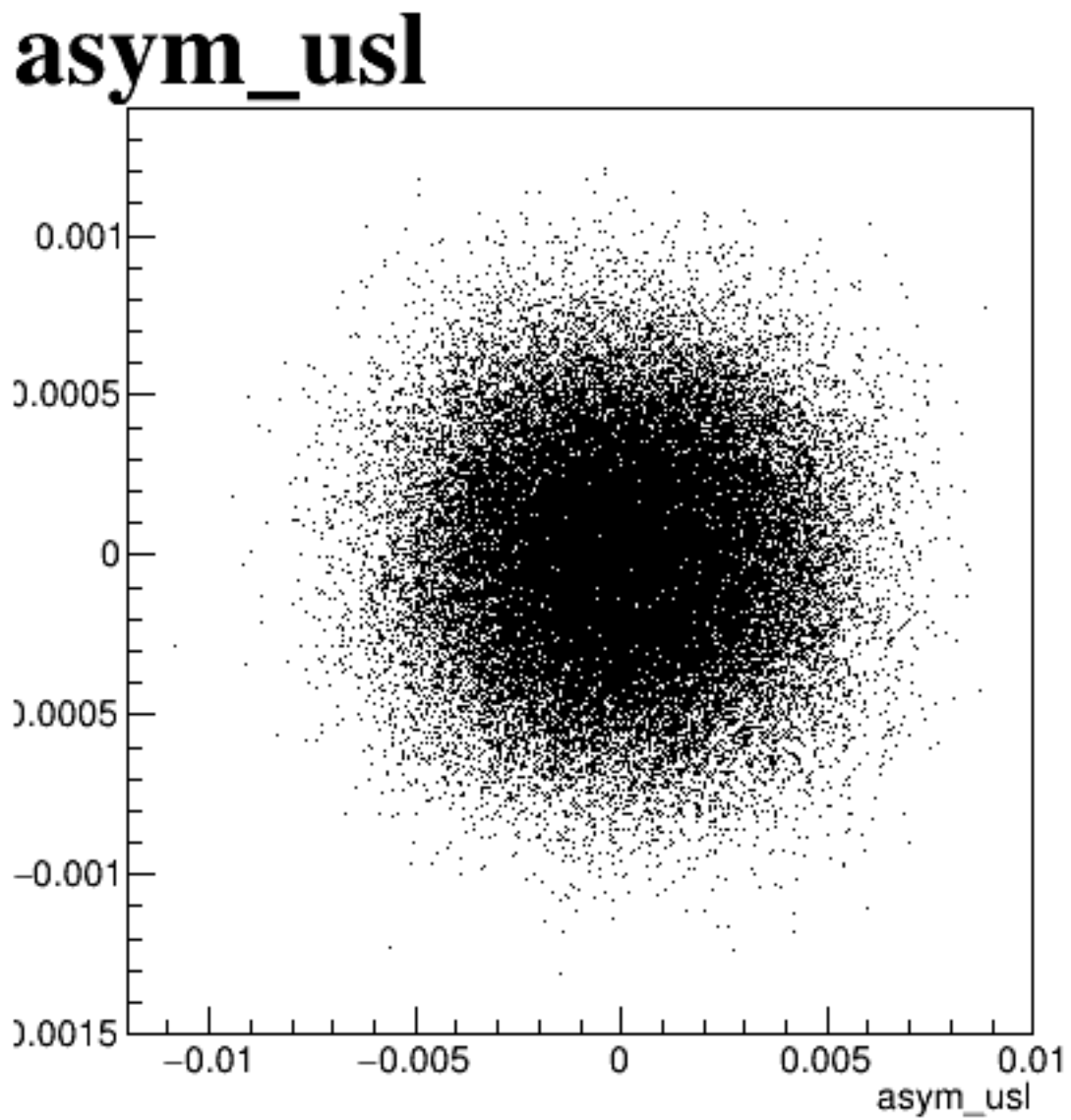
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Entries	53377
Mean	$-1.697\text{e-}07$
Mean y	$-1.697\text{e-}07$
Std Dev	0.0003764
Std Dev y	0.0003764

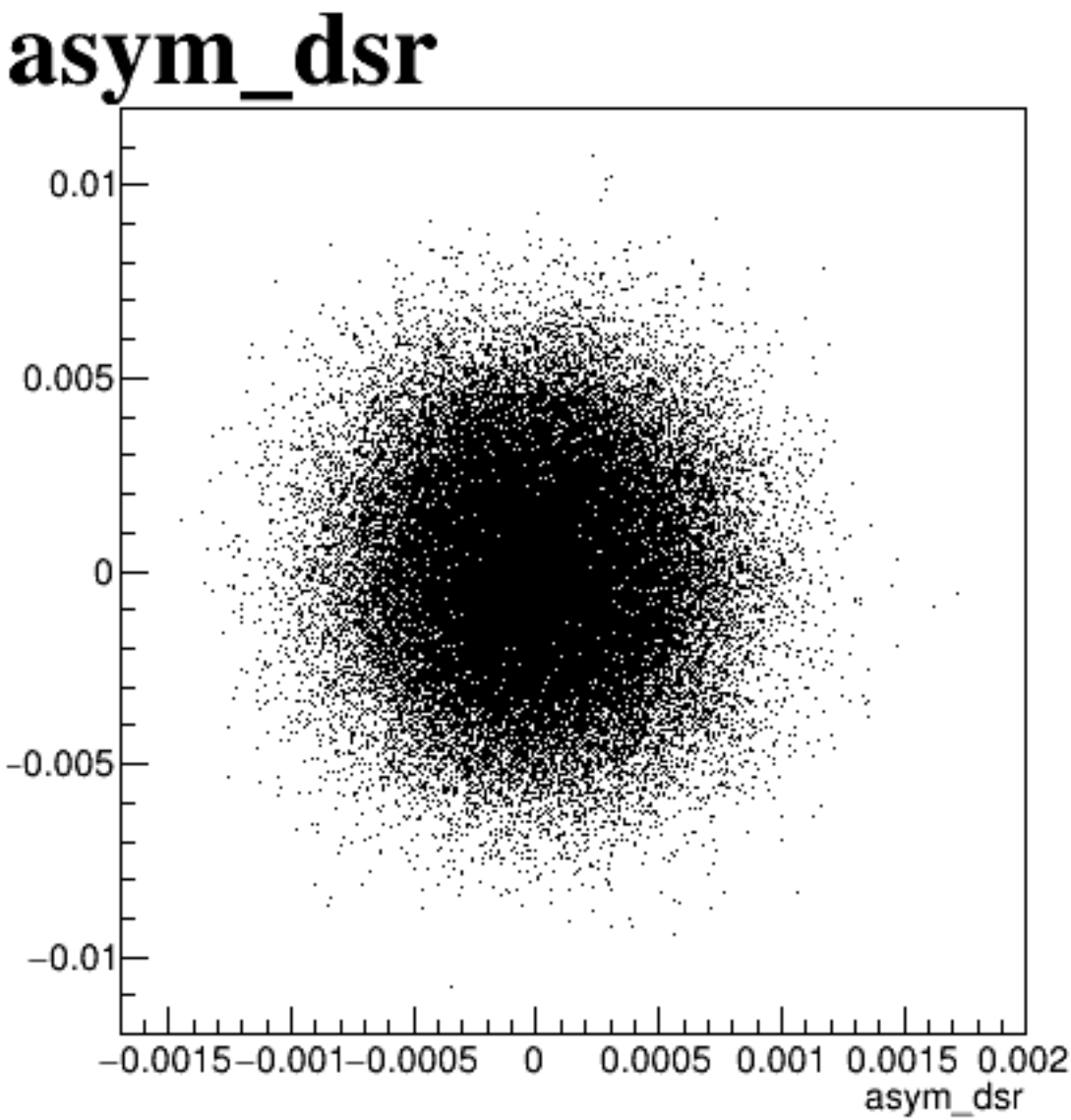
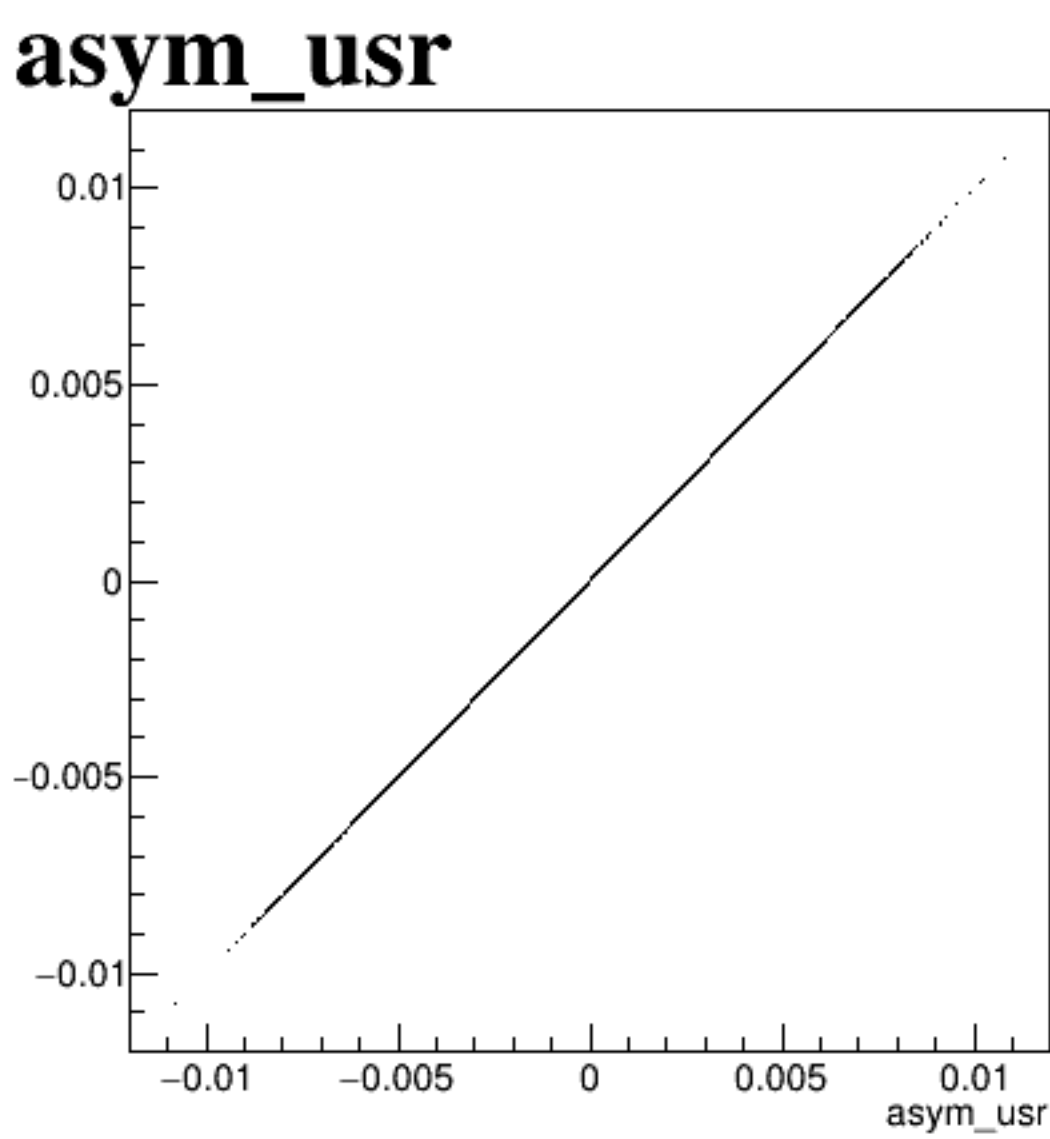
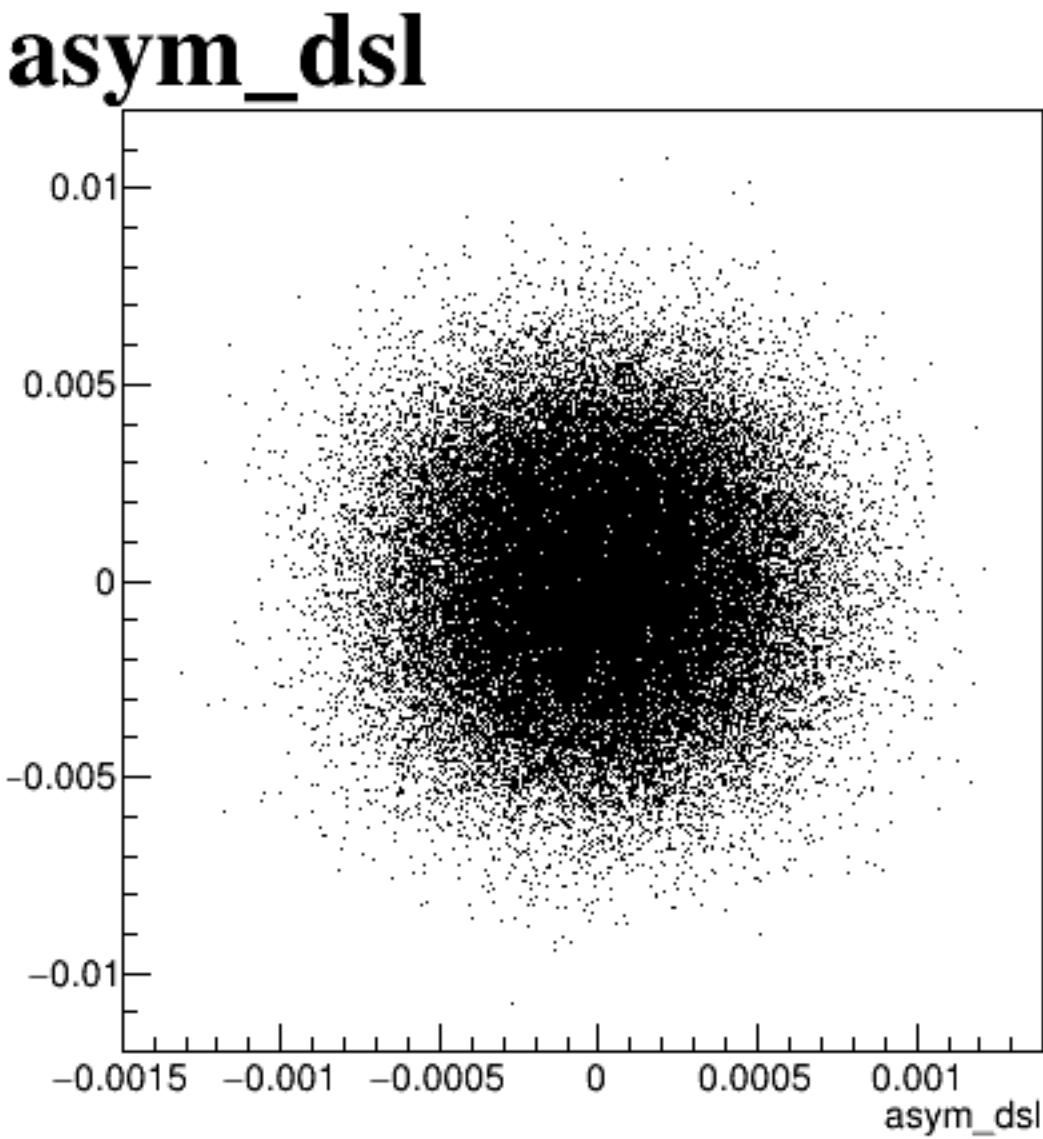
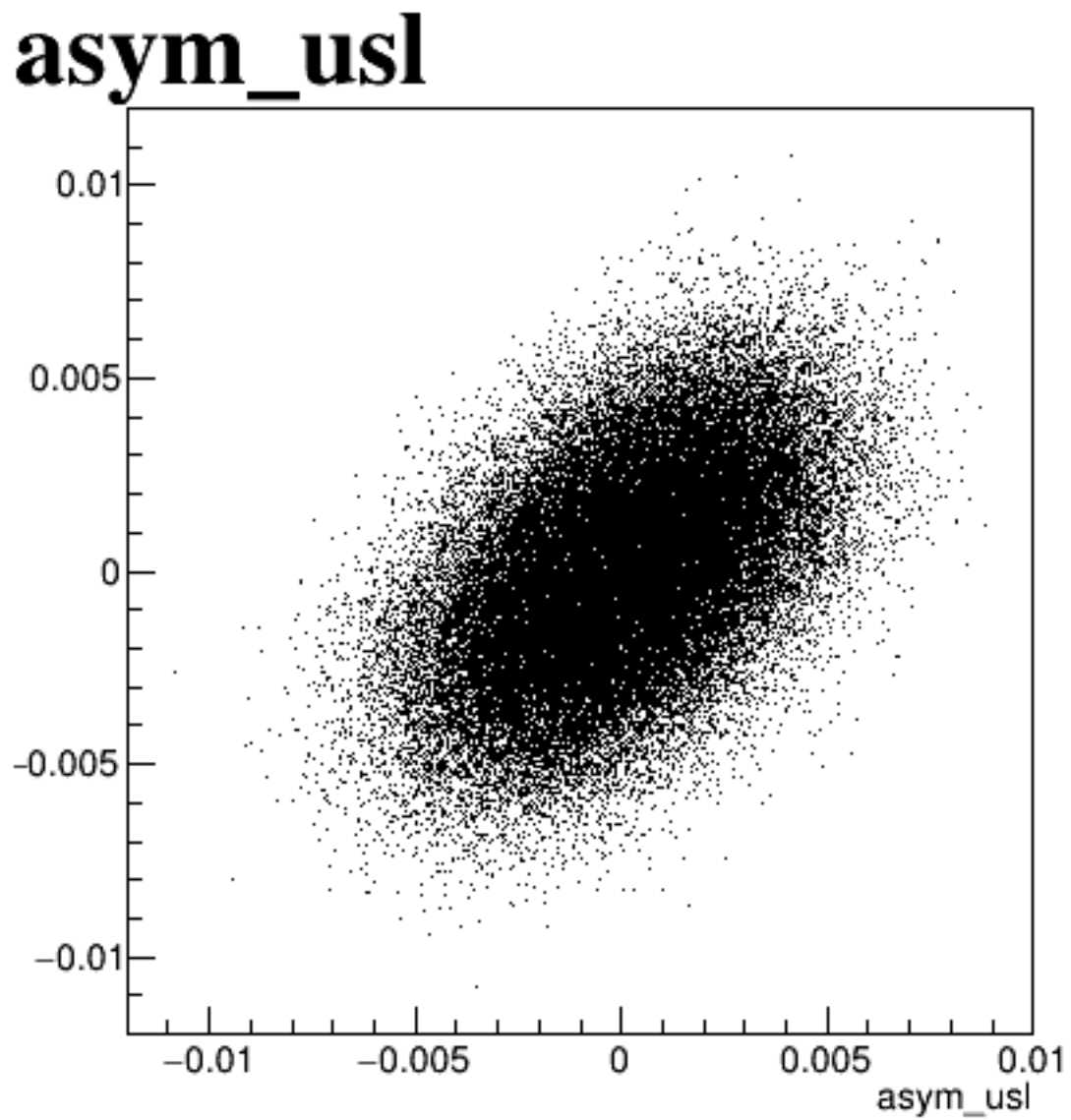
asym_usl



asym_dsl



asym_usr



asym_dsr

