

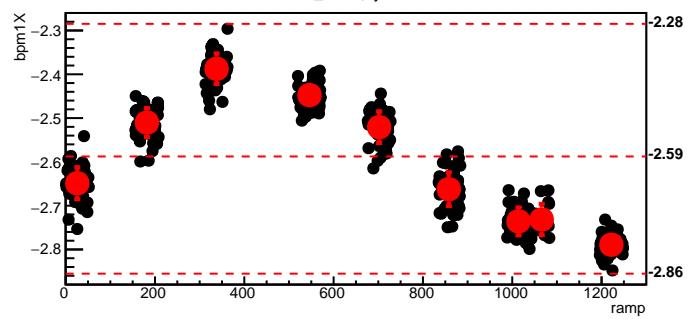




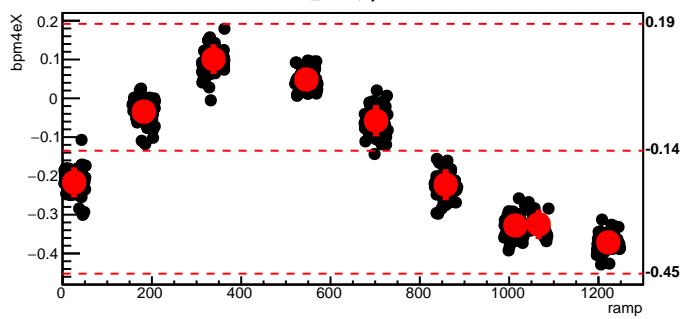




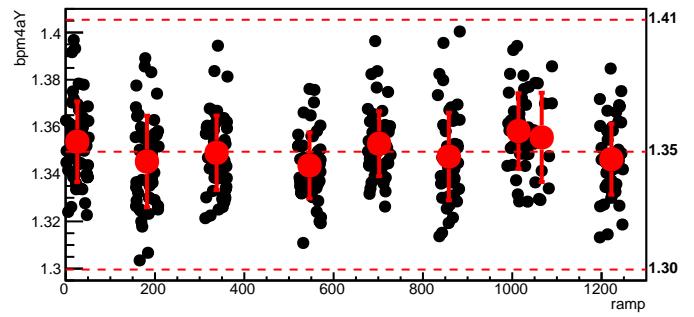
bmod Trim5,cyc=6079



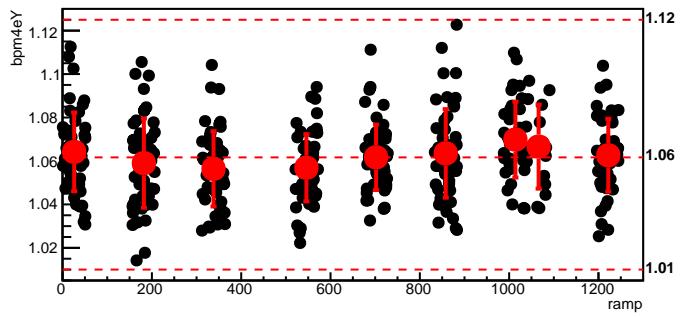
bmod Trim5,cyc=6079



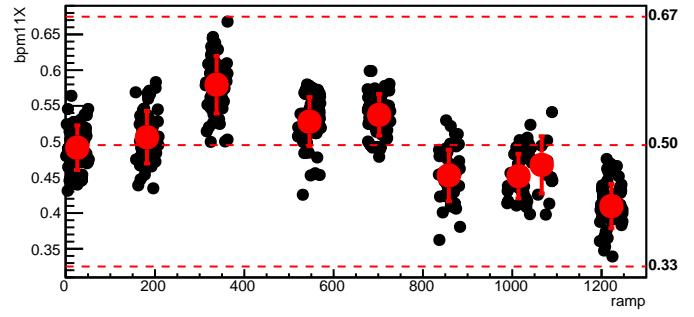
bmod Trim5,cyc=6079



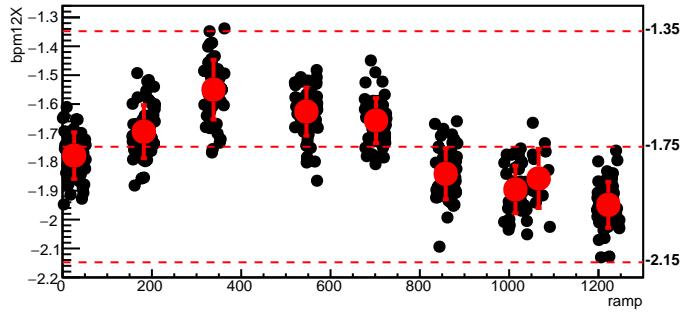
bmod Trim5,cyc=6079



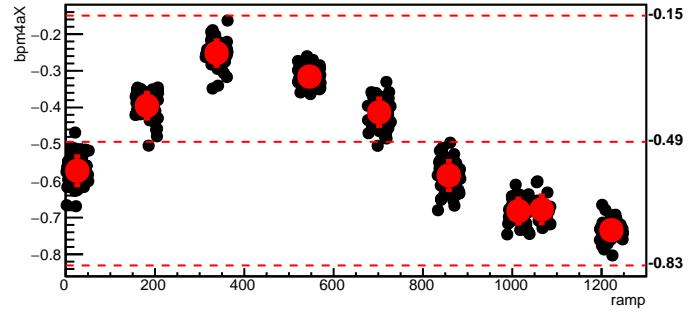
bmod Trim5,cyc=6079



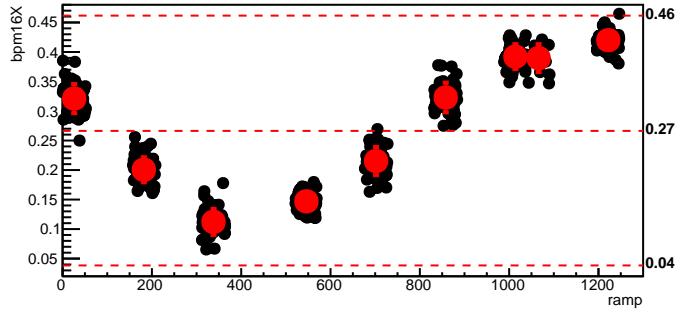
bmod Trim5,cyc=6079



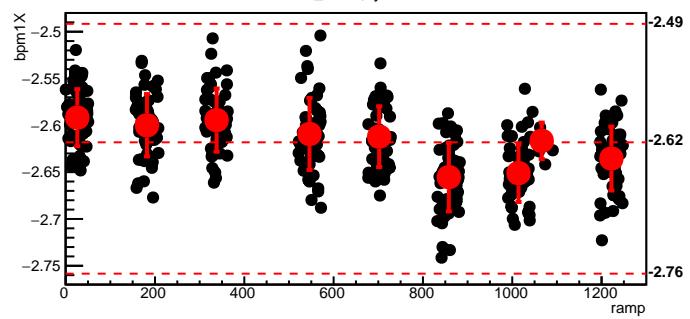
bmod Trim5,cyc=6079



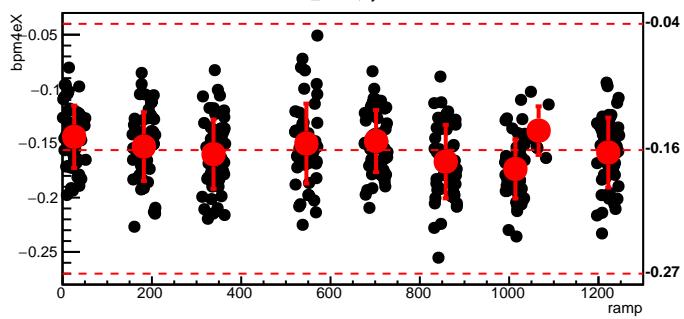
bmod Trim5,cyc=6079



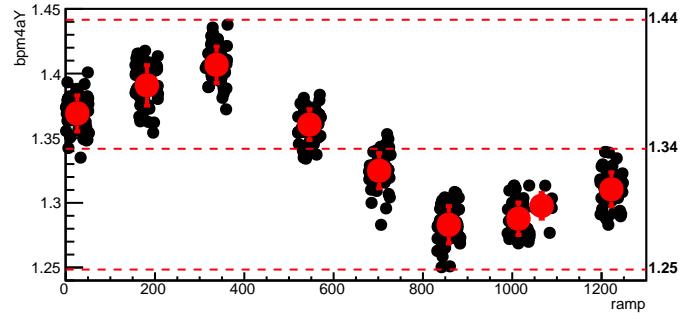
bmod Trim6,cyc=6079



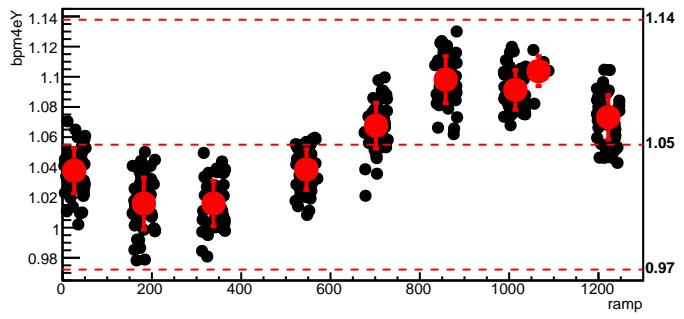
bmod Trim6,cyc=6079



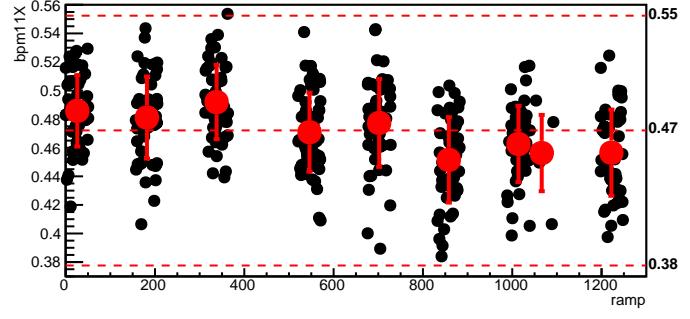
bmod Trim6,cyc=6079



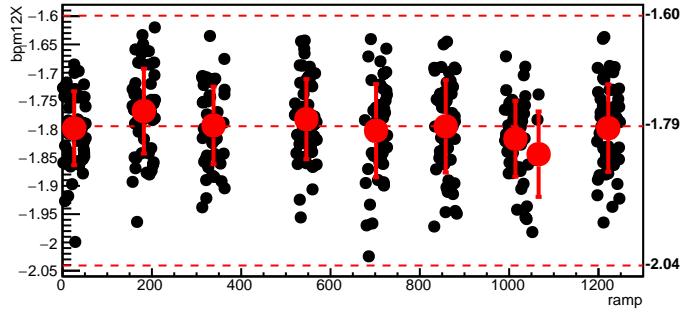
bmod Trim6,cyc=6079



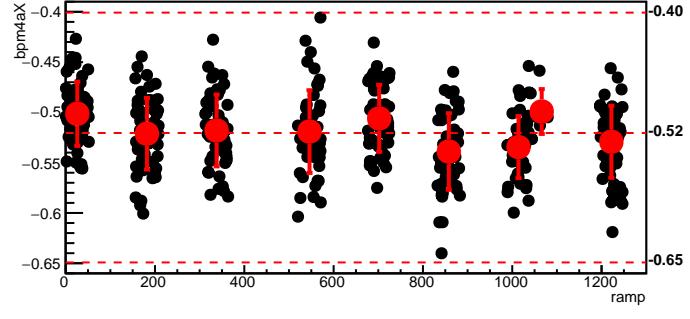
bmod Trim6,cyc=6079



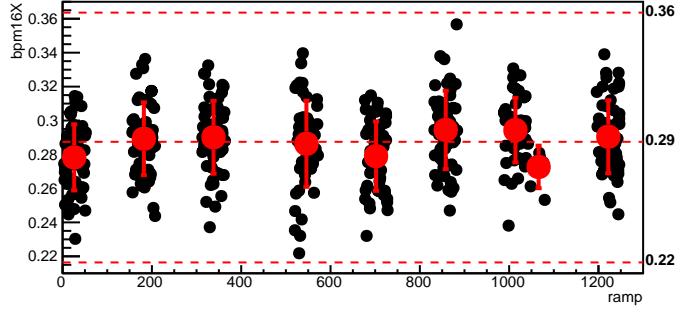
bmod Trim6,cyc=6079



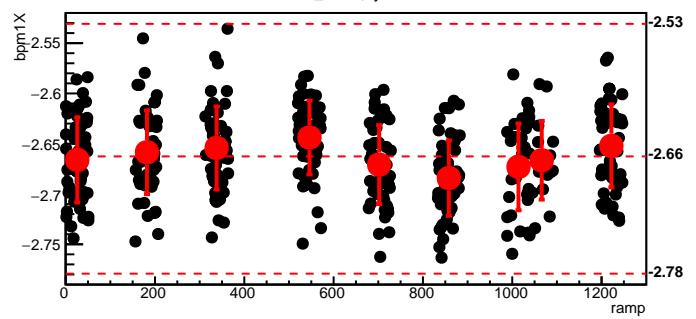
bmod Trim6,cyc=6079



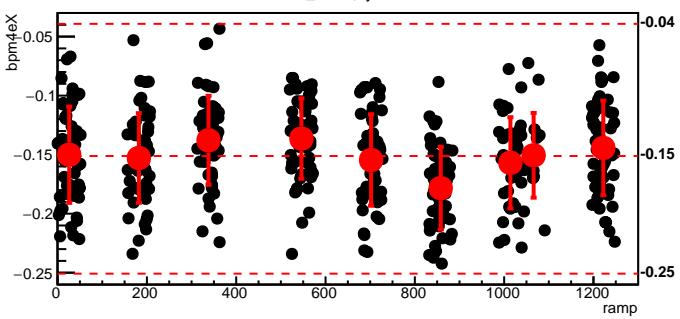
bmod Trim6,cyc=6079



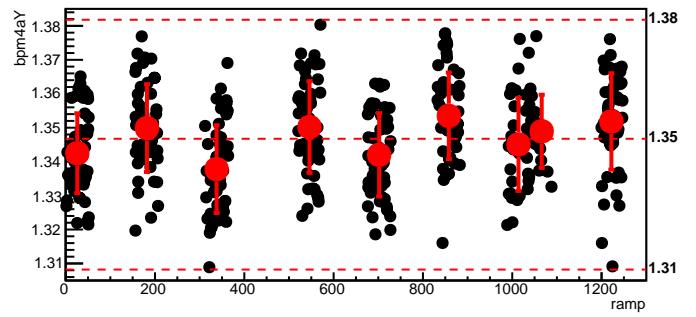
bmod Trim7,cyc=6079



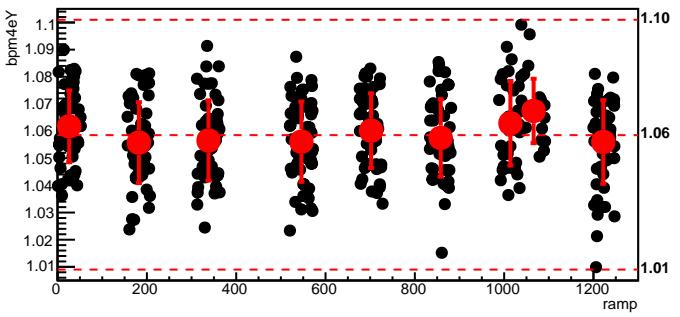
bmod Trim7,cyc=6079



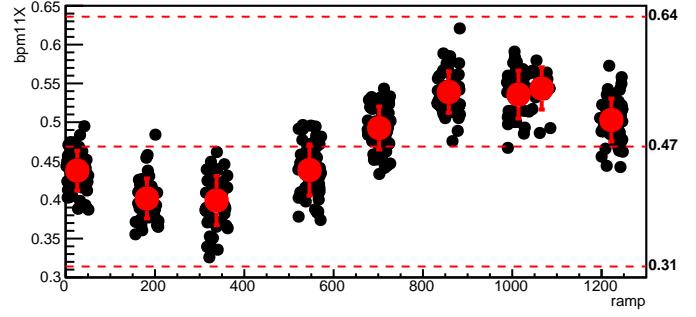
bmod Trim7,cyc=6079



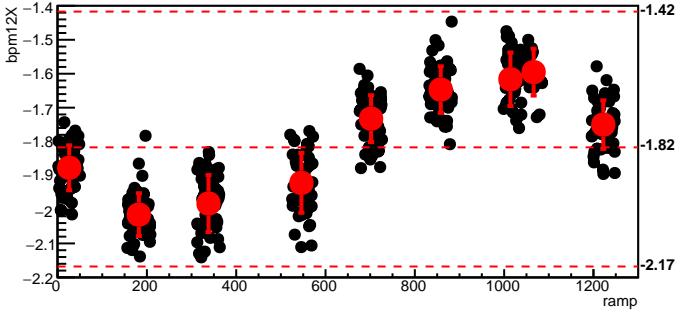
bmod Trim7,cyc=6079



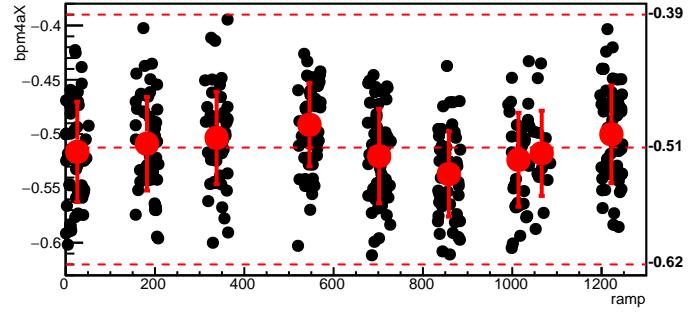
bmod Trim7,cyc=6079



bmod Trim7,cyc=6079



bmod Trim7,cyc=6079



bmod Trim7,cyc=6079

