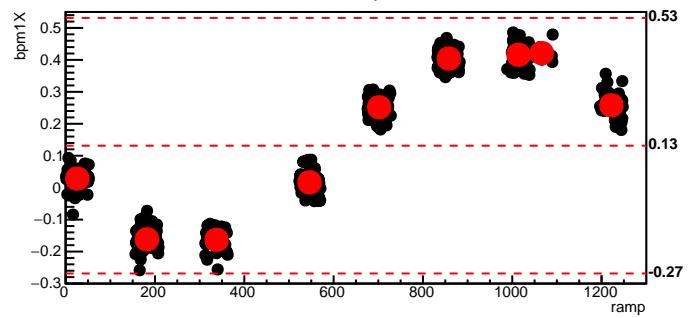
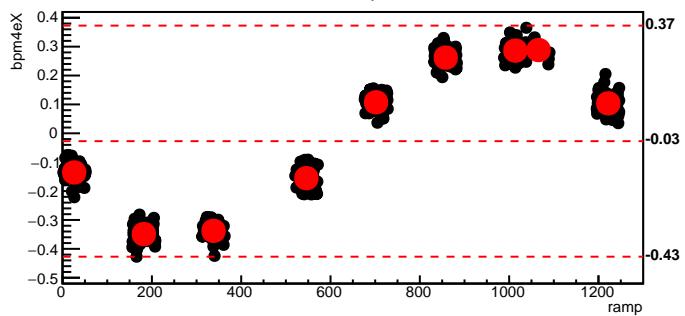


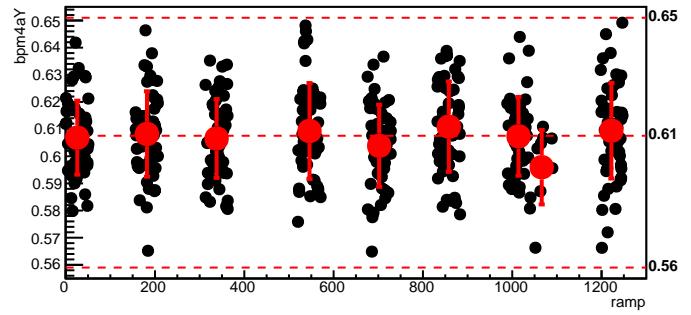
bmod Trim1,cyc=6096



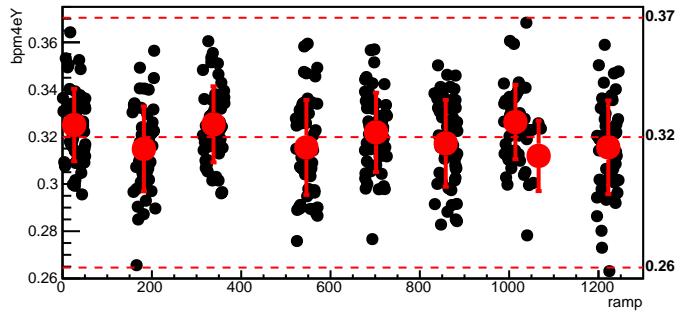
bmod Trim1,cyc=6096



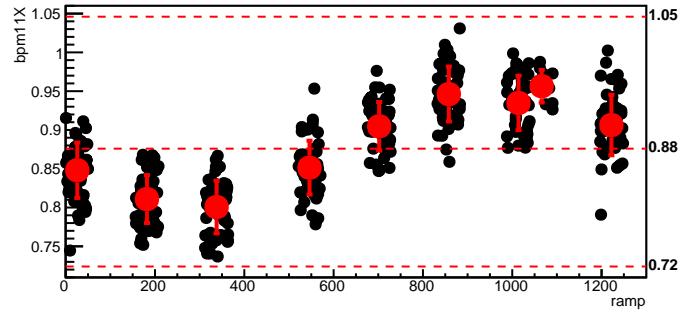
bmod Trim1,cyc=6096



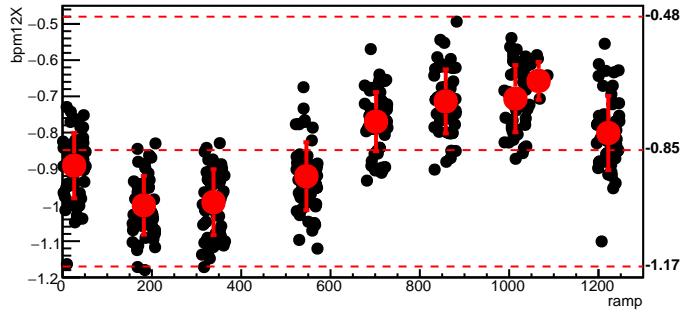
bmod Trim1,cyc=6096



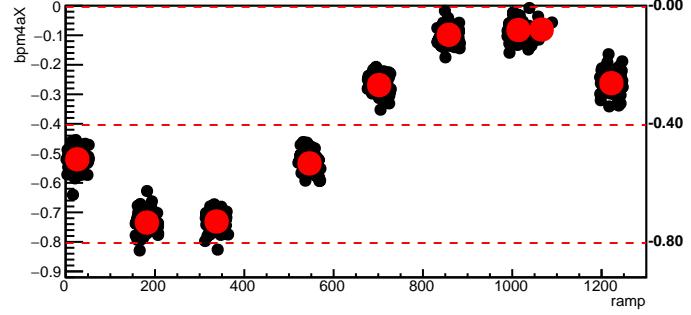
bmod Trim1,cyc=6096



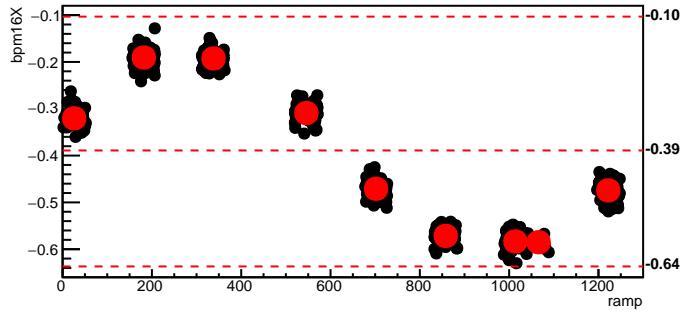
bmod Trim1,cyc=6096



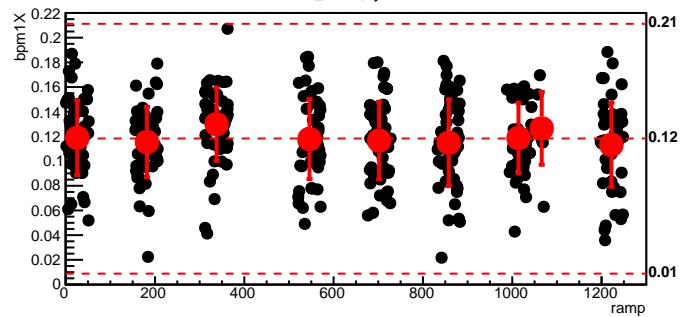
bmod Trim1,cyc=6096



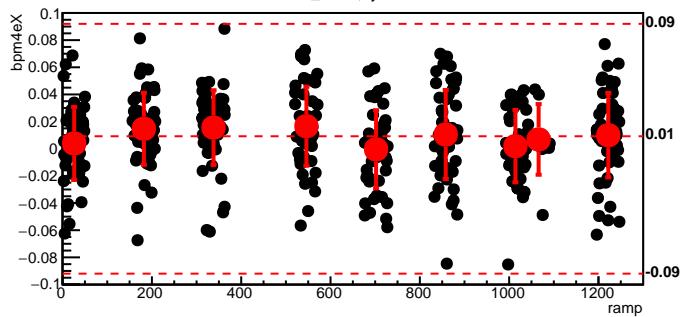
bmod Trim1,cyc=6096



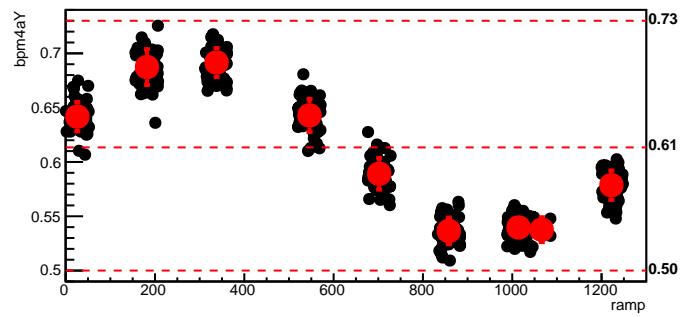
bmod Trim2,cyc=6096



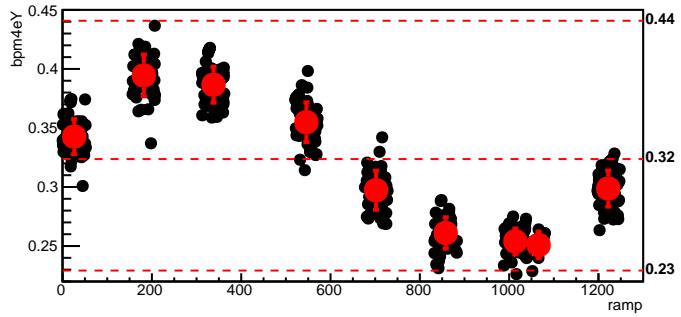
bmod Trim2,cyc=6096



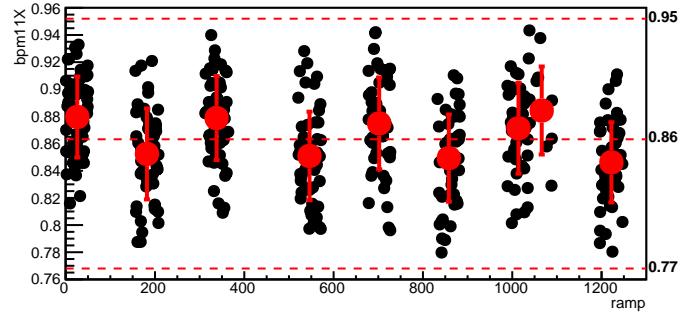
bmod Trim2,cyc=6096



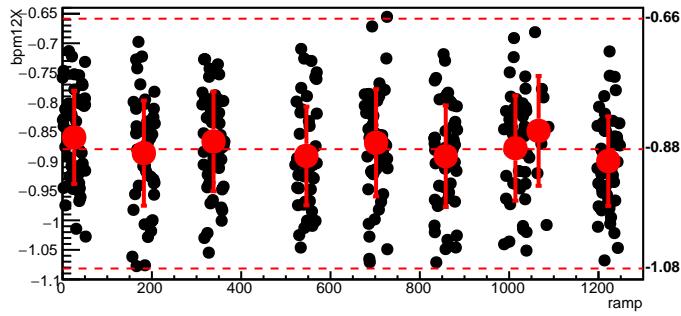
bmod Trim2,cyc=6096



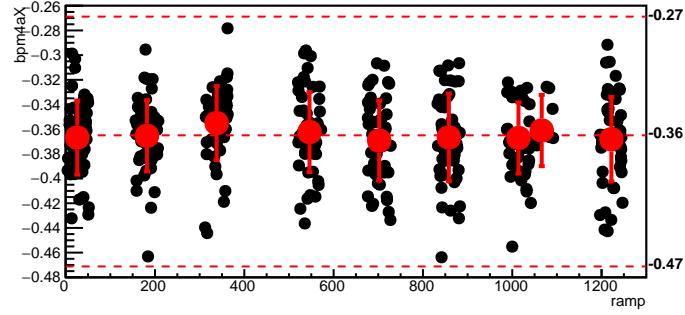
bmod Trim2,cyc=6096



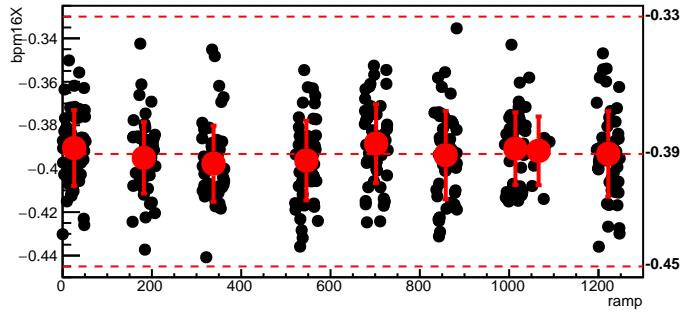
bmod Trim2,cyc=6096



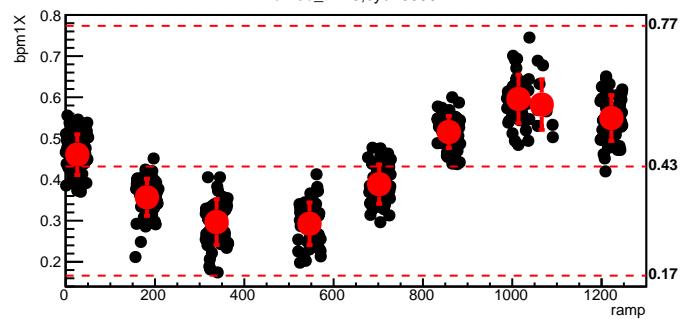
bmod Trim2,cyc=6096



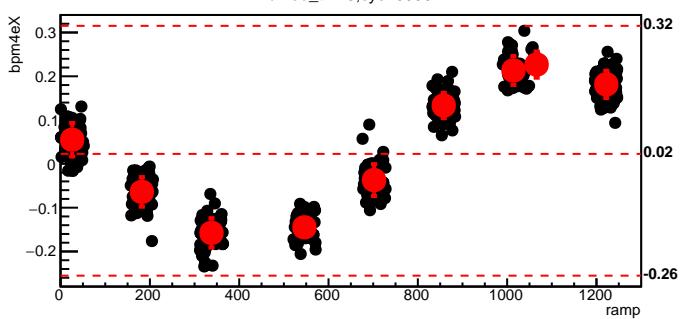
bmod Trim2,cyc=6096



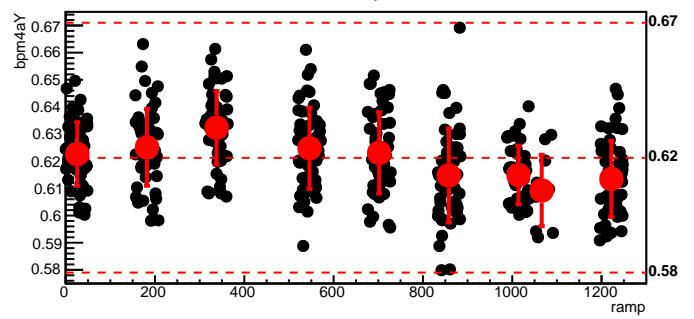
bmod Trim3,cyc=6096



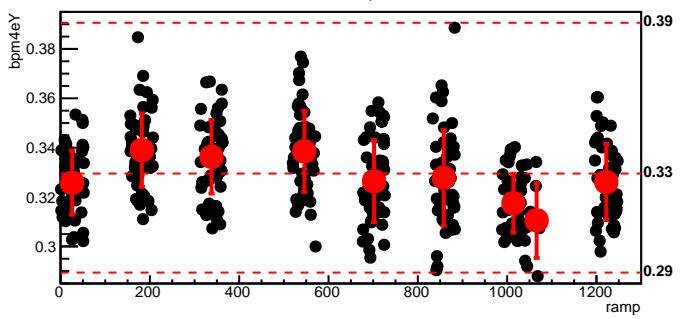
bmod Trim3,cyc=6096



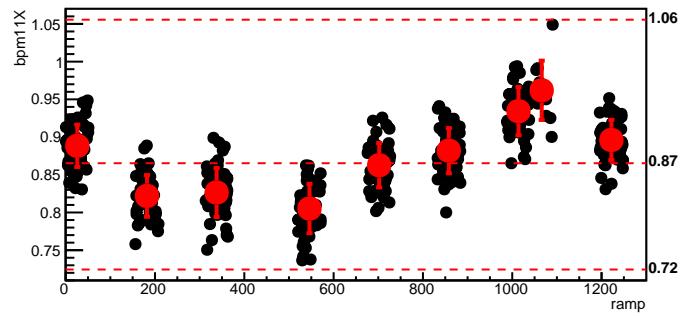
bmod Trim3,cyc=6096



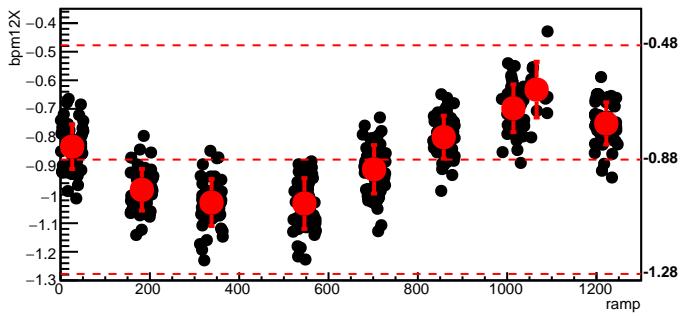
bmod Trim3,cyc=6096



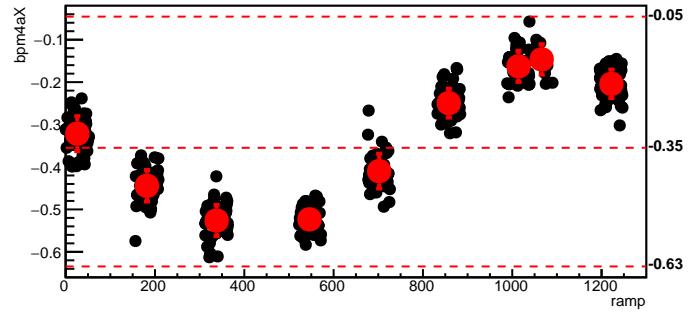
bmod Trim3,cyc=6096



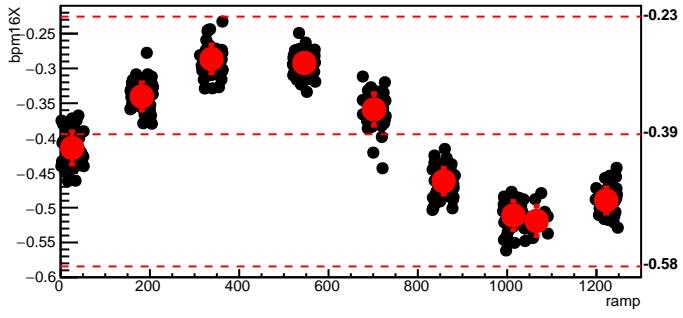
bmod Trim3,cyc=6096



bmod Trim3,cyc=6096



bmod Trim3,cyc=6096

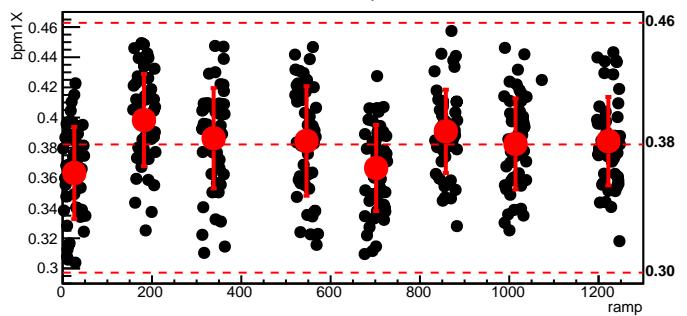




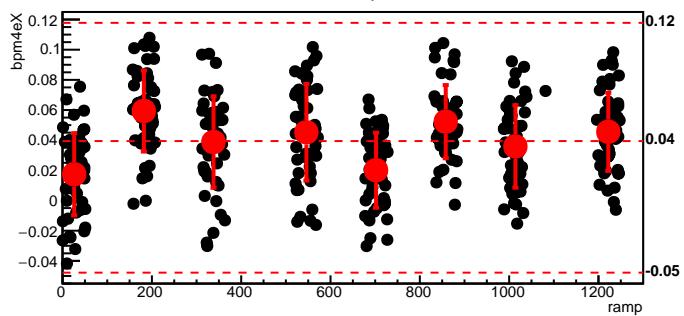




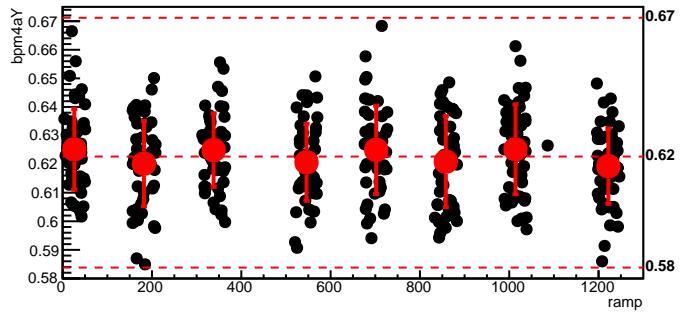
bmod Trim7,cyc=6096



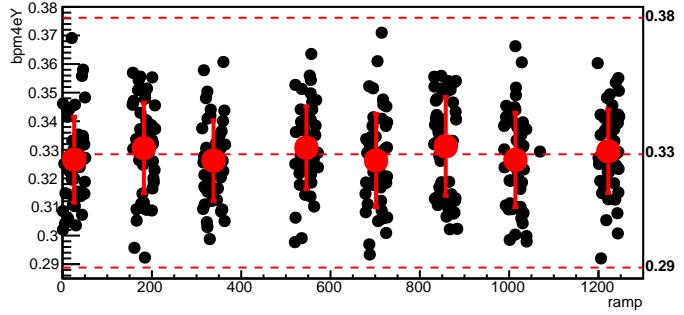
bmod Trim7,cyc=6096



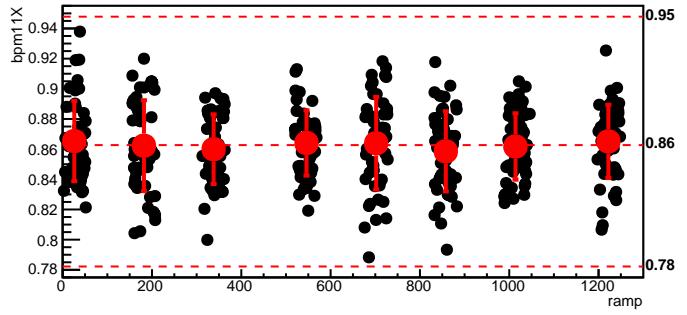
bmod Trim7,cyc=6096



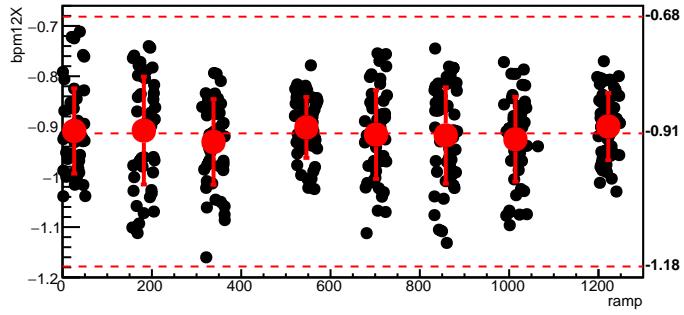
bmod Trim7,cyc=6096



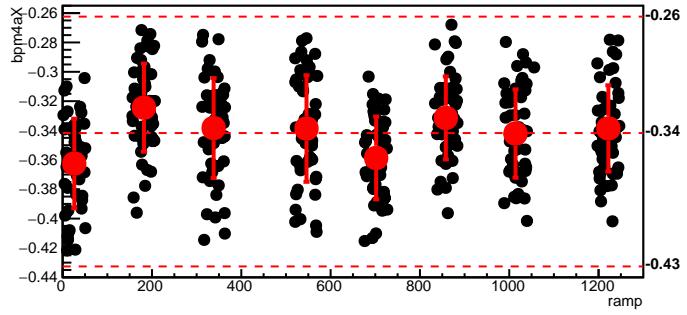
bmod Trim7,cyc=6096



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bmod Trim7,cyc=6096

