

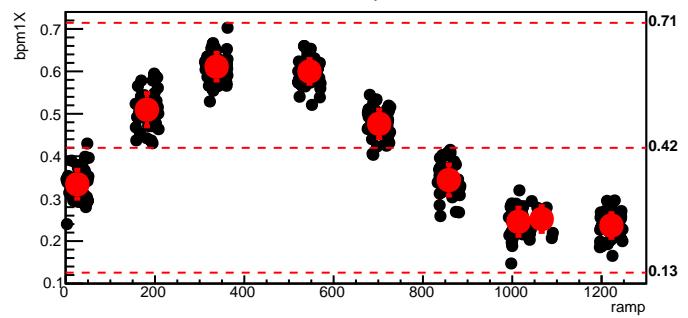




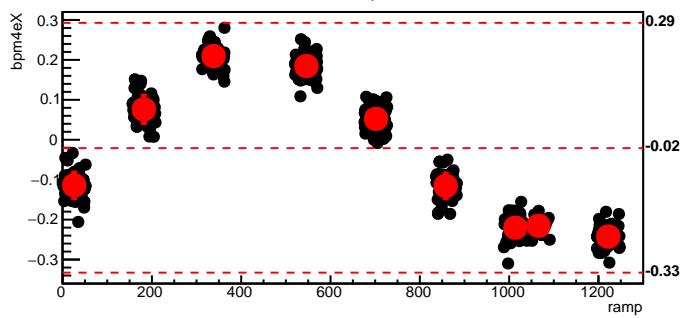




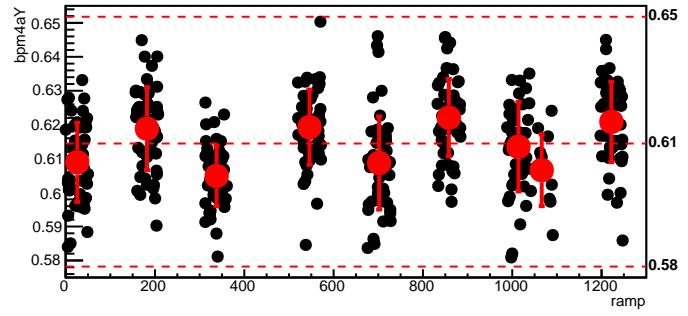
bmod Trim5,cyc=6105



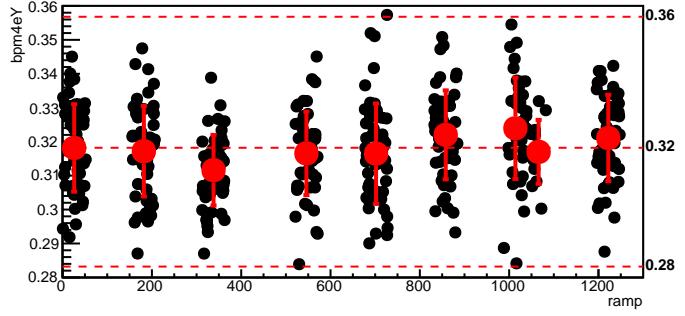
bmod Trim5,cyc=6105



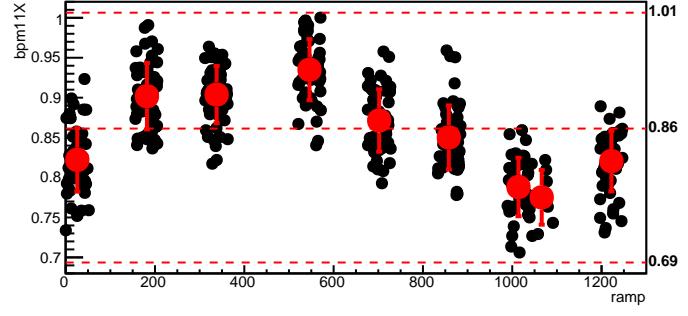
bmod Trim5,cyc=6105



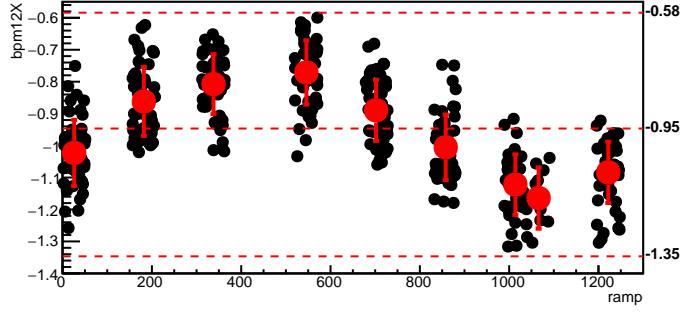
bmod Trim5,cyc=6105



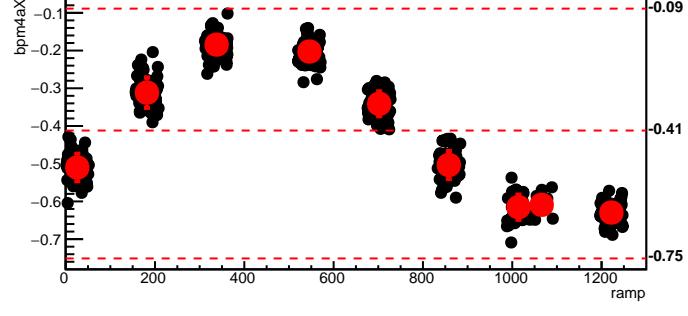
bmod Trim5,cyc=6105



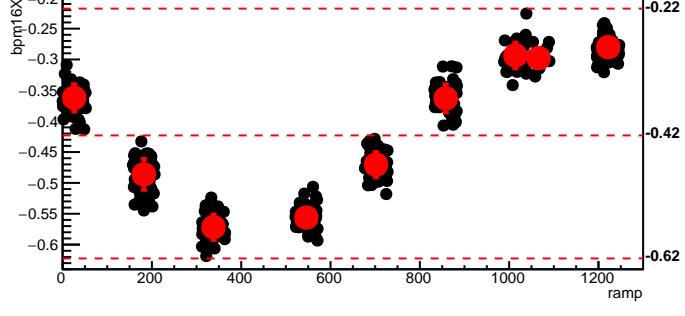
bmod Trim5,cyc=6105



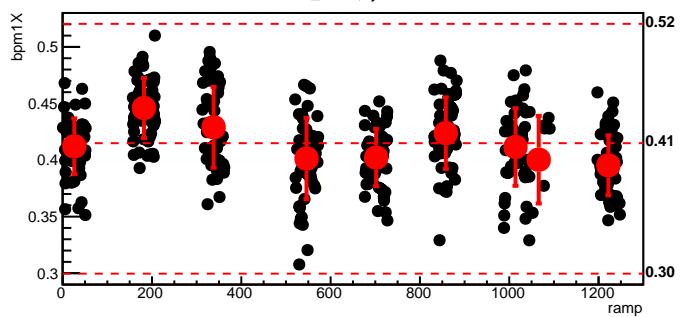
bmod Trim5,cyc=6105



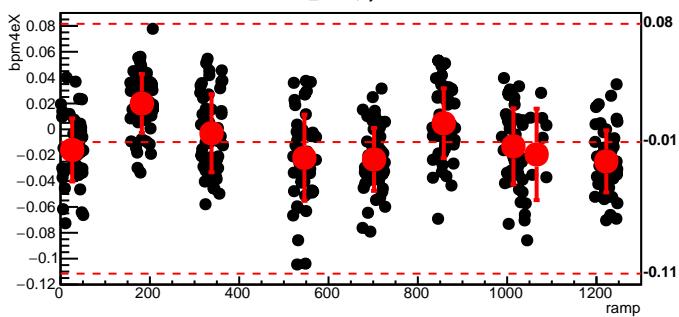
bmod Trim5,cyc=6105



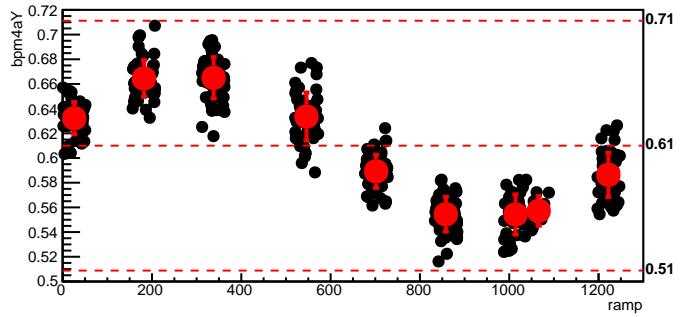
bmod Trim6,cyc=6105



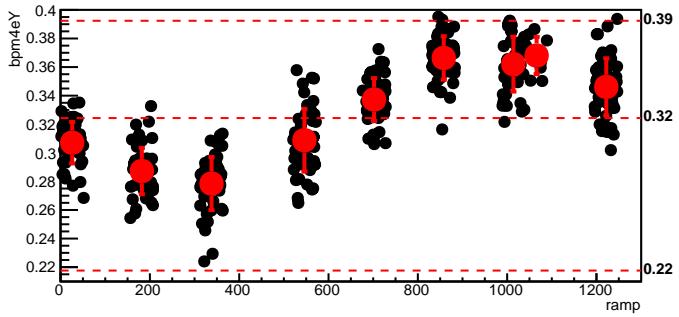
bmod Trim6,cyc=6105



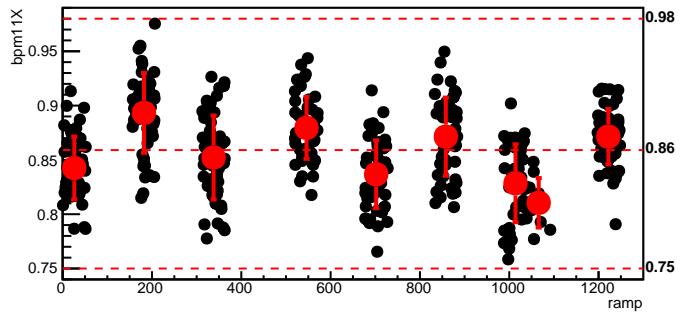
bmod Trim6,cyc=6105



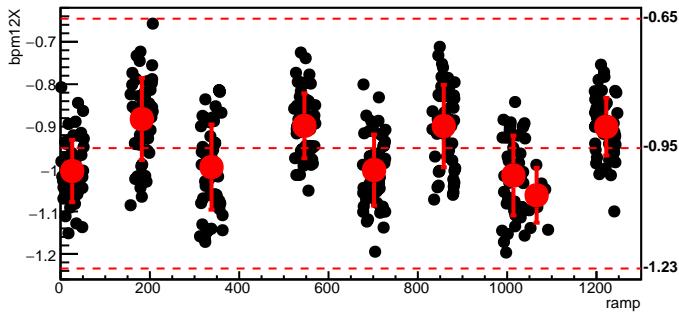
bmod Trim6,cyc=6105



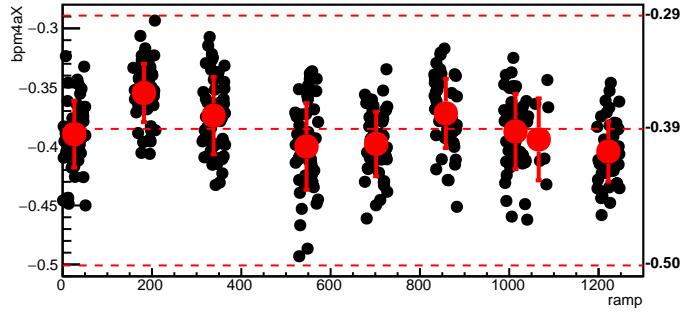
bmod Trim6,cyc=6105



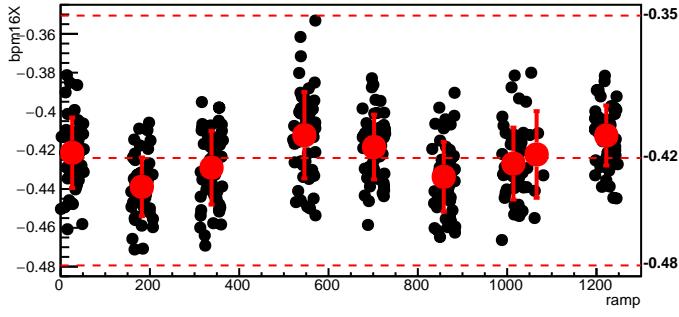
bmod Trim6,cyc=6105



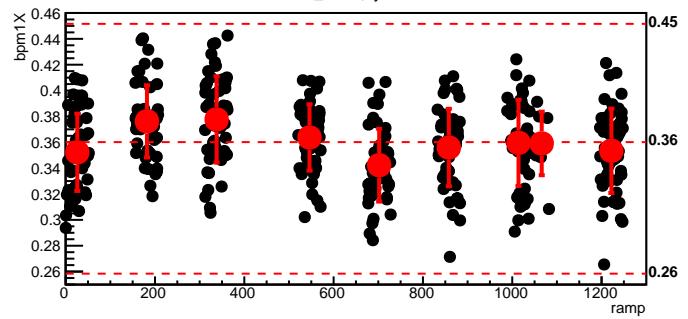
bmod Trim6,cyc=6105



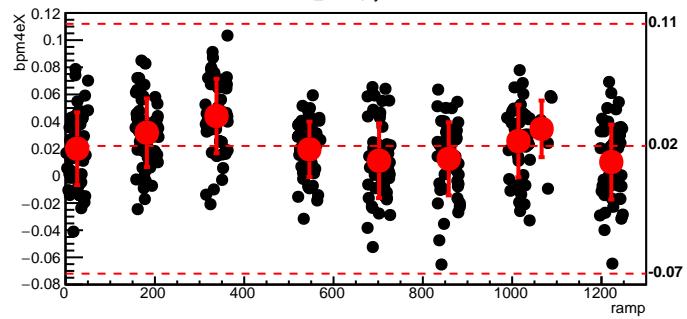
bmod Trim6,cyc=6105



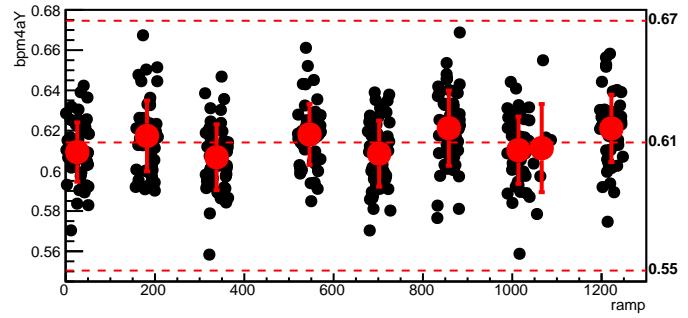
bmod Trim7,cyc=6105



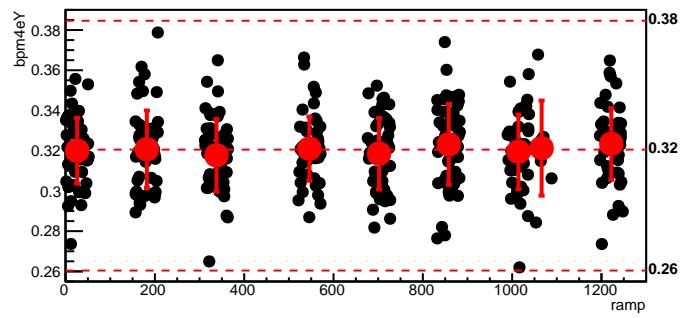
bmod Trim7,cyc=6105



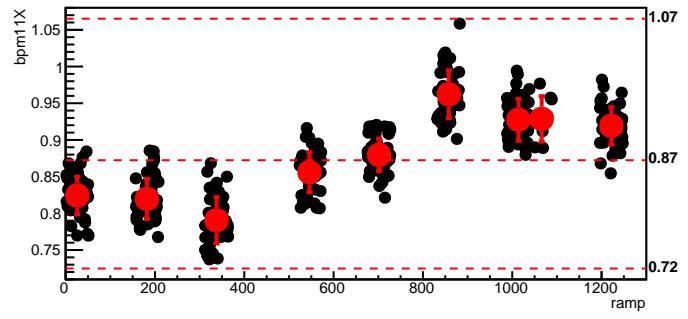
bmod Trim7,cyc=6105



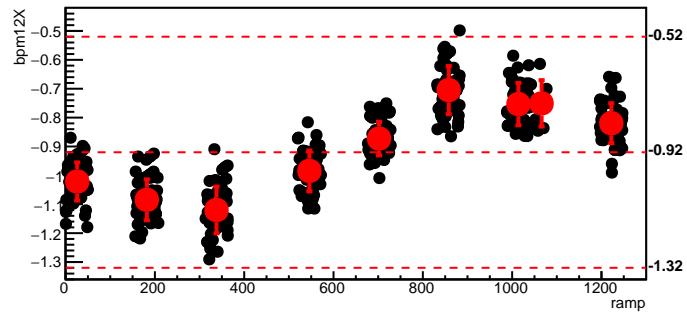
bmod Trim7,cyc=6105



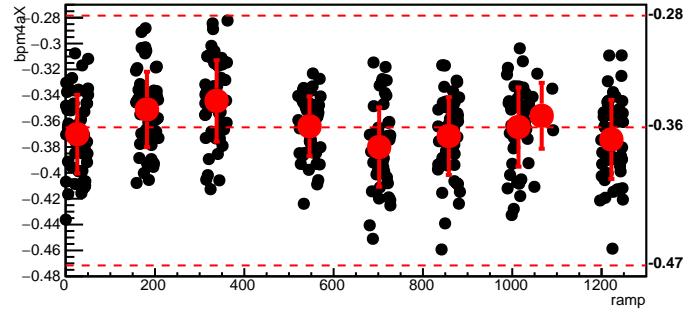
bmod Trim7,cyc=6105



bmod Trim7,cyc=6105



bmod Trim7,cyc=6105



bmod Trim7,cyc=6105

