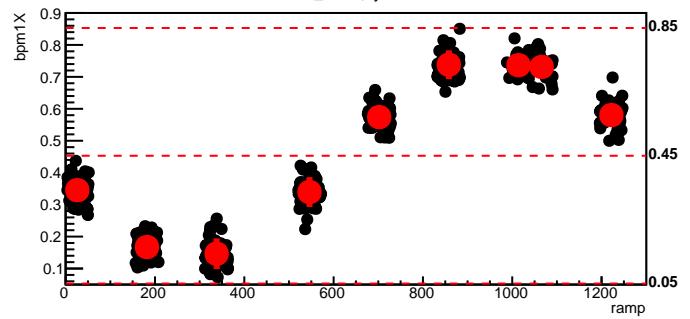
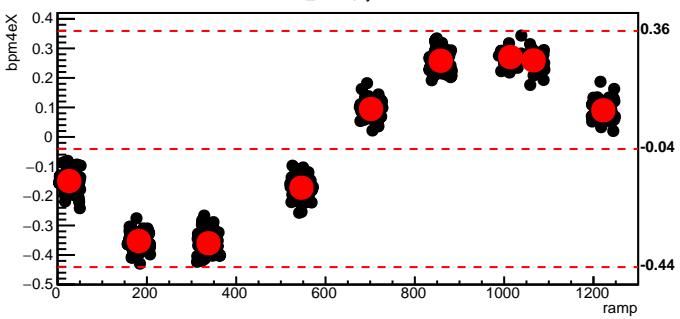


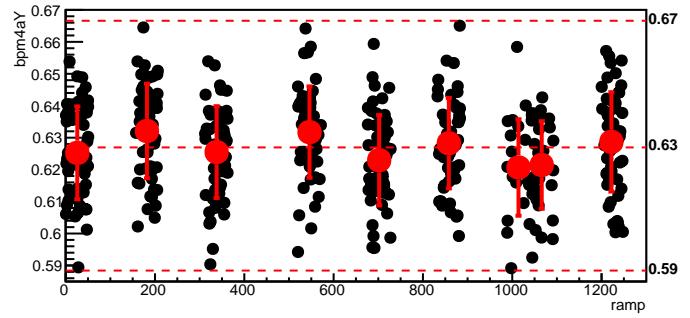
bmod Trim1,cyc=6130



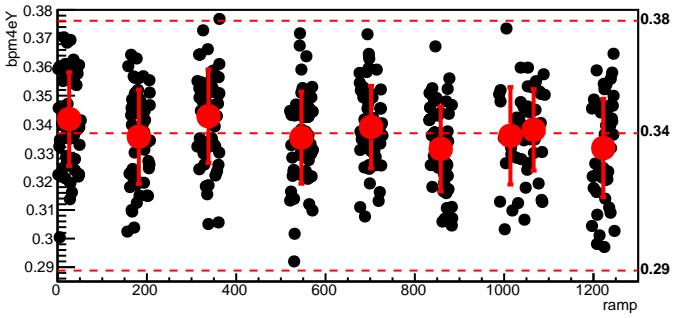
bmod Trim1,cyc=6130



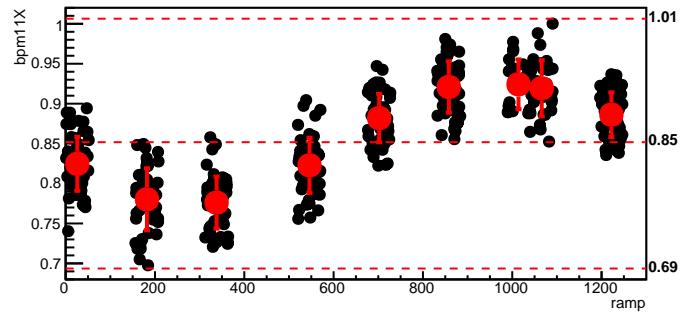
bmod Trim1,cyc=6130



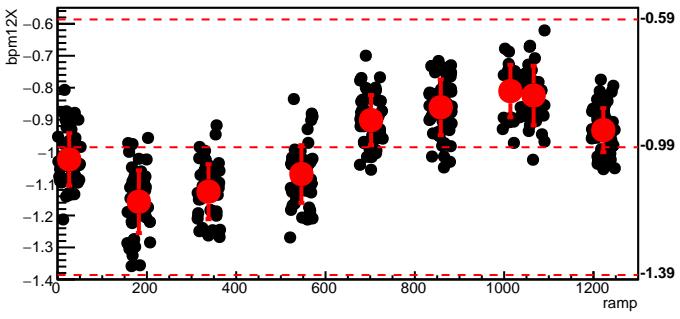
bmod Trim1,cyc=6130



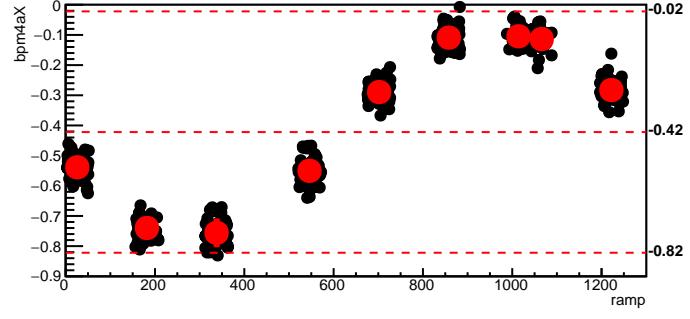
bmod Trim1,cyc=6130



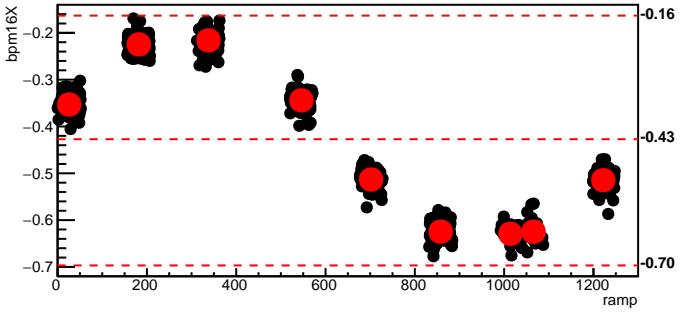
bmod Trim1,cyc=6130



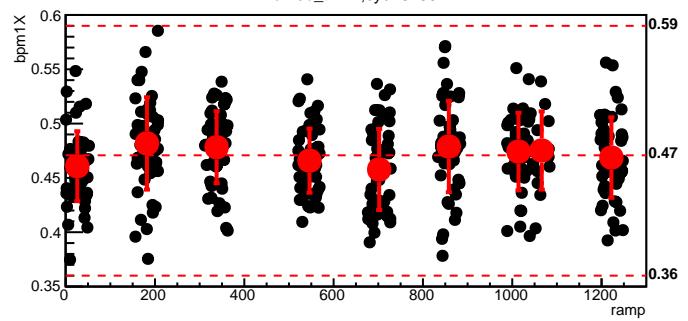
bmod Trim1,cyc=6130



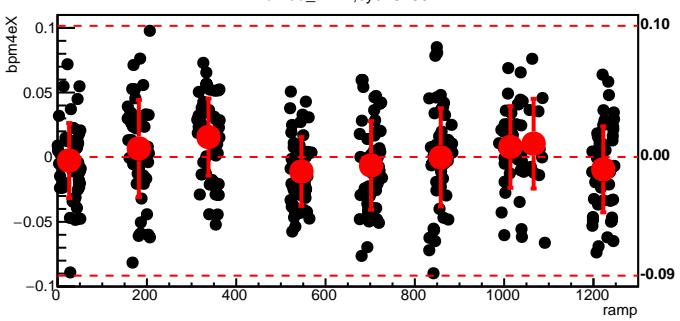
bmod Trim1,cyc=6130



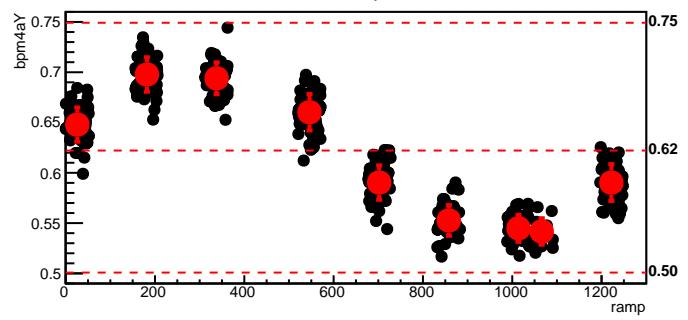
bmod Trim2,cyc=6130



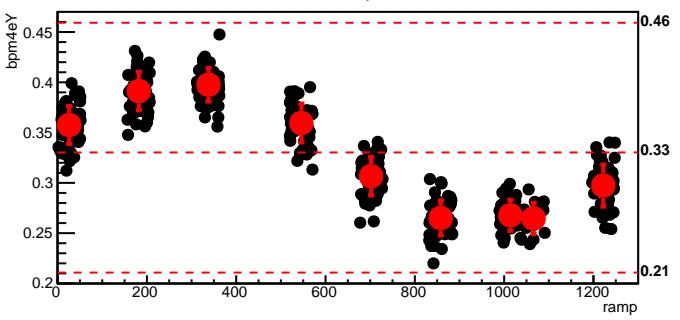
bmod Trim2,cyc=6130



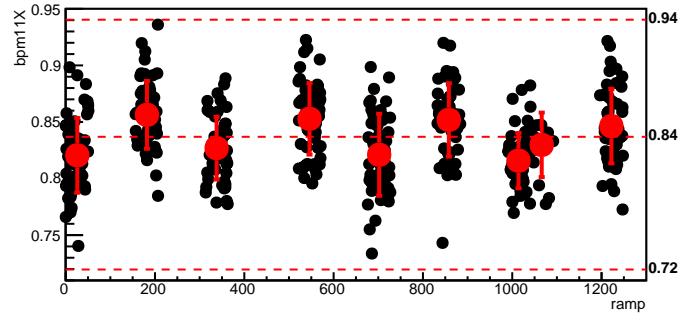
bmod Trim2,cyc=6130



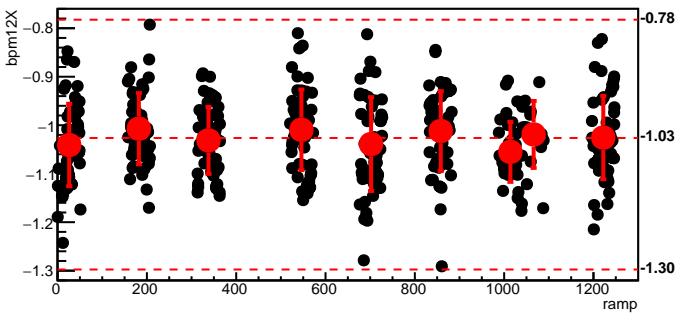
bmod Trim2,cyc=6130



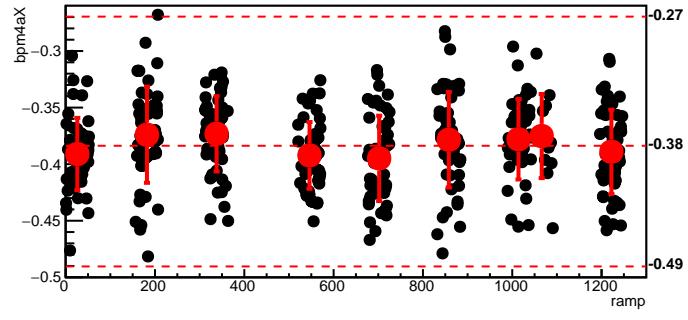
bmod Trim2,cyc=6130



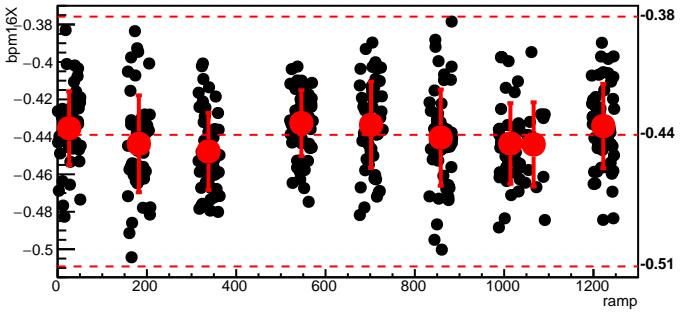
bmod Trim2,cyc=6130



bmod Trim2,cyc=6130



bmod Trim2,cyc=6130



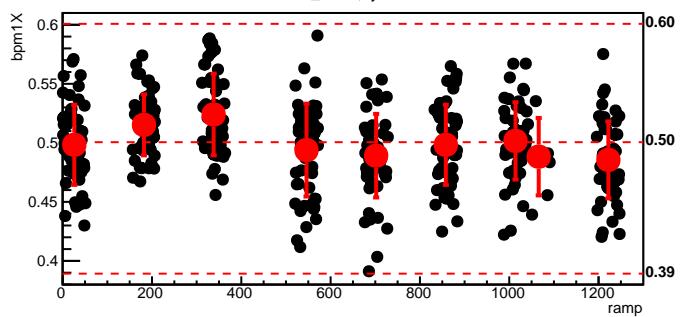




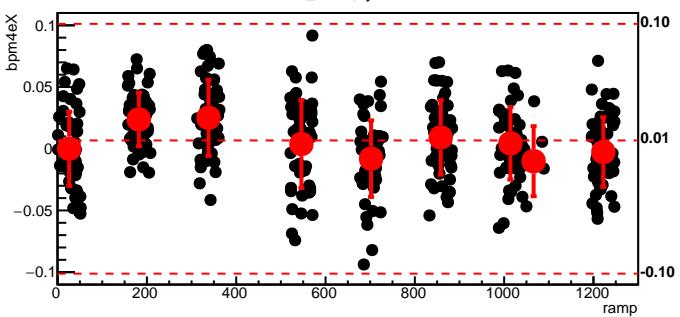




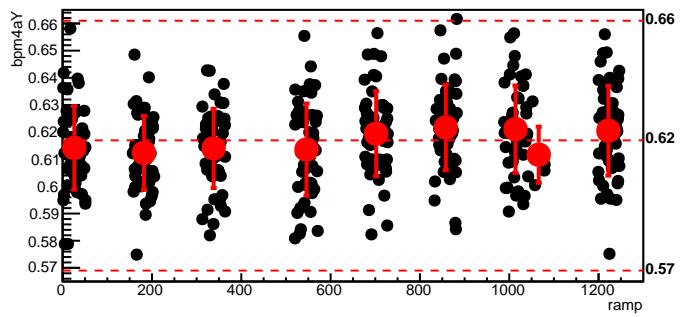
bmod Trim7,cyc=6130



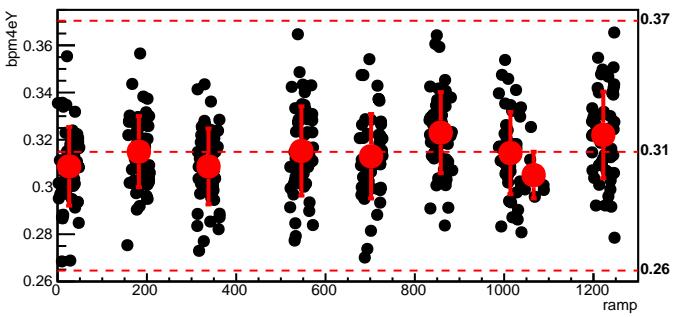
bmod Trim7,cyc=6130



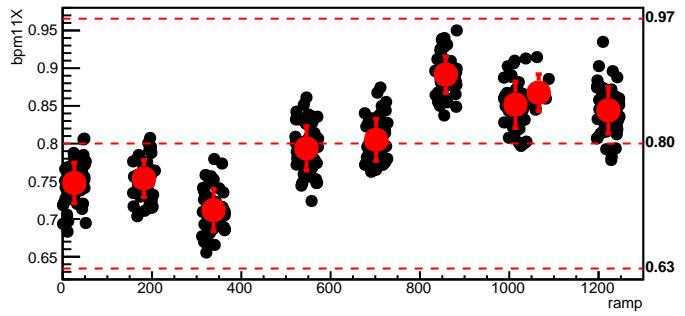
bmod Trim7,cyc=6130



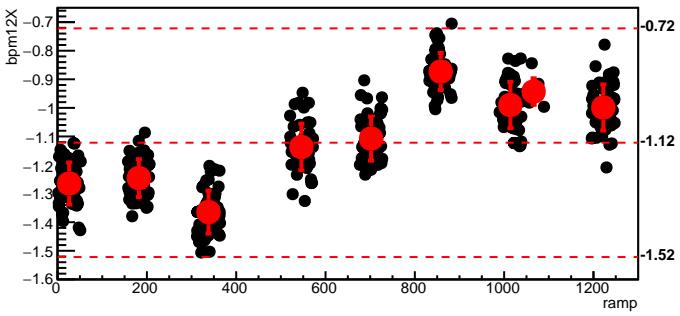
bmod Trim7,cyc=6130



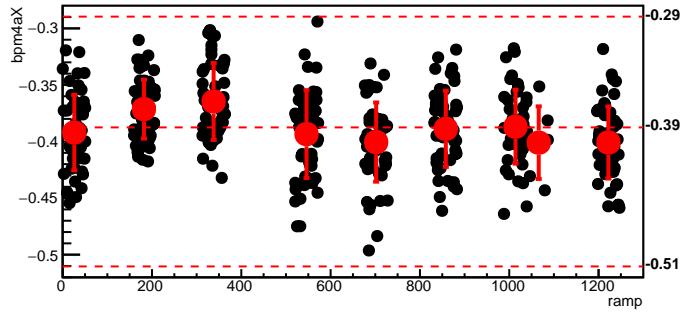
bmod Trim7,cyc=6130



bmod Trim7,cyc=6130



bmod Trim7,cyc=6130



bmod Trim7,cyc=6130

