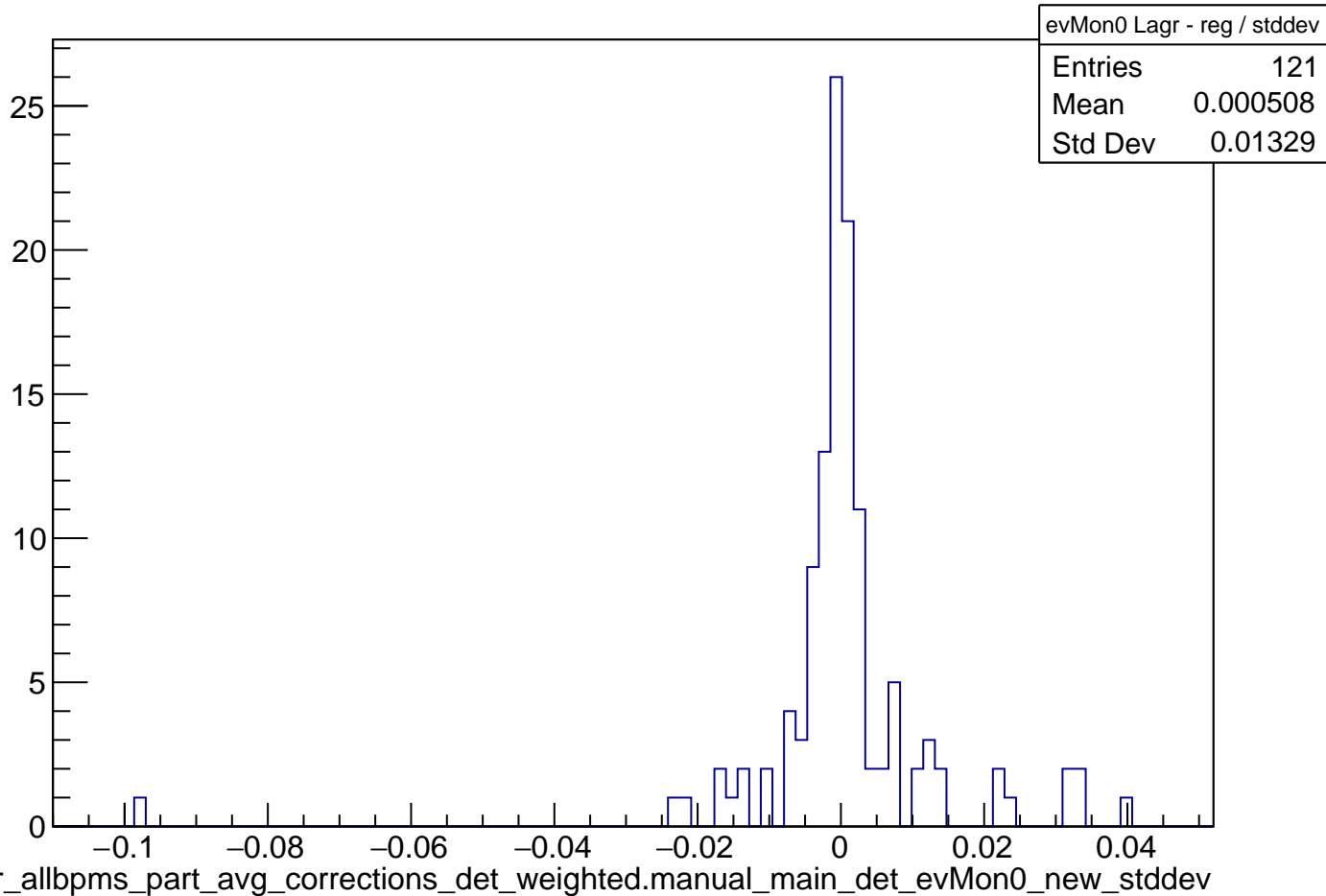
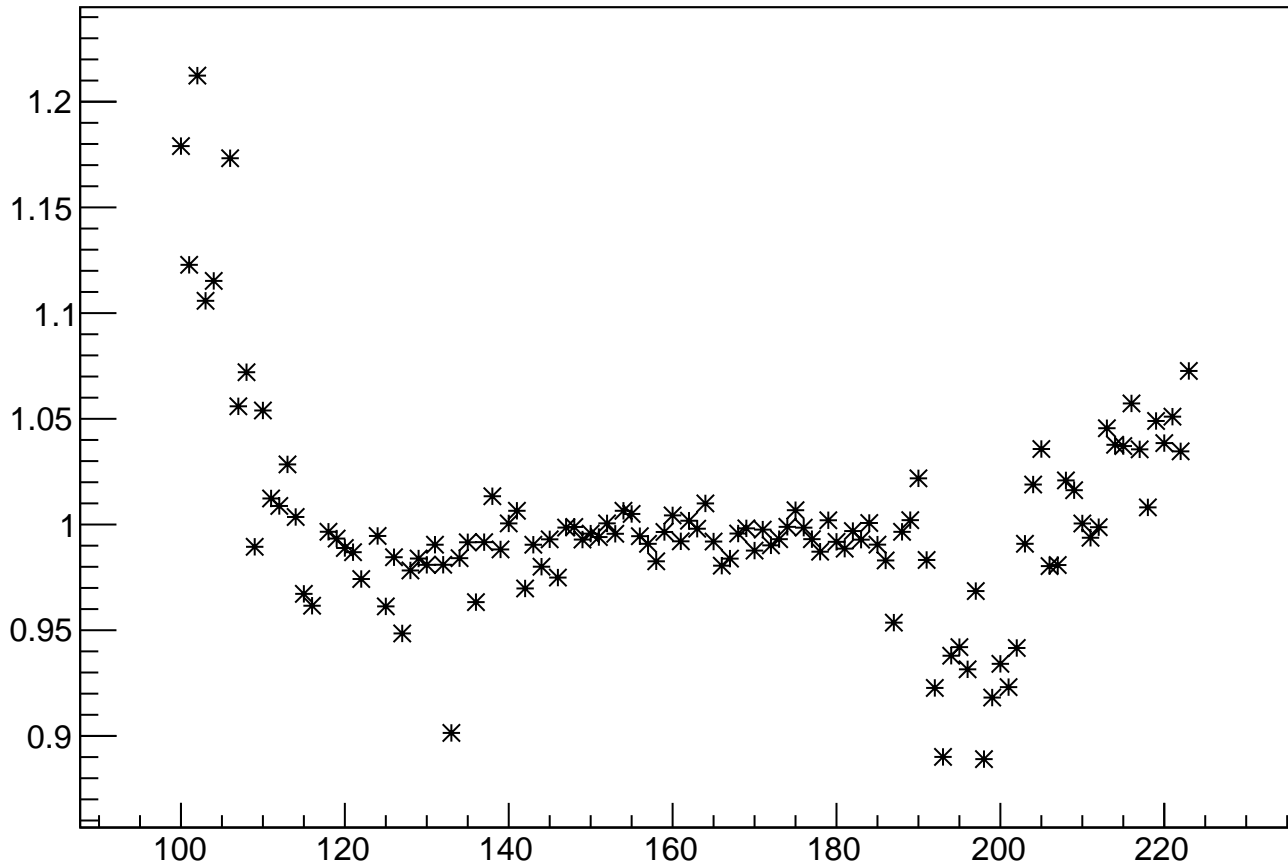


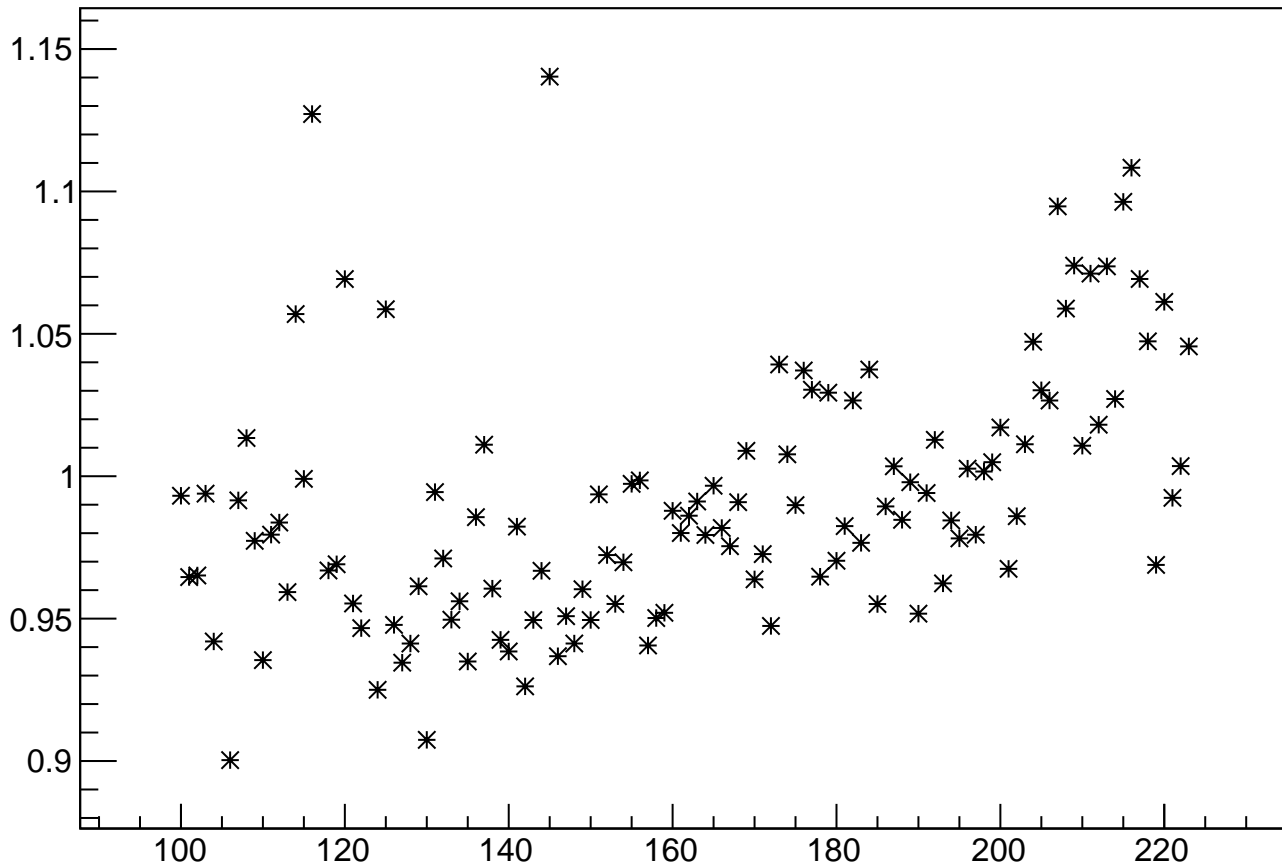
# evMon0 Lagr - reg slugwise mean / slug's stddev



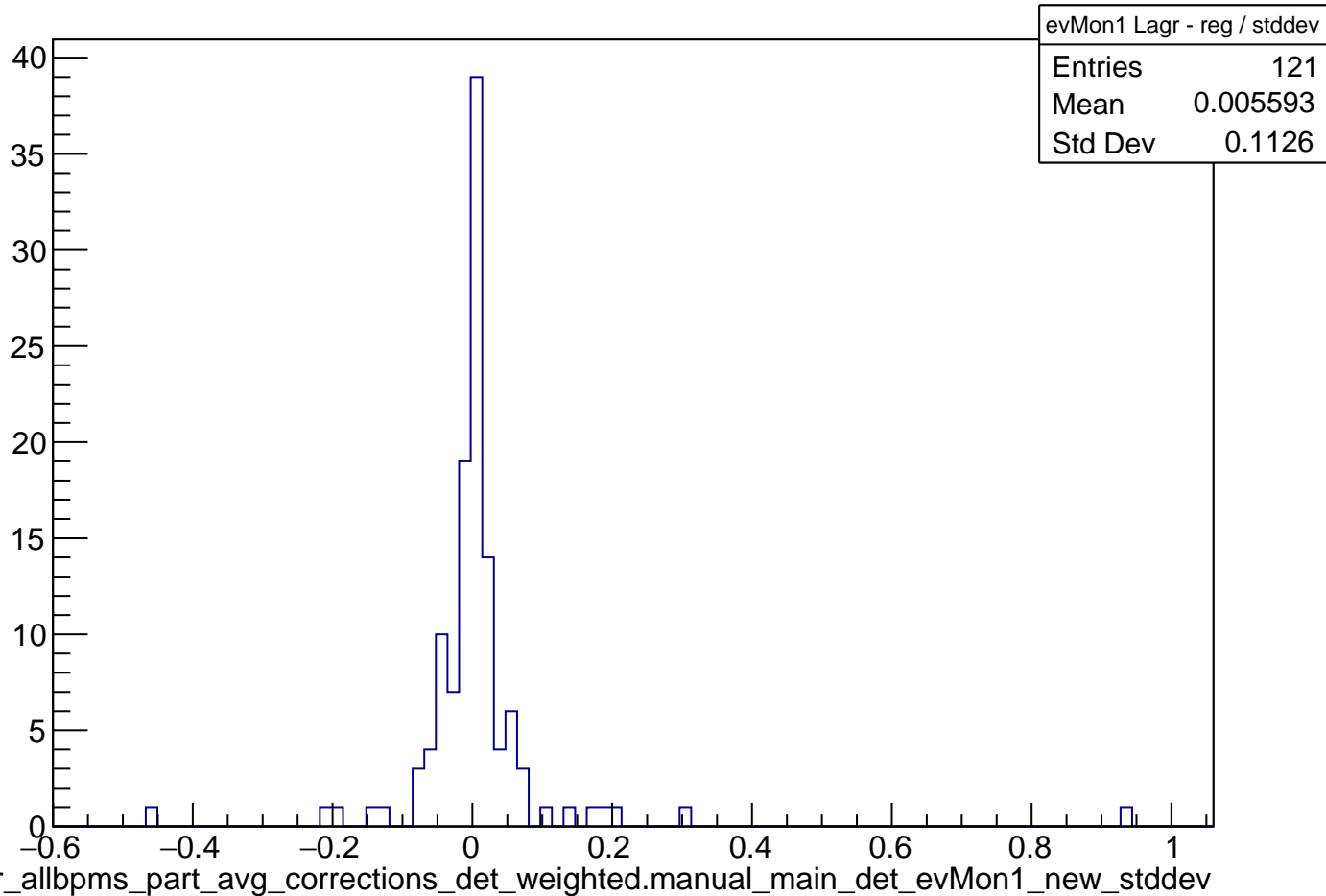
evMon0 reg stddev / Lagr slugwise mean's stddev



evMon0 IncludeBMOD stddev / ErrorFlag slugwise mean's stddev

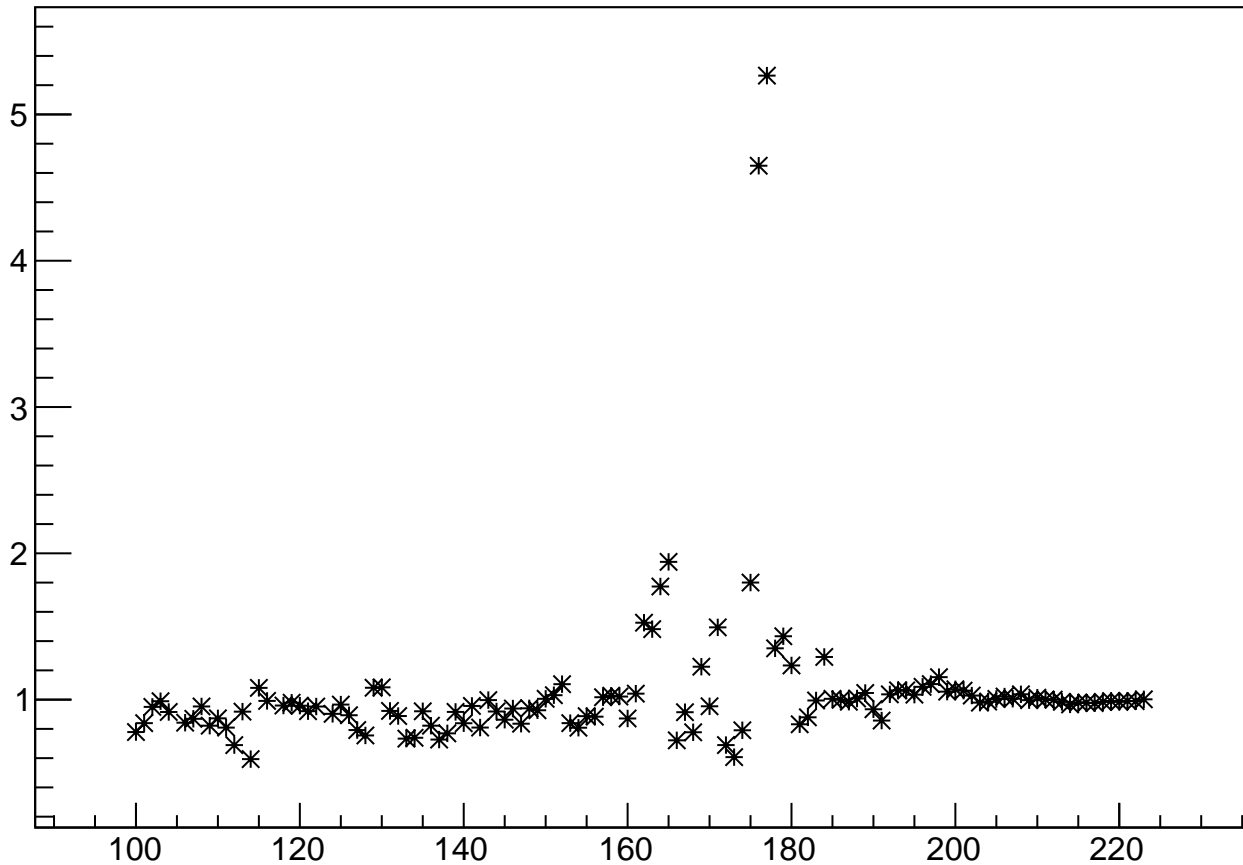


# evMon1 Lagr - reg slugwise mean / slug's stddev

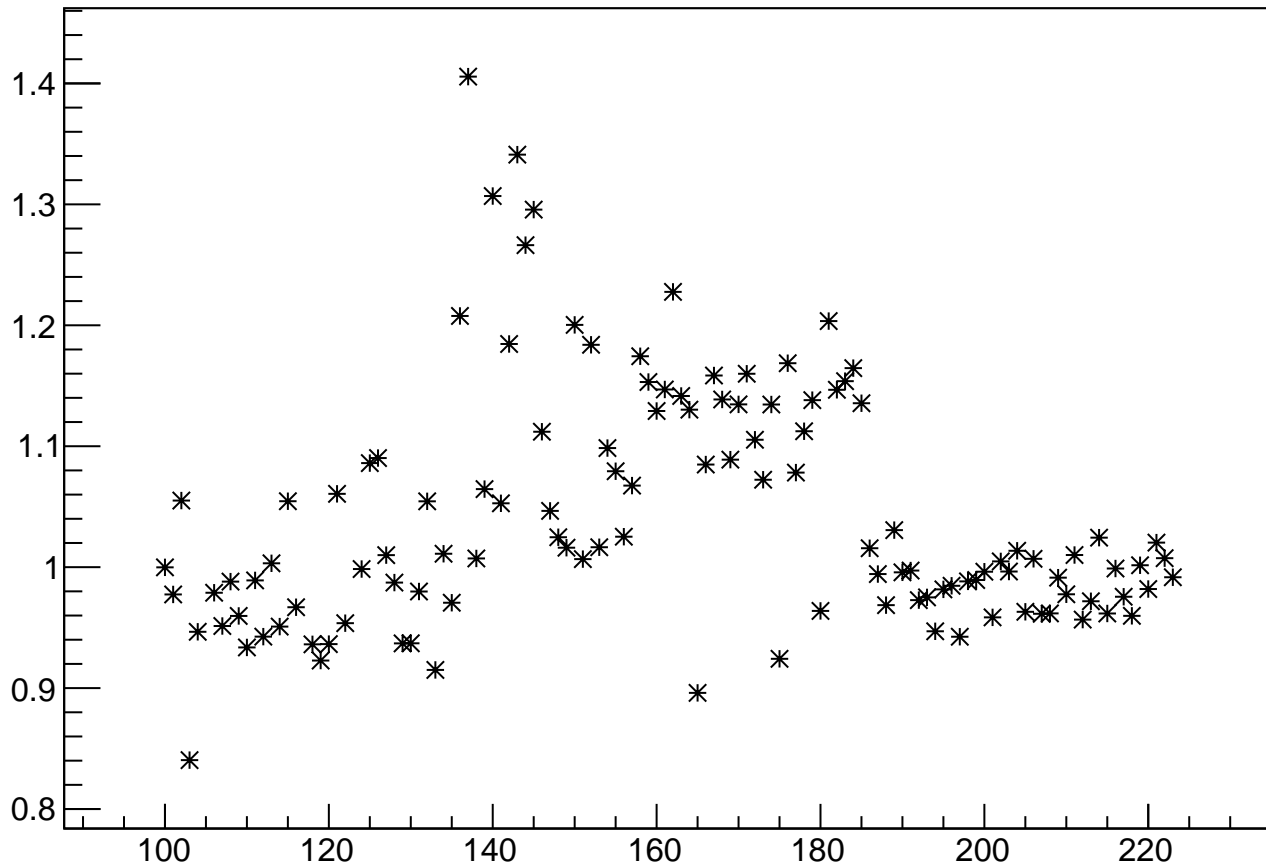


en\_lagr\_allbpms\_part\_avg\_corrections\_det\_weighted.manual\_main\_det\_evMon1\_new\_stddev

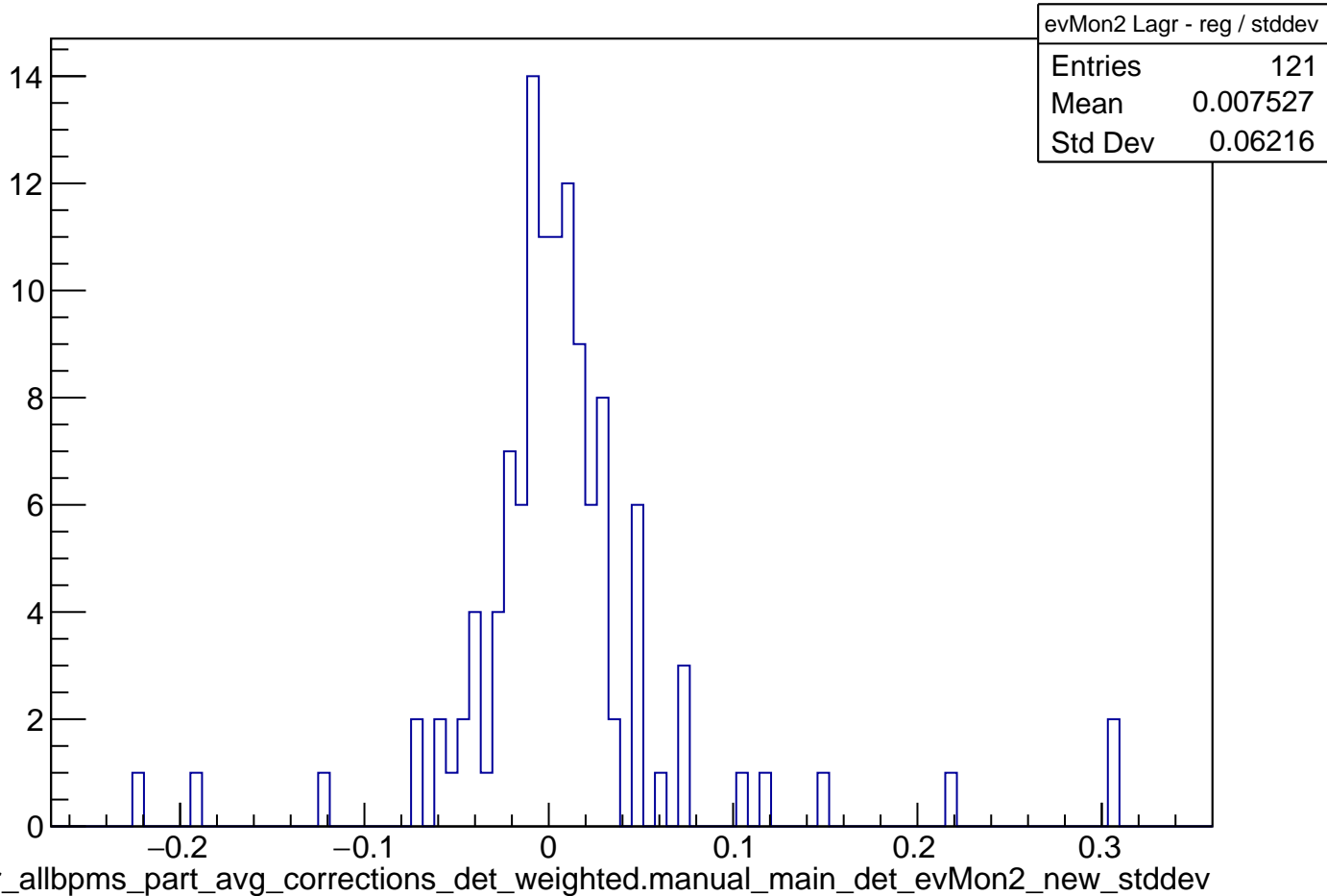
evMon1 reg stddev / Lagr slugwise mean's stddev



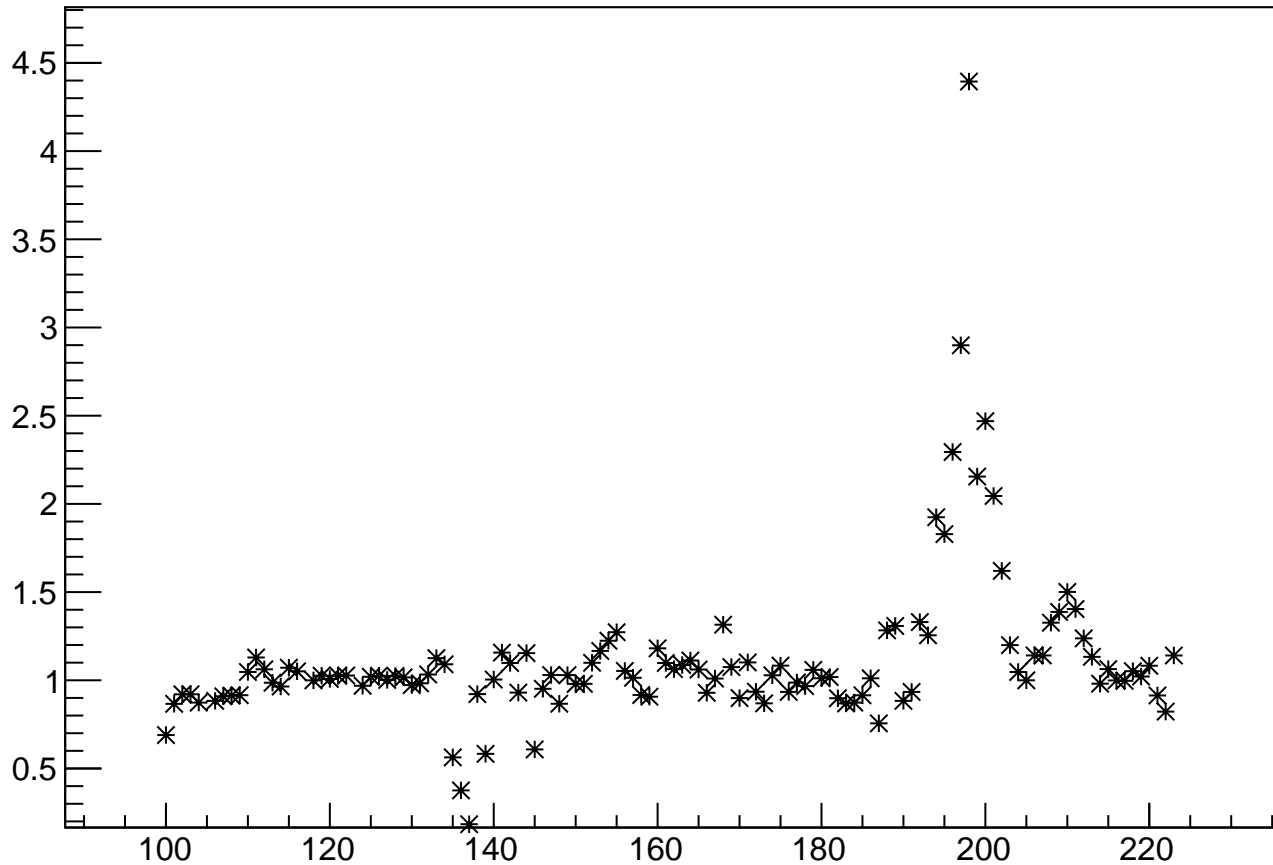
evMon1 IncludeBMOD stddev / ErrorFlag slugwise mean's stddev



# evMon2 Lagr - reg slugwise mean / slug's stddev

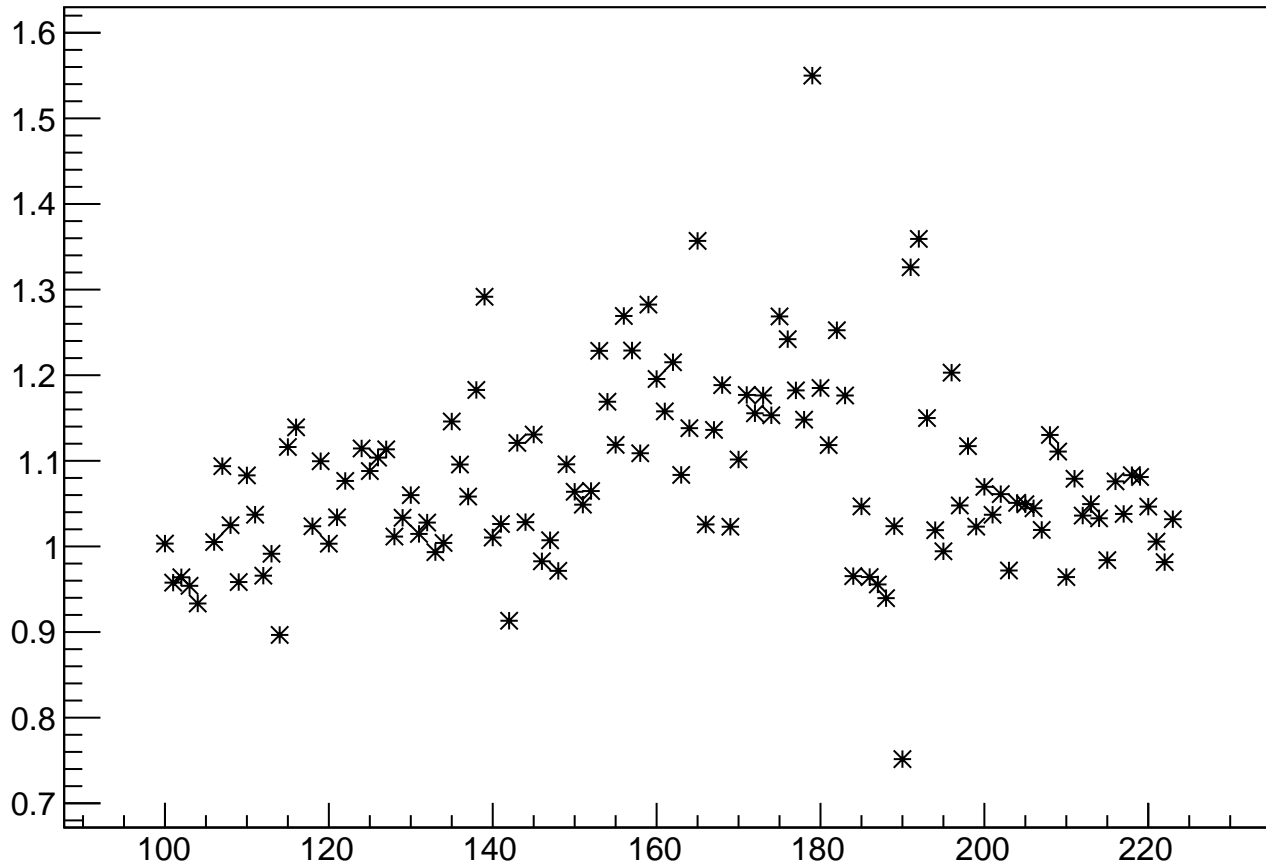


evMon2 reg stddev / Lagr slugwise mean's stddev

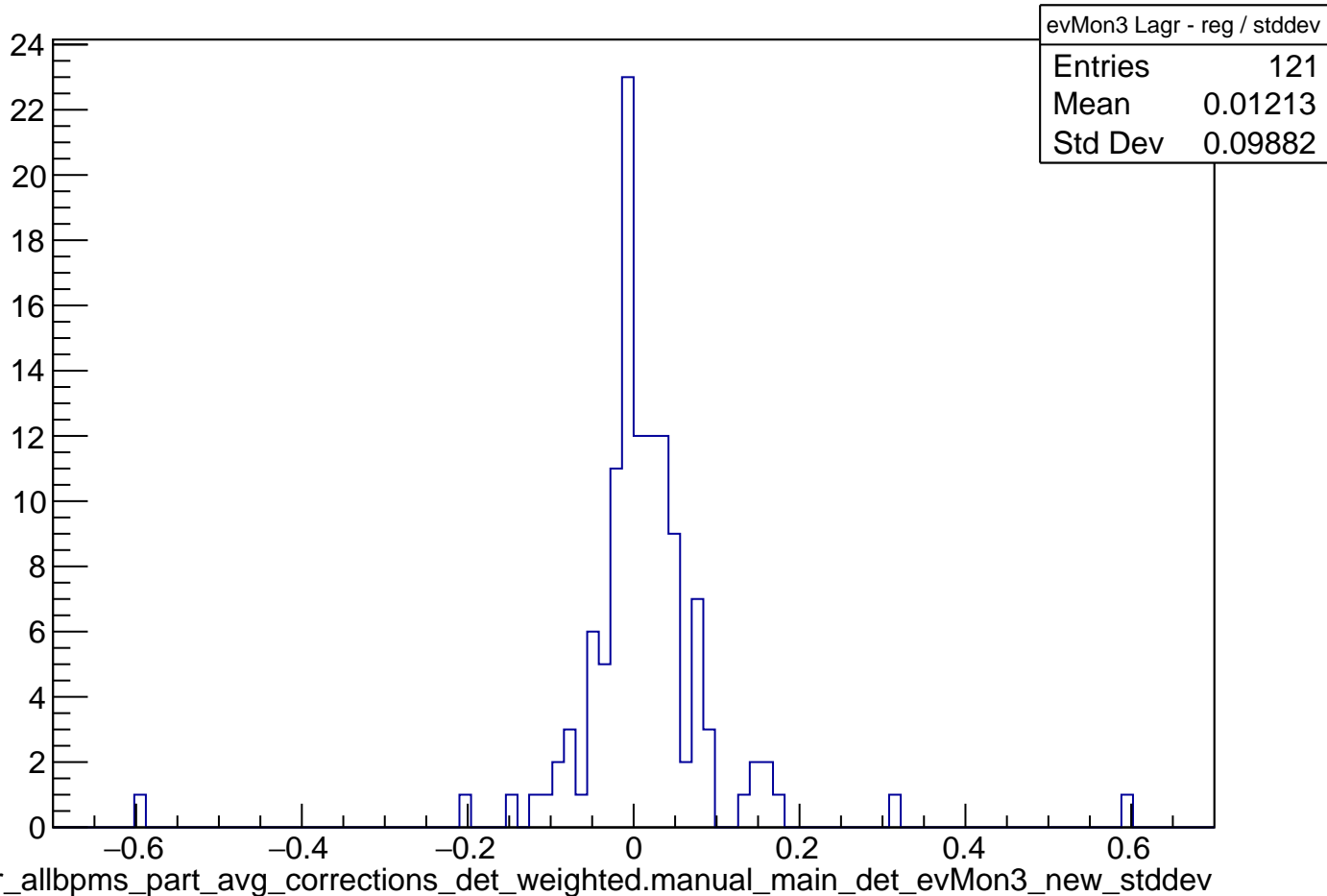




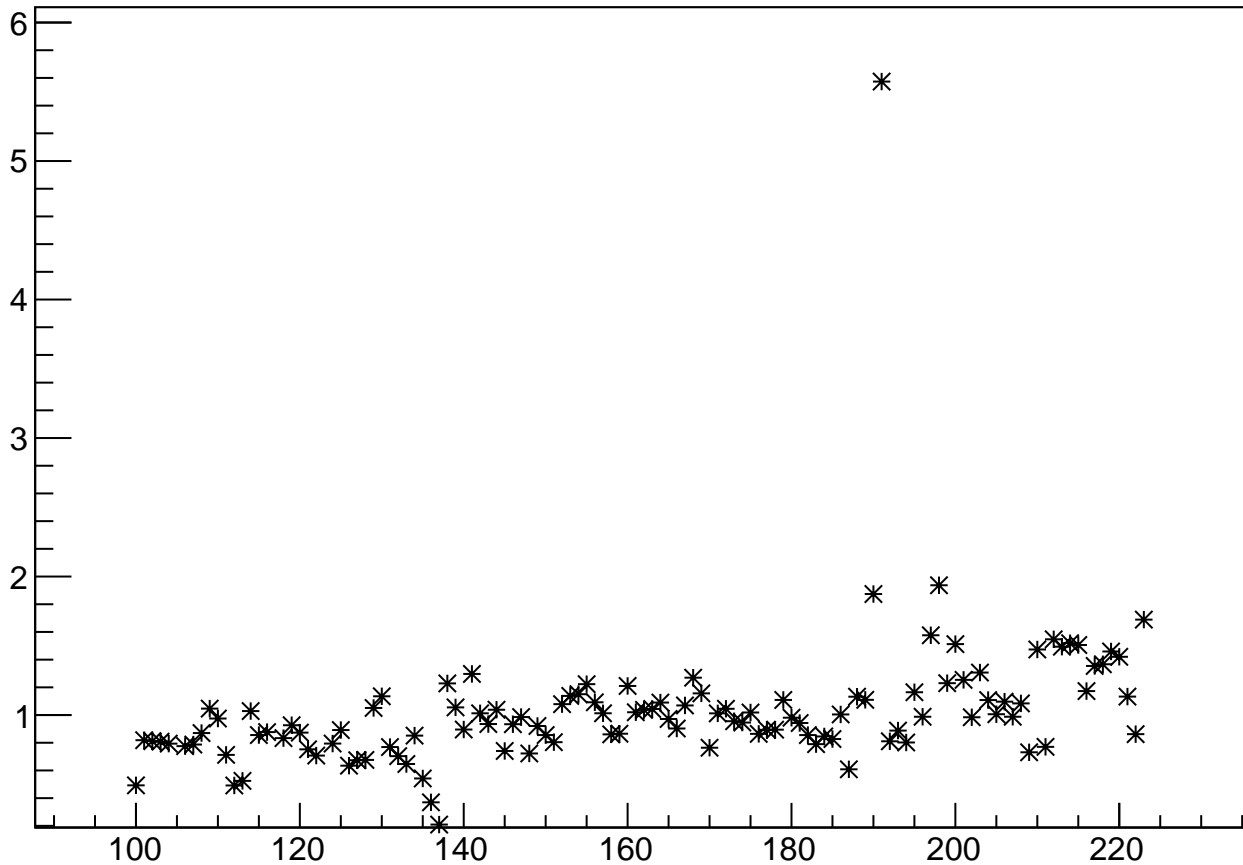
evMon2 IncludeBMOD stddev / ErrorFlag slugwise mean's stddev



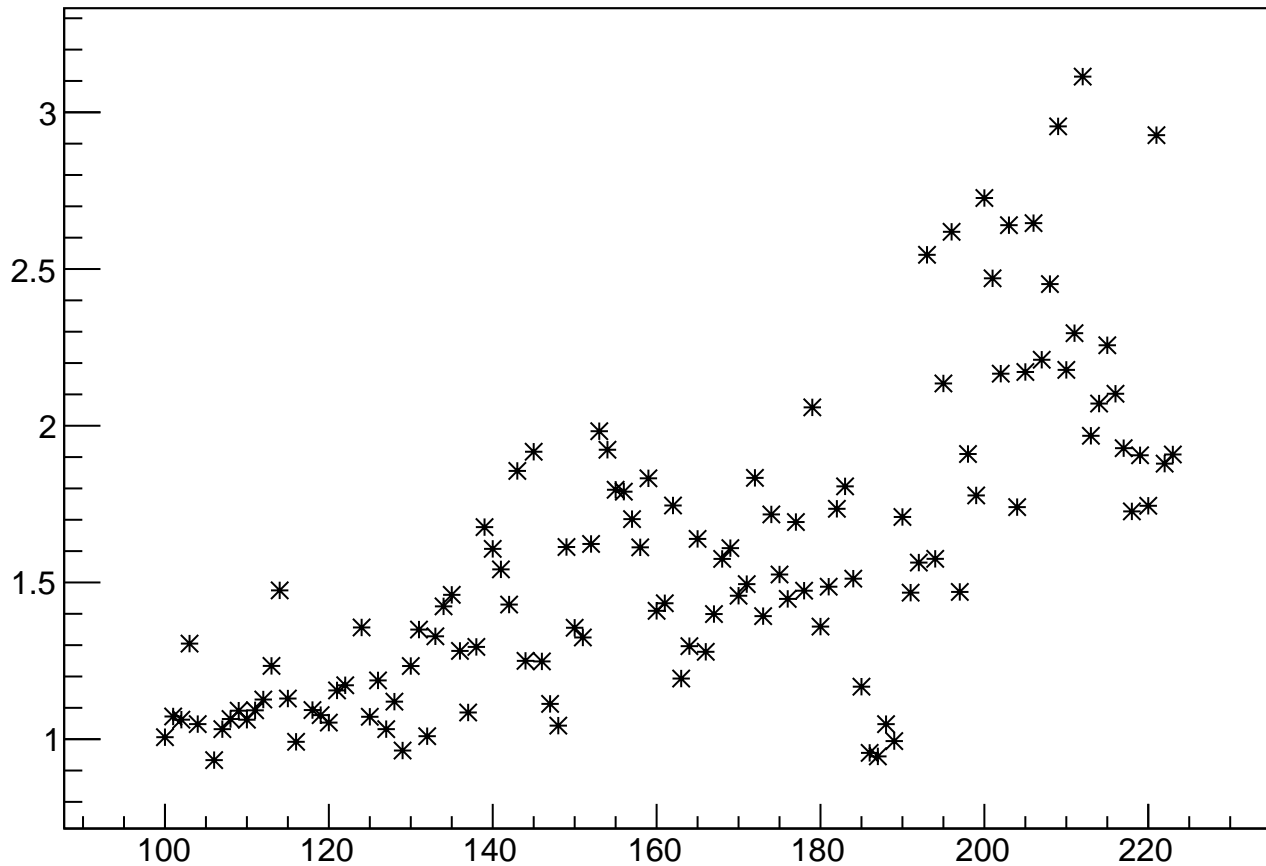
# evMon3 Lagr - reg slugwise mean / slug's stddev



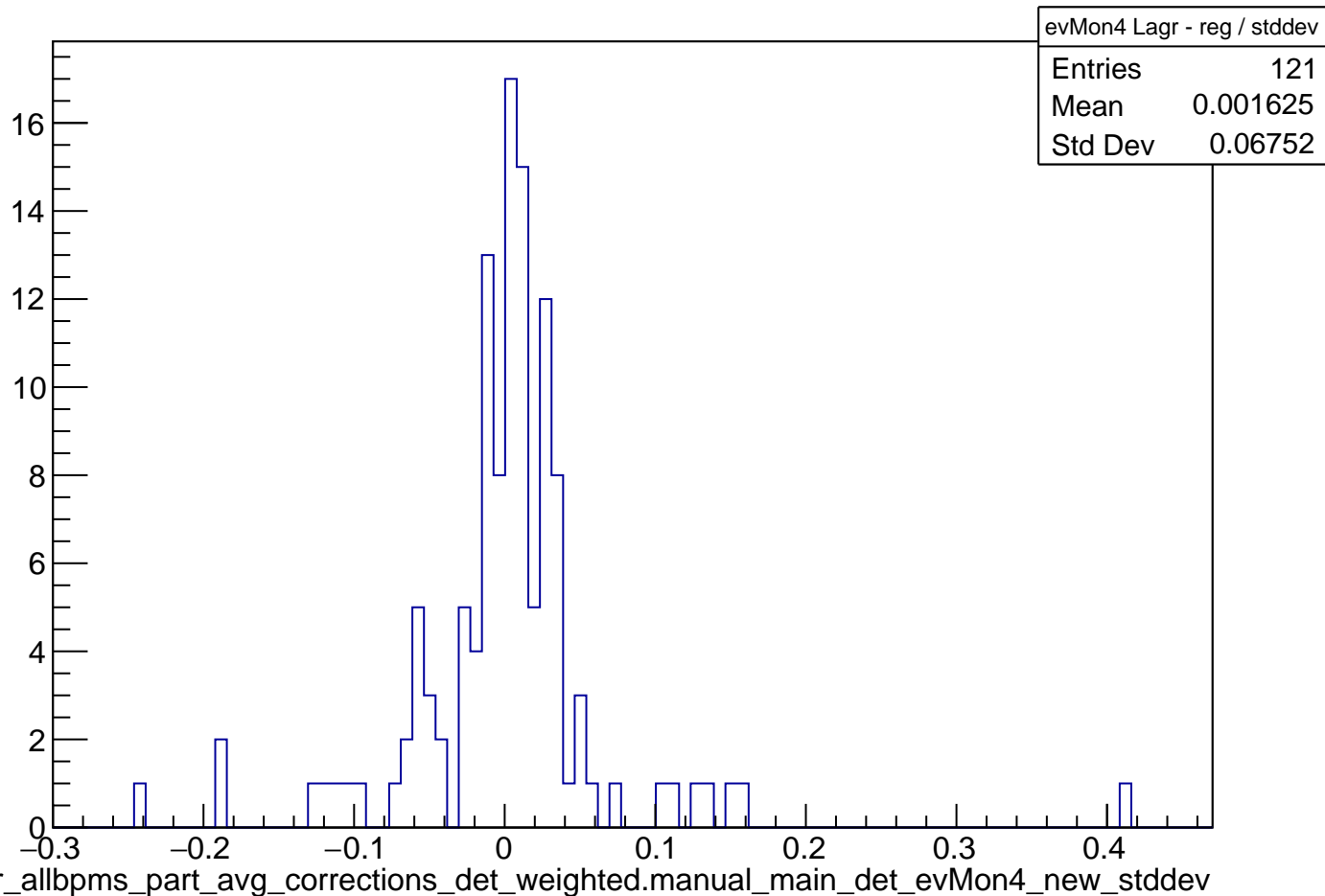
evMon3 reg stddev / Lagr slugwise mean's stddev



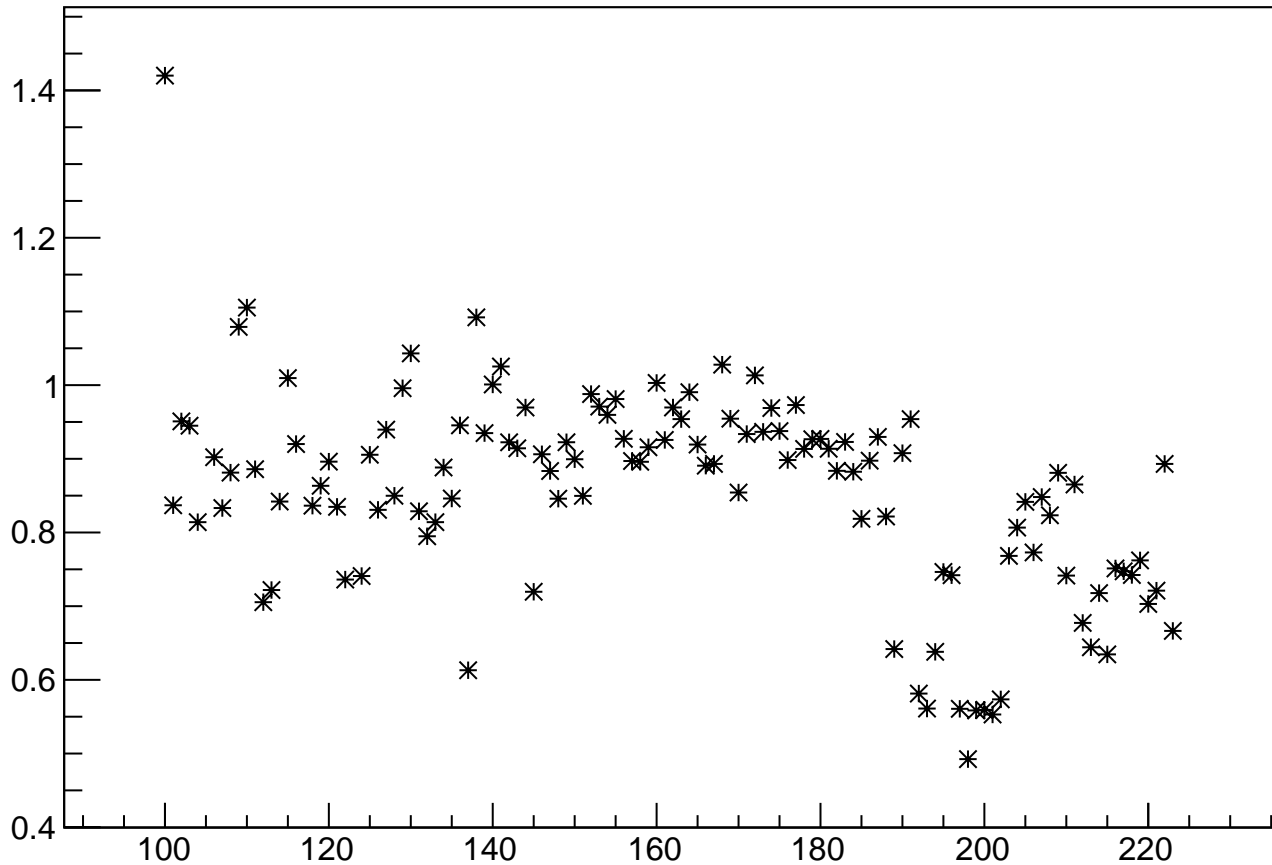
evMon3 IncludeBMOD stddev / ErrorFlag slugwise mean's stddev



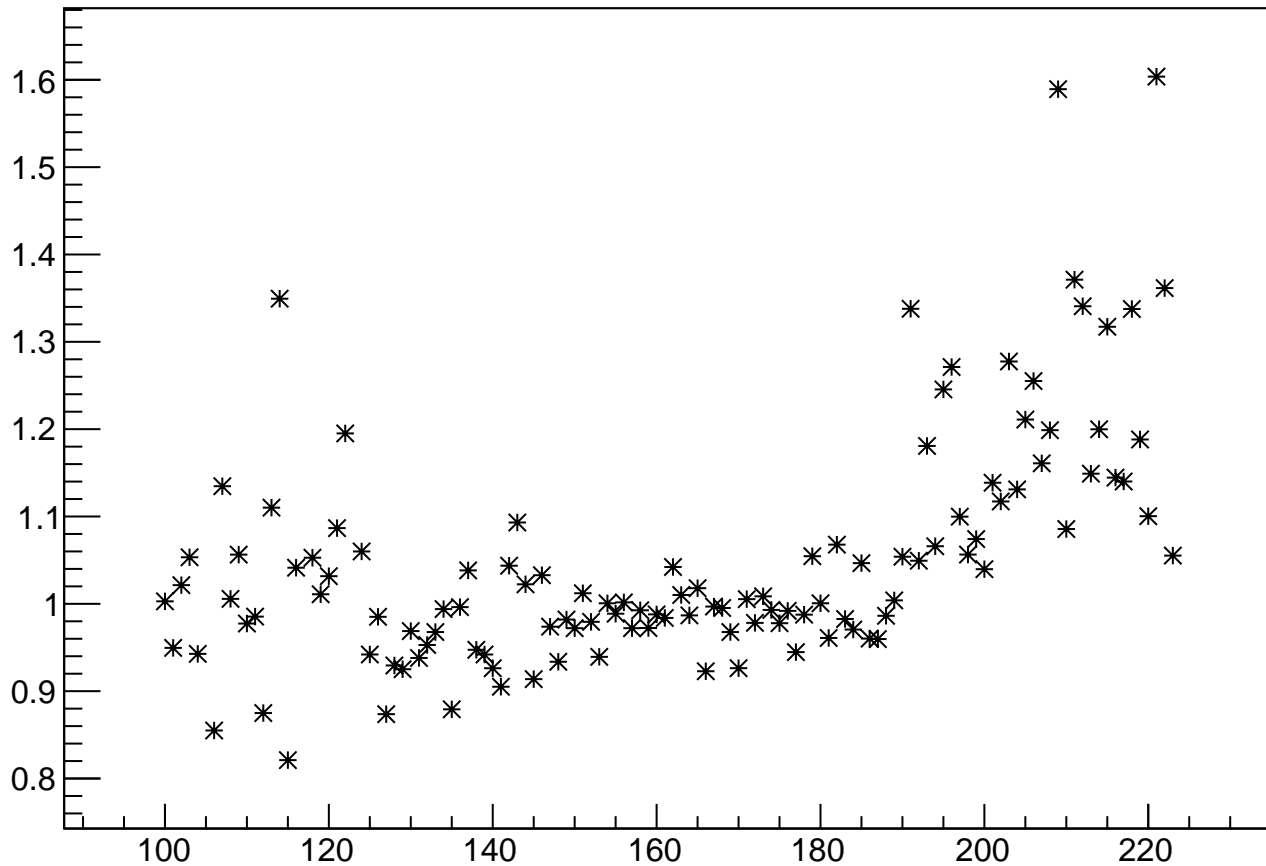
# evMon4 Lagr - reg slugwise mean / slug's stddev



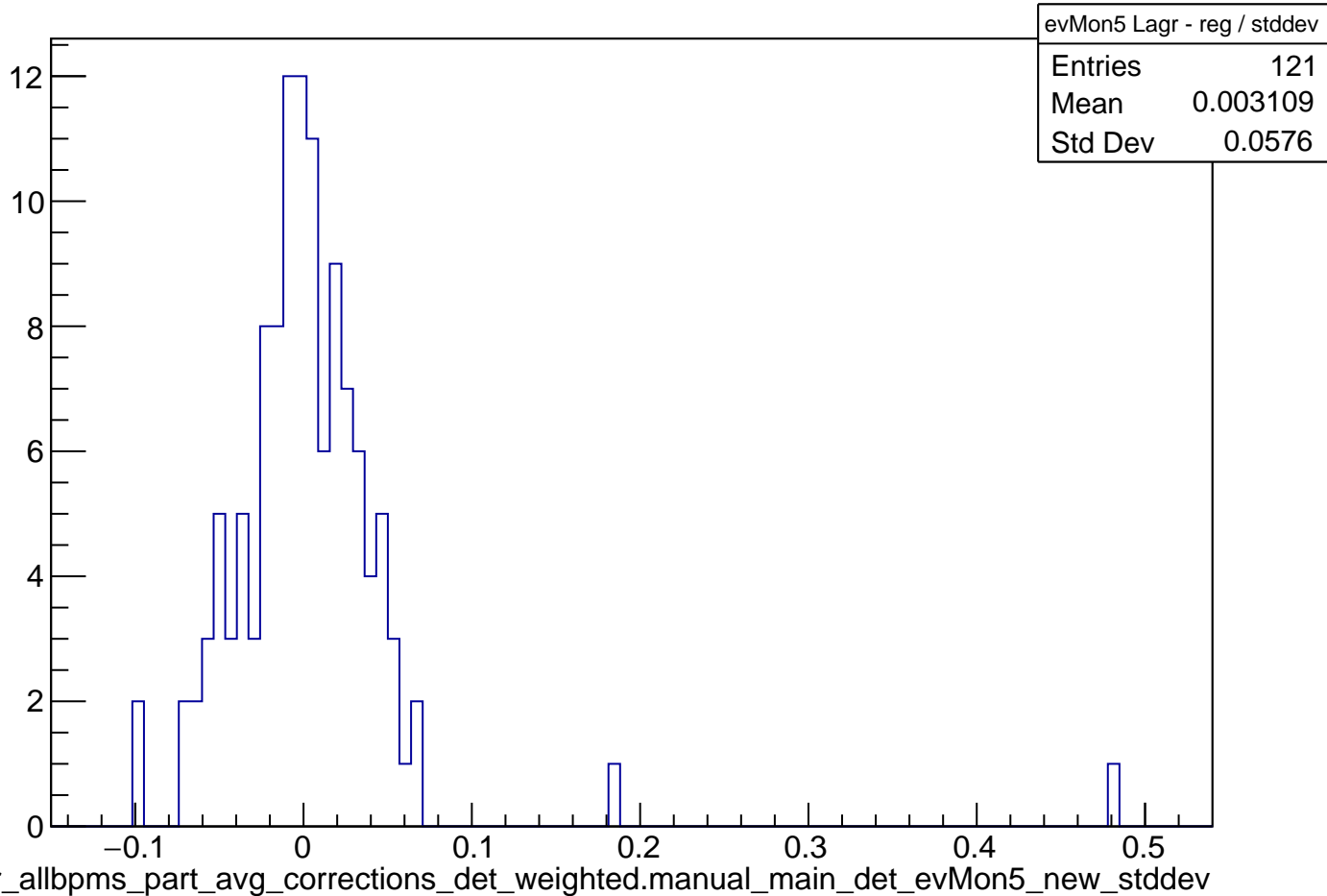
evMon4 reg stddev / Lagr slugwise mean's stddev



evMon4 IncludeBMOD stddev / ErrorFlag slugwise mean's stddev

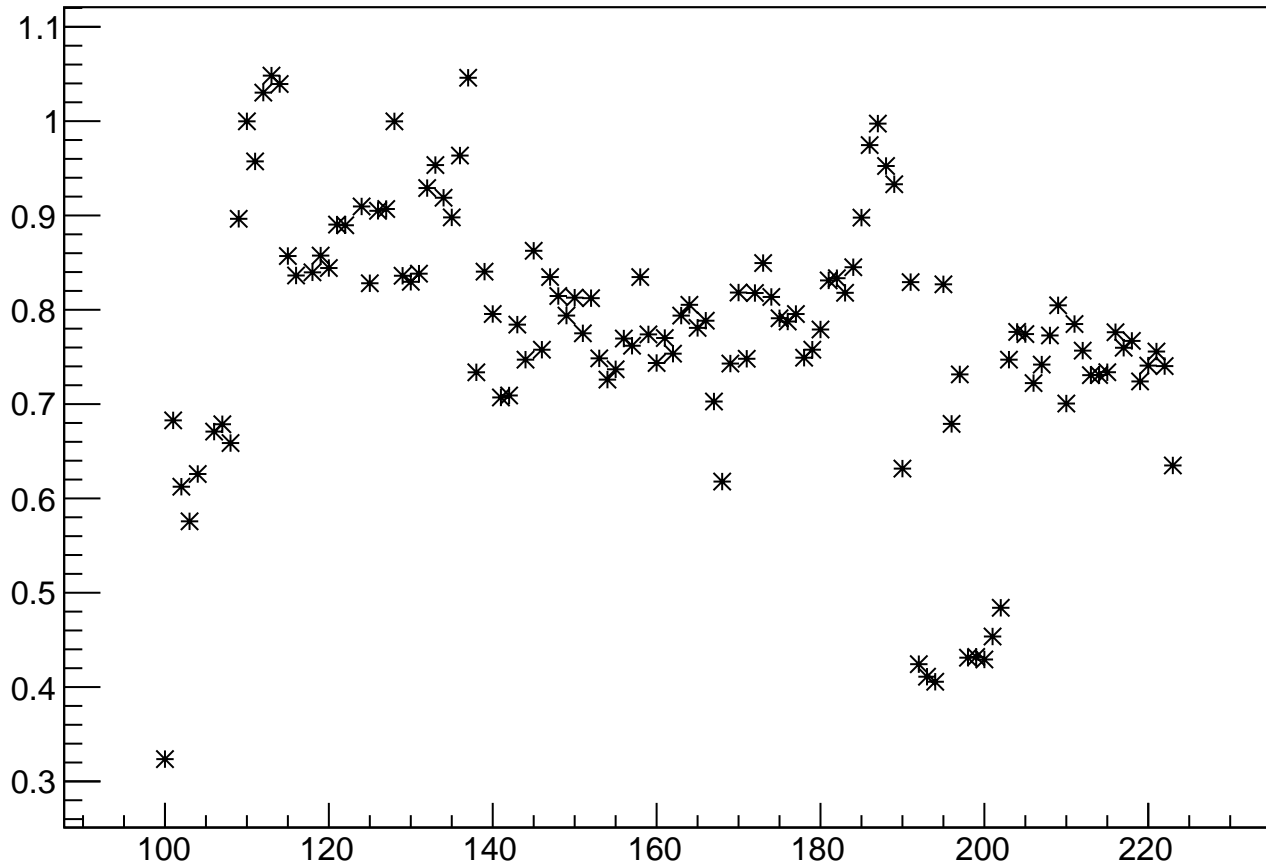


# evMon5 Lagr - reg slugwise mean / slug's stddev

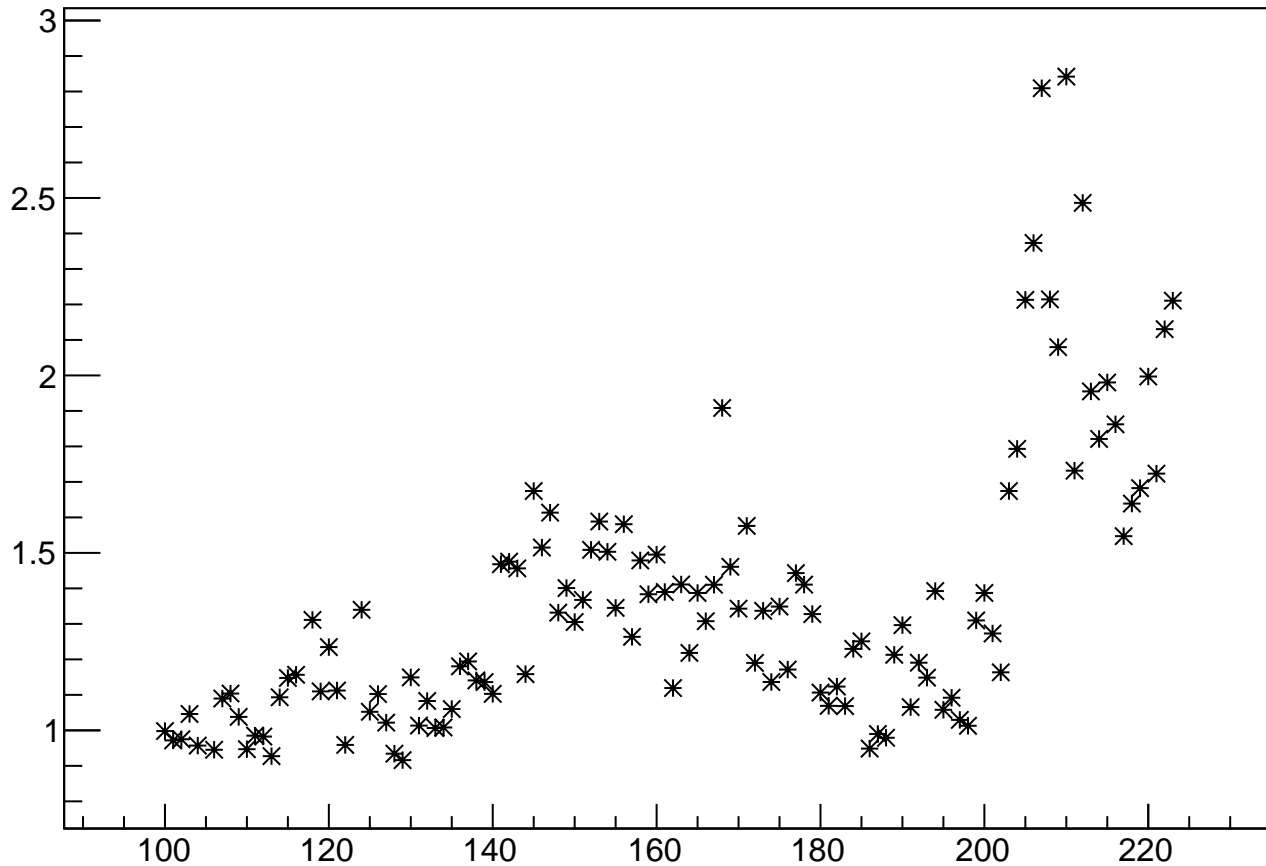




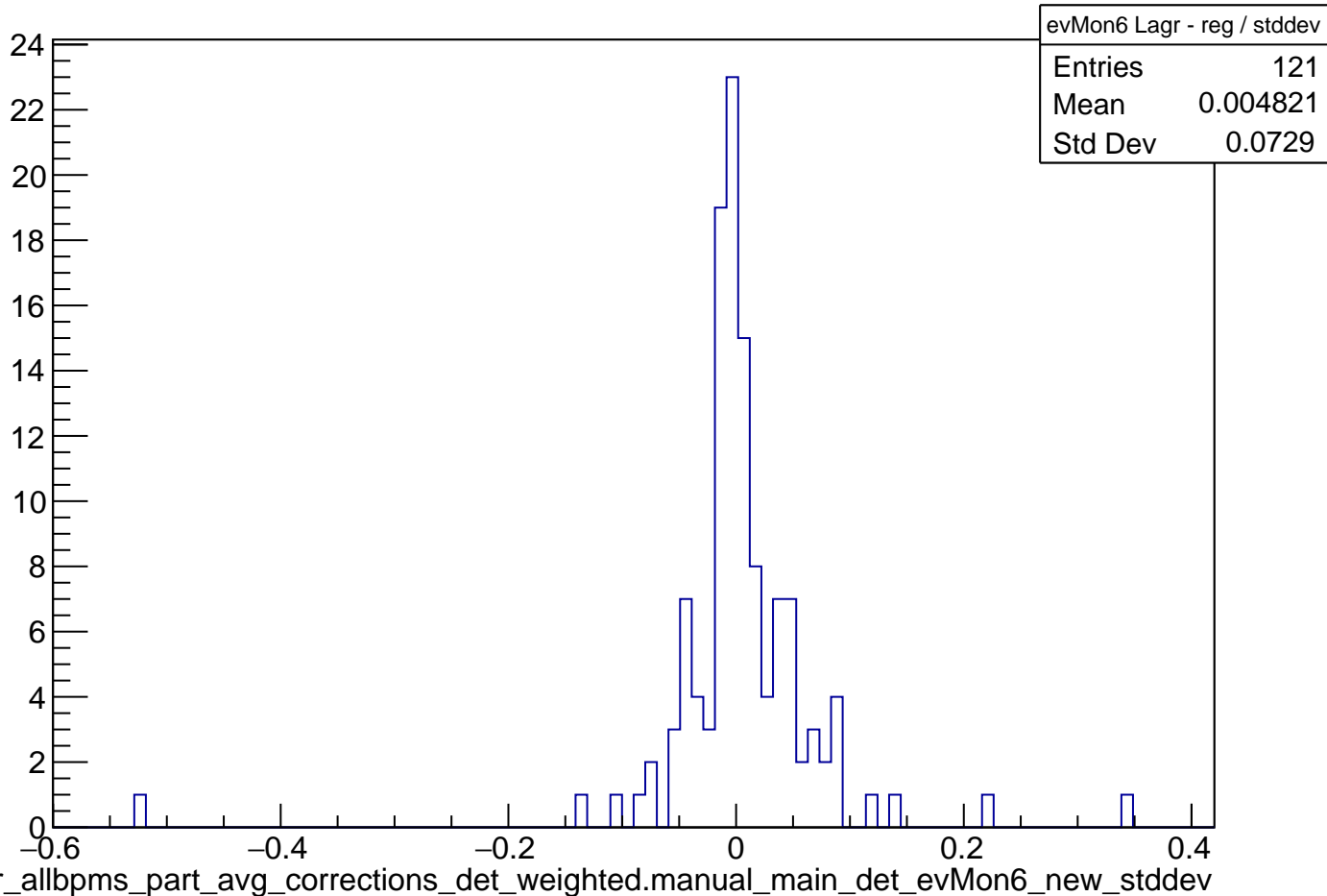
# evMon5 reg stddev / Lagr slugwise mean's stddev



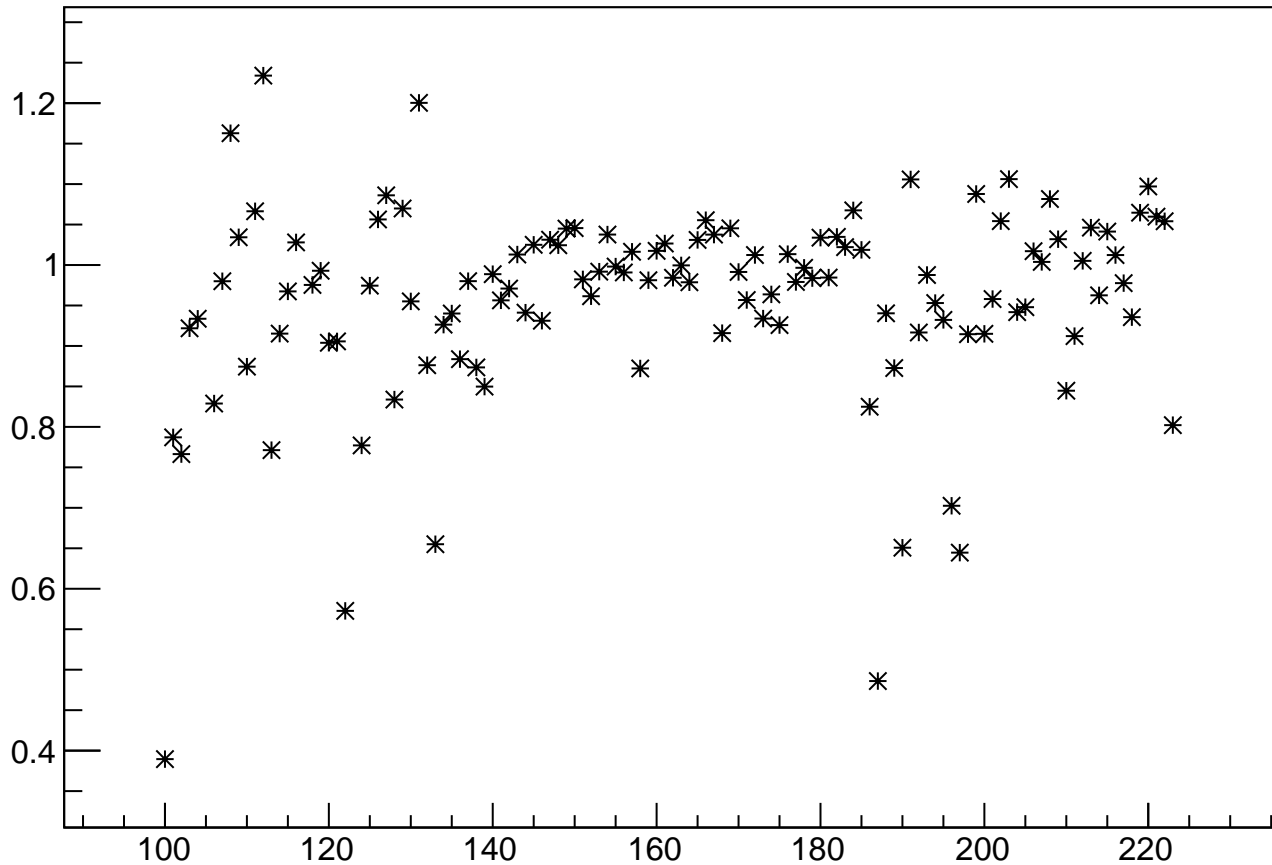
evMon5 IncludeBMOD stddev / ErrorFlag slugwise mean's stddev



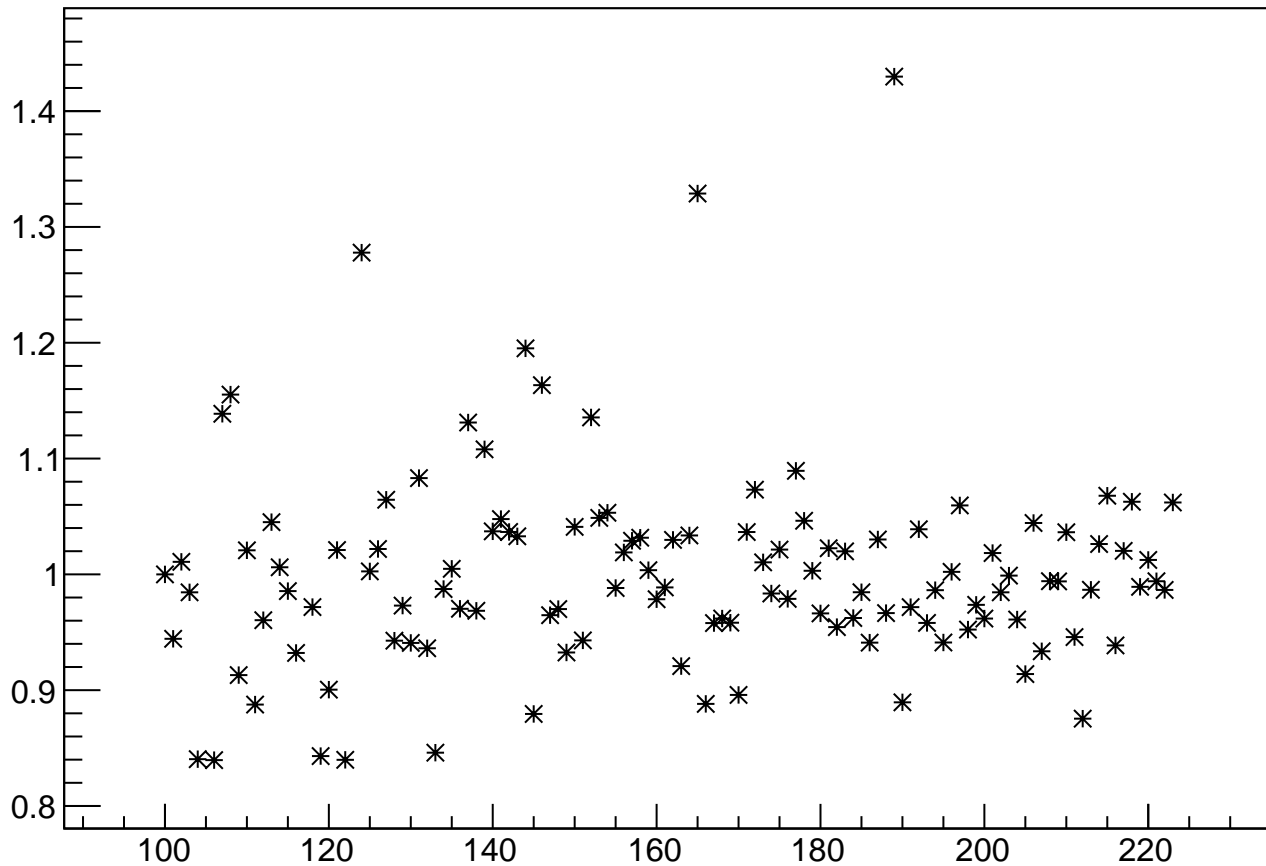
# evMon6 Lagr - reg slugwise mean / slug's stddev



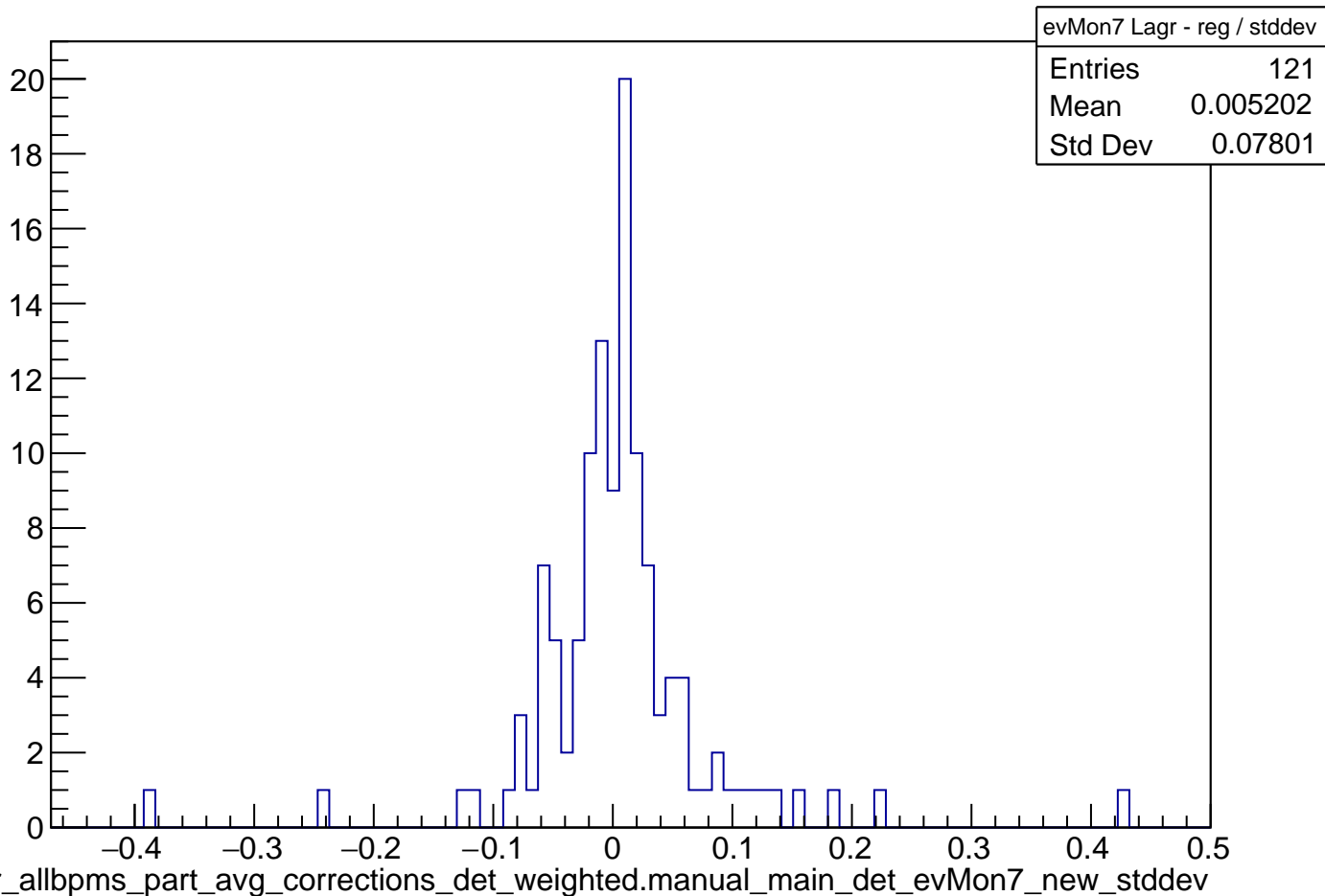
evMon6 reg stddev / Lagr slugwise mean's stddev



evMon6 IncludeBMOD stddev / ErrorFlag slugwise mean's stddev

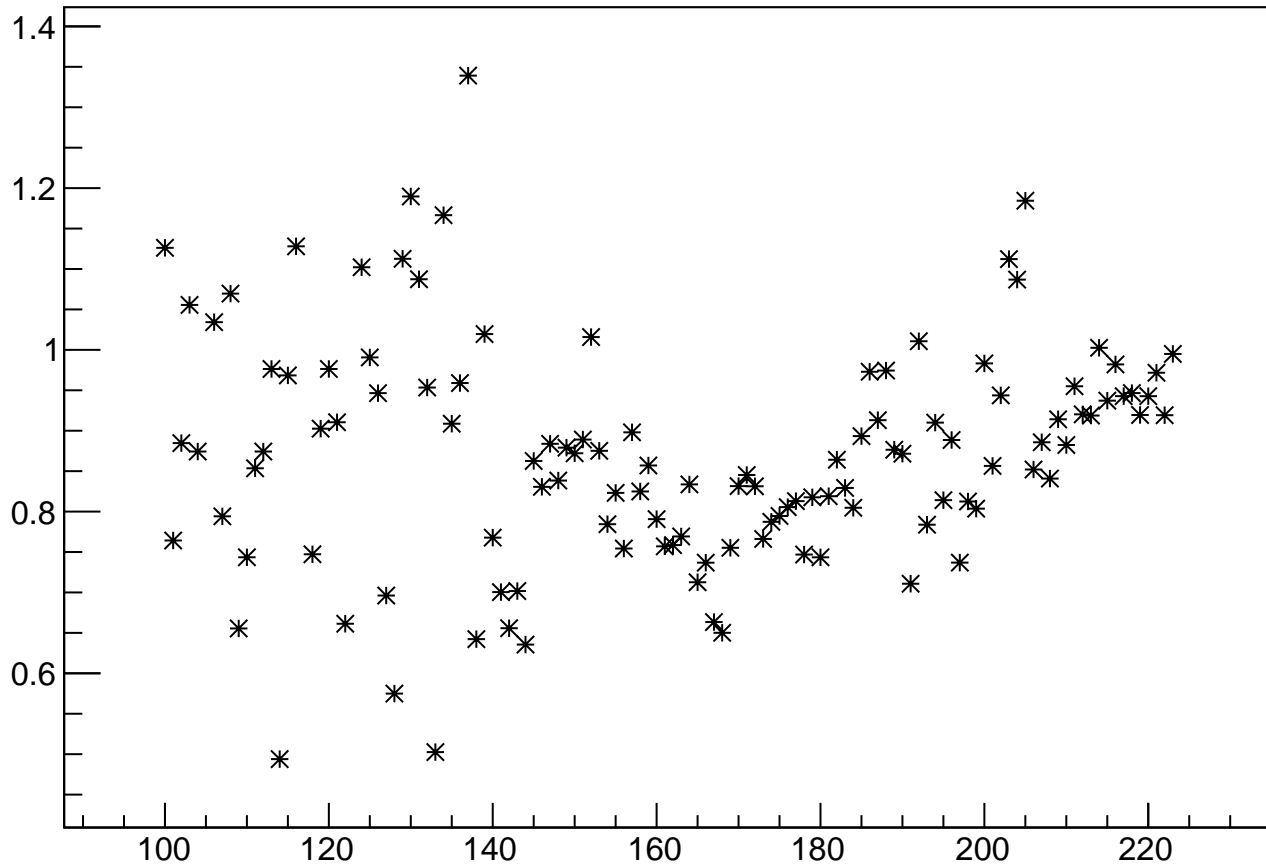


# evMon7 Lagr - reg slugwise mean / slug's stddev

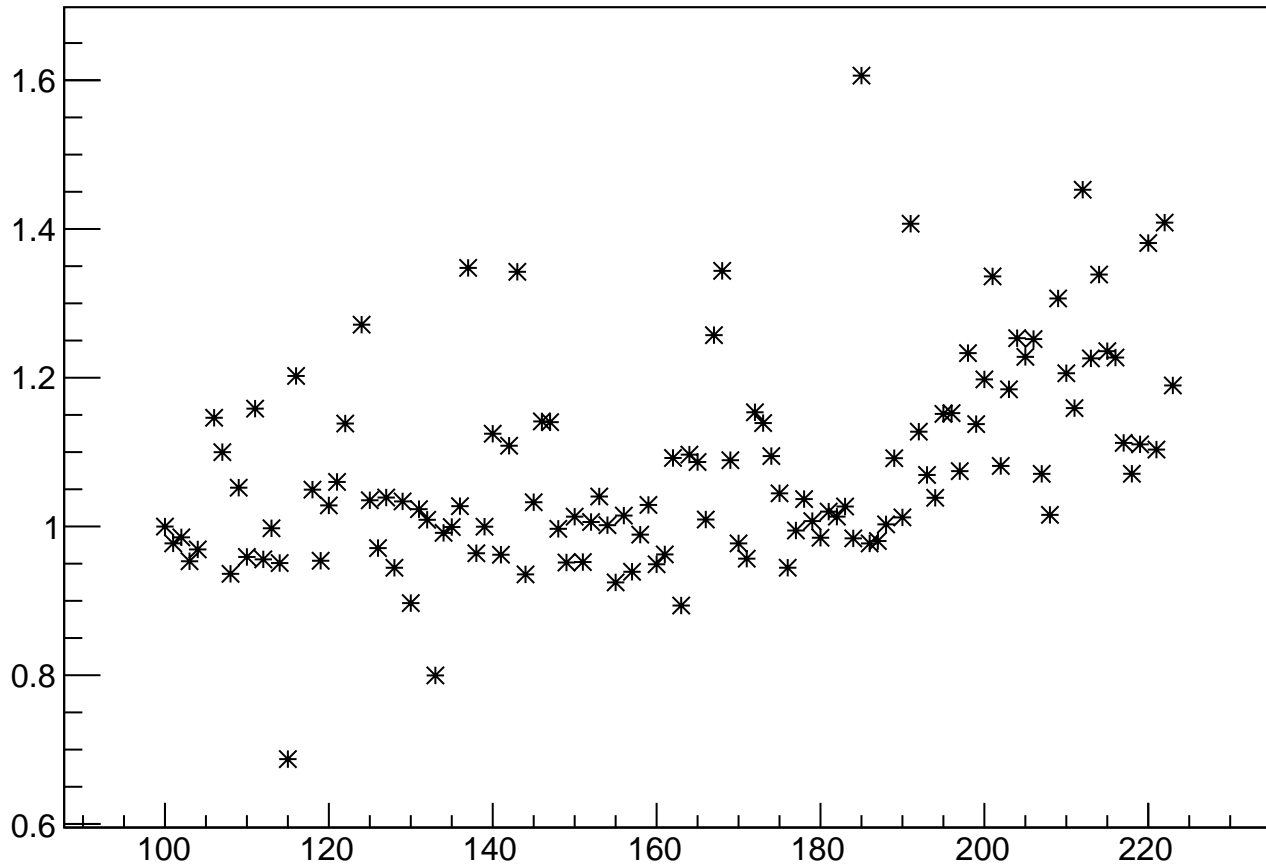


en\_lagr\_allbpms\_part\_avg\_corrections\_det\_weighted.manual\_main\_det\_evMon7\_new\_stddev

evMon7 reg stddev / Lagr slugwise mean's stddev



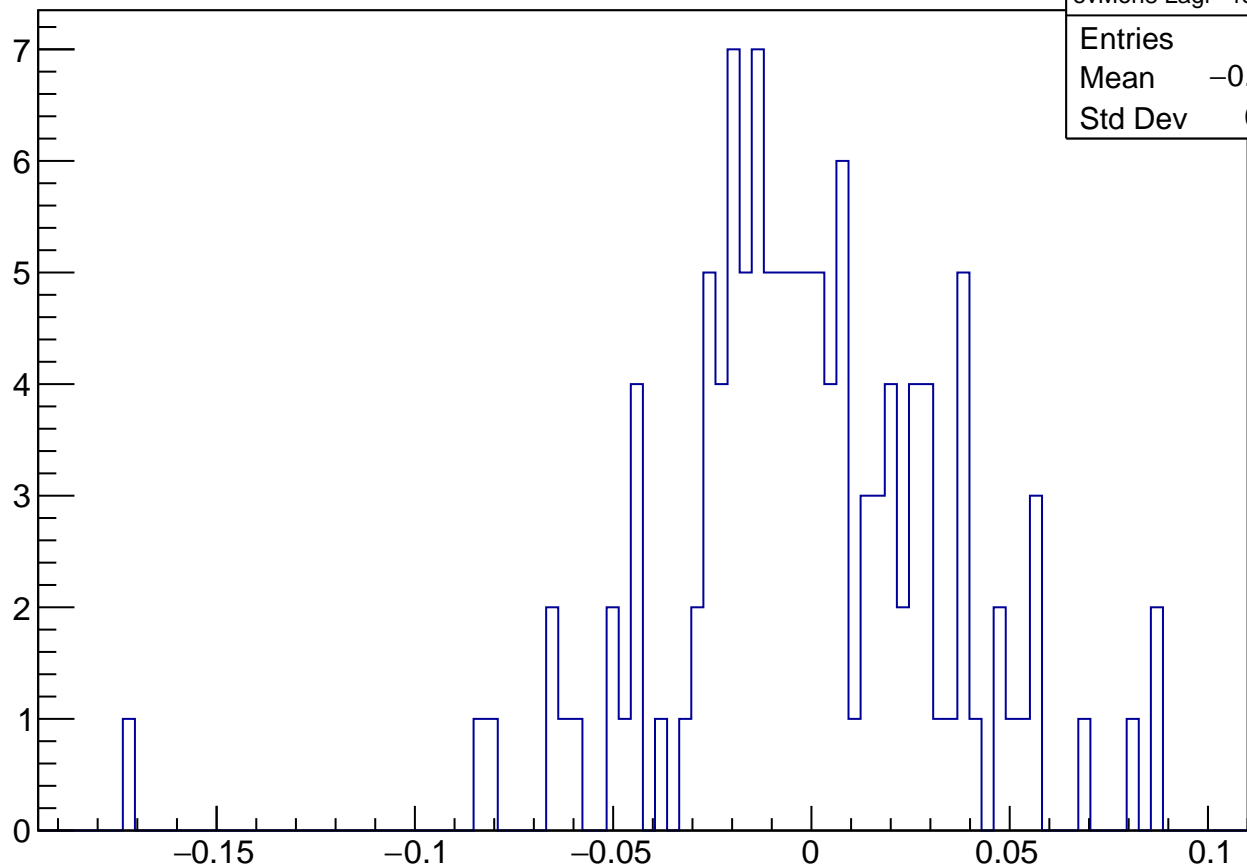
evMon7 IncludeBMOD stddev / ErrorFlag slugwise mean's stddev





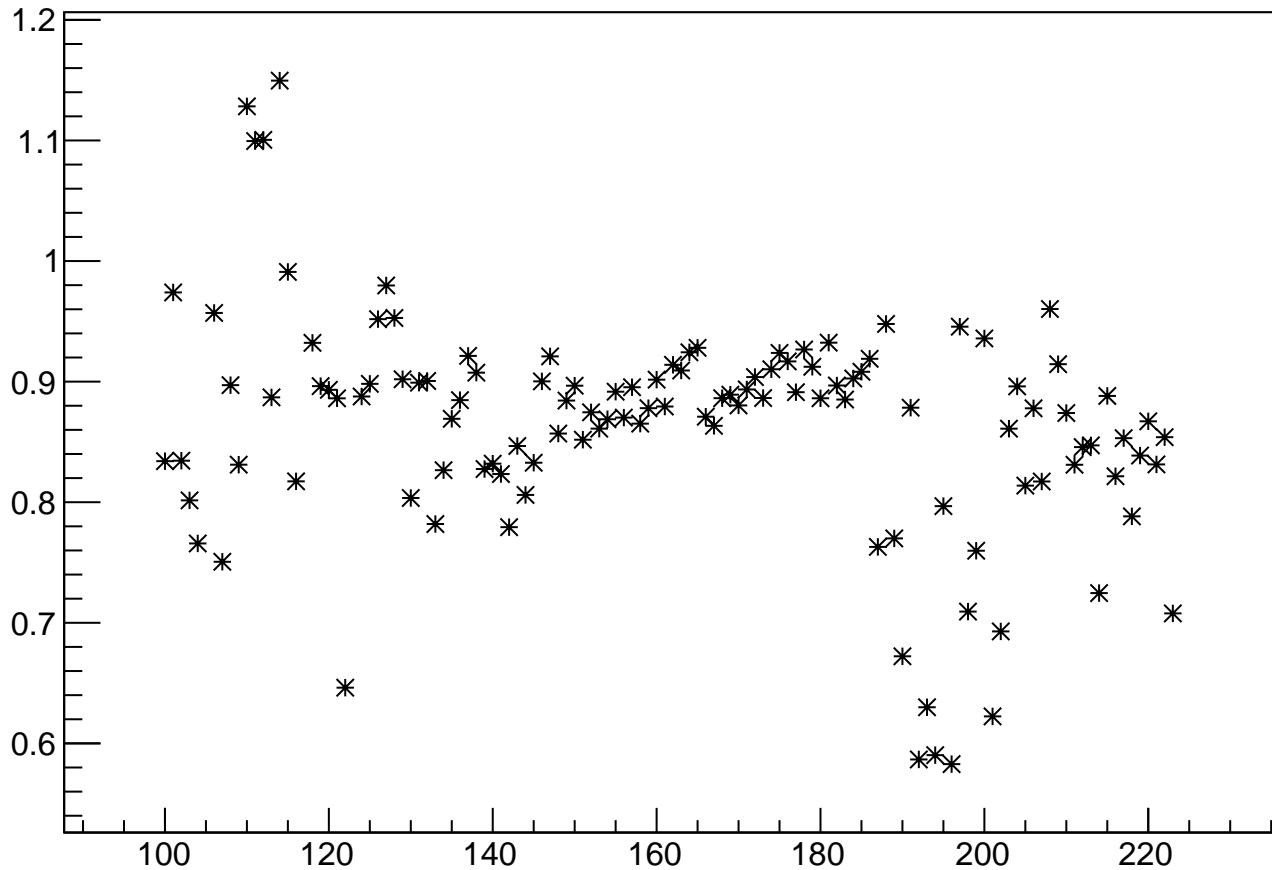
# evMon8 Lagr - reg slugwise mean / slug's stddev

evMon8 Lagr - reg / stddev	
Entries	121
Mean	-0.001454
Std Dev	0.03615

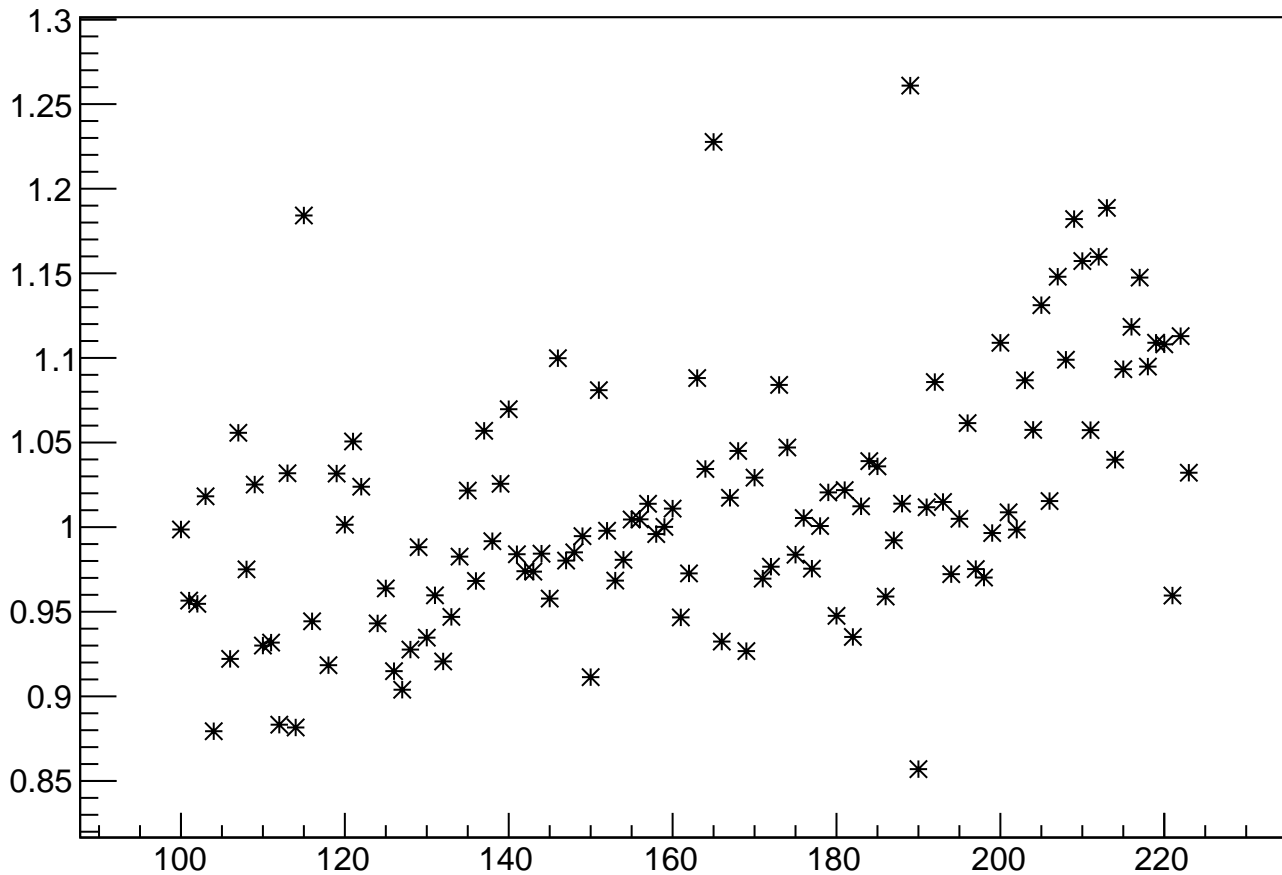


en\_lagr\_allbpms\_part\_avg\_corrections\_det\_weighted.manual\_main\_det\_evMon8\_new\_stddev

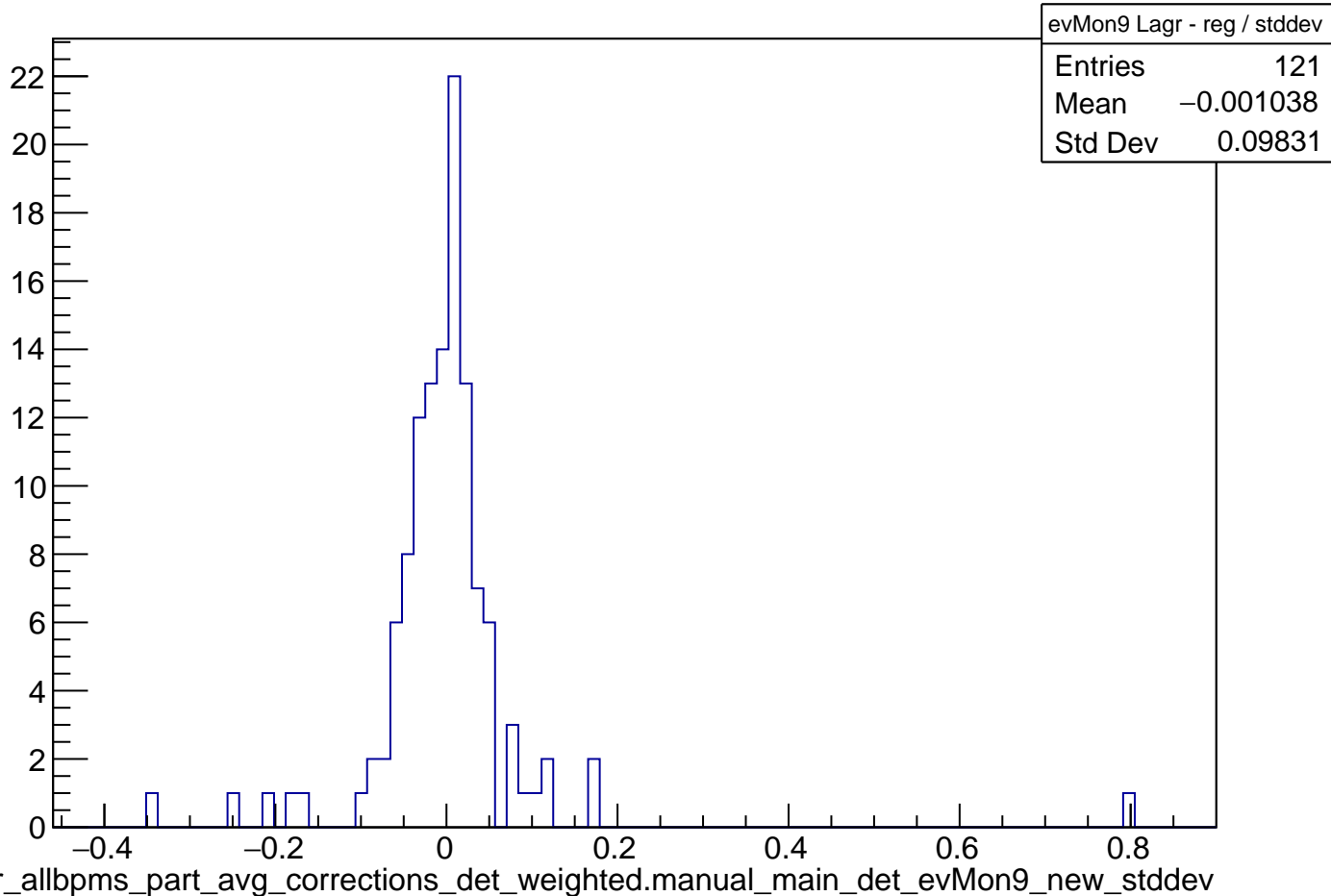
evMon8 reg stddev / Lagr slugwise mean's stddev



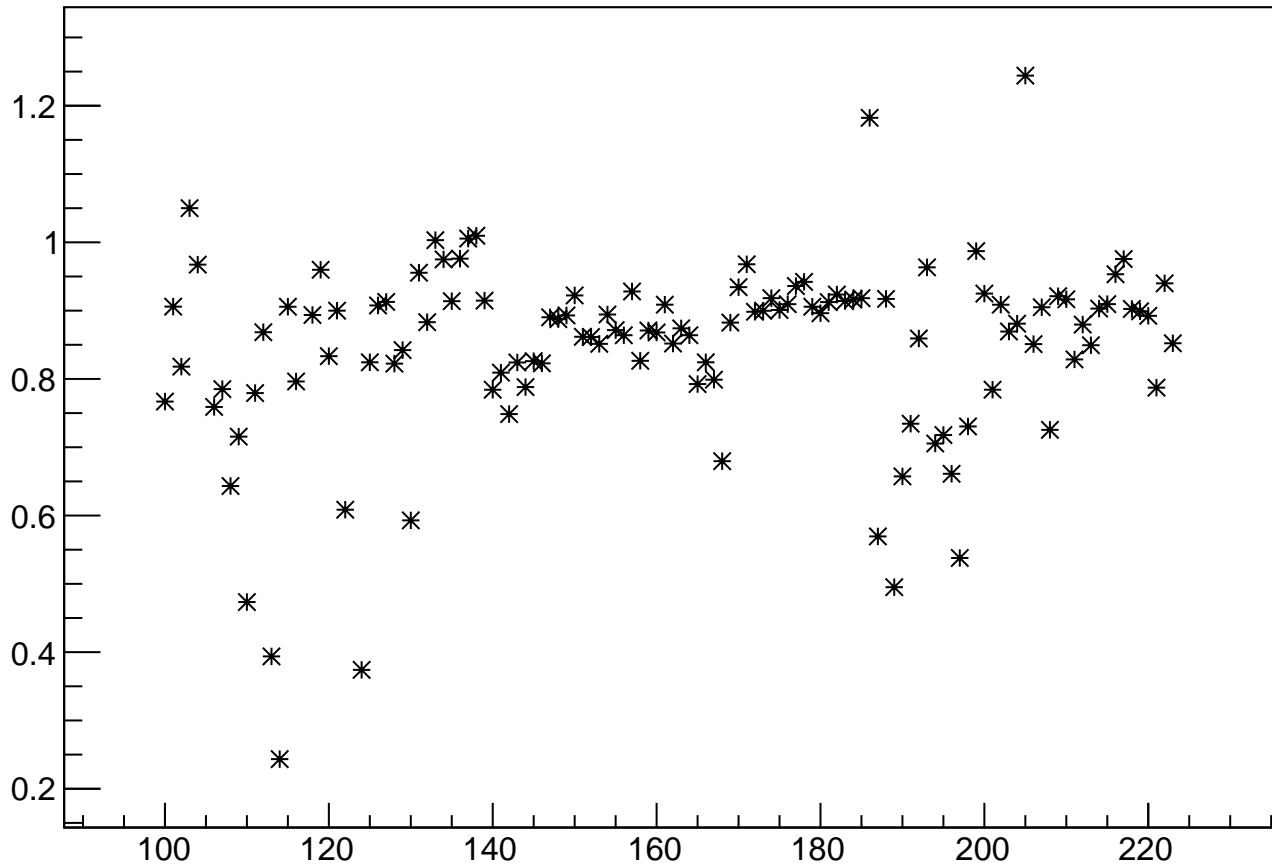
evMon8 IncludeBMOD stddev / ErrorFlag slugwise mean's stddev



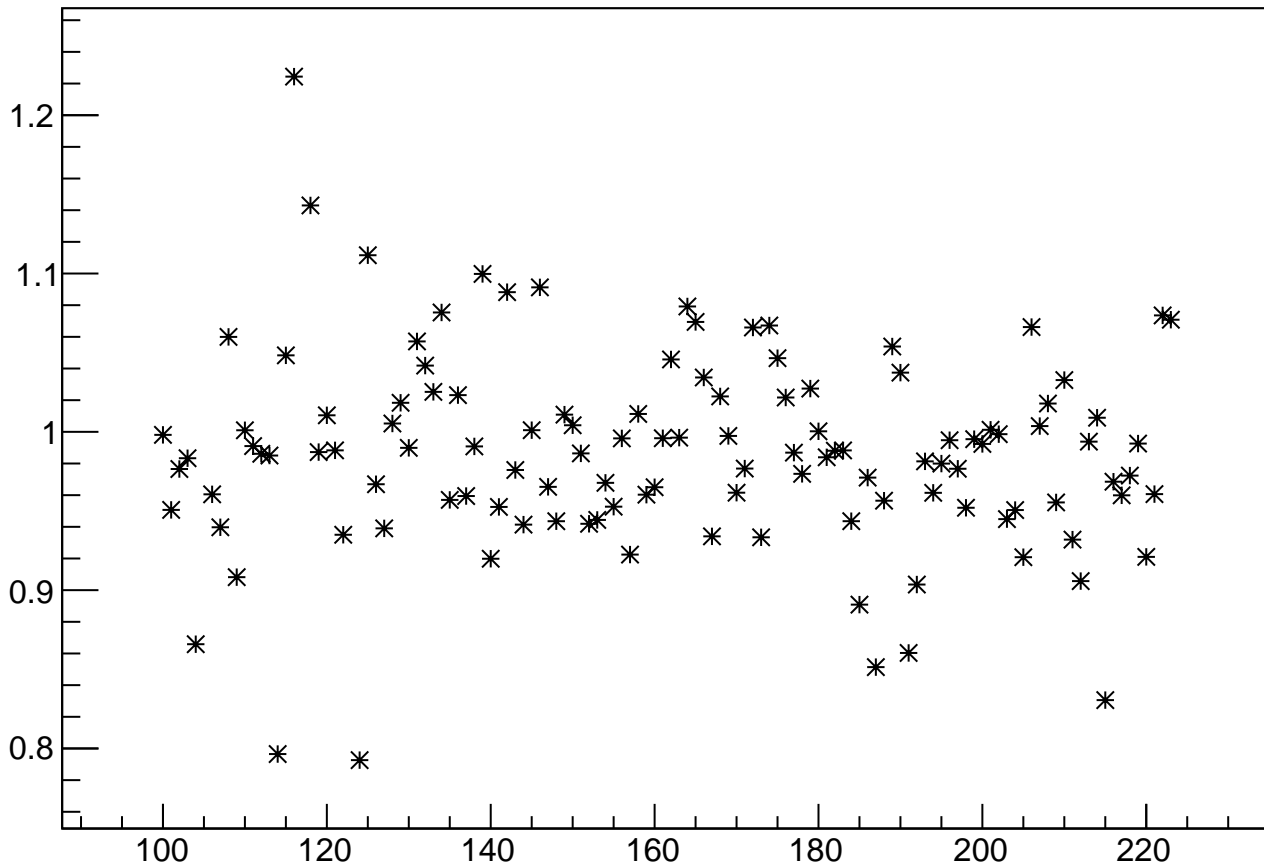
# evMon9 Lagr - reg slugwise mean / slug's stddev



evMon9 reg stddev / Lagr slugwise mean's stddev

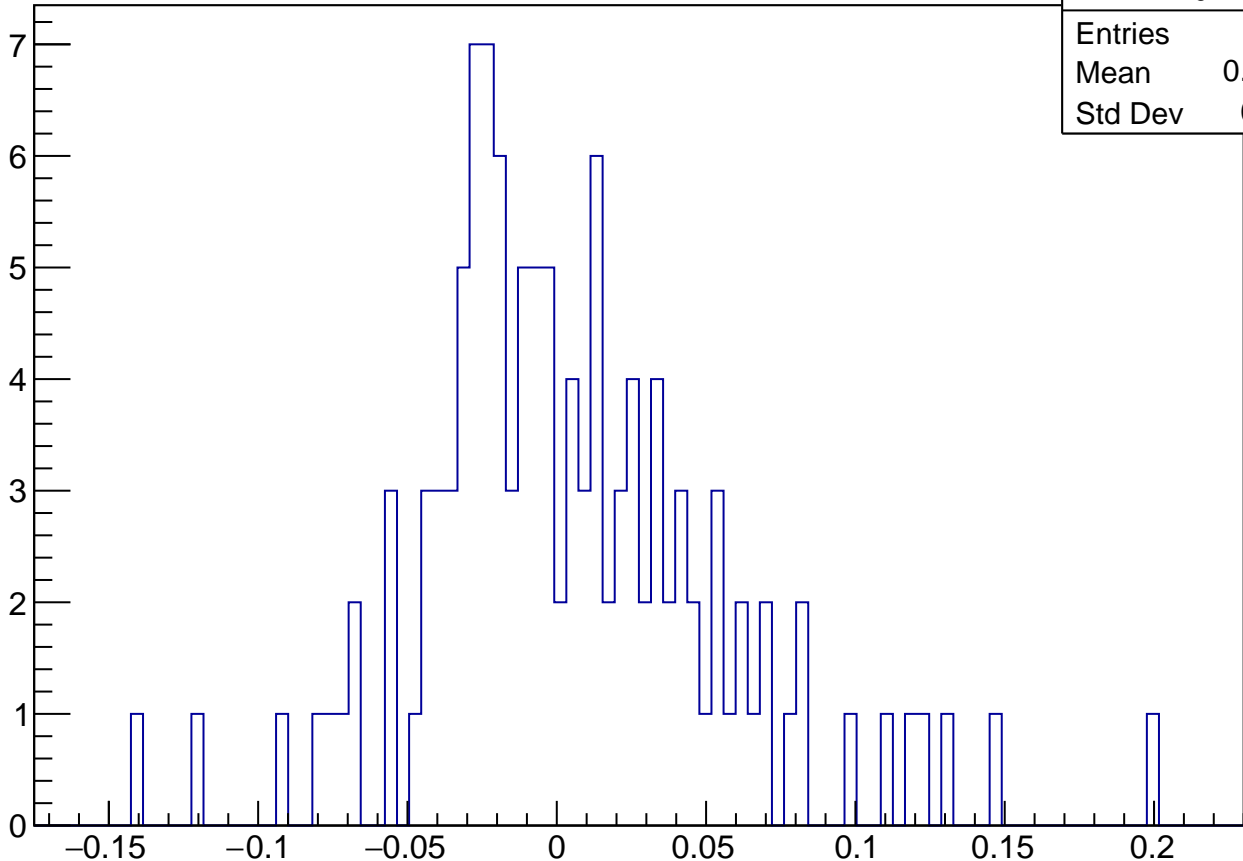


evMon9 IncludeBMOD stddev / ErrorFlag slugwise mean's stddev



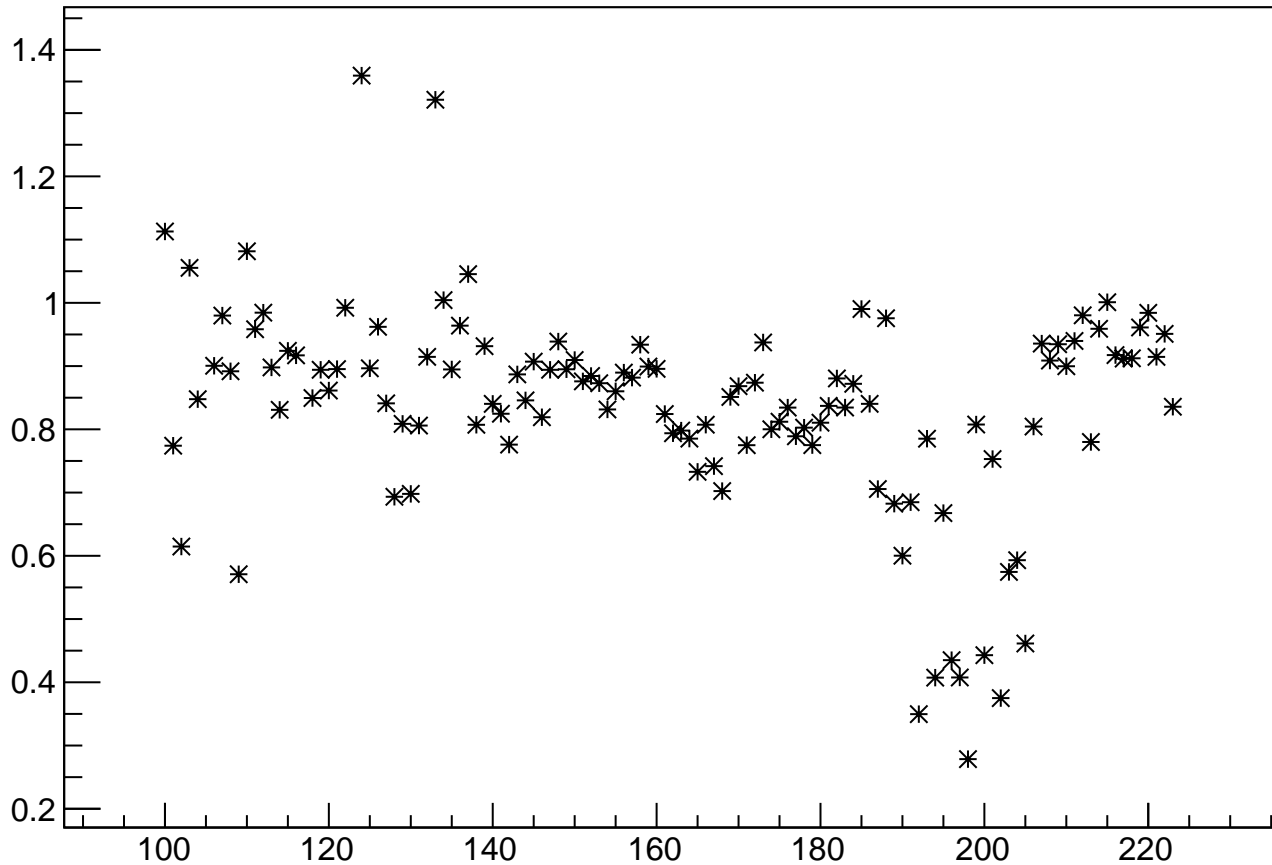
# evMon10 Lagr - reg slugwise mean / slug's stddev

evMon10 Lagr - reg / stddev	
Entries	121
Mean	0.004279
Std Dev	0.05155



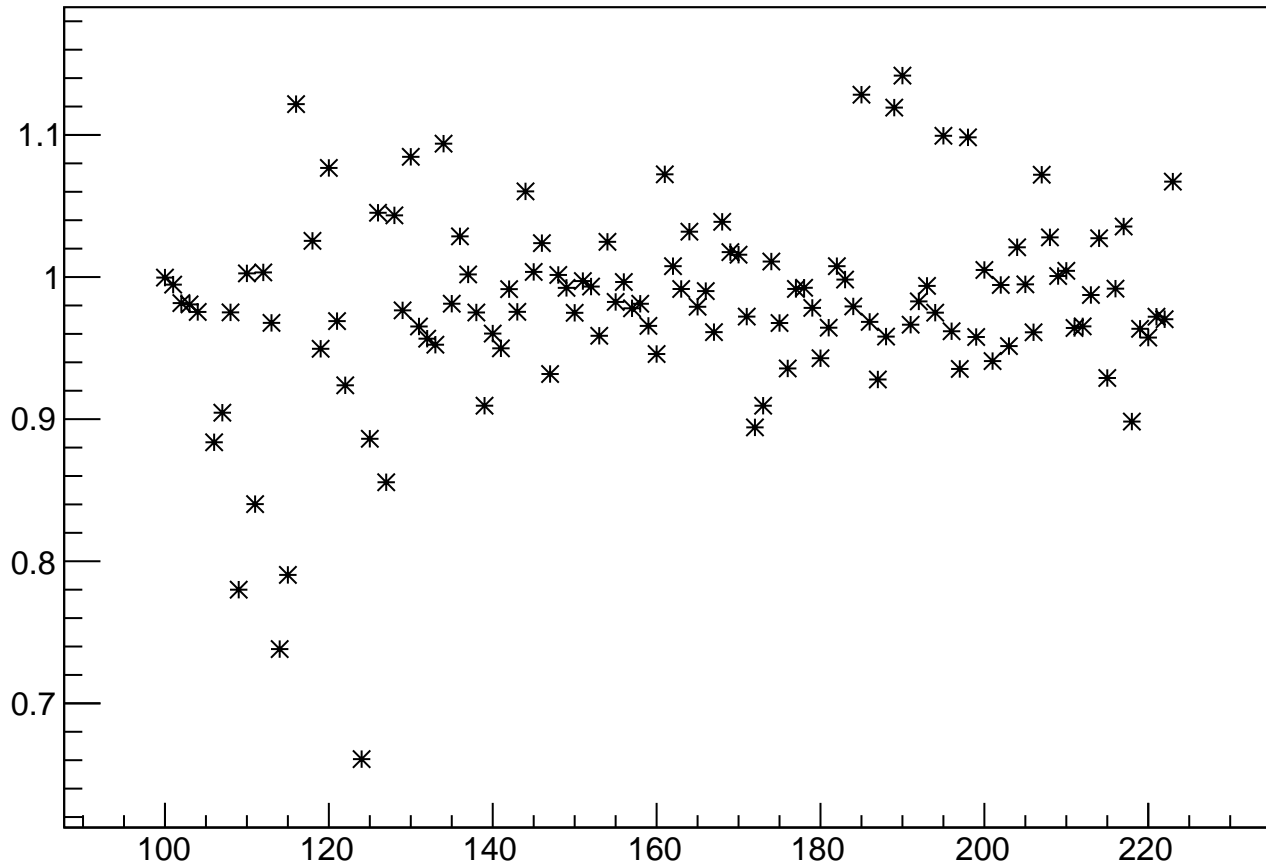
h\_lagr\_allbpms\_part\_avg\_corrections\_det\_weighted.manual\_main\_det\_evMon10\_new\_stddev

evMon10 reg stddev / Lagr slugwise mean's stddev

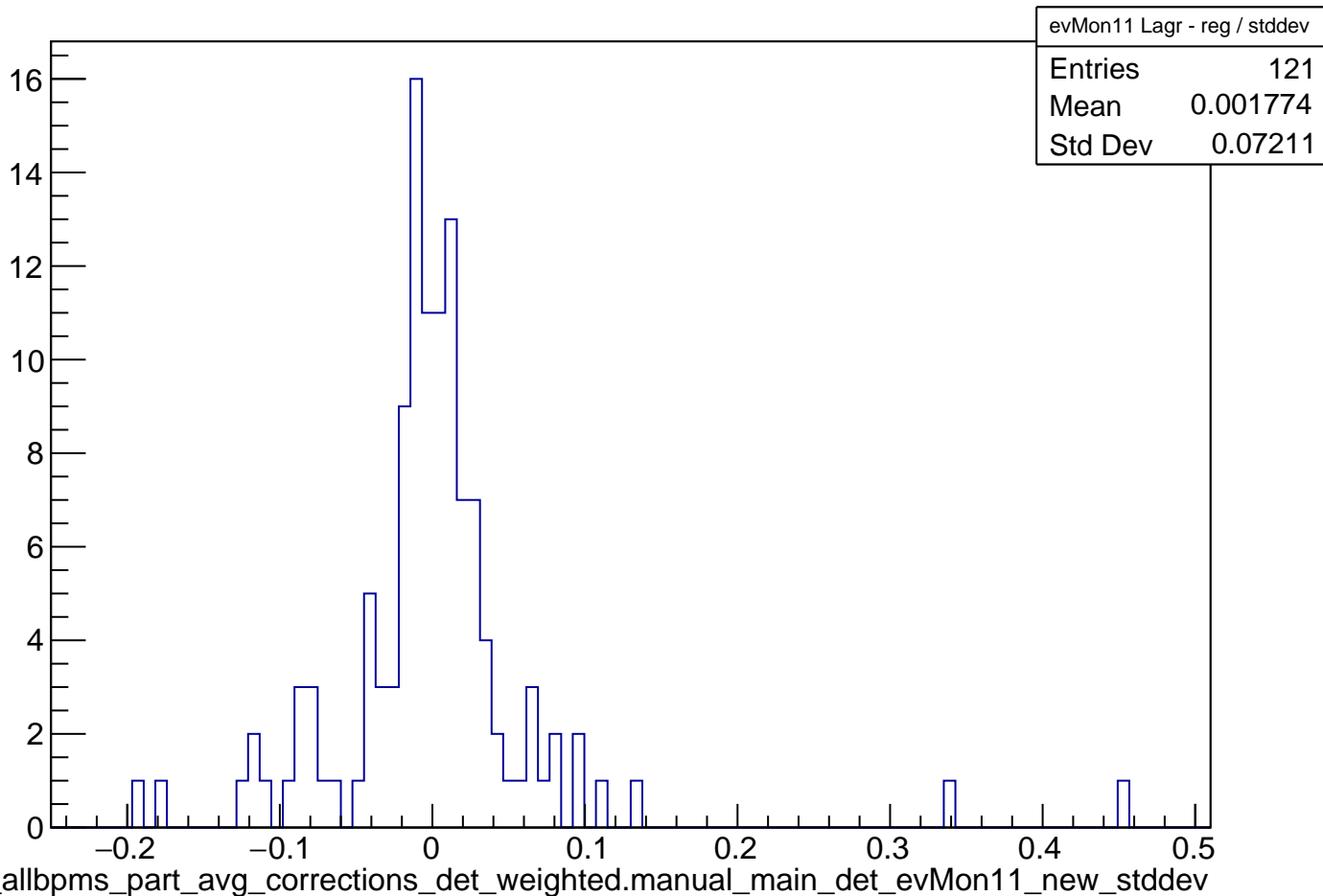




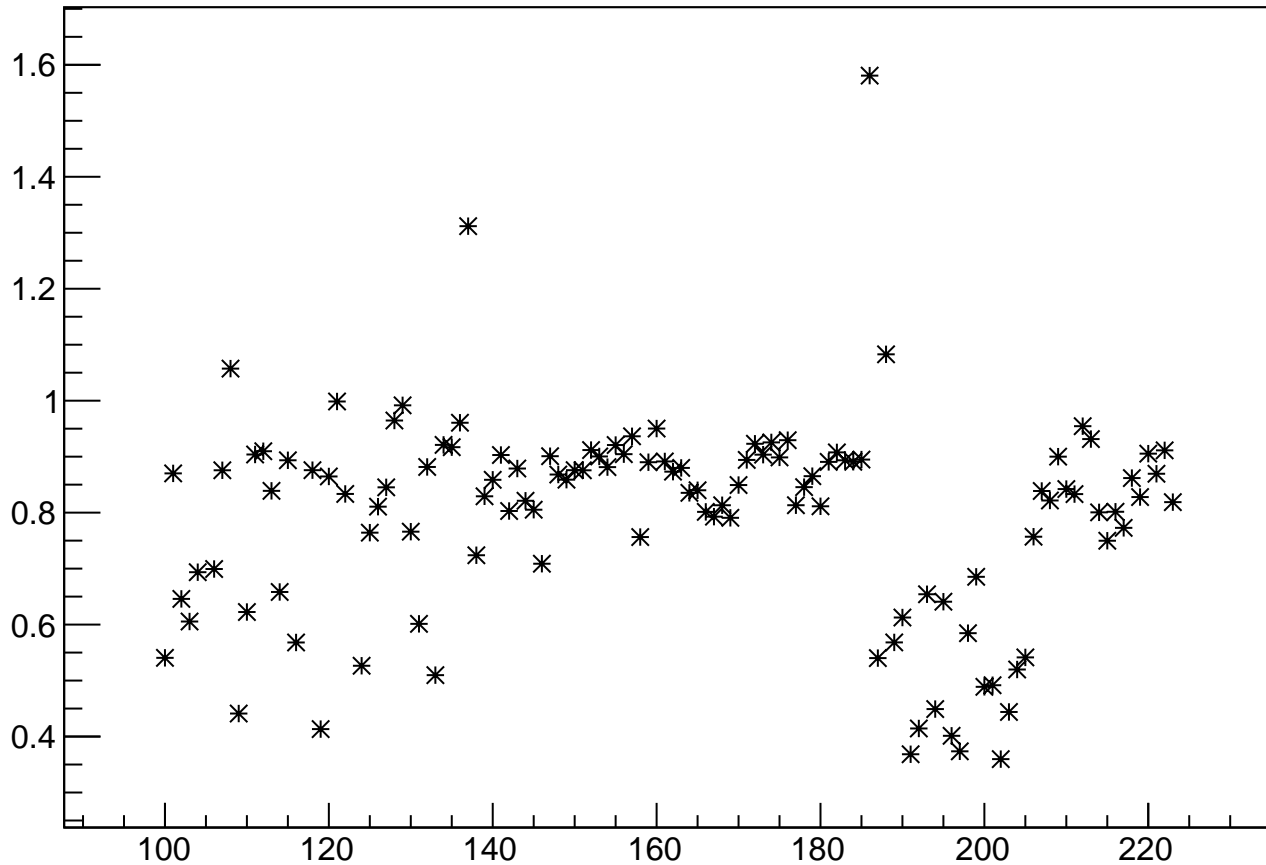
evMon10 IncludeBMOD stddev / ErrorFlag slugwise mean's stddev



# evMon11 Lagr - reg slugwise mean / slug's stddev



evMon11 reg stddev / Lagr slugwise mean's stddev



evMon11 IncludeBMOD stddev / ErrorFlag slugwise mean's stddev

