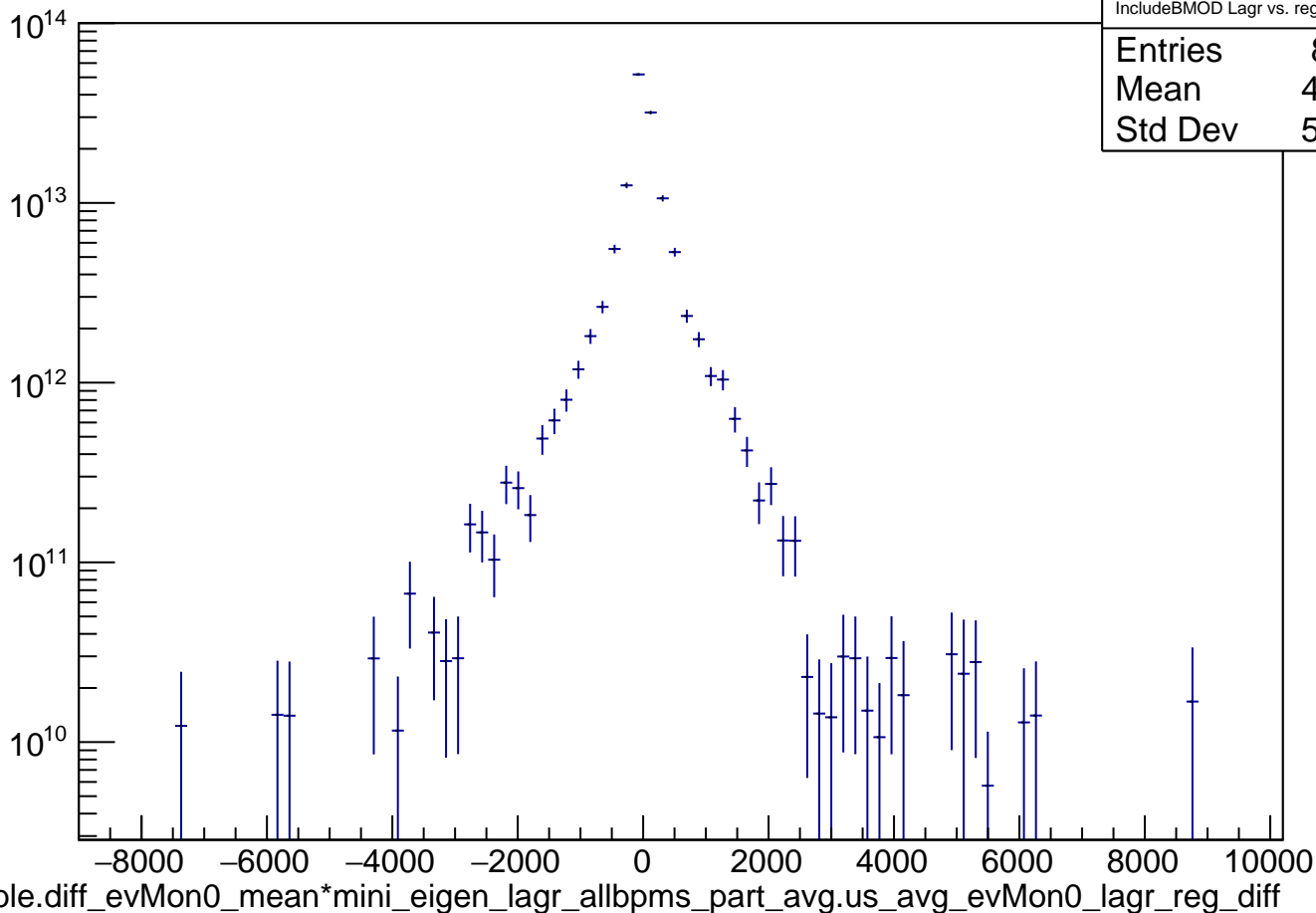
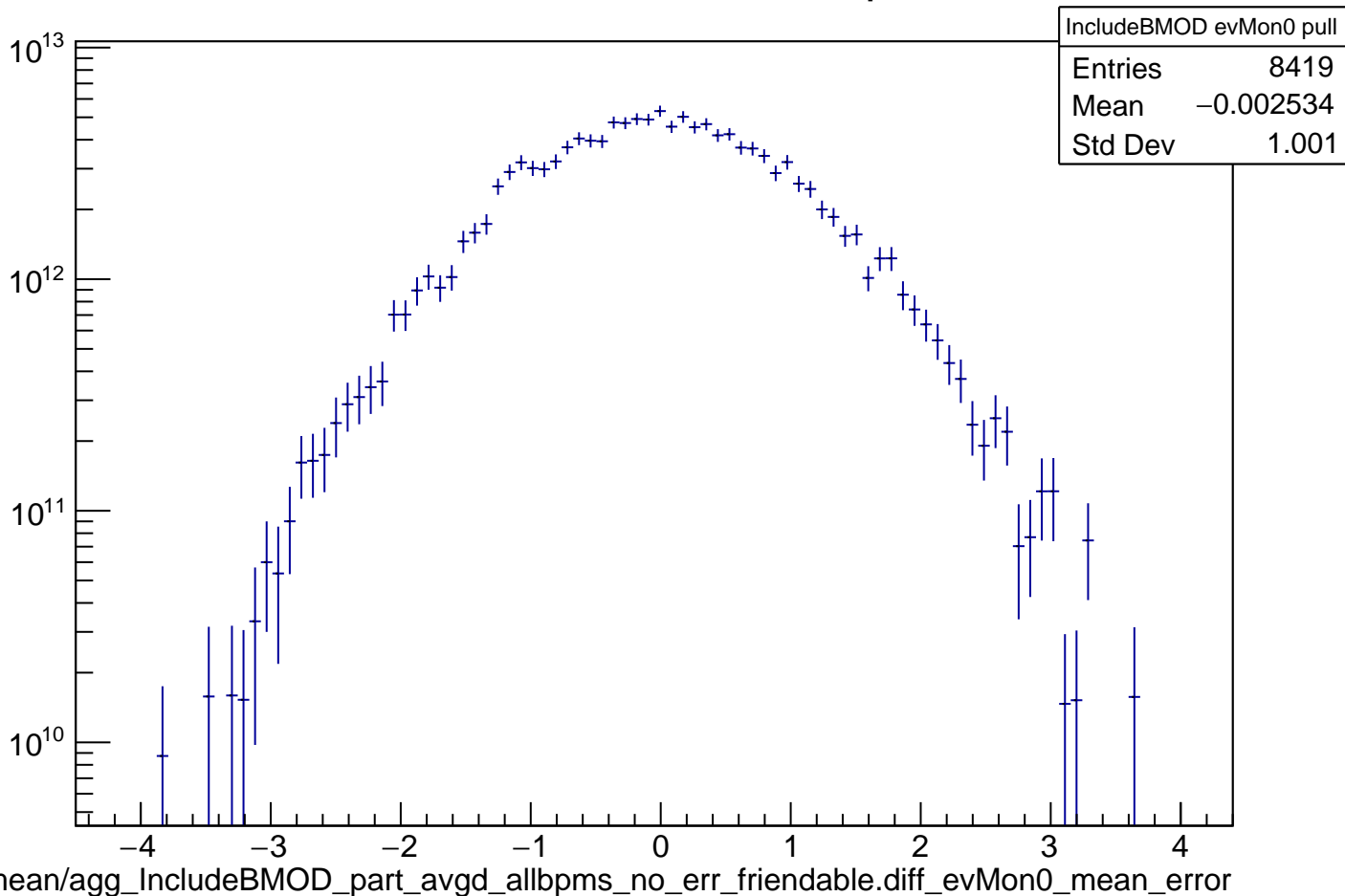


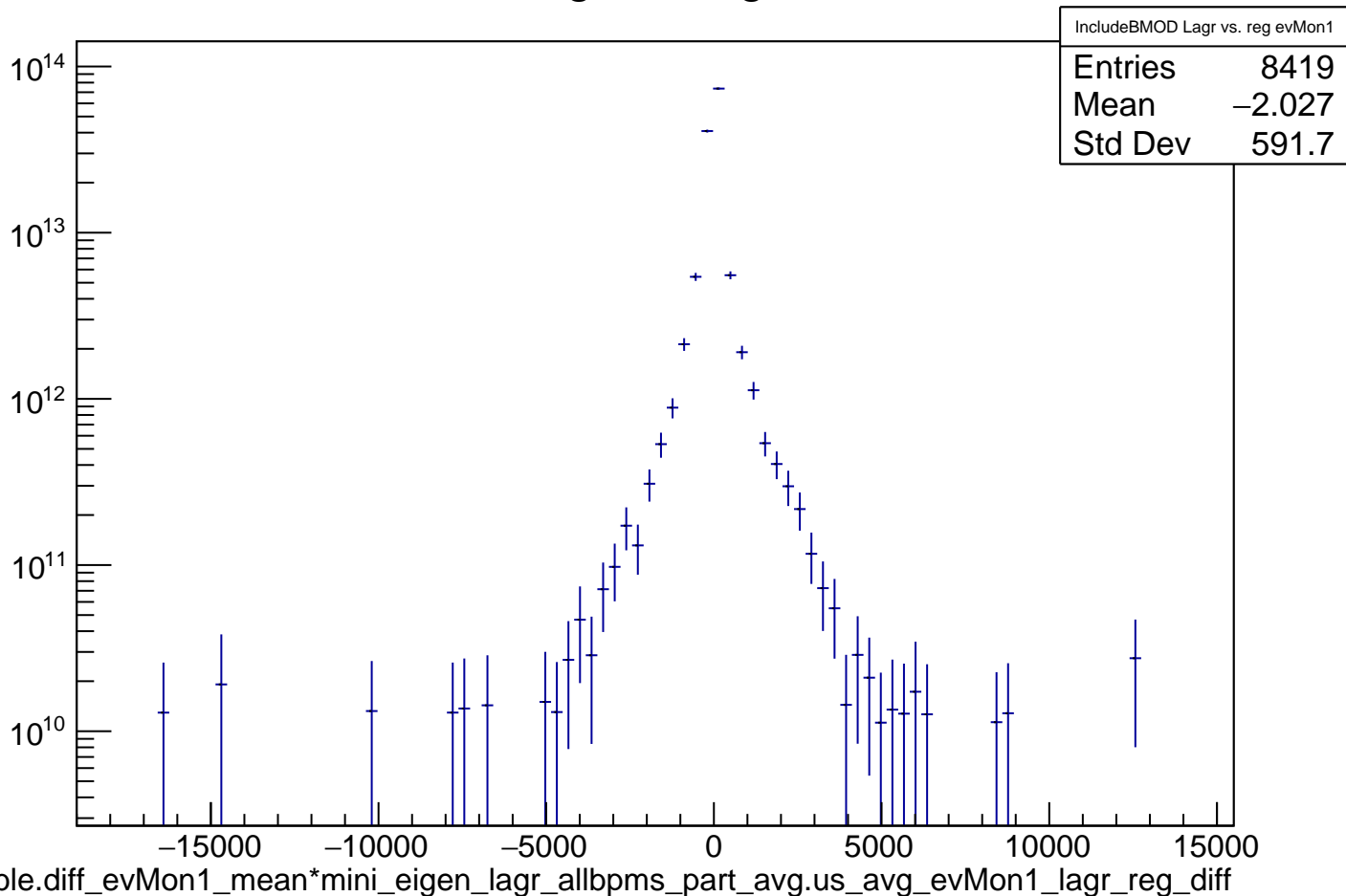
IncludeBMOD Lagr vs. reg evMon0 cor diff



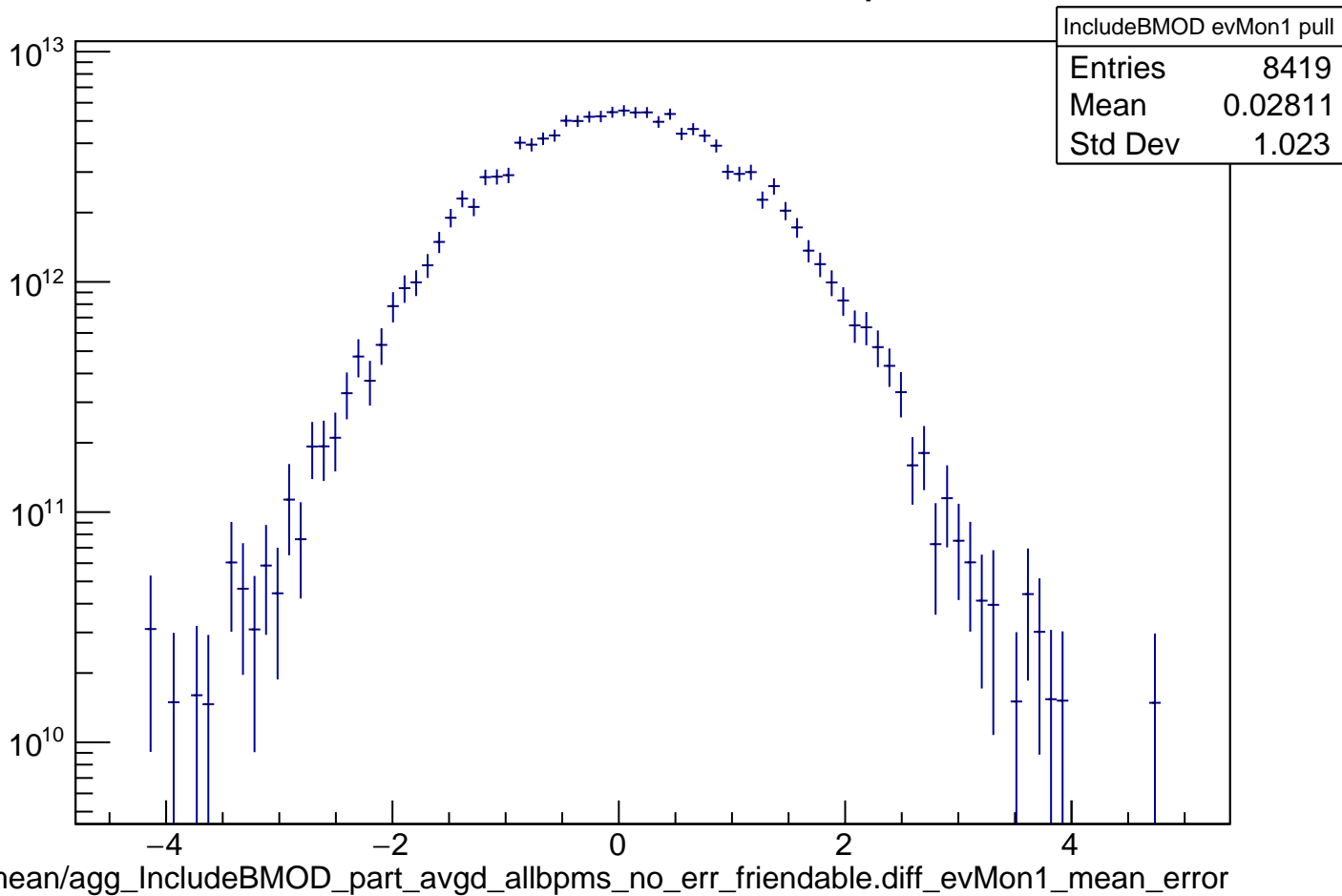
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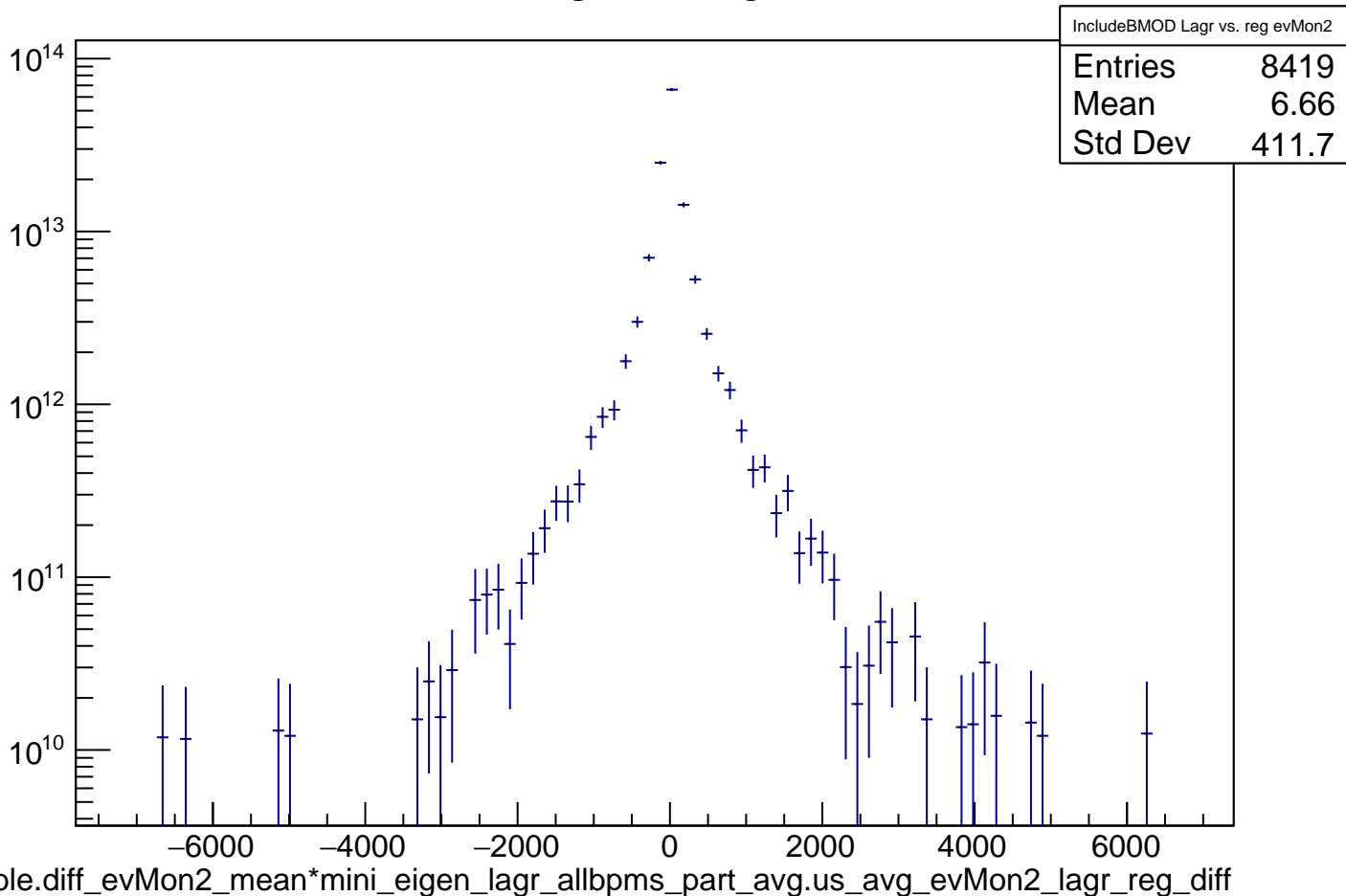
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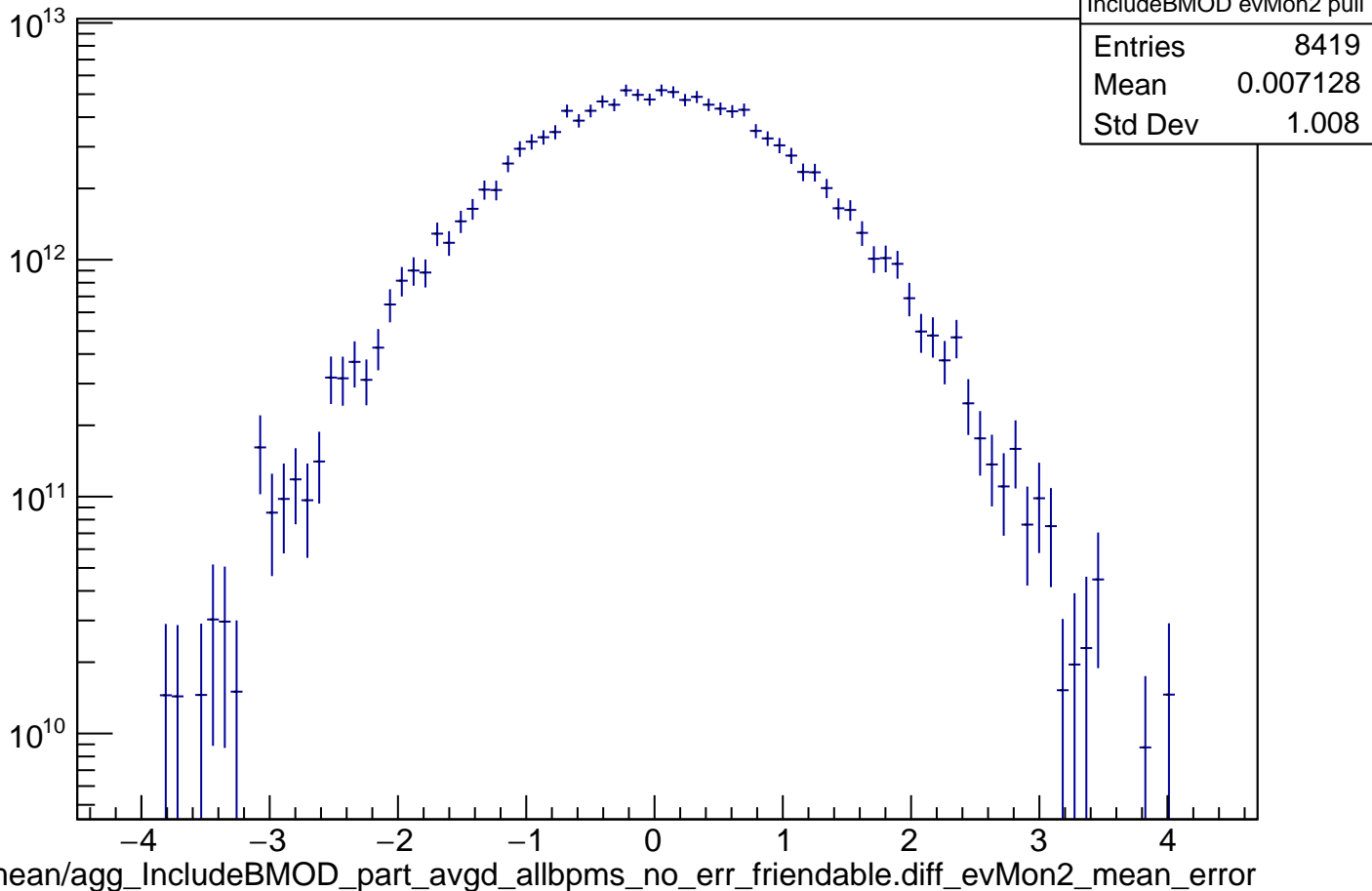
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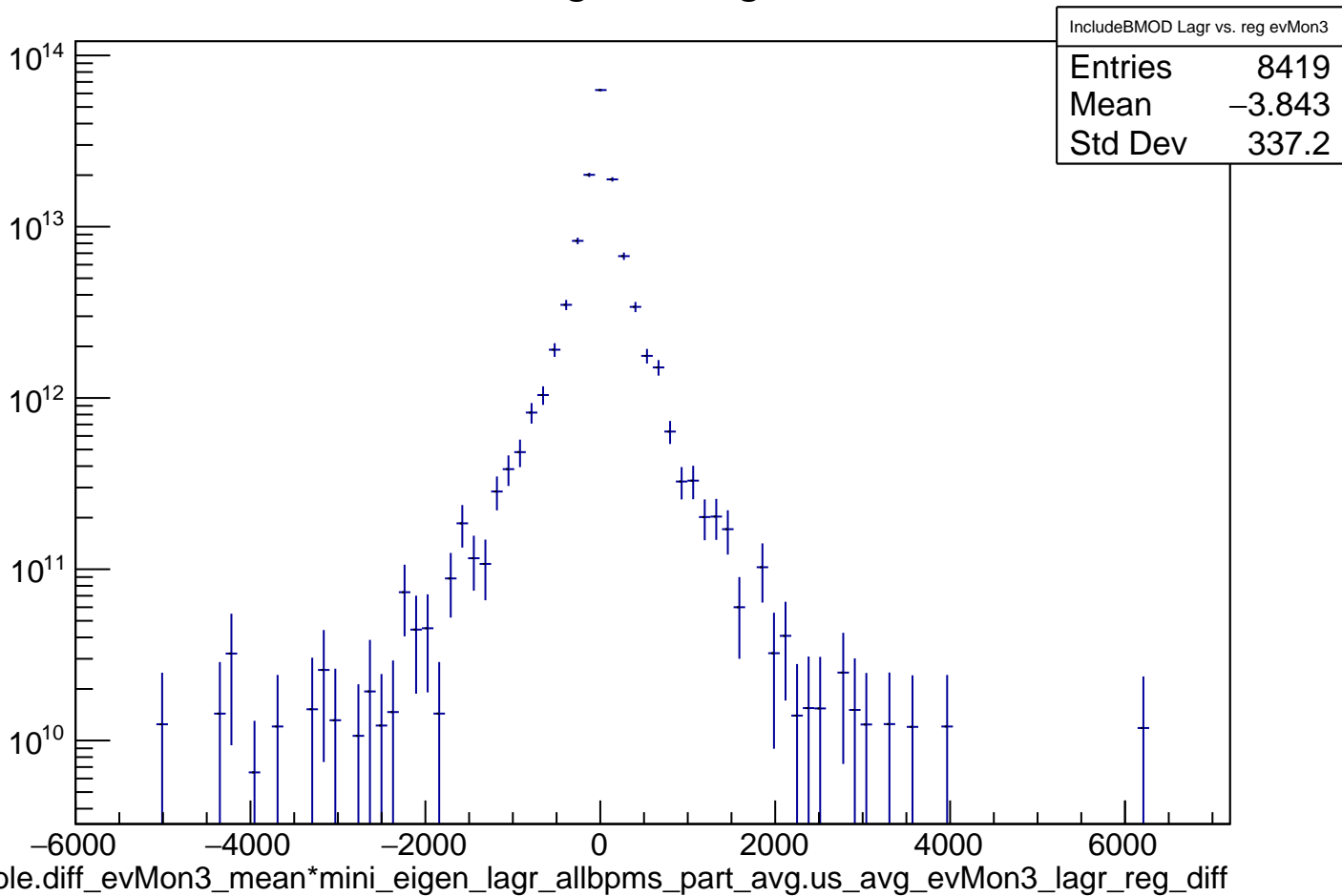
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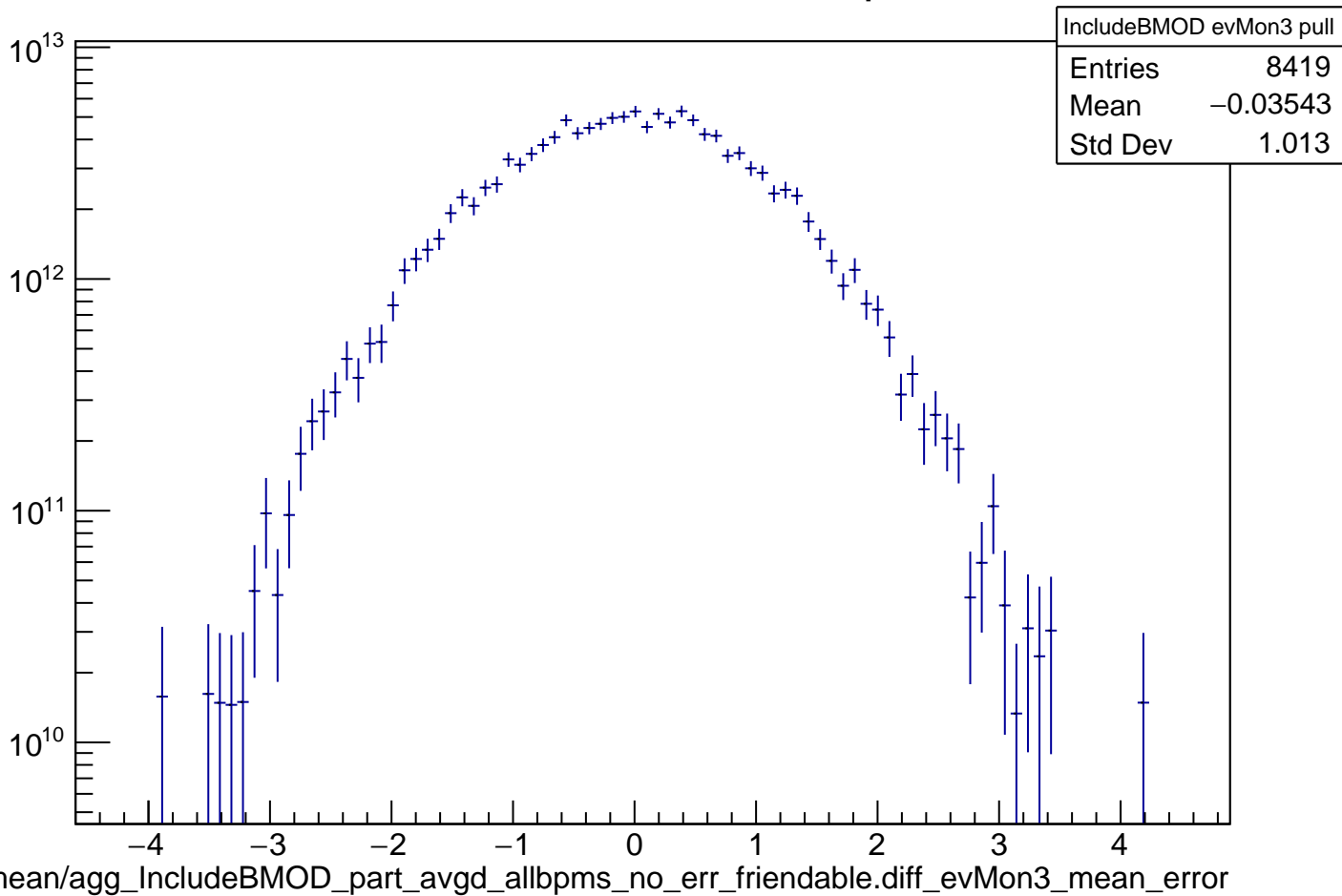
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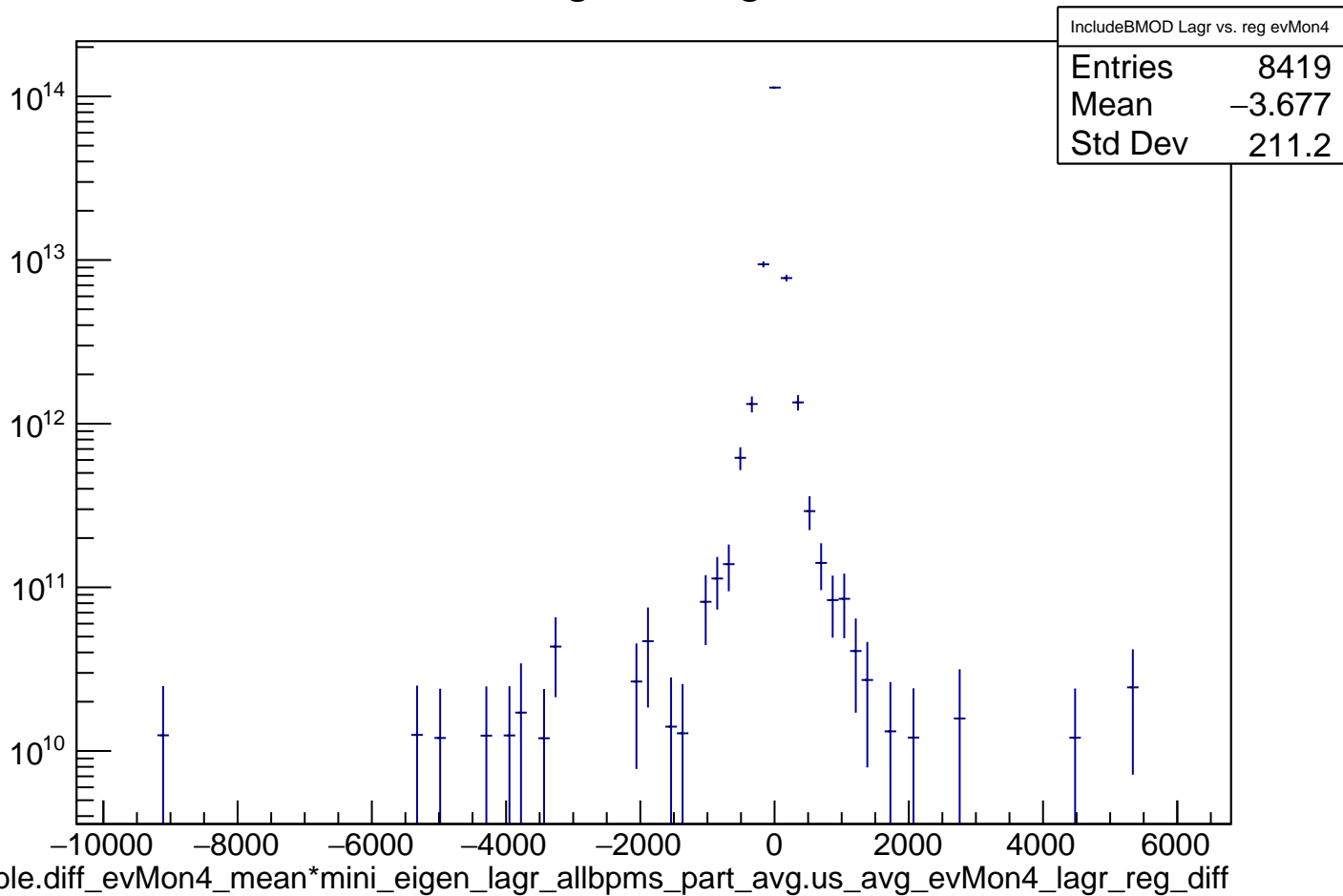
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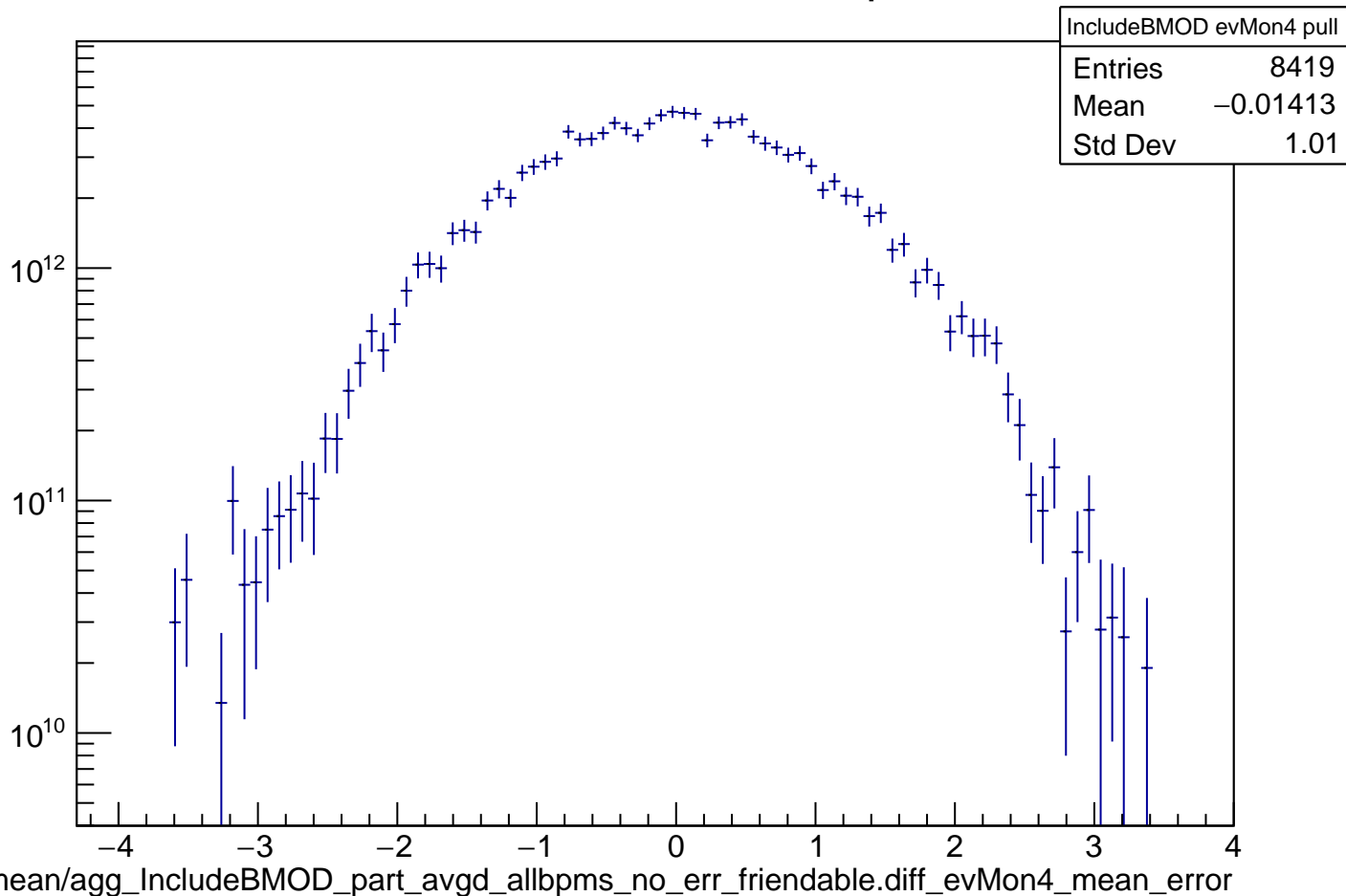
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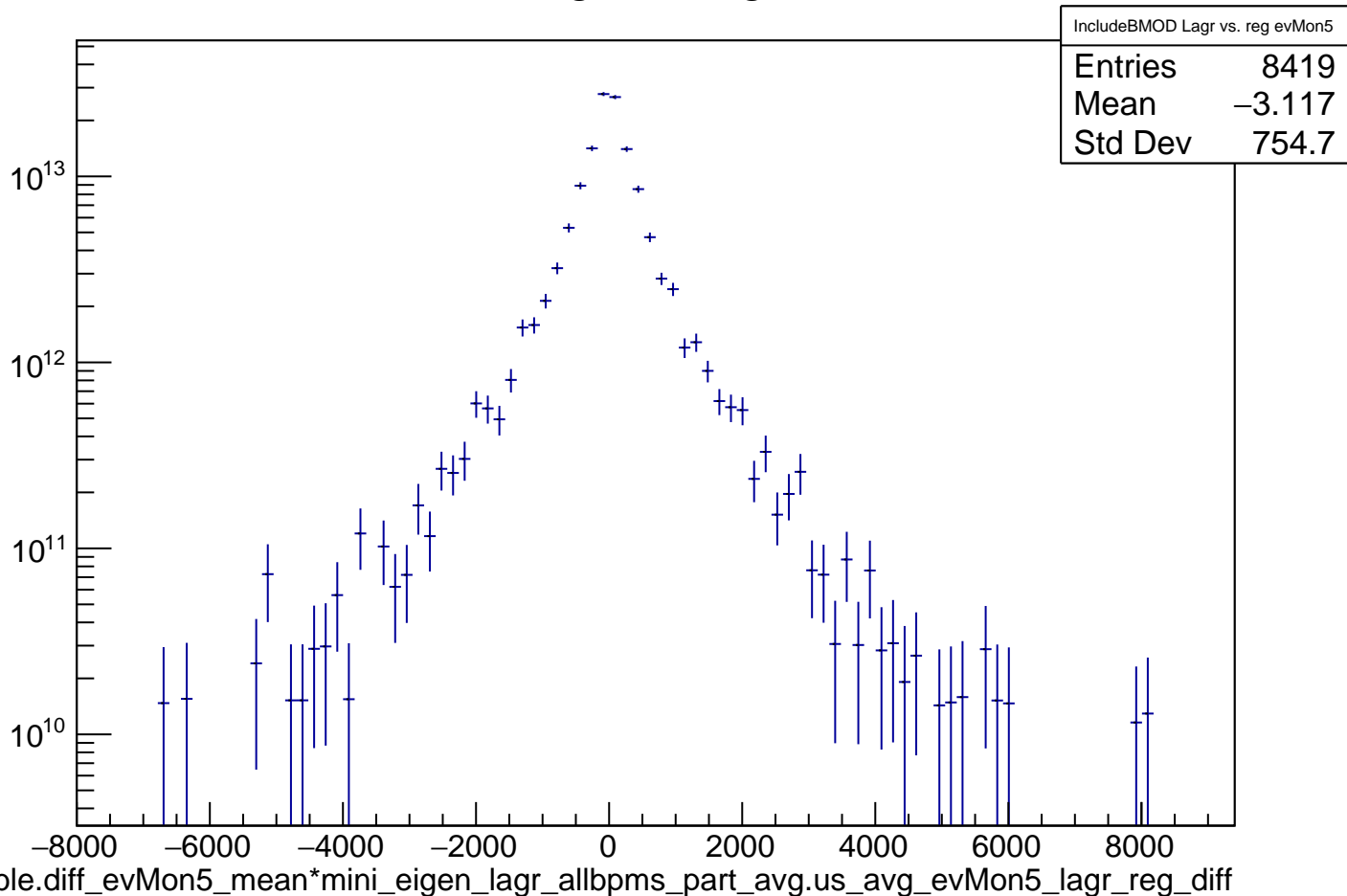
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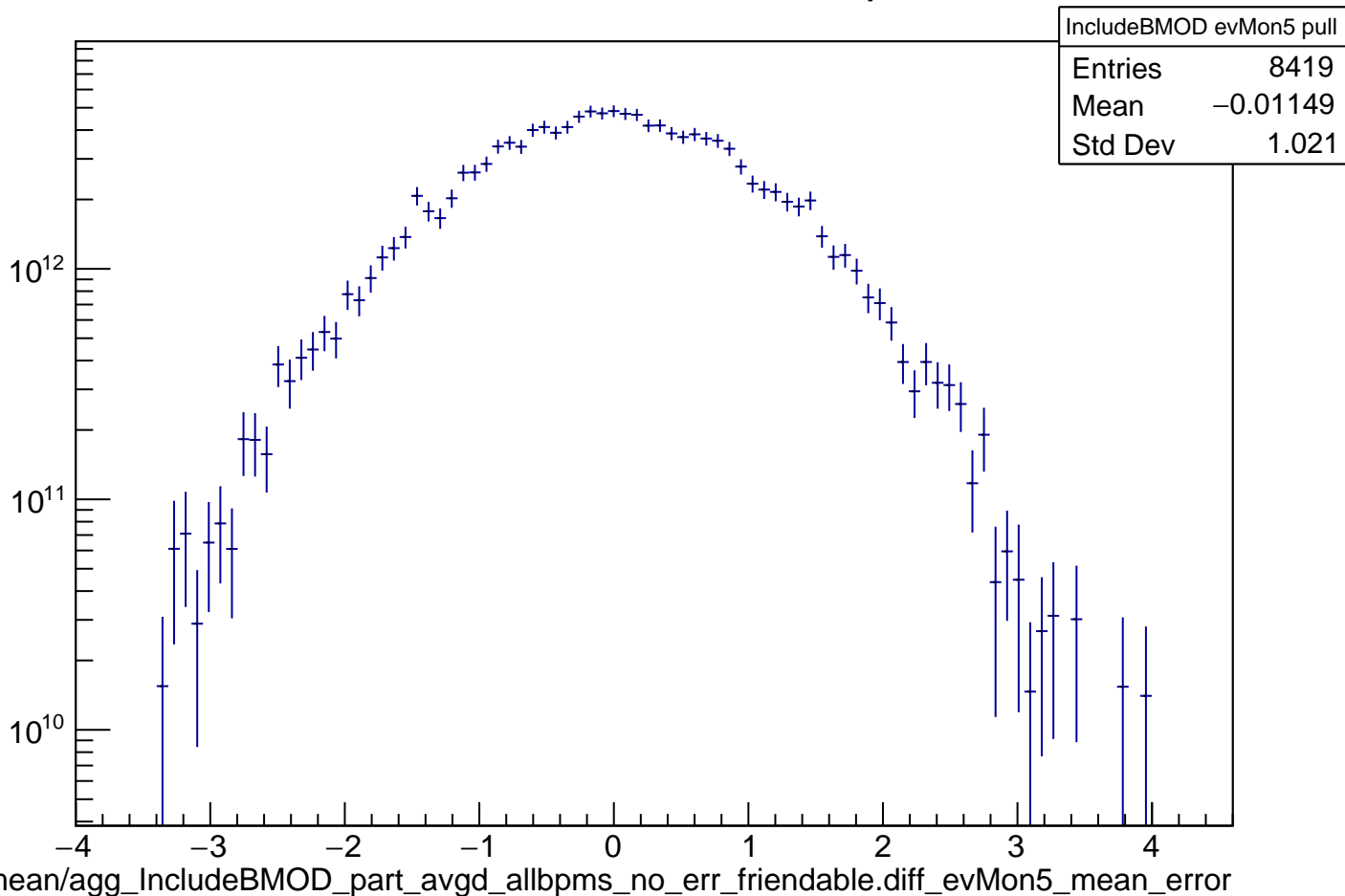
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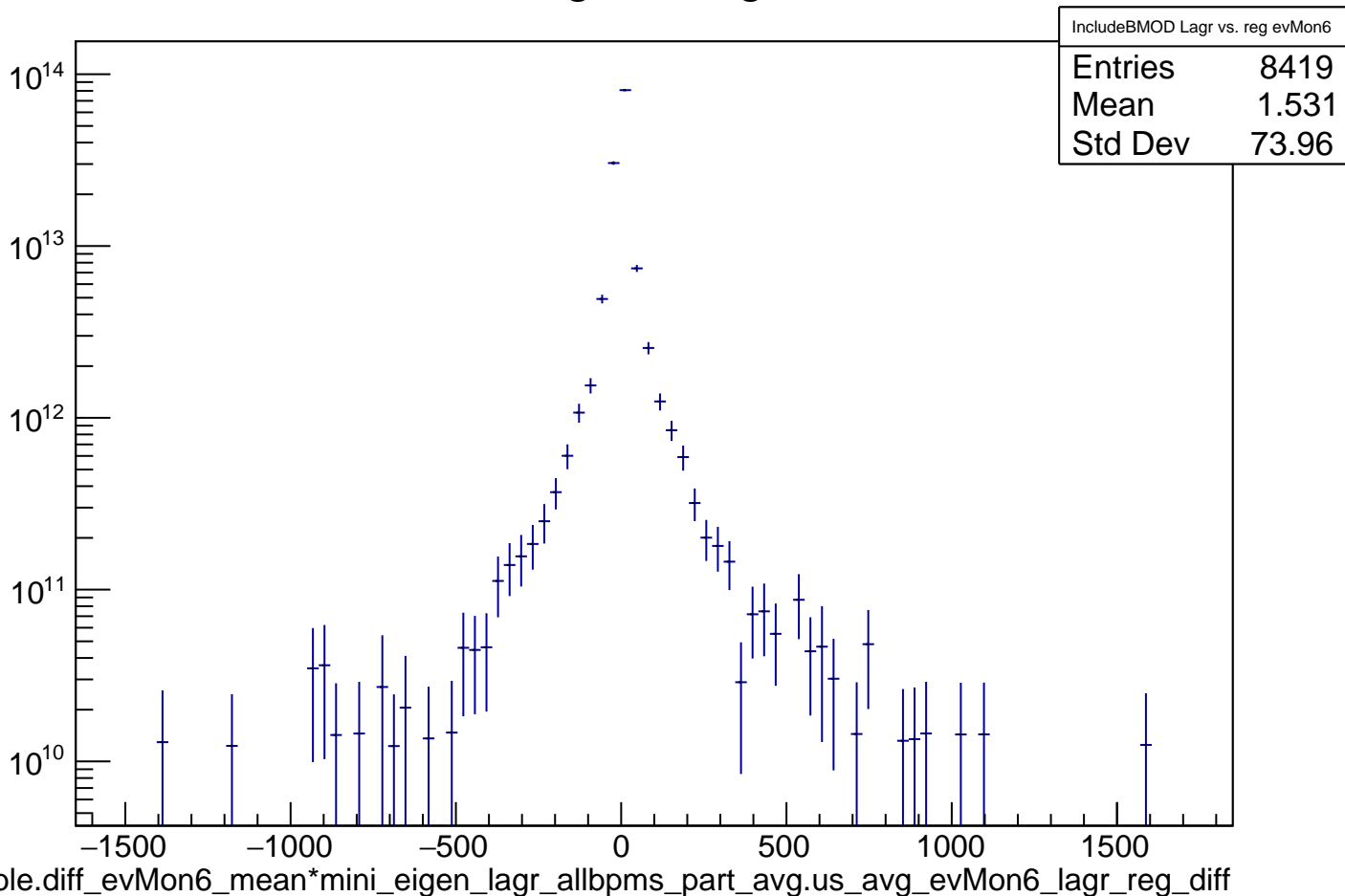
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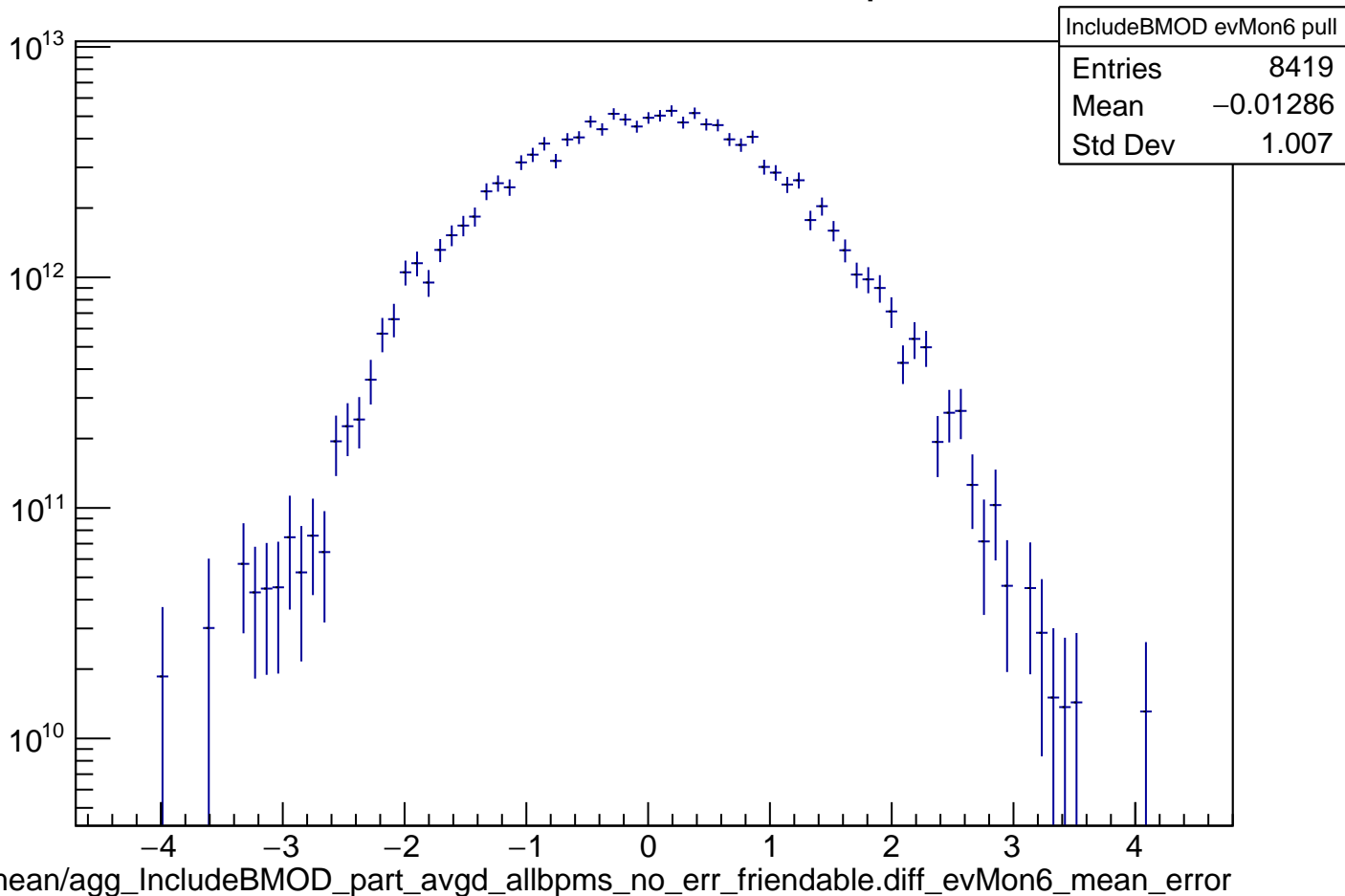
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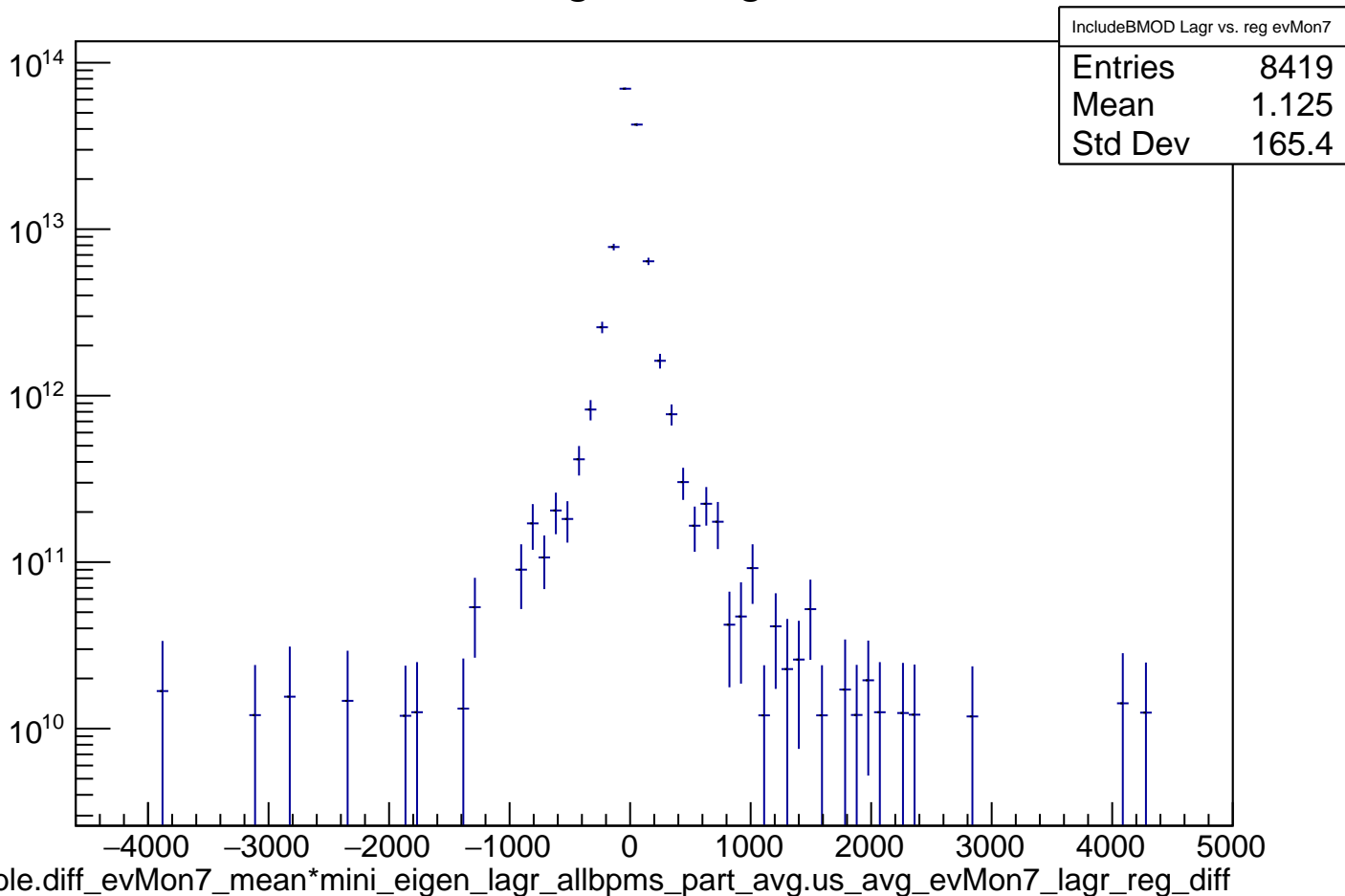
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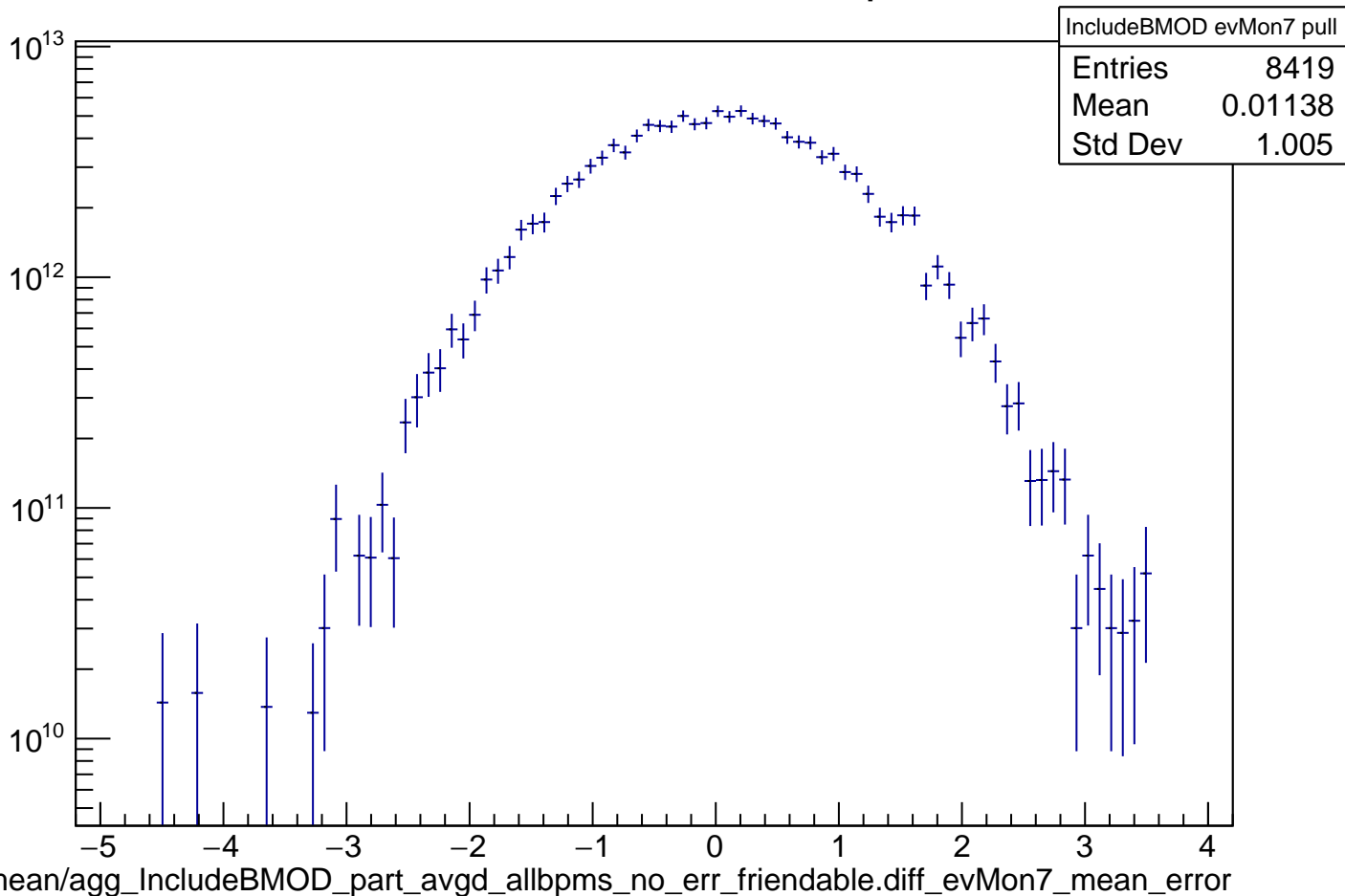
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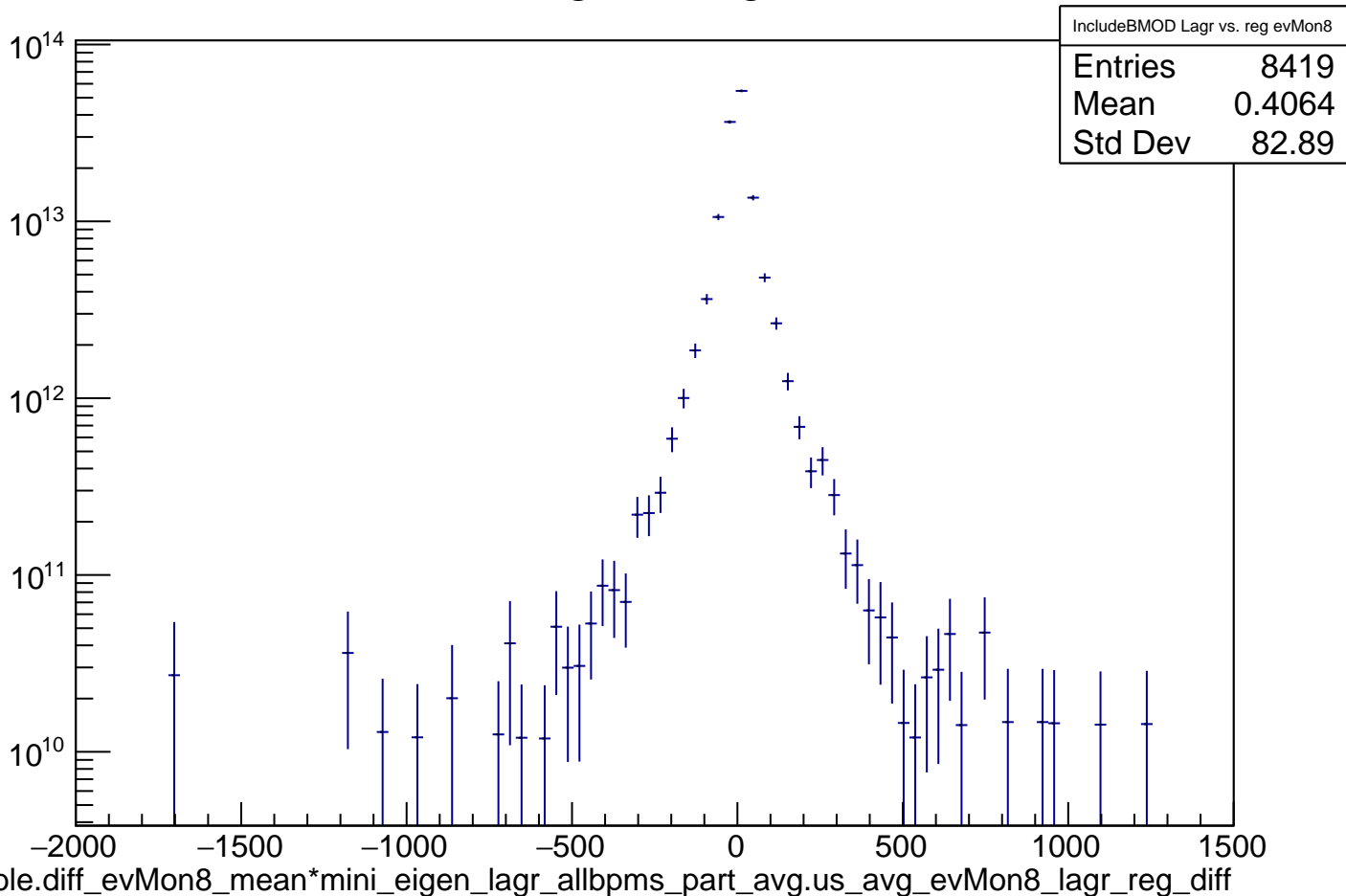
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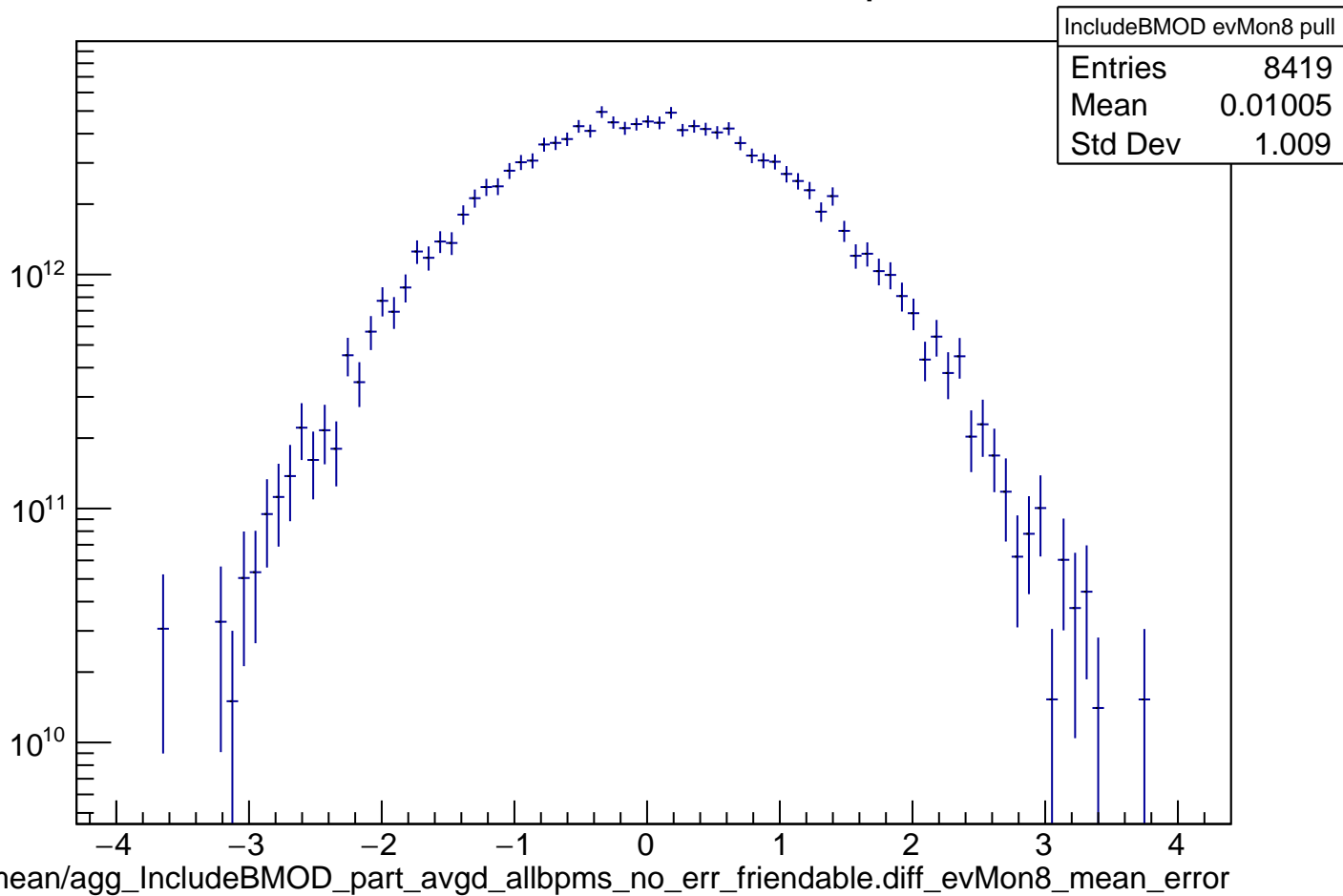
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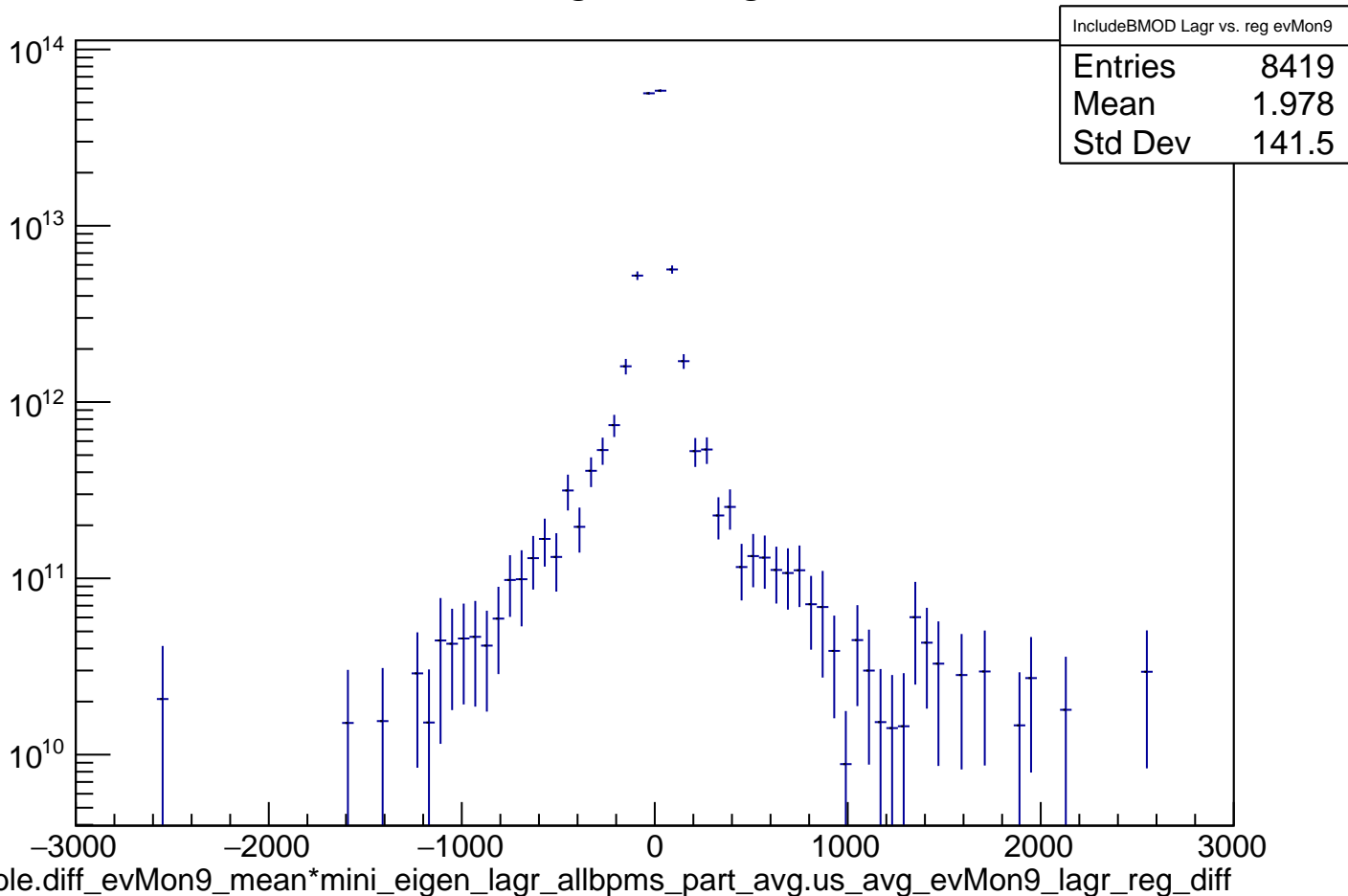
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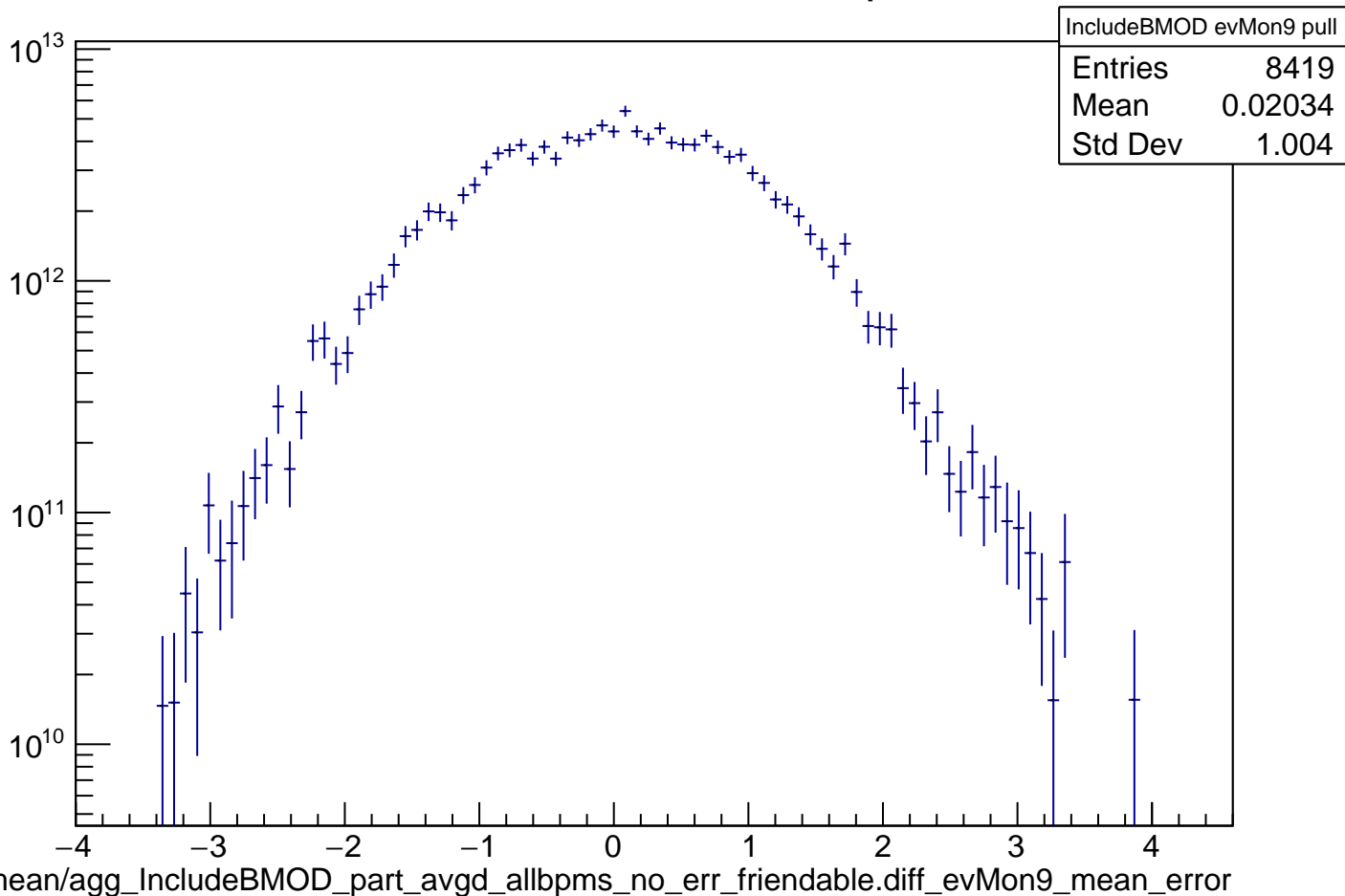
IncludeBMOD evMon8 pull



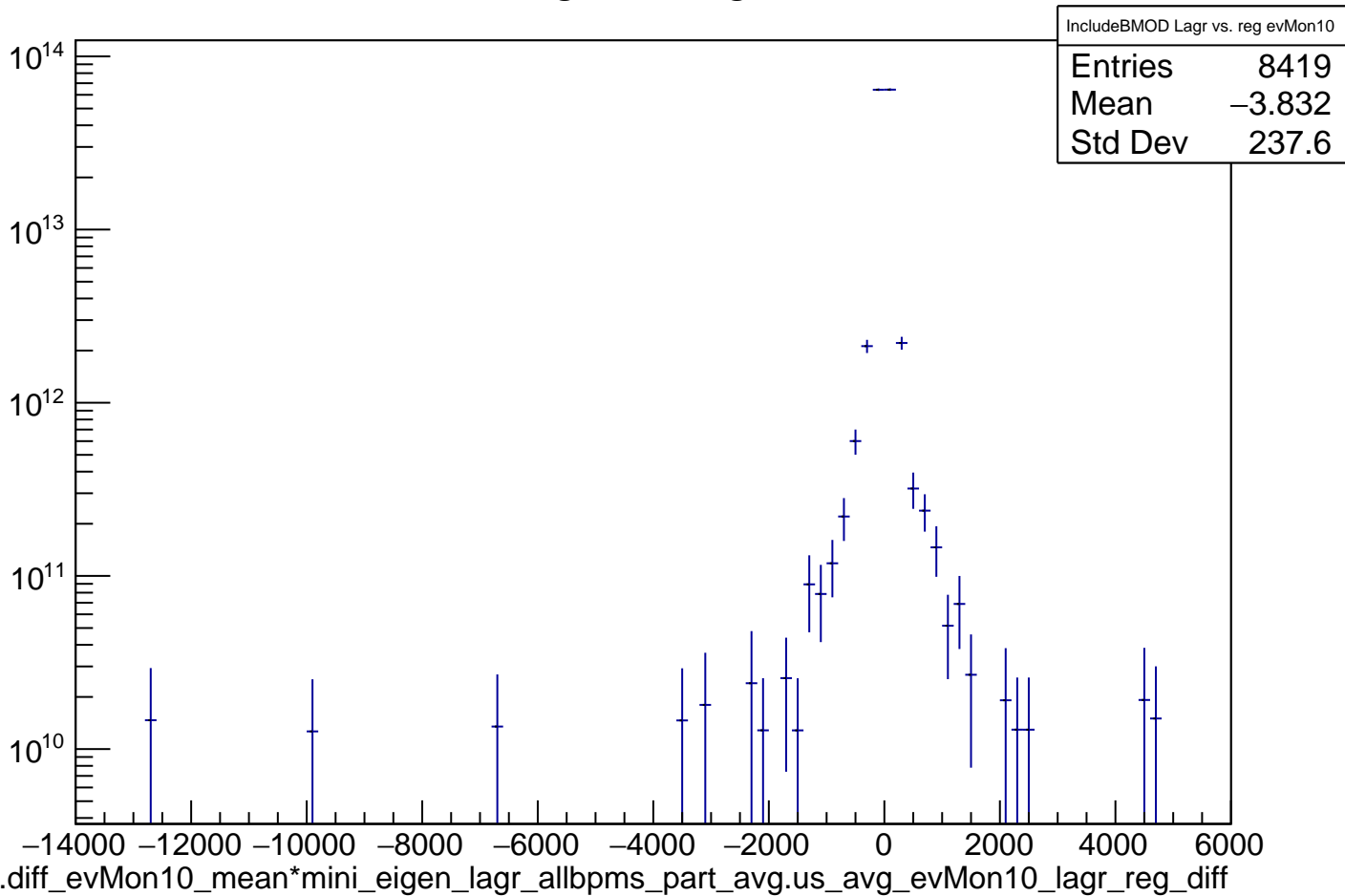
IncludeBMOD Lagr vs. reg evMon9 cor diff



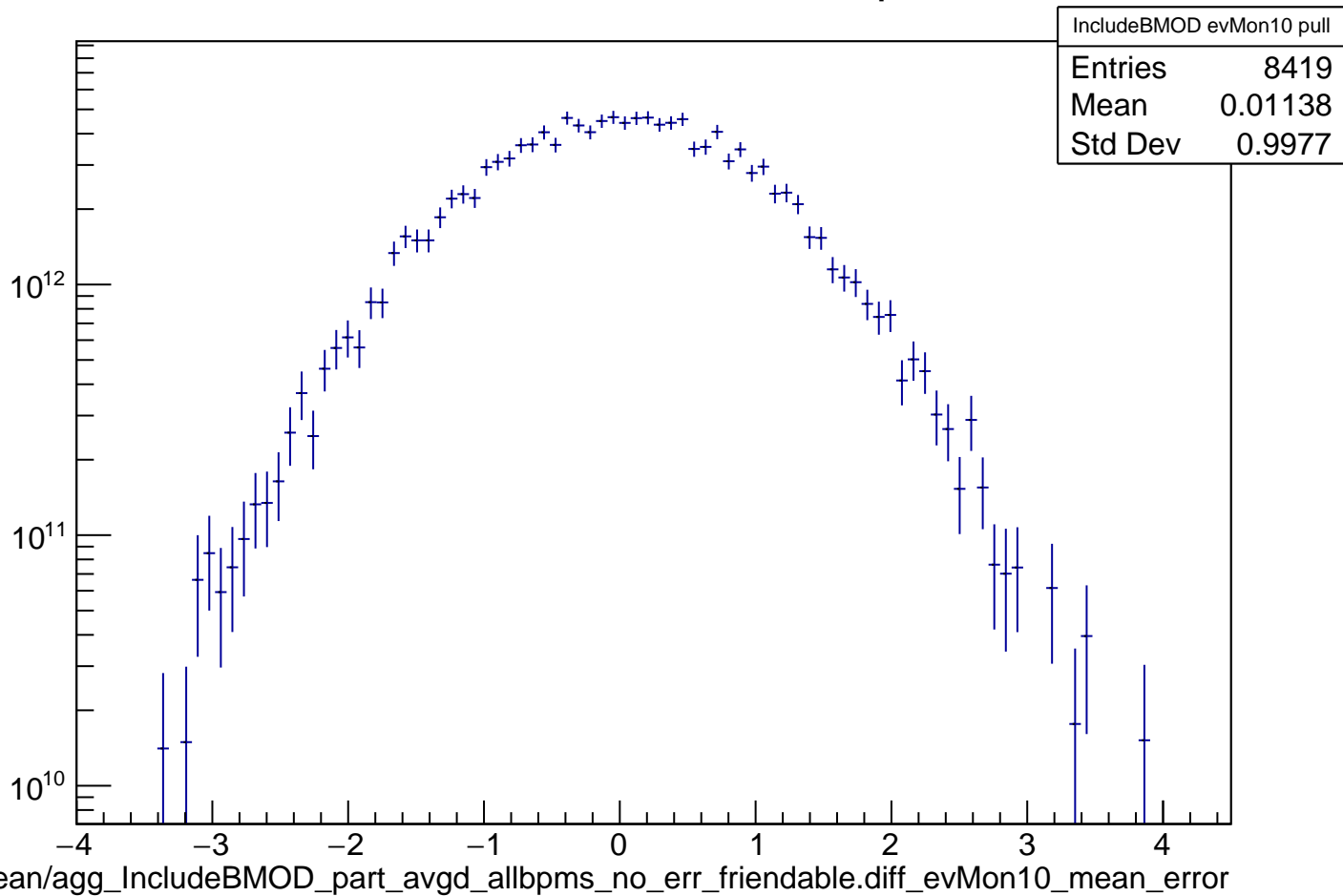
IncludeBMOD evMon9 pull



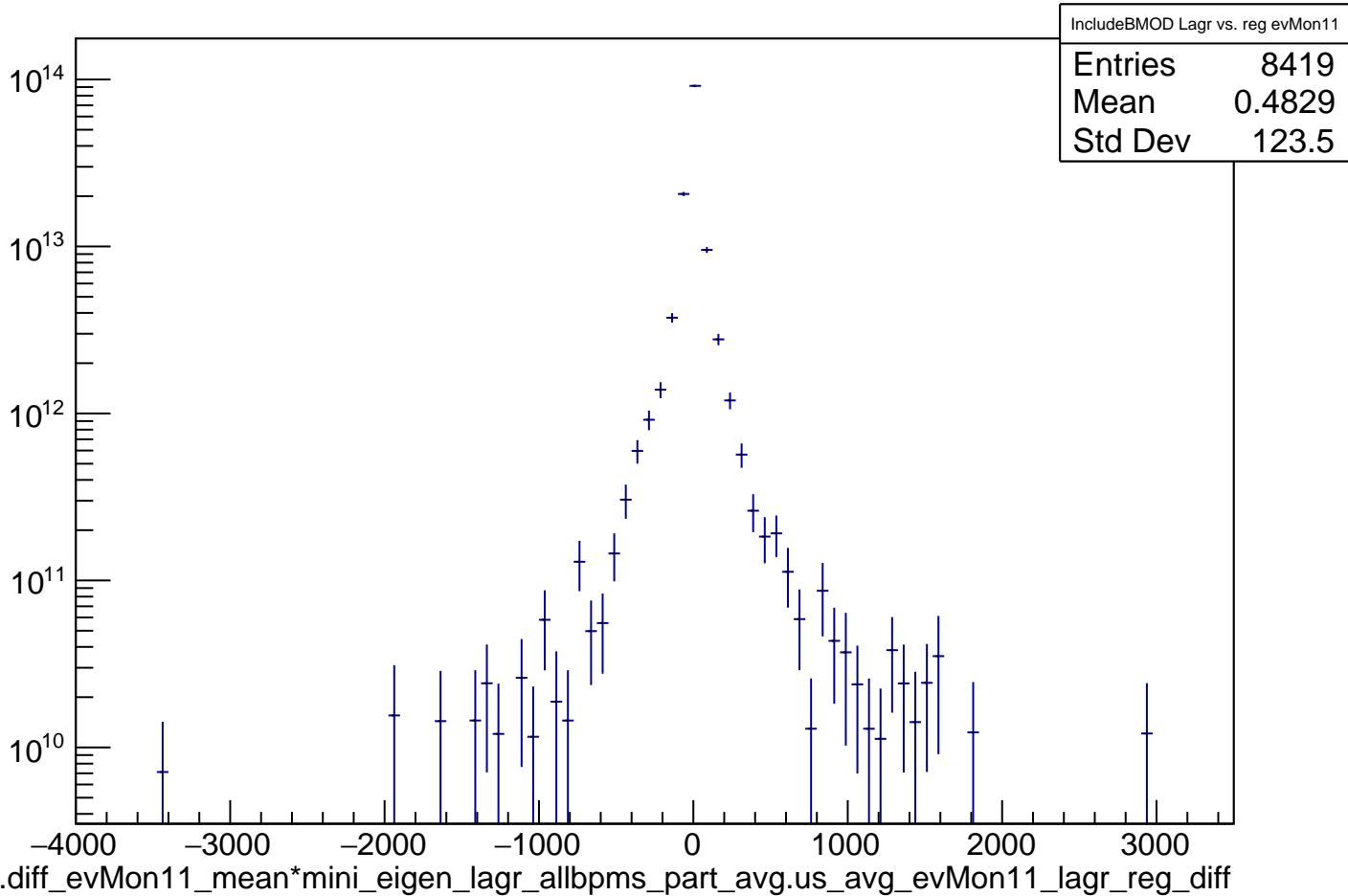
IncludeBMOD Lagr vs. reg evMon10 cor diff



IncludeBMOD evMon10 pull



IncludeBMOD Lagr vs. reg evMon11 cor diff



IncludeBMOD evMon11 pull

