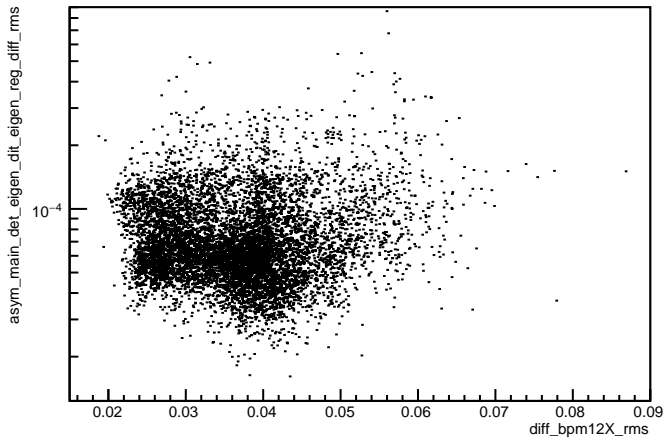
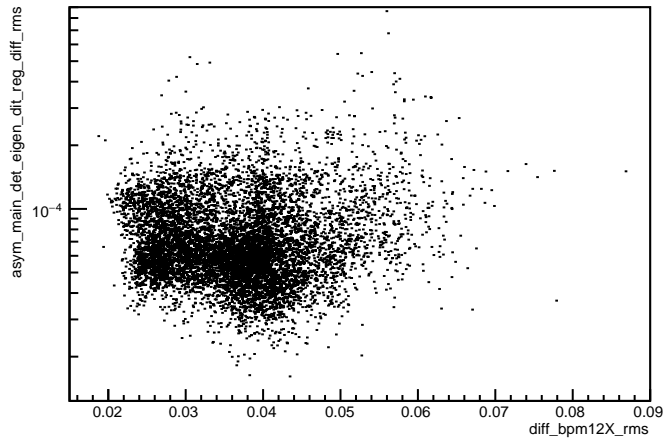


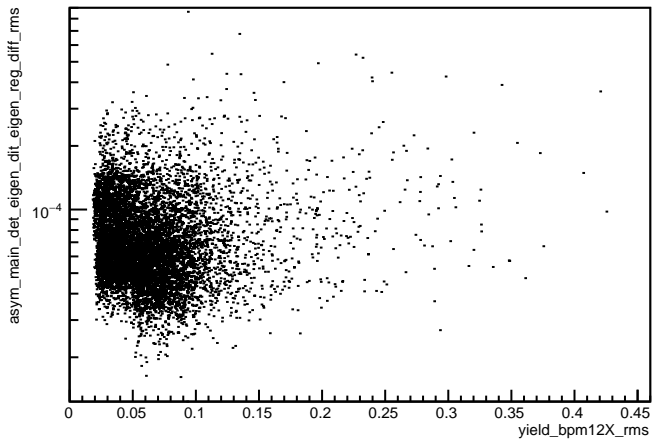
asym\_main\_det\_eigen\_dit\_eigen\_reg\_diff\_rms:diff\_bpm12X\_rms (rocb\_run\_flag=1 && rocb\_run\_type==1 && rocb\_flip\_state<=2)



asym\_main\_det\_eigen\_dit\_reg\_diff\_rms:diff\_bpm12X\_rms (rocb\_run\_flag=1 && rocb\_run\_type==1 && rocb\_flip\_state<=2)



asym\_main\_det\_eigen\_dit\_eigen\_reg\_diff\_rms:yield\_bpm12X\_rms (rocb\_run\_flag=1 && rocb\_run\_type==1 && rocb\_flip\_state<=2)



asym\_main\_det\_eigen\_dit\_reg\_diff\_rms:yield\_bpm12X\_rms (rocb\_run\_flag=1 && rocb\_run\_type==1 && rocb\_flip\_state<=2)

