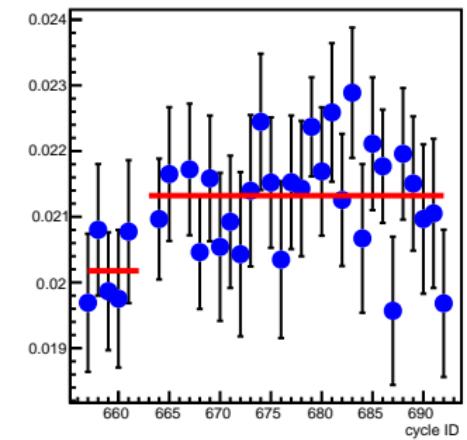
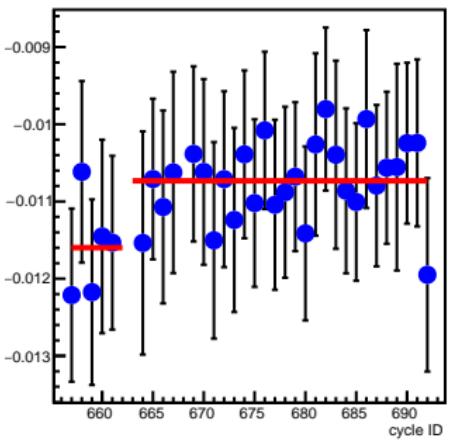


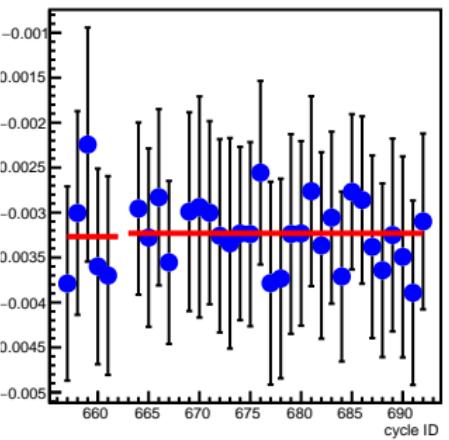
bpm4aX\_coil1(um/count)



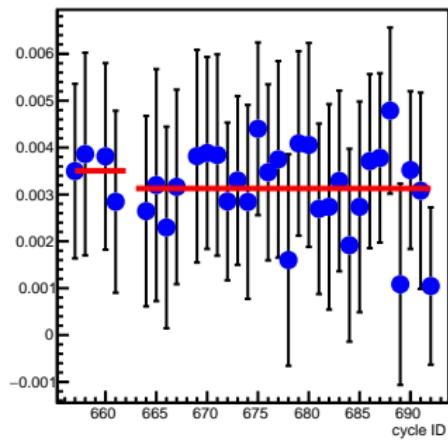
bpm4aX\_coil3(um/count)



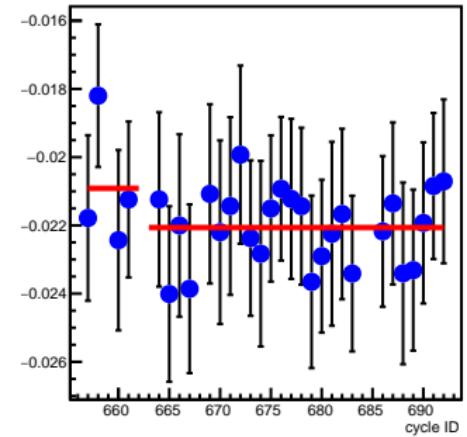
bpm4aX\_coil4(um/count)



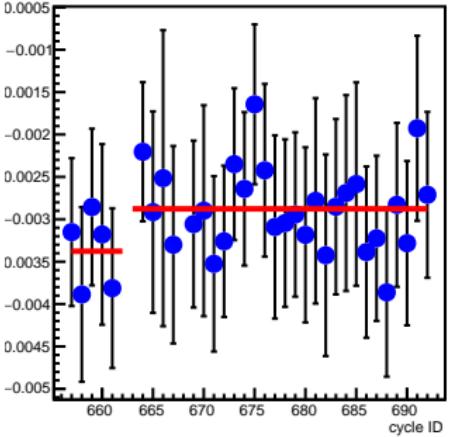
bpm4aX\_coil6(um/count)



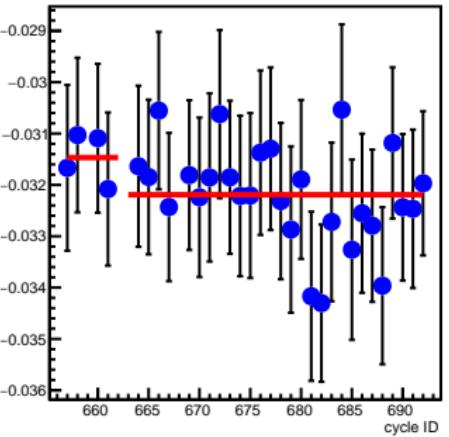
bpm4aX\_coil7(um/count)



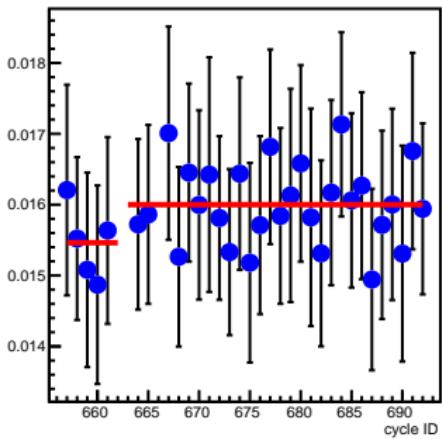
bpm4aX\_coil2(um/count)



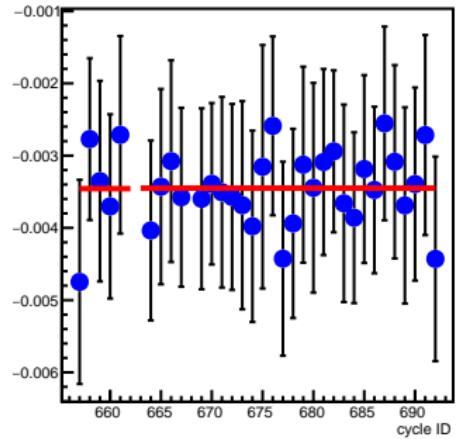
bpm4aX\_coil5(um/count)



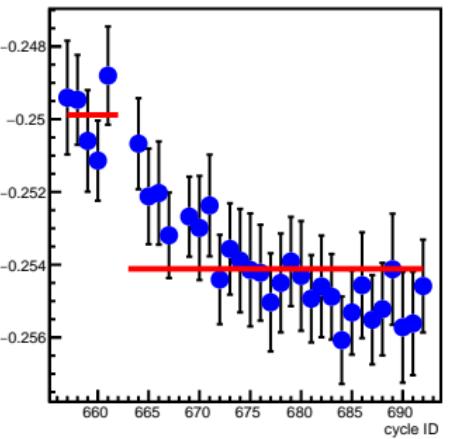
bpm4aY\_coil1(um/count)



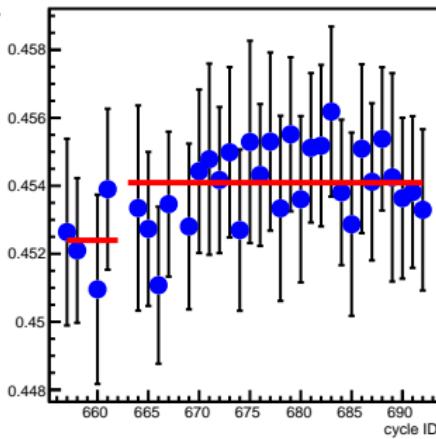
bpm4aY\_coil3(um/count)



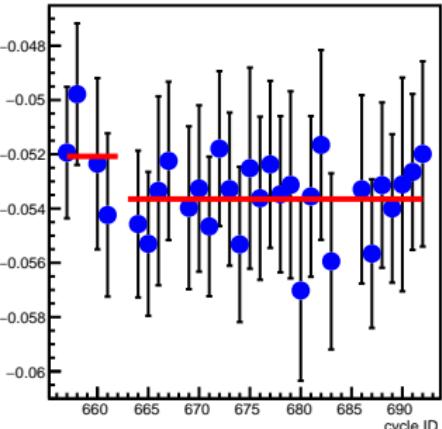
bpm4aY\_coil4(um/count)



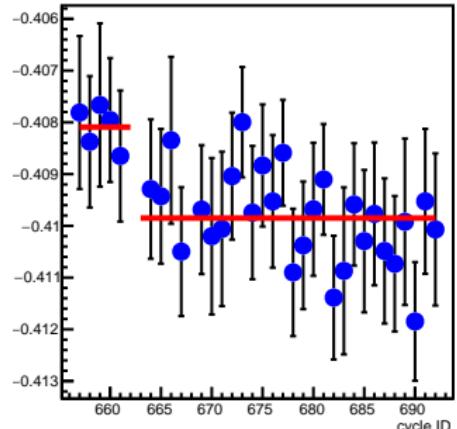
bpm4aY\_coil6(um/count)



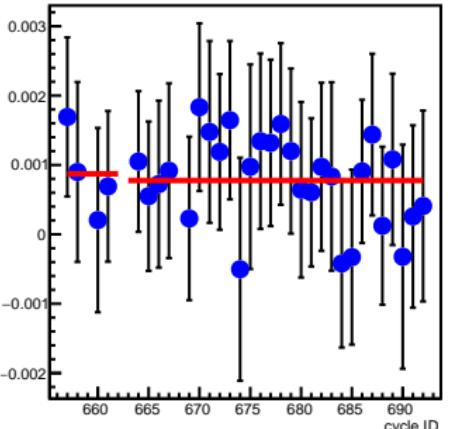
bpm4aY\_coil7(um/count)



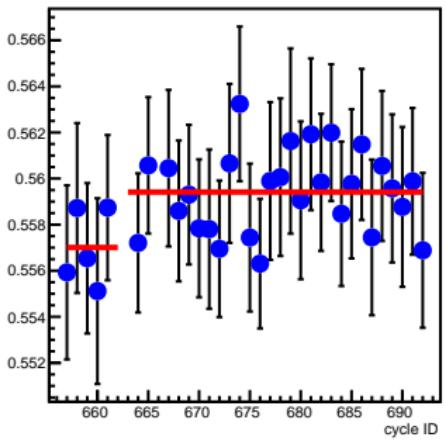
bpm4aY\_coil2(um/count)



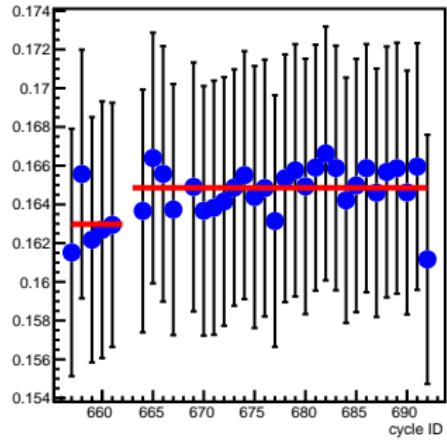
bpm4aY\_coil5(um/count)



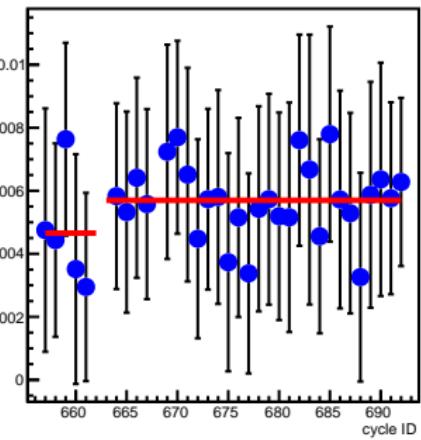
bpm4eX\_coil1(um/count)



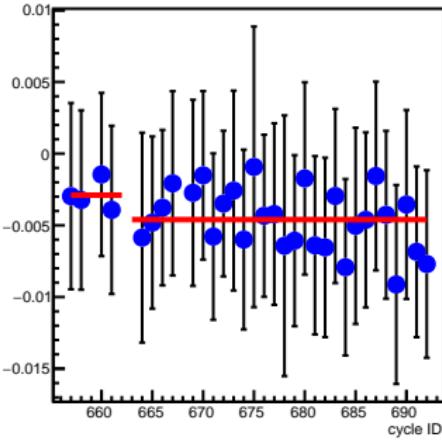
bpm4eX\_coil3(um/count)



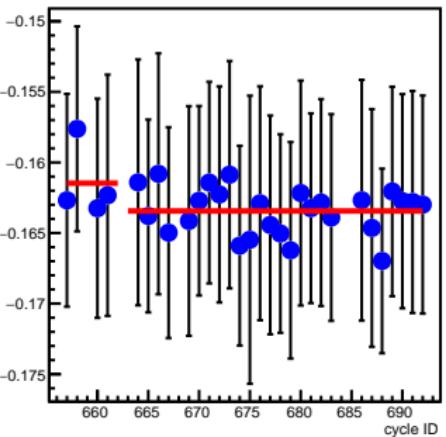
bpm4eX\_coil4(um/count)



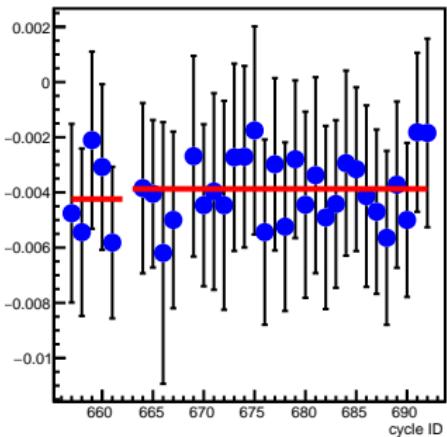
bpm4eX\_coil6(um/count)



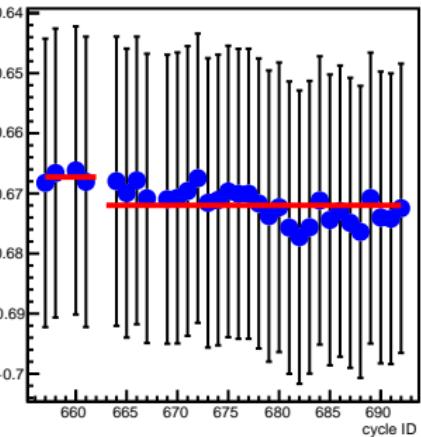
bpm4eX\_coil7(um/count)



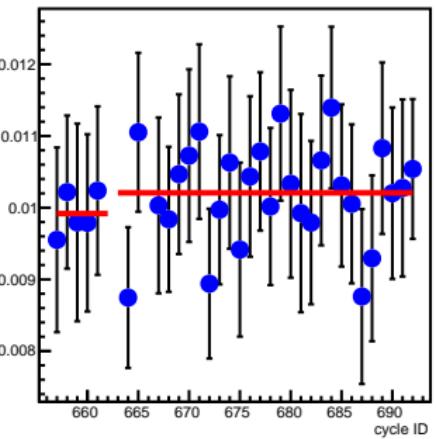
bpm4eX\_coil2(um/count)



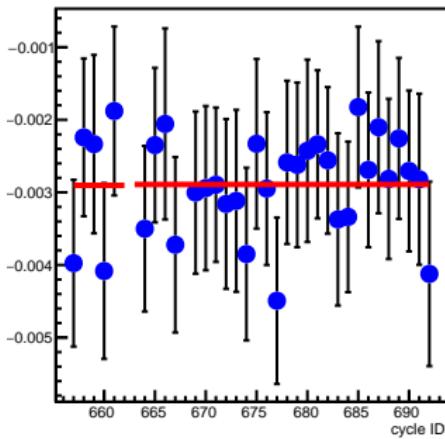
bpm4eX\_coil5(um/count)



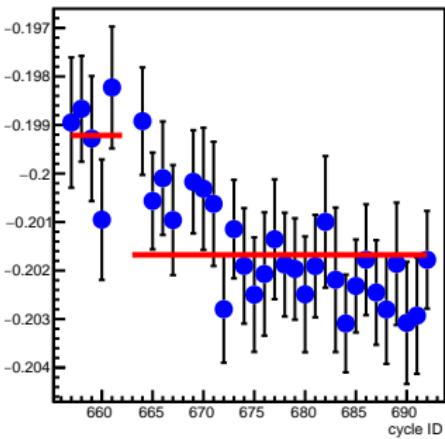
bpm4eY\_coil1(um/count)



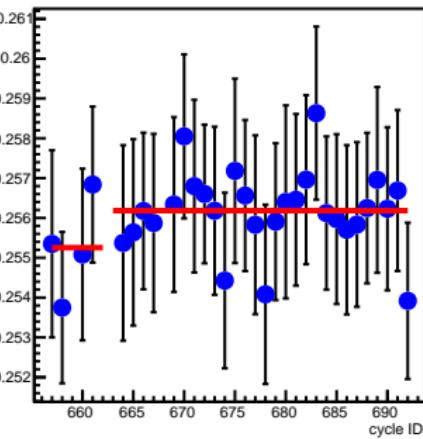
bpm4eY\_coil3(um/count)



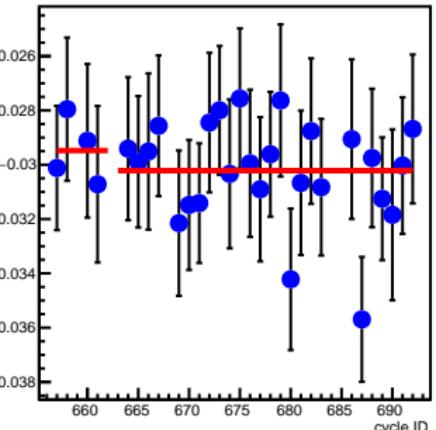
bpm4eY\_coil4(um/count)



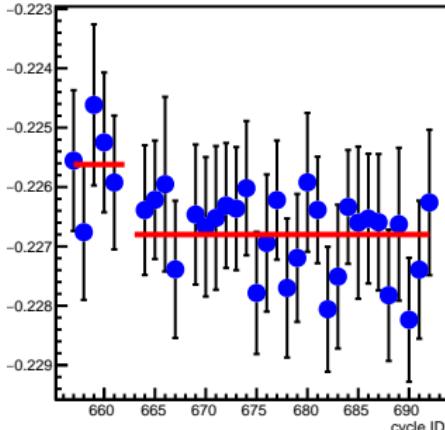
bpm4eY\_coil6(um/count)



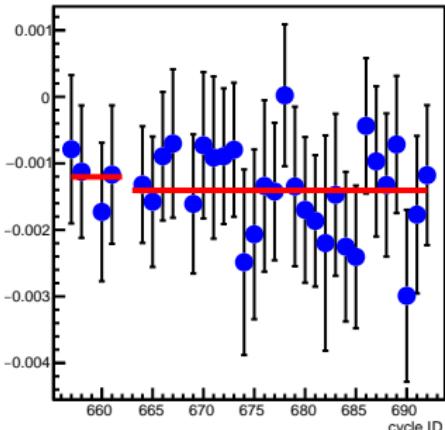
bpm4eY\_coil7(um/count)



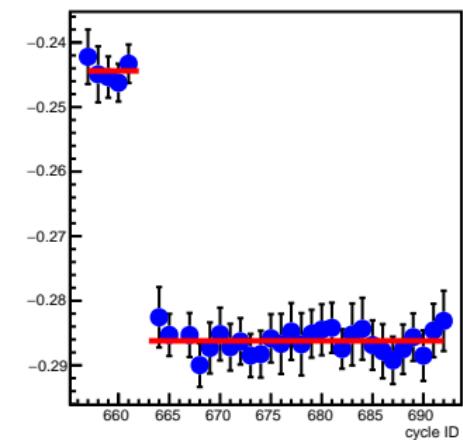
bpm4eY\_coil2(um/count)



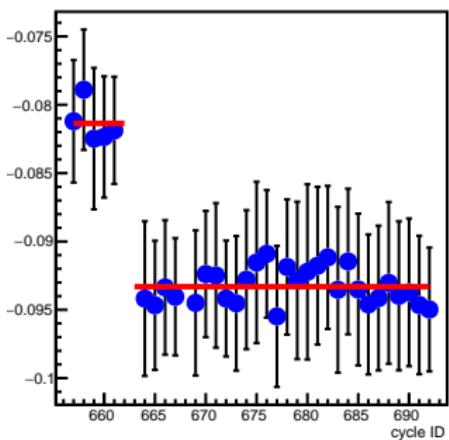
bpm4eY\_coil5(um/count)



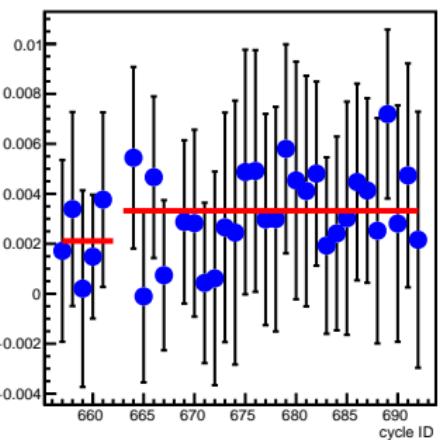
bpm11X12X\_coil1(um/count)



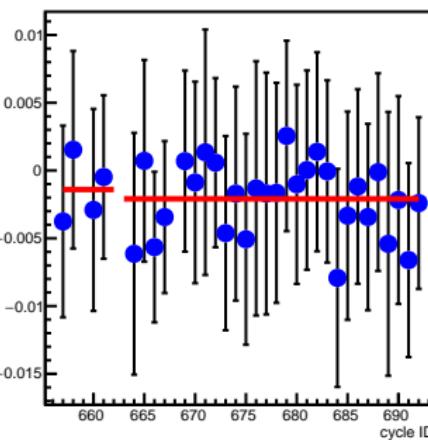
bpm11X12X\_coil3(um/count)



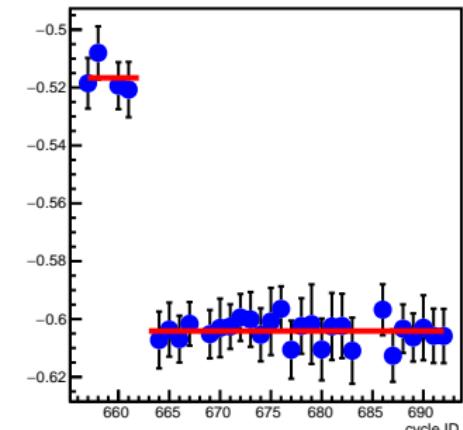
bpm11X12X\_coil4(um/count)



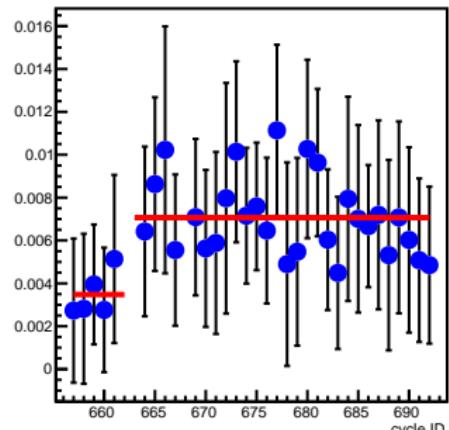
bpm11X12X\_coil6(um/count)



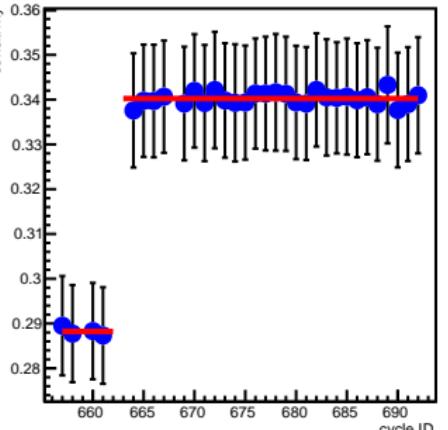
bpm11X12X\_coil7(um/count)



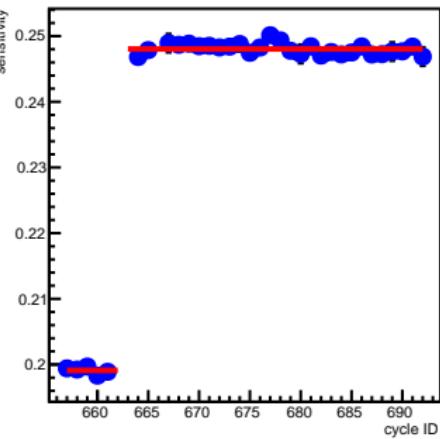
bpm11X12X\_coil2(um/count)



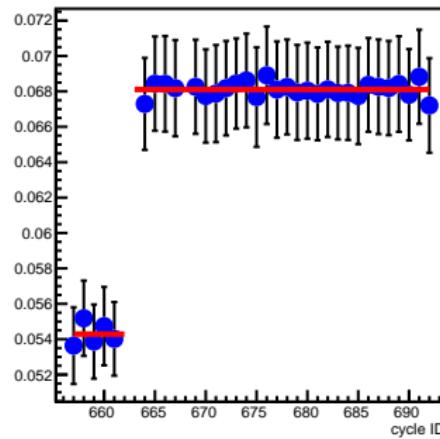
bpm11X12X\_coil5(um/count)



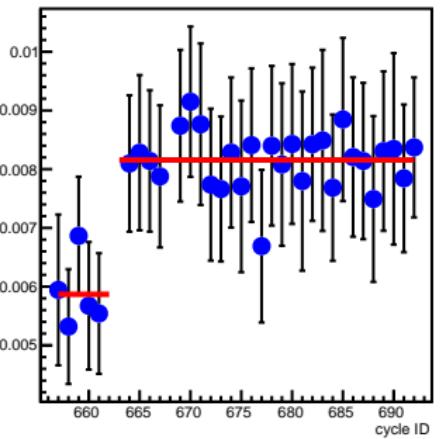
bpm16X\_coil1(um/count)



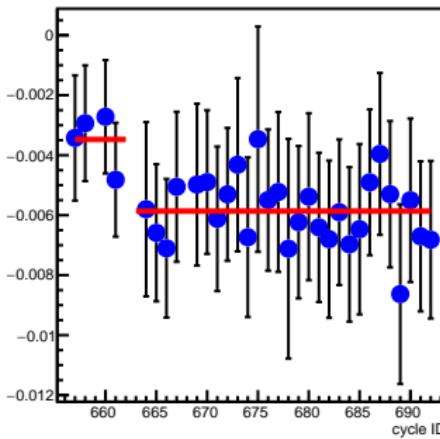
bpm16X\_coil3(um/count)



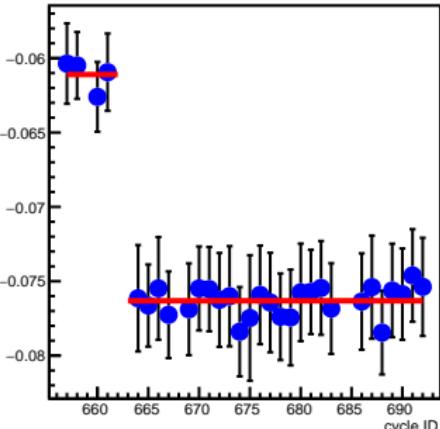
bpm16X\_coil4(um/count)



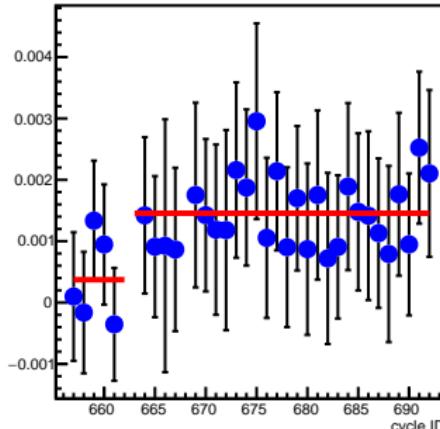
bpm16X\_coil6(um/count)



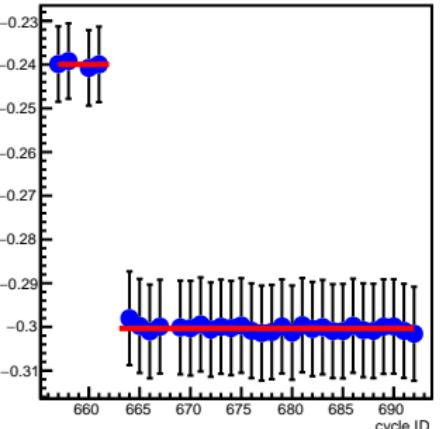
bpm16X\_coil7(um/count)



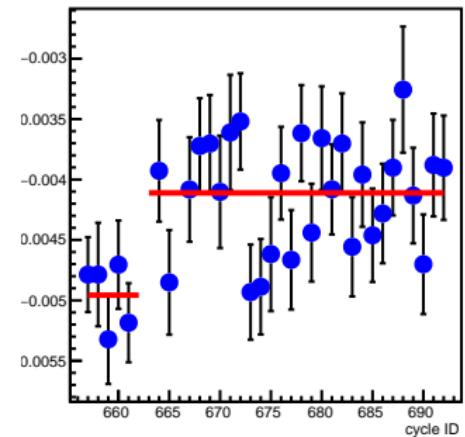
bpm16X\_coil2(um/count)



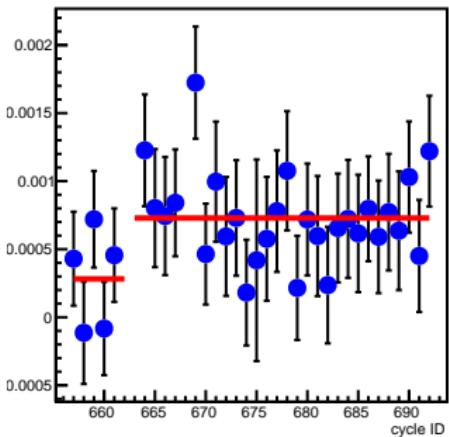
bpm16X\_coil5(um/count)



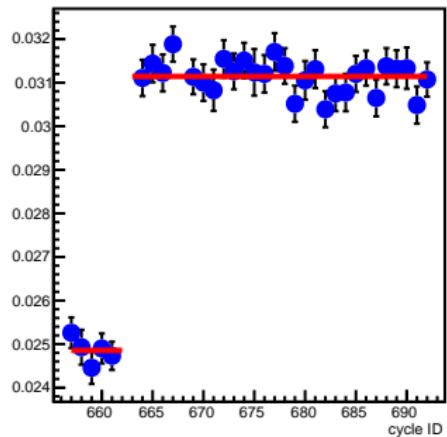
bpm16Y\_coil1(um/count)



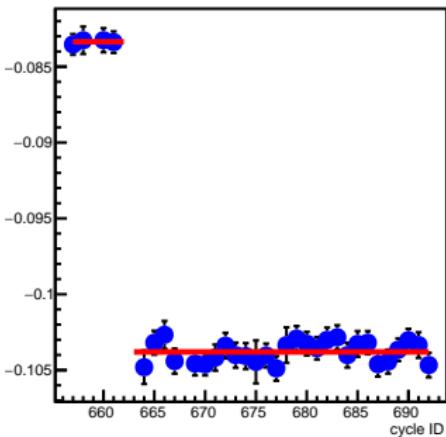
bpm16Y\_coil3(um/count)



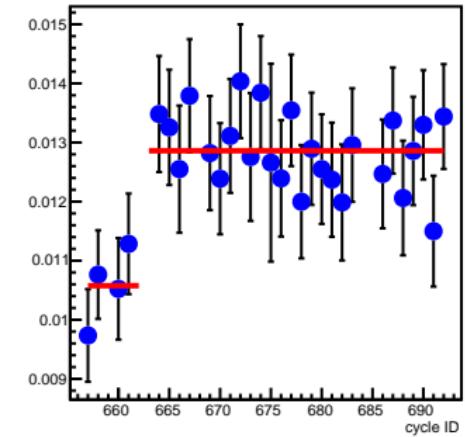
bpm16Y\_coil4(um/count)



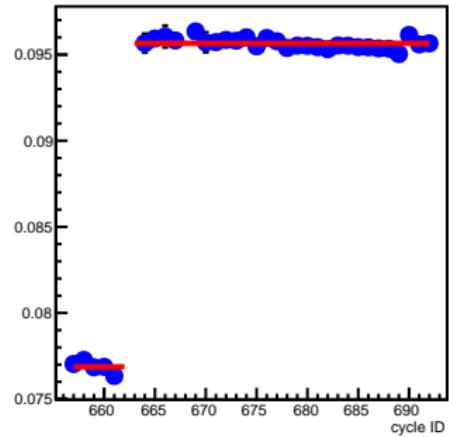
bpm16Y\_coil6(um/count)



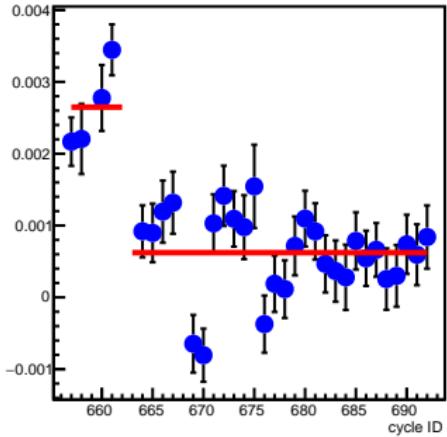
bpm16Y\_coil7(um/count)

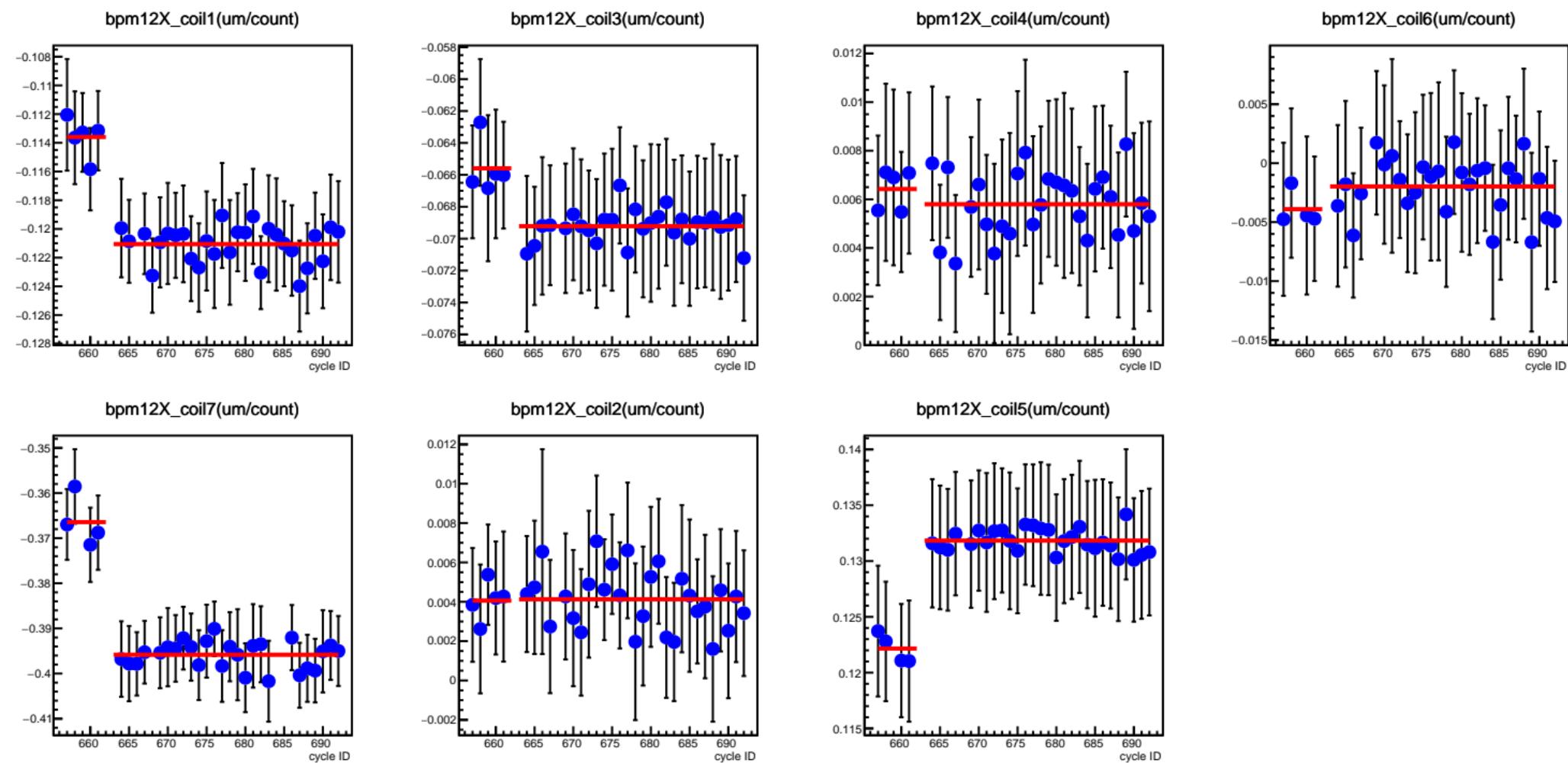


bpm16Y\_coil2(um/count)

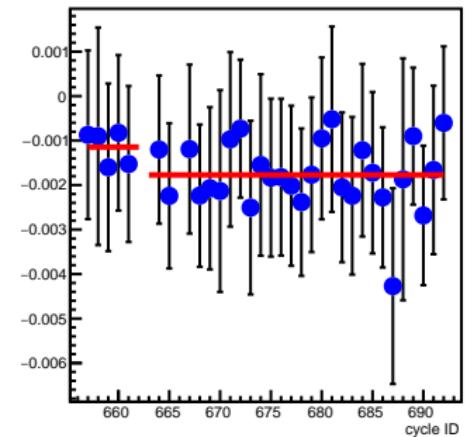


bpm16Y\_coil5(um/count)

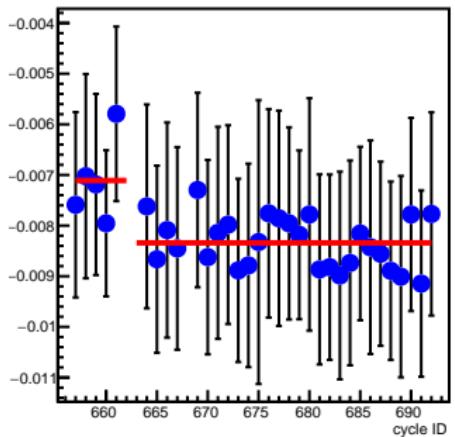




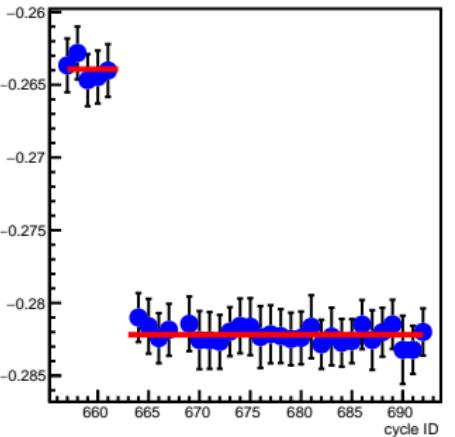
bpm12Y\_coil1(um/count)



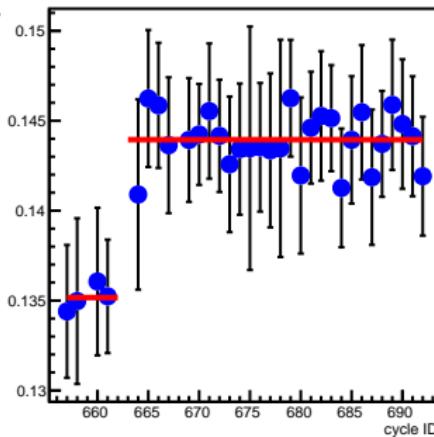
bpm12Y\_coil3(um/count)



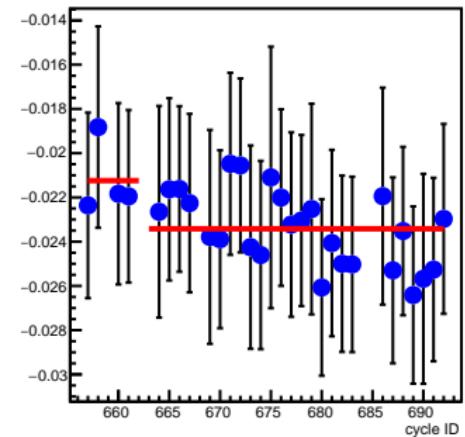
bpm12Y\_coil4(um/count)



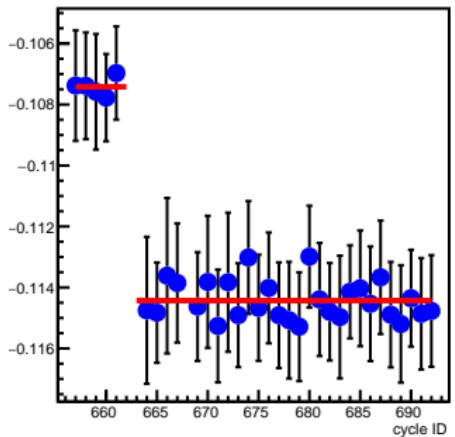
bpm12Y\_coil6(um/count)



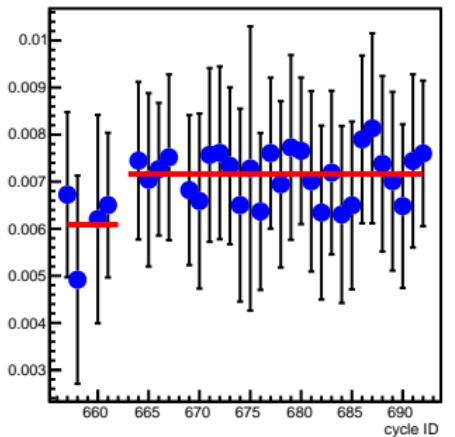
bpm12Y\_coil7(um/count)



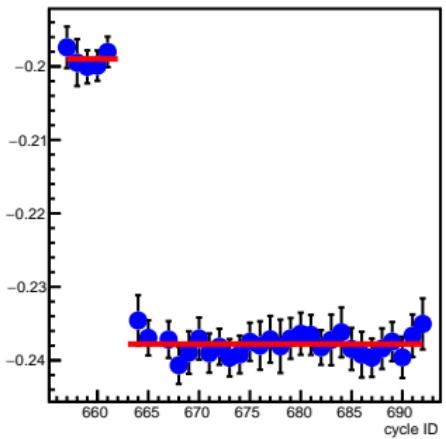
bpm12Y\_coil2(um/count)



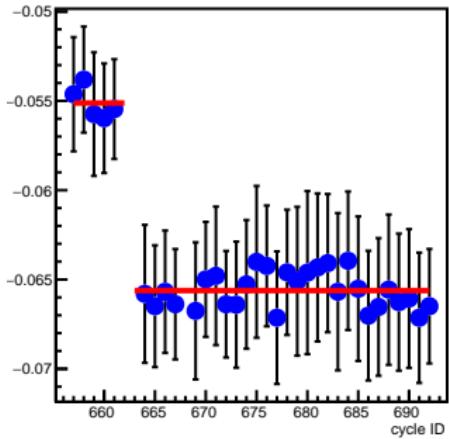
bpm12Y\_coil5(um/count)



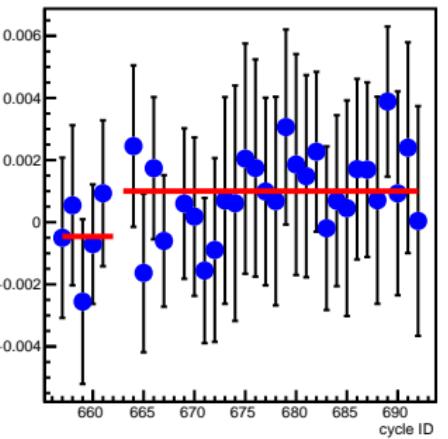
bpm11X\_coil1(um/count)



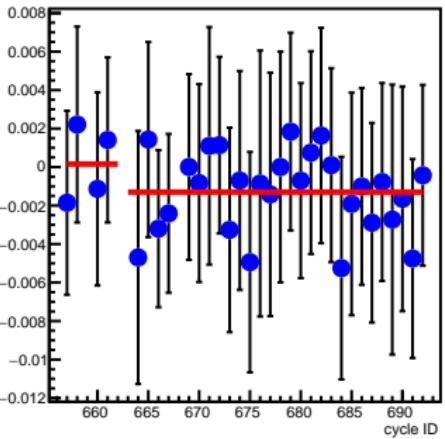
bpm11X\_coil3(um/count)



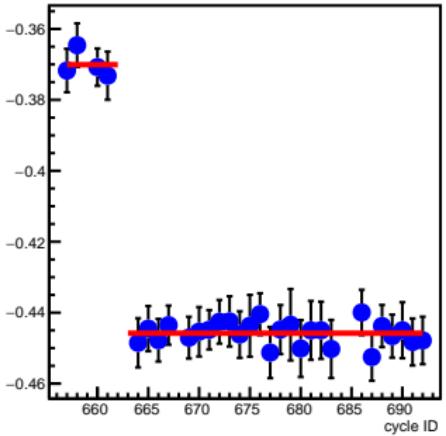
bpm11X\_coil4(um/count)



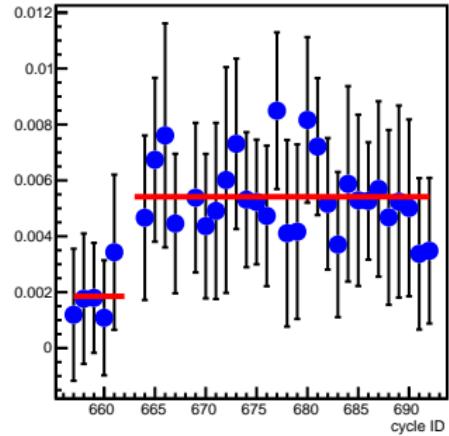
bpm11X\_coil6(um/count)



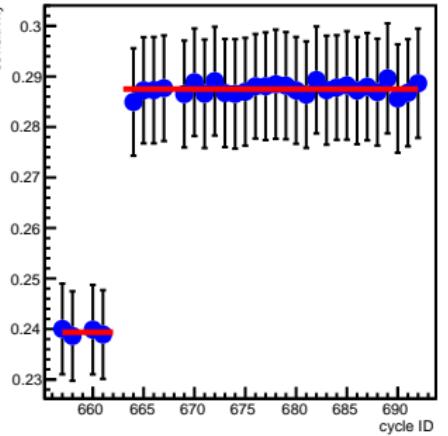
bpm11X\_coil7(um/count)



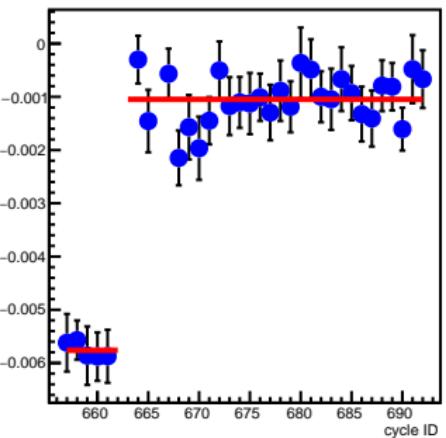
bpm11X\_coil2(um/count)



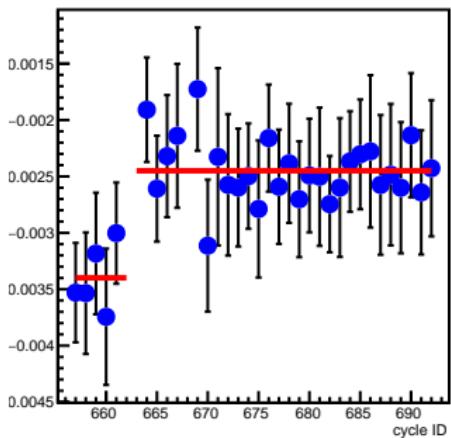
bpm11X\_coil5(um/count)



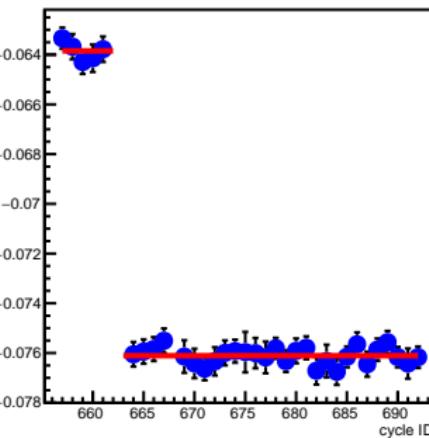
bpm11Y\_coil1(um/count)



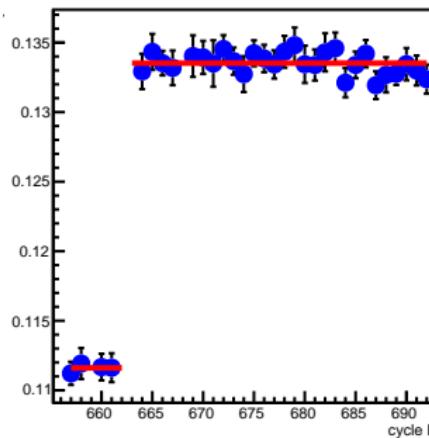
bpm11Y\_coil3(um/count)



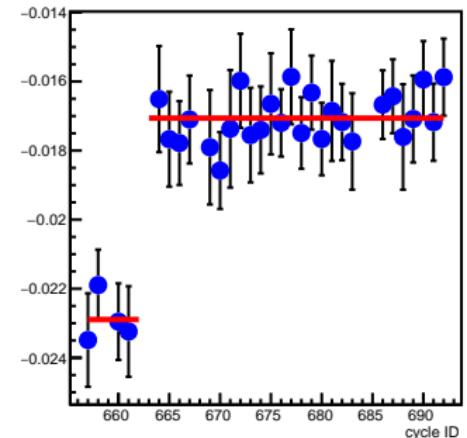
bpm11Y\_coil4(um/count)



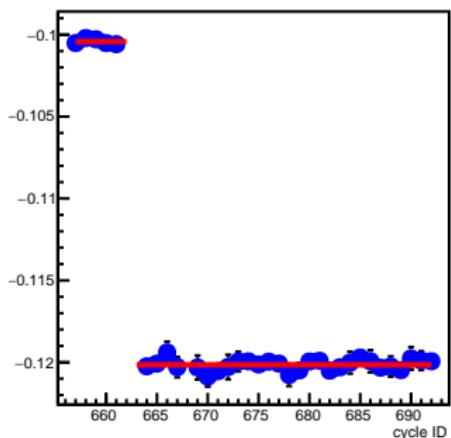
bpm11Y\_coil6(um/count)



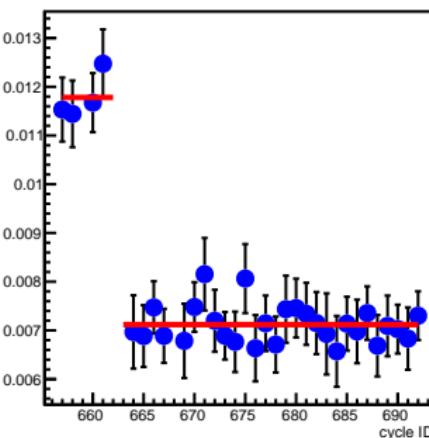
bpm11Y\_coil7(um/count)



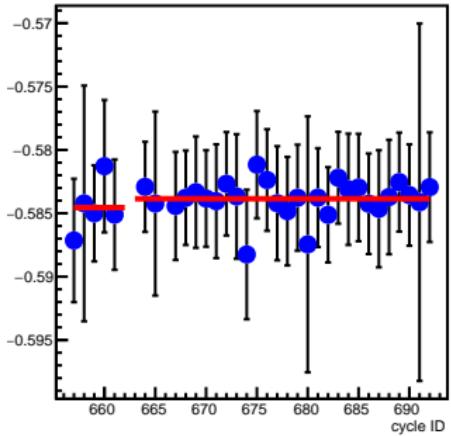
bpm11Y\_coil2(um/count)



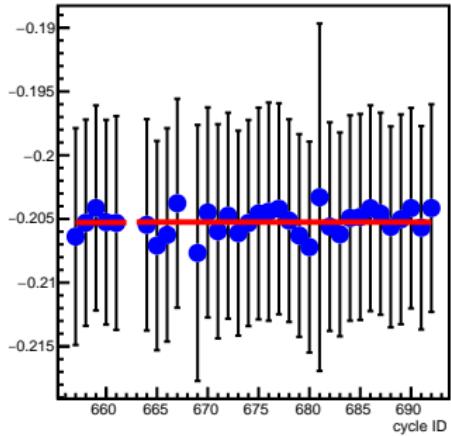
bpm11Y\_coil5(um/count)



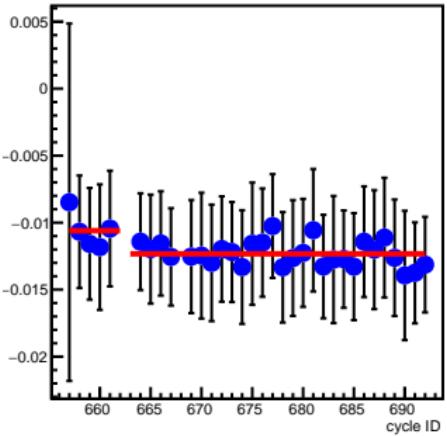
bpm1X\_coil1(um/count)



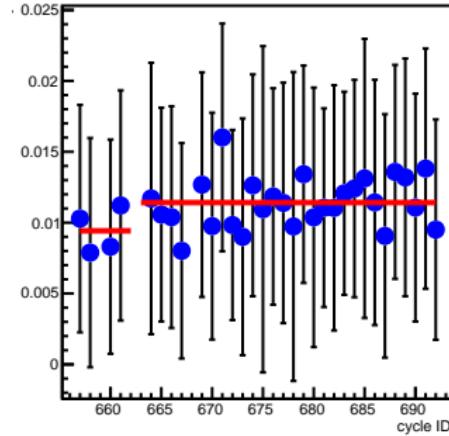
bpm1X\_coil3(um/count)



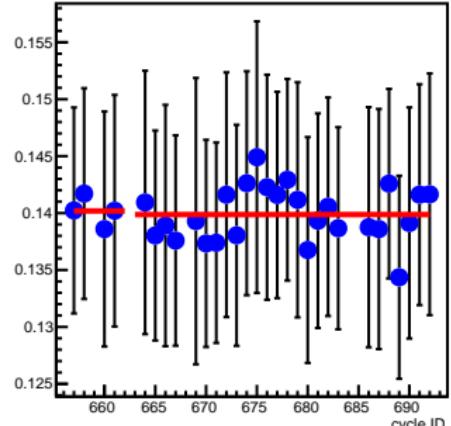
bpm1X\_coil4(um/count)



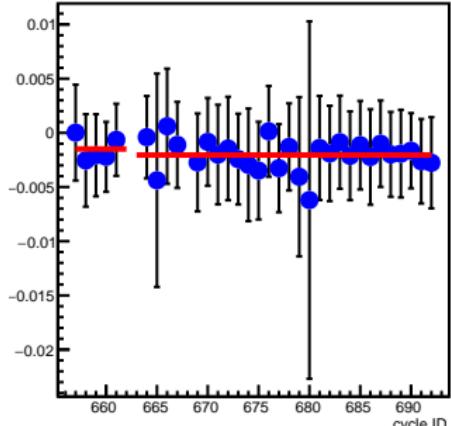
bpm1X\_coil6(um/count)



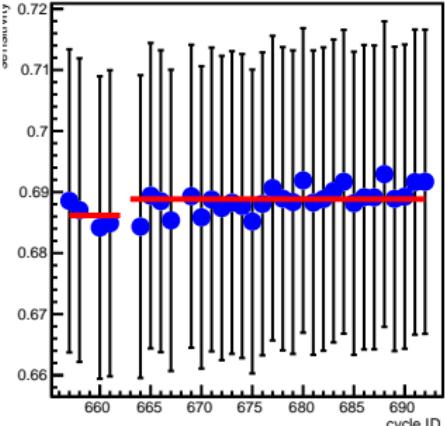
bpm1X\_coil7(um/count)



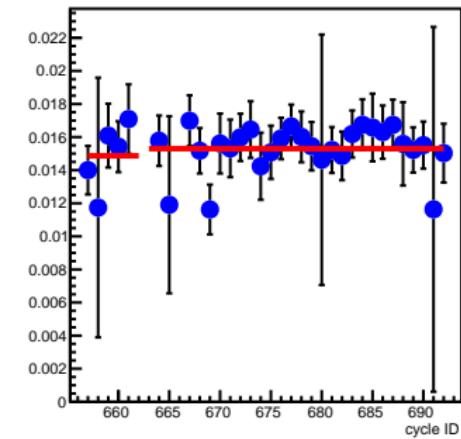
bpm1X\_coil2(um/count)



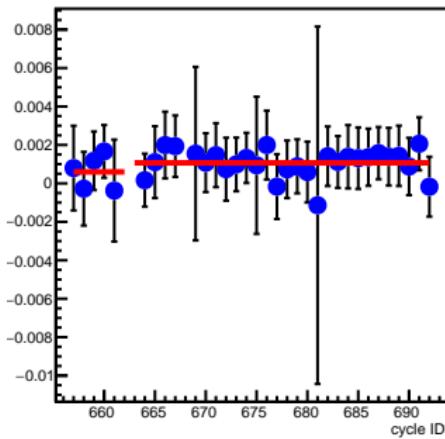
bpm1X\_coil5(um/count)



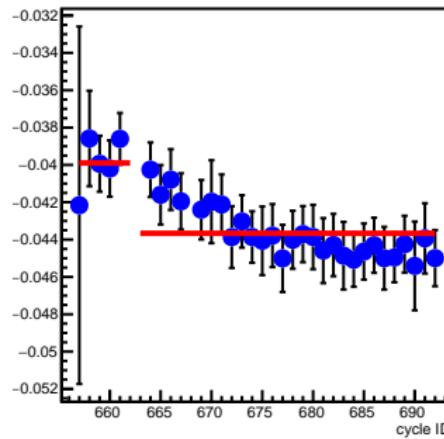
bpm1Y\_coil1(um/count)



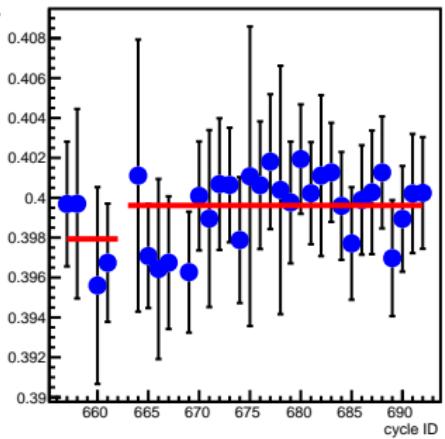
bpm1Y\_coil3(um/count)



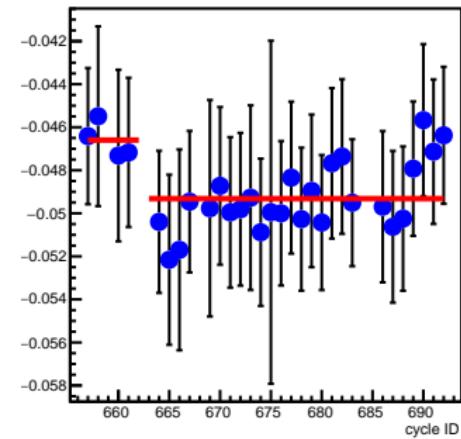
bpm1Y\_coil4(um/count)



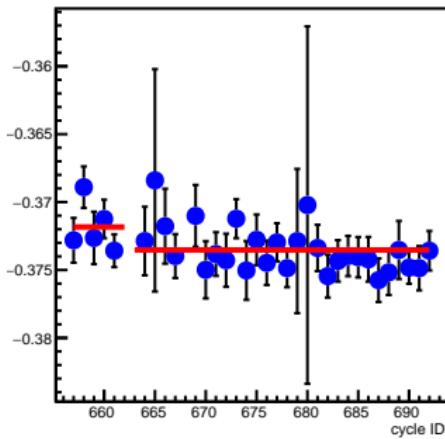
bpm1Y\_coil6(um/count)



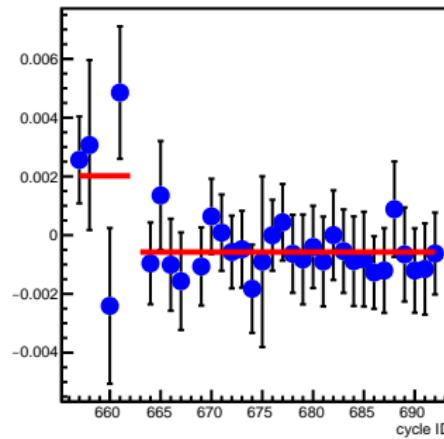
bpm1Y\_coil7(um/count)



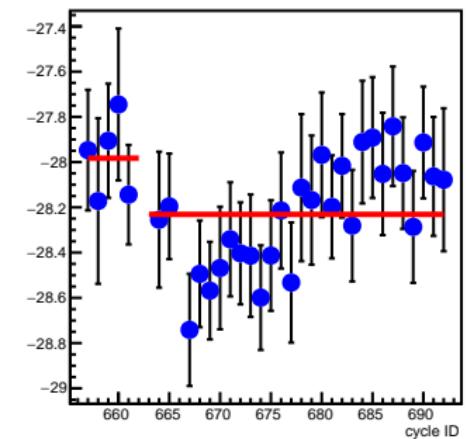
bpm1Y\_coil2(um/count)



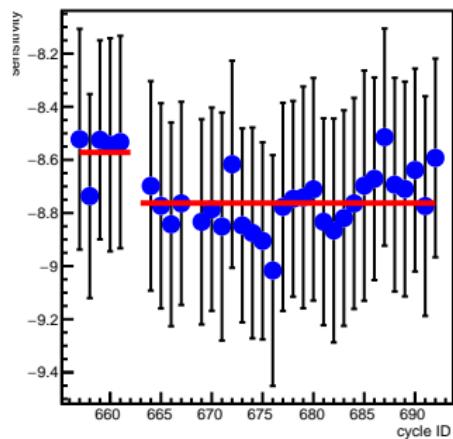
bpm1Y\_coil5(um/count)



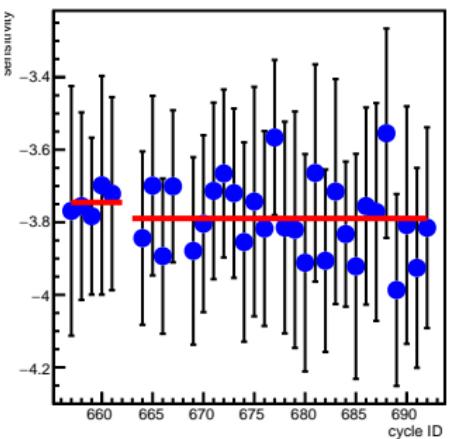
usl\_coil1(ppm/count)



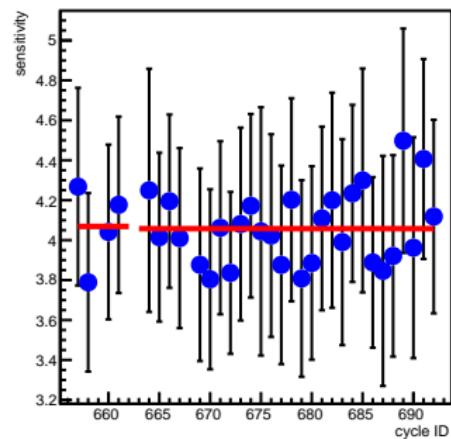
usl\_coil3(ppm/count)



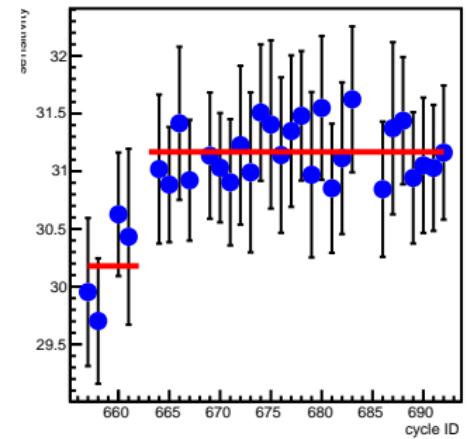
usl\_coil4(ppm/count)



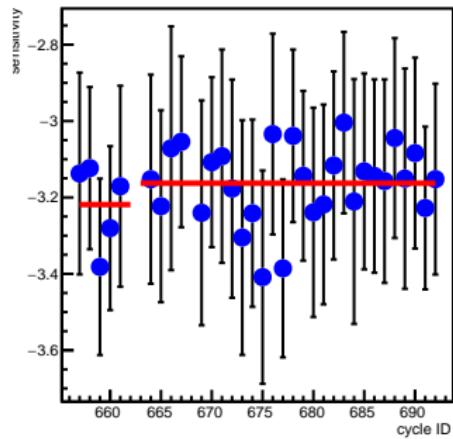
usl\_coil6(ppm/count)



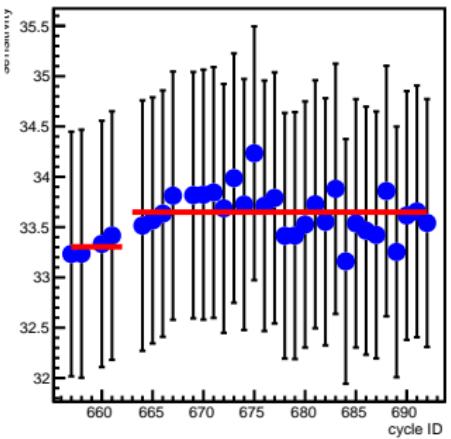
usl\_coil7(ppm/count)



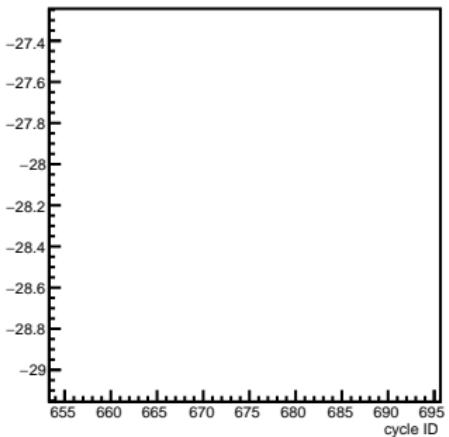
usl\_coil2(ppm/count)



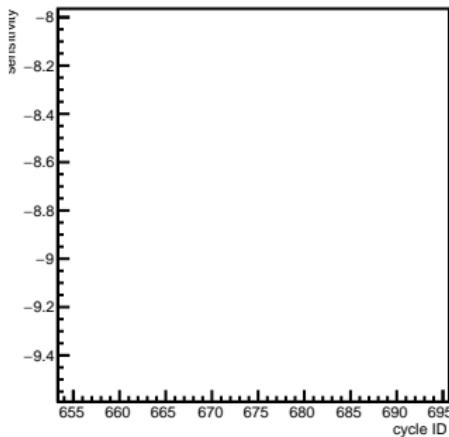
usl\_coil5(ppm/count)



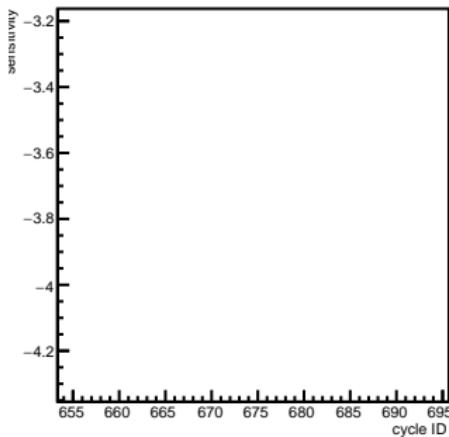
usr\_coil1(ppm/count)



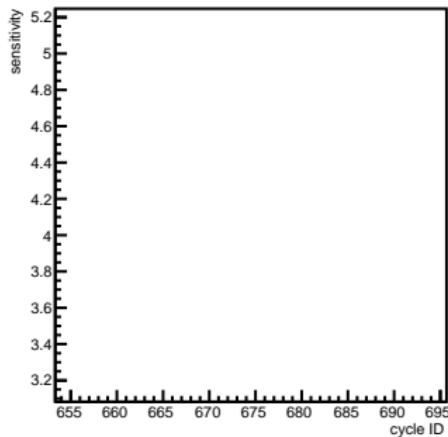
usr\_coil3(ppm/count)



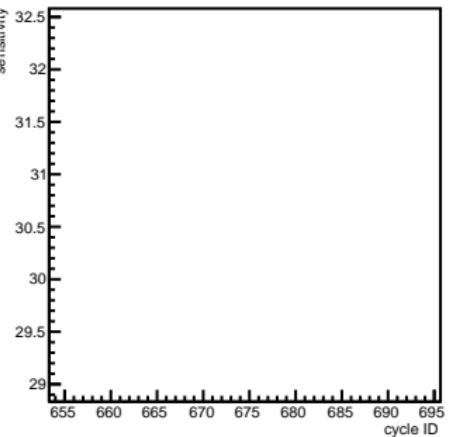
usr\_coil4(ppm/count)



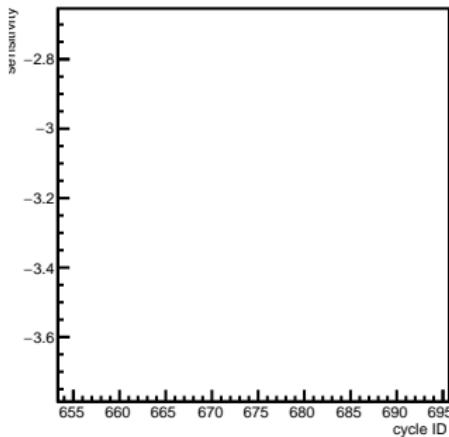
usr\_coil6(ppm/count)



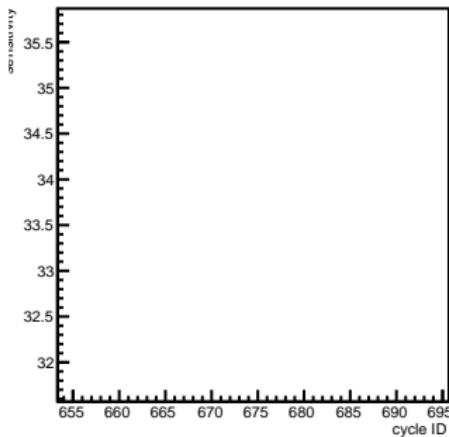
usr\_coil7(ppm/count)



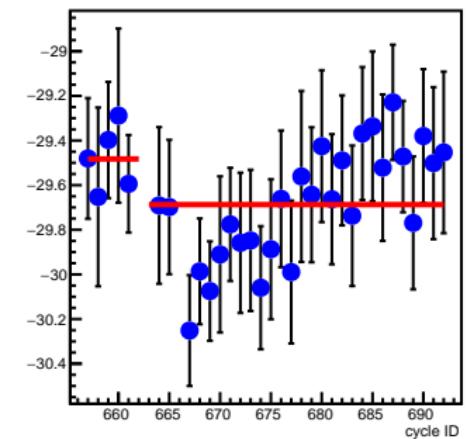
usr\_coil2(ppm/count)



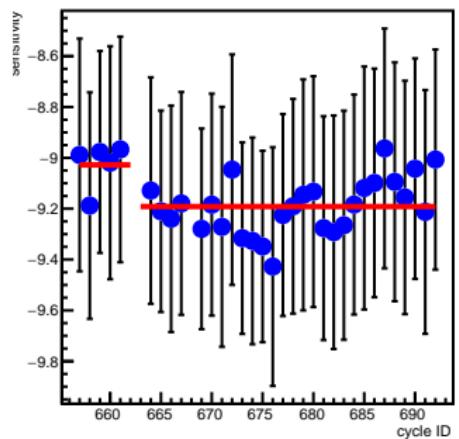
usr\_coil5(ppm/count)



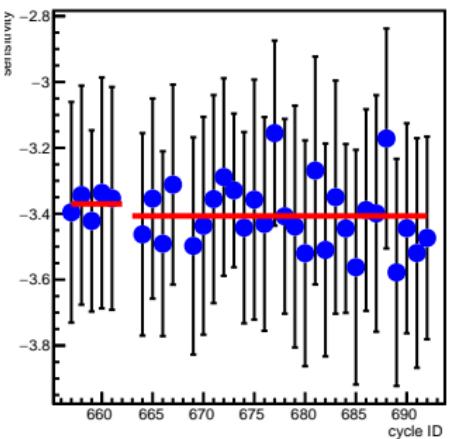
dsl\_coil1(ppm/count)



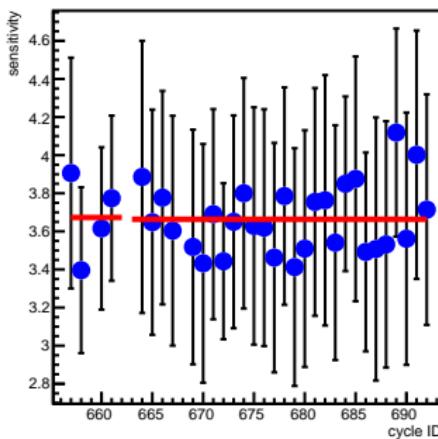
dsl\_coil3(ppm/count)



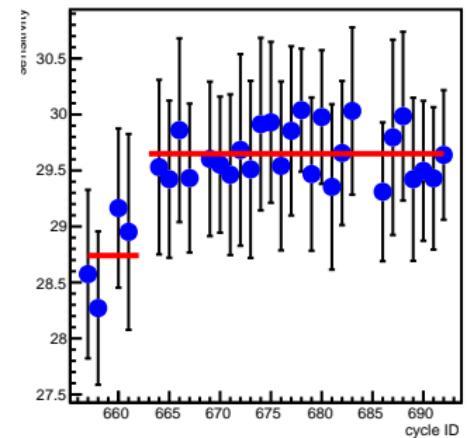
dsl\_coil4(ppm/count)



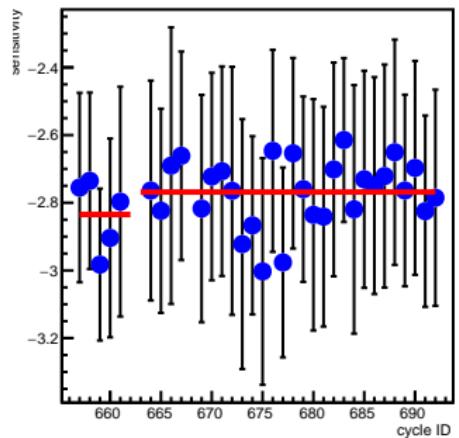
dsl\_coil6(ppm/count)



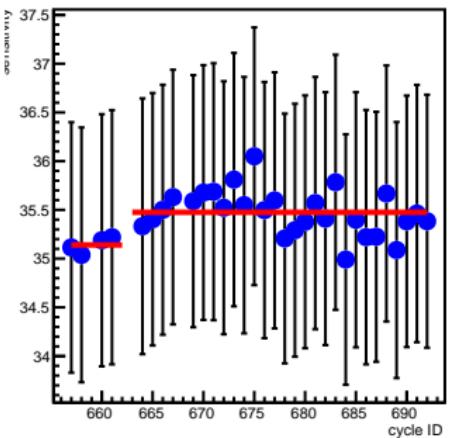
dsl\_coil7(ppm/count)



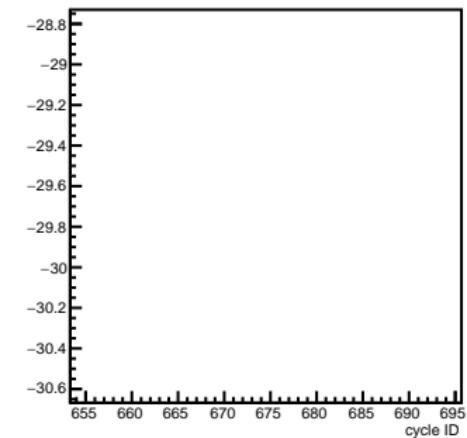
dsl\_coil2(ppm/count)



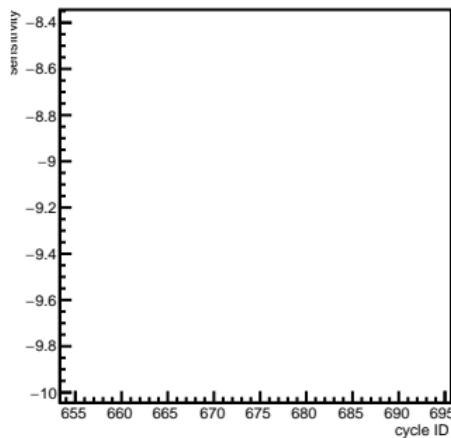
dsl\_coil5(ppm/count)



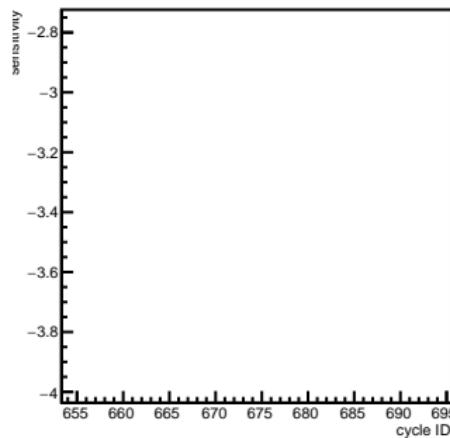
dsr\_coil1(ppm/count)



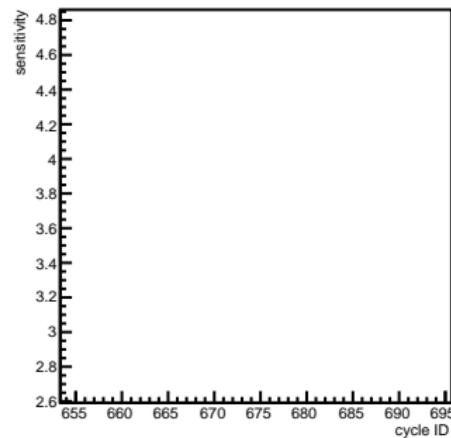
dsr\_coil3(ppm/count)



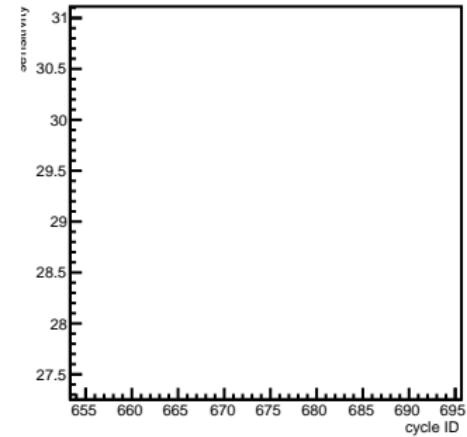
dsr\_coil4(ppm/count)



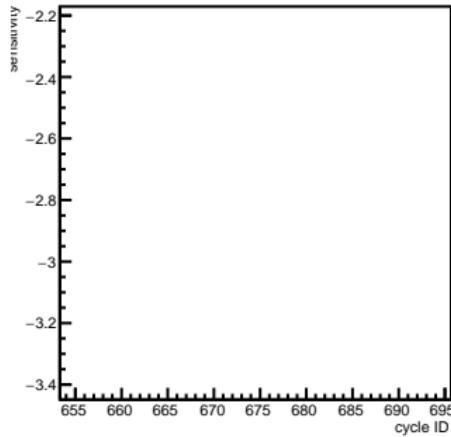
dsr\_coil6(ppm/count)



dsr\_coil7(ppm/count)



dsr\_coil2(ppm/count)



dsr\_coil5(ppm/count)

