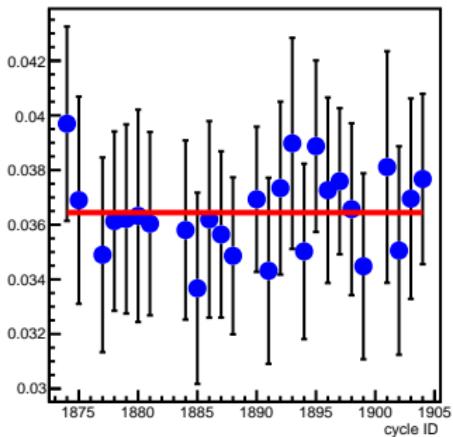
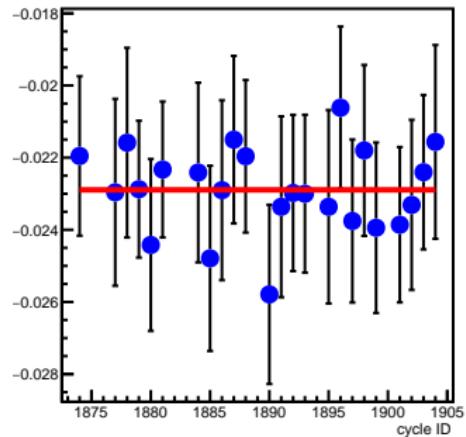


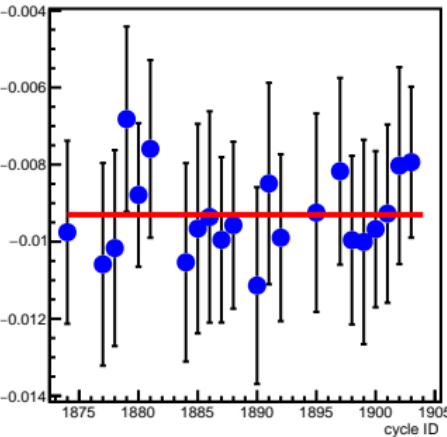
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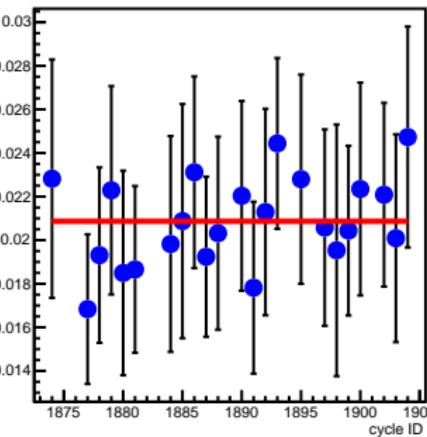
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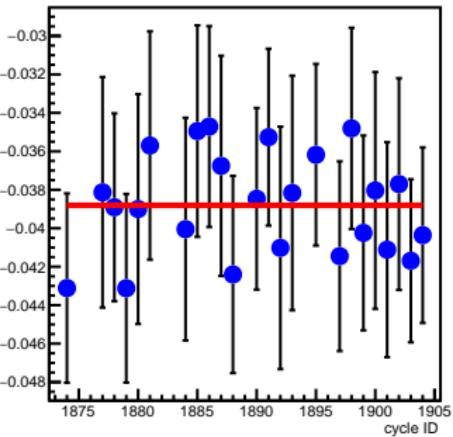
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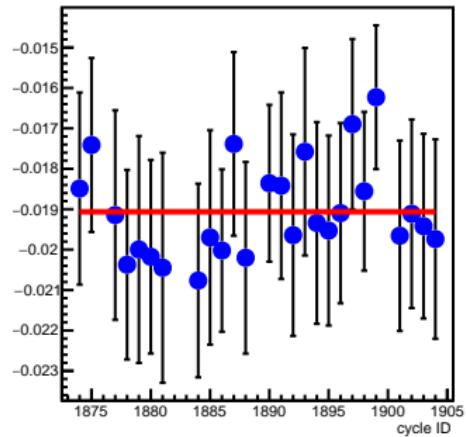
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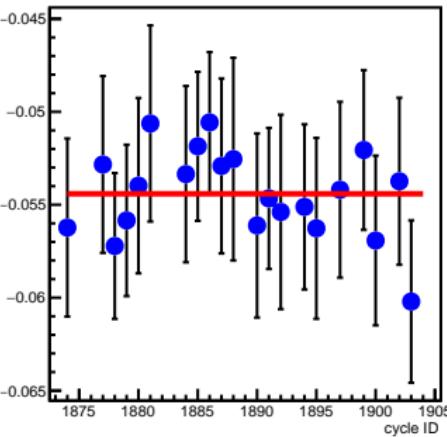
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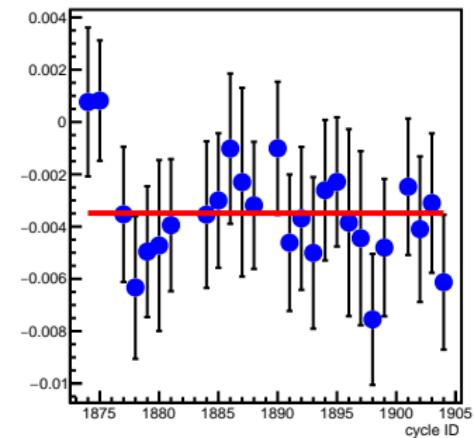
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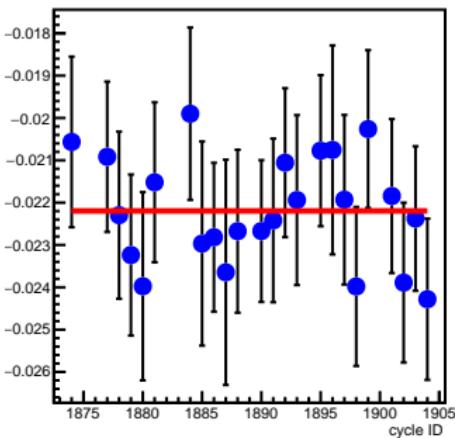
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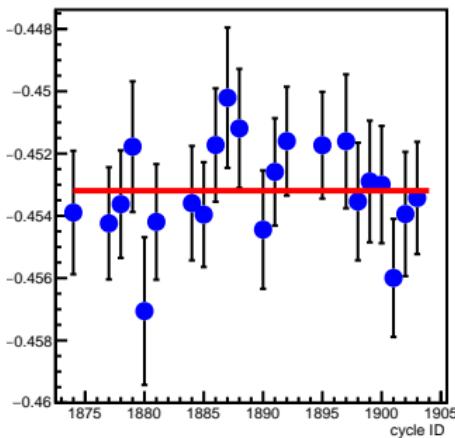
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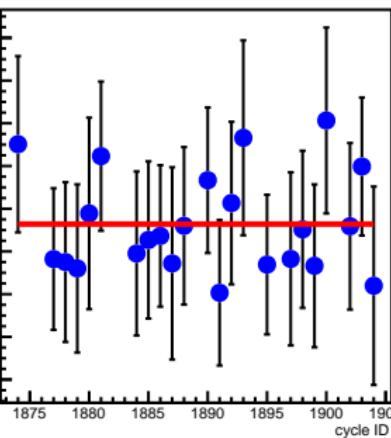
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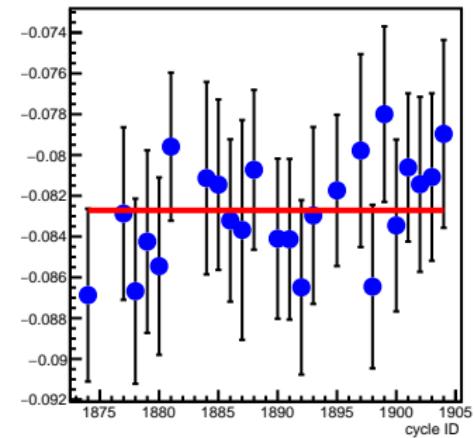
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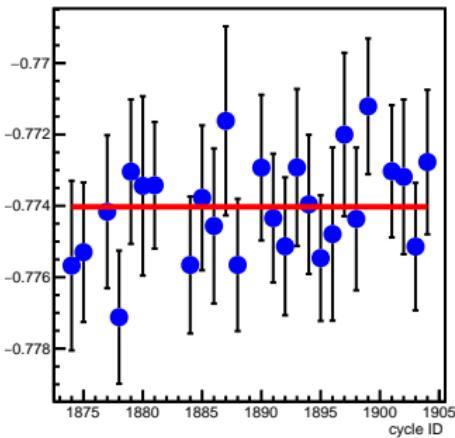
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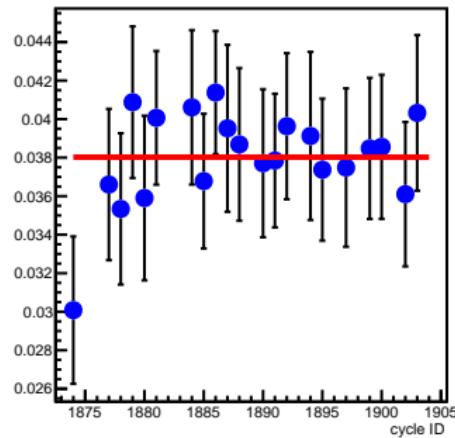
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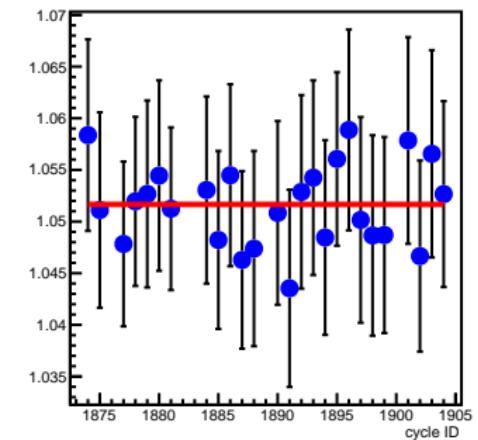
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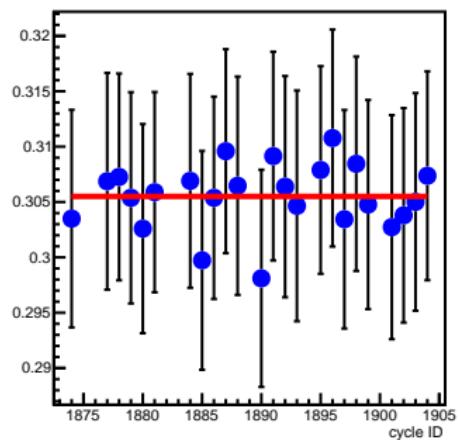
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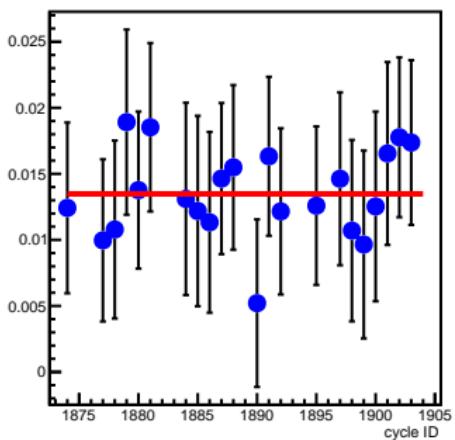
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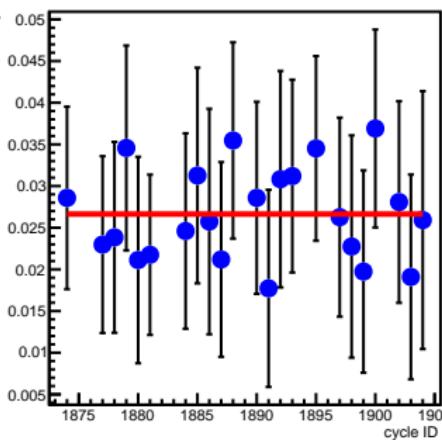
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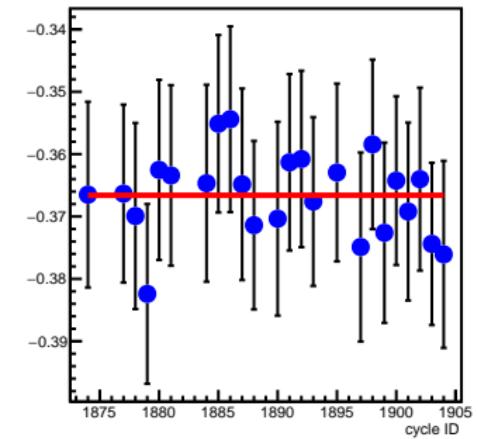
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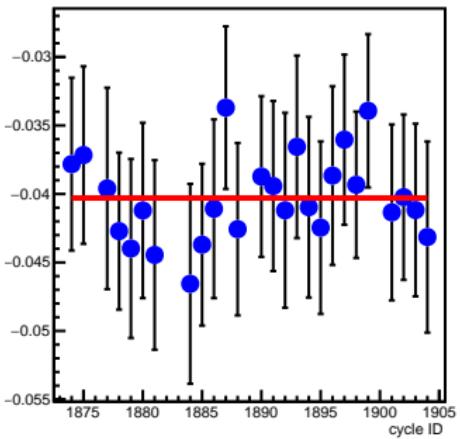
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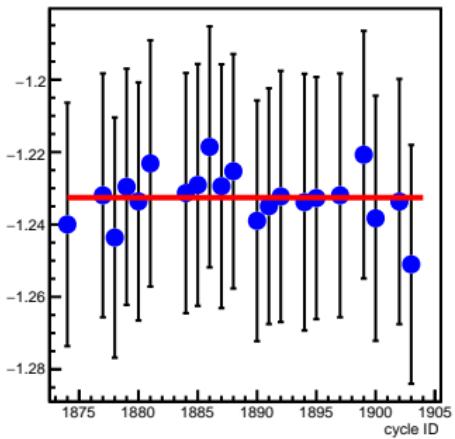
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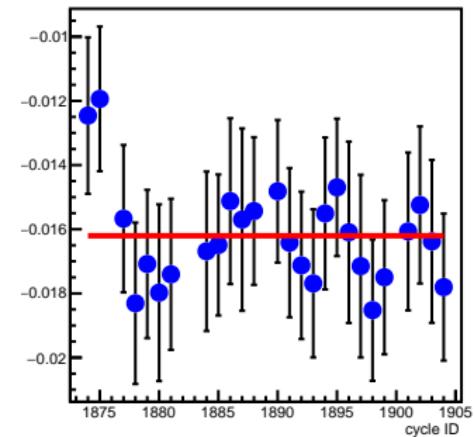
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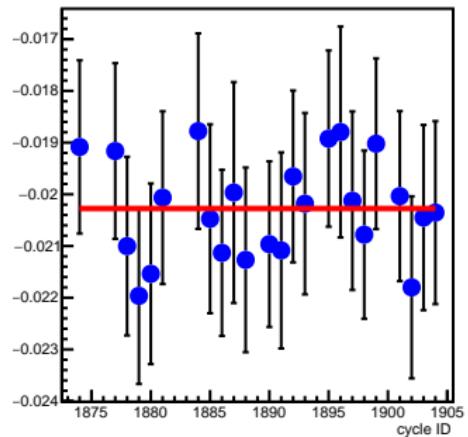
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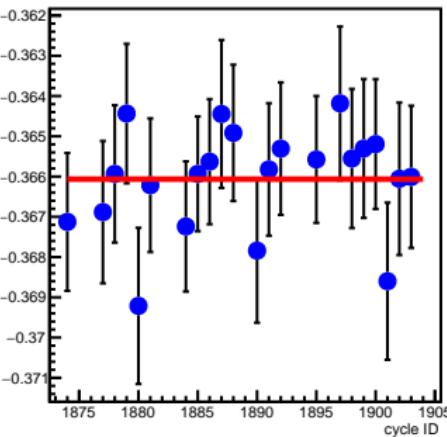
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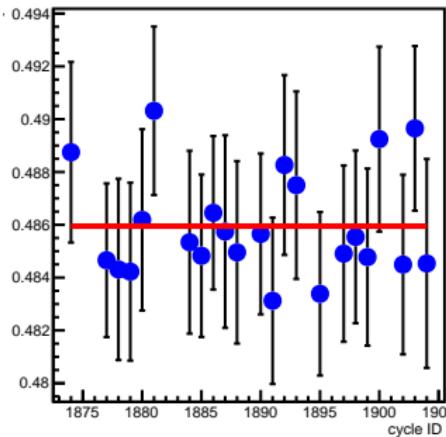
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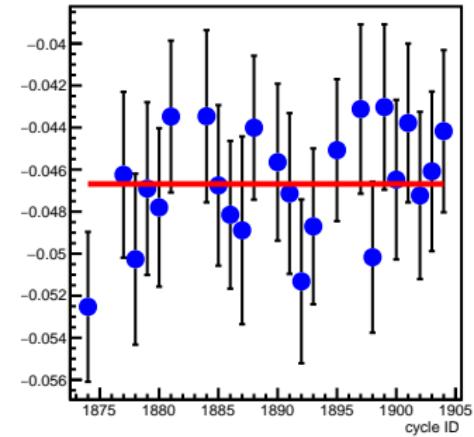
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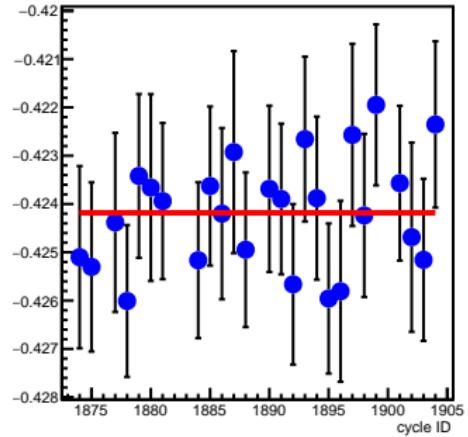
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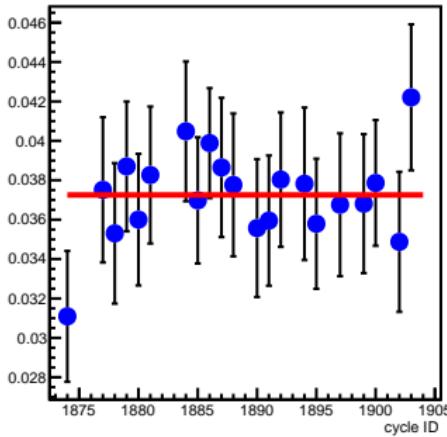
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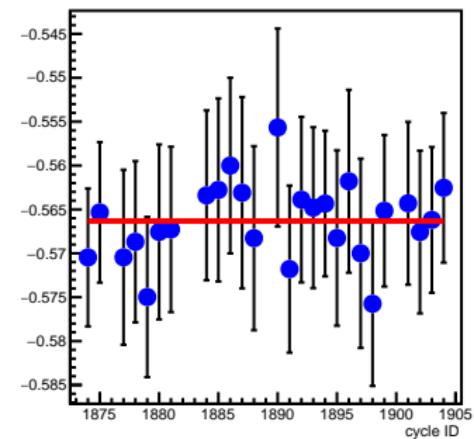
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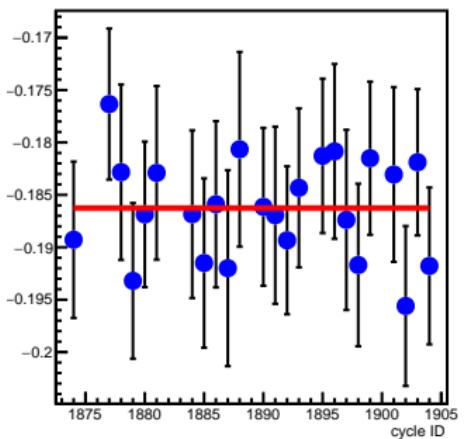
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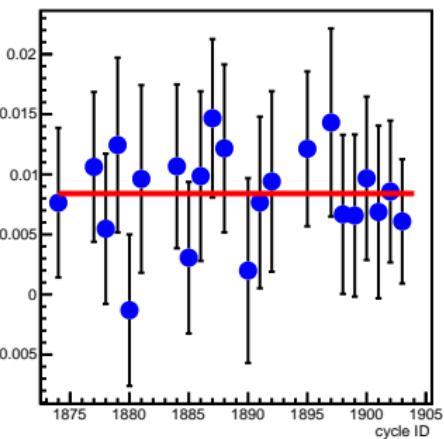
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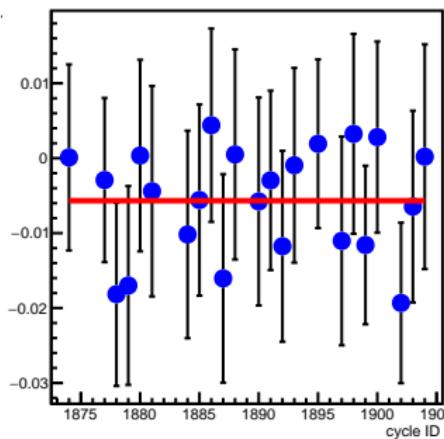
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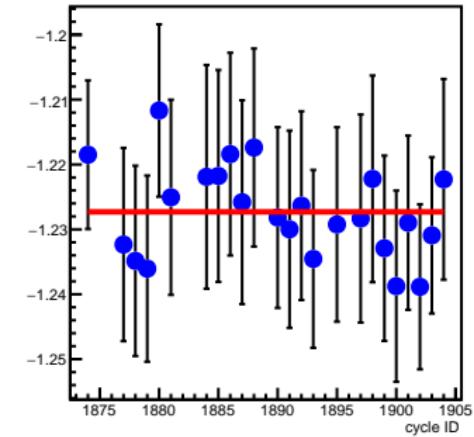
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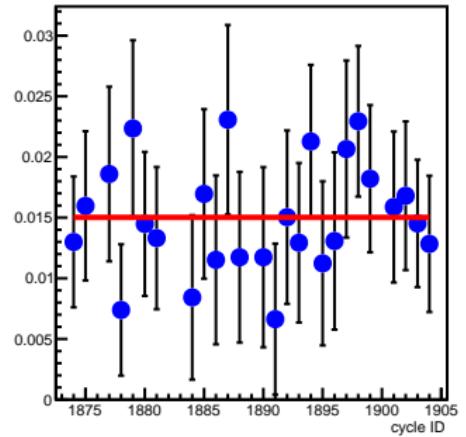
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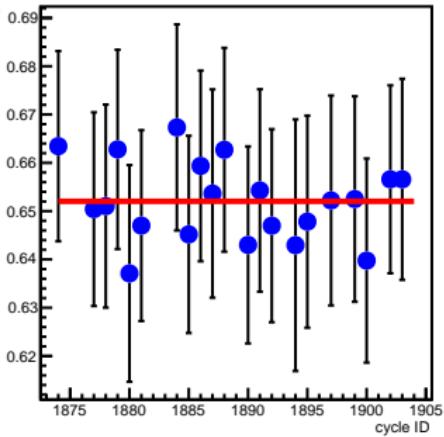
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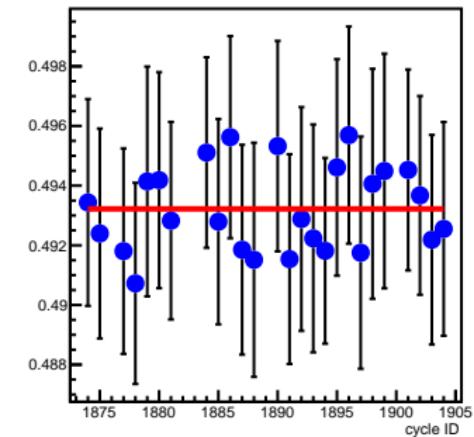
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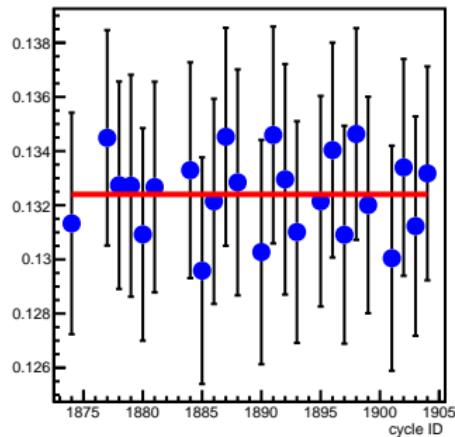
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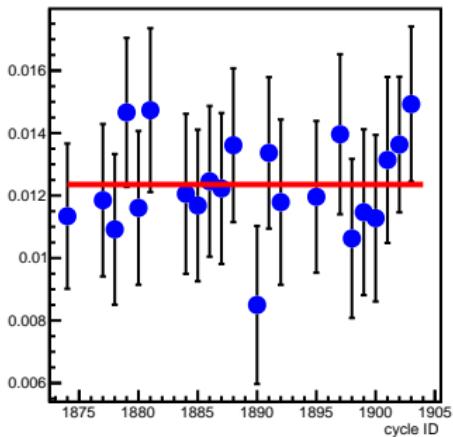
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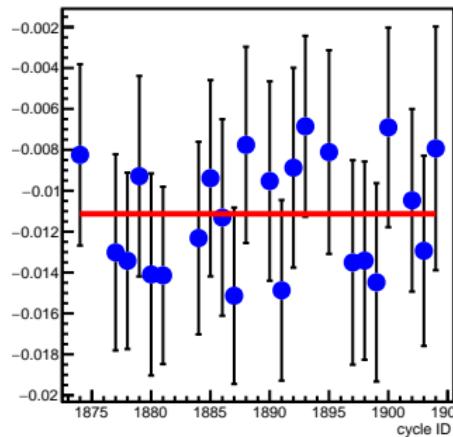
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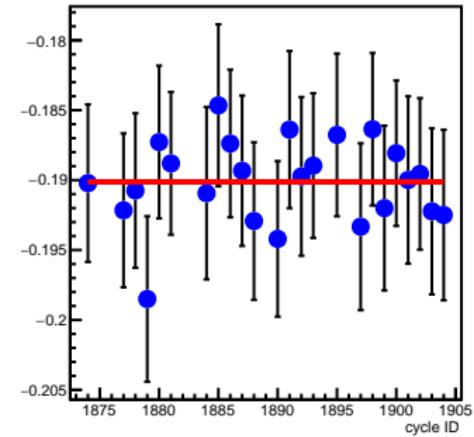
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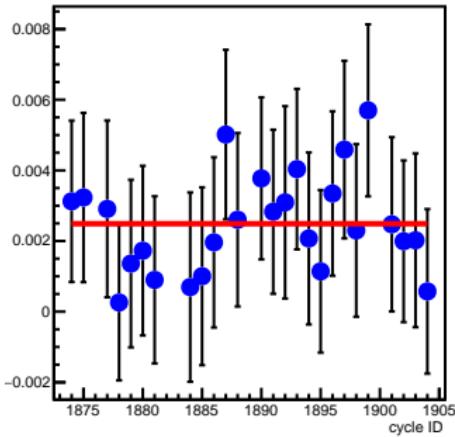
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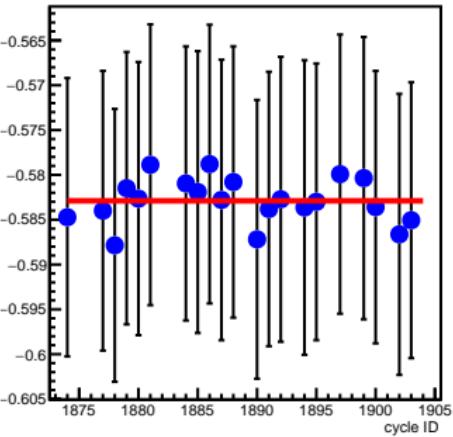
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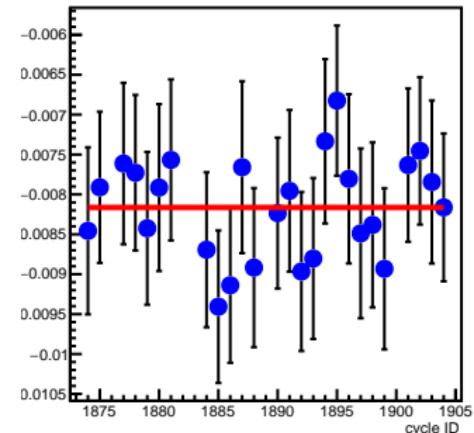
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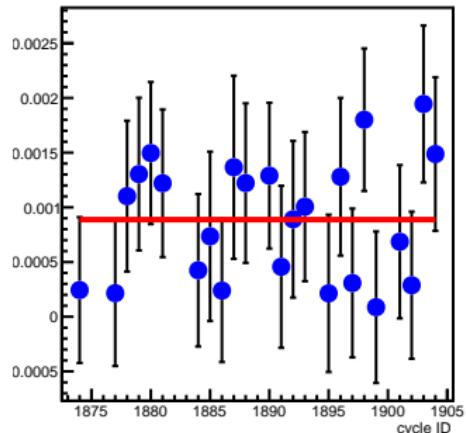
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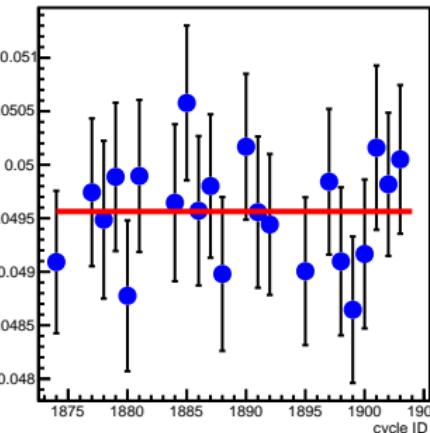
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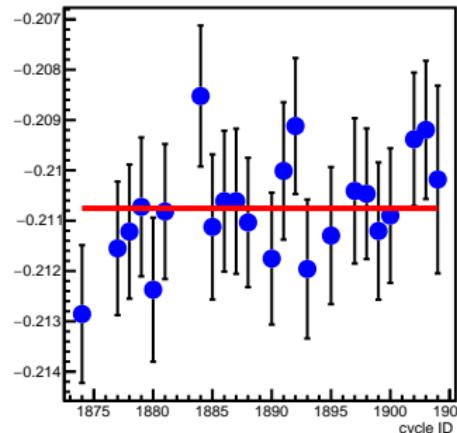
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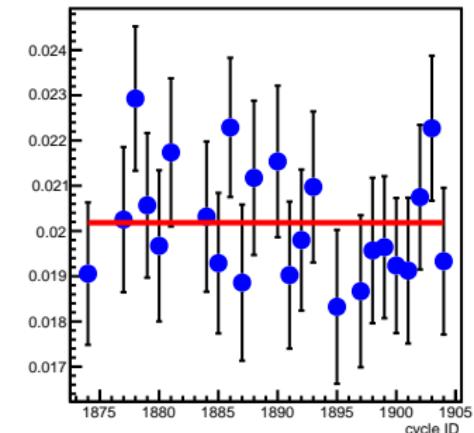
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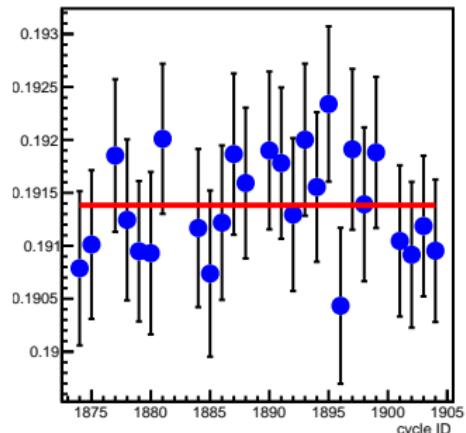
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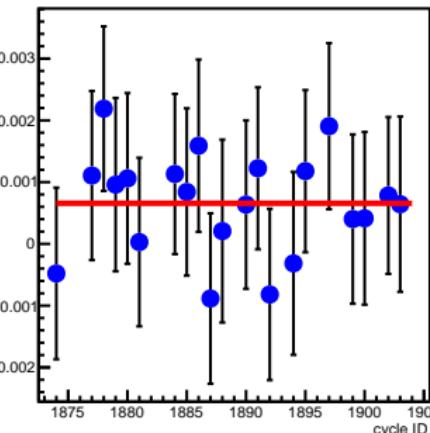
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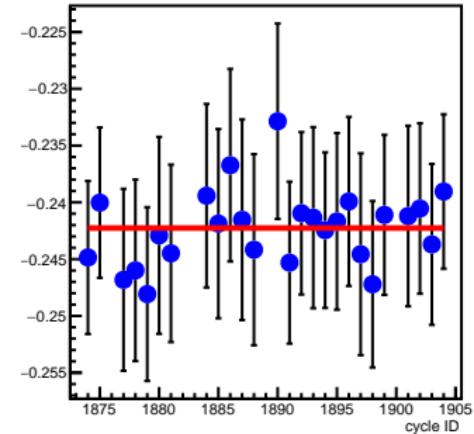
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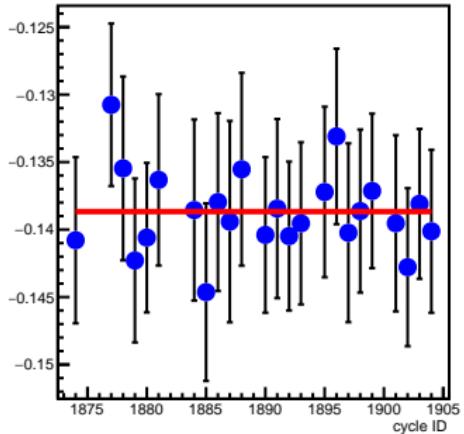
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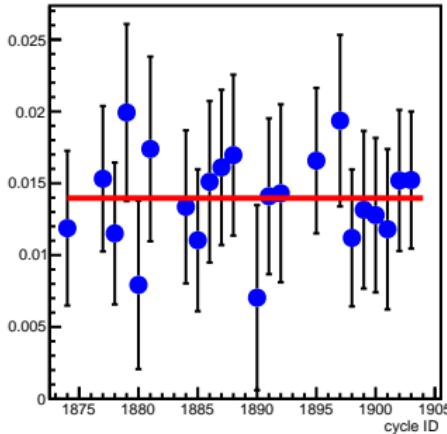
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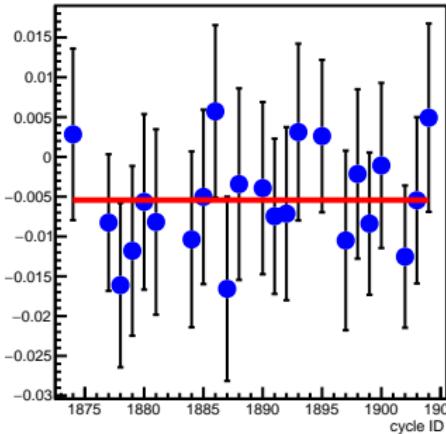
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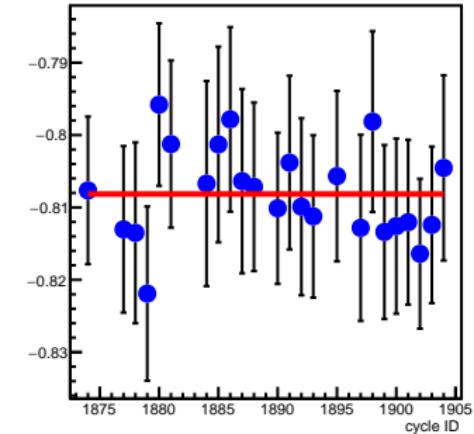
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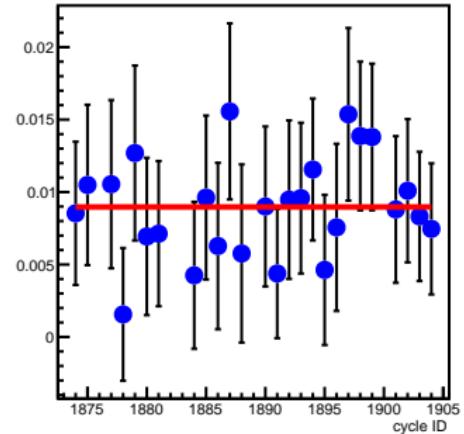
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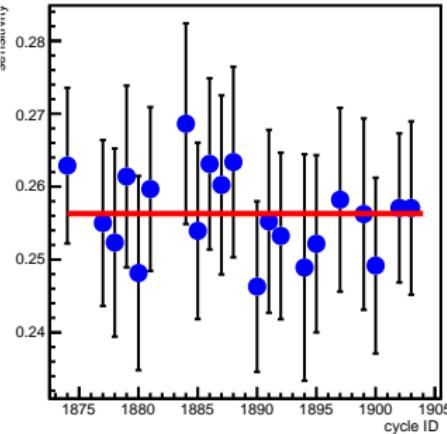
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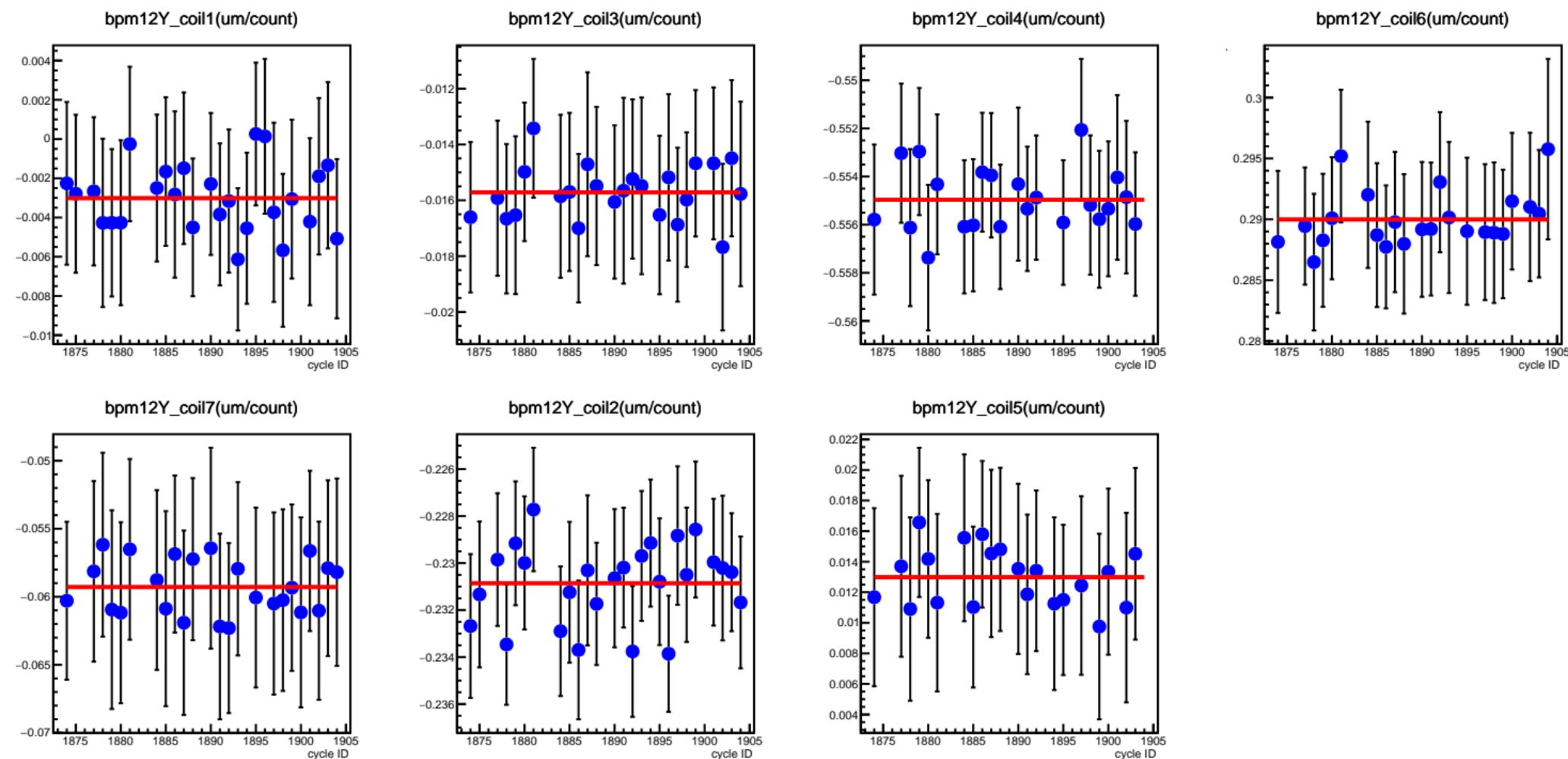


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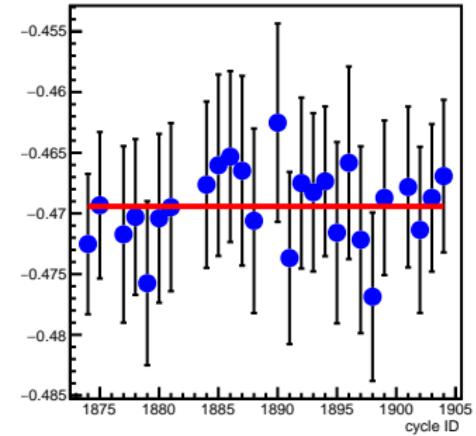


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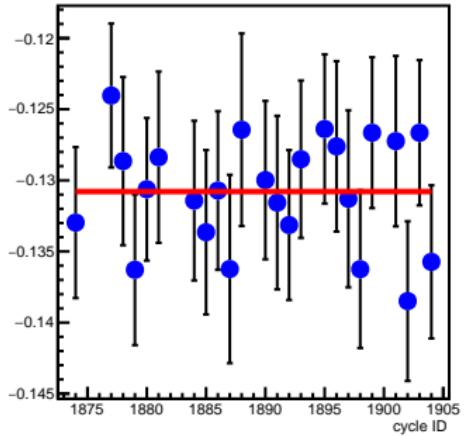




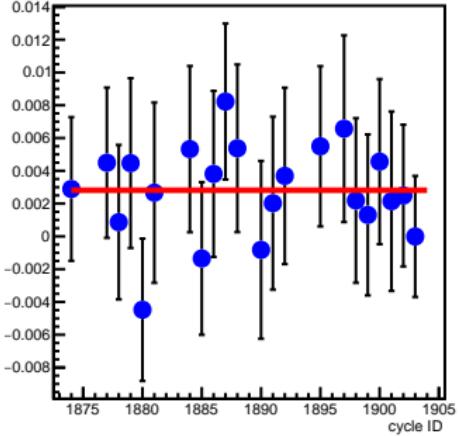
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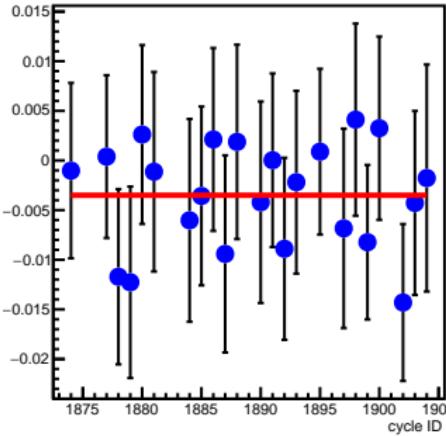
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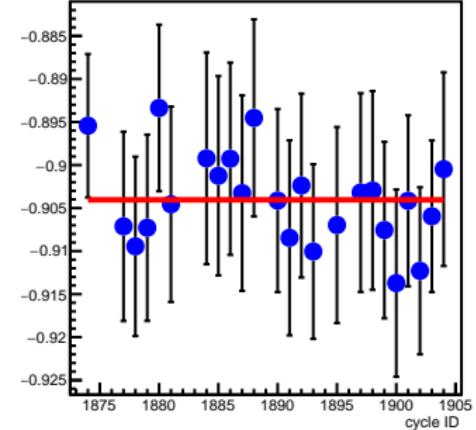
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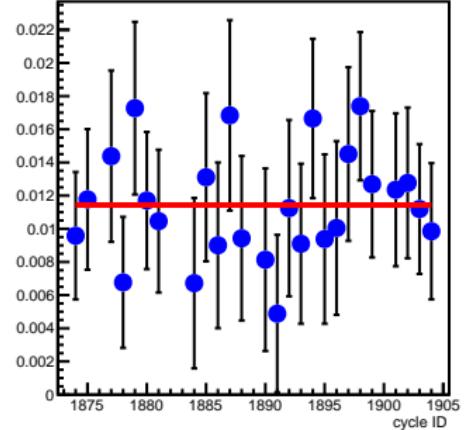
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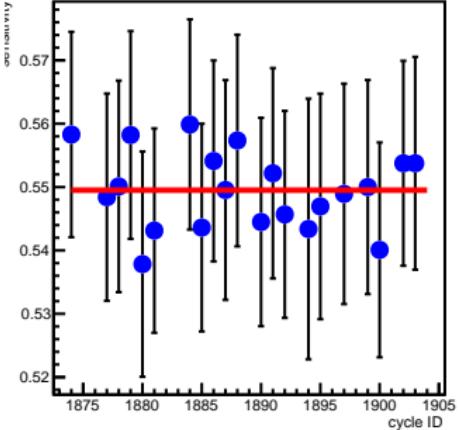
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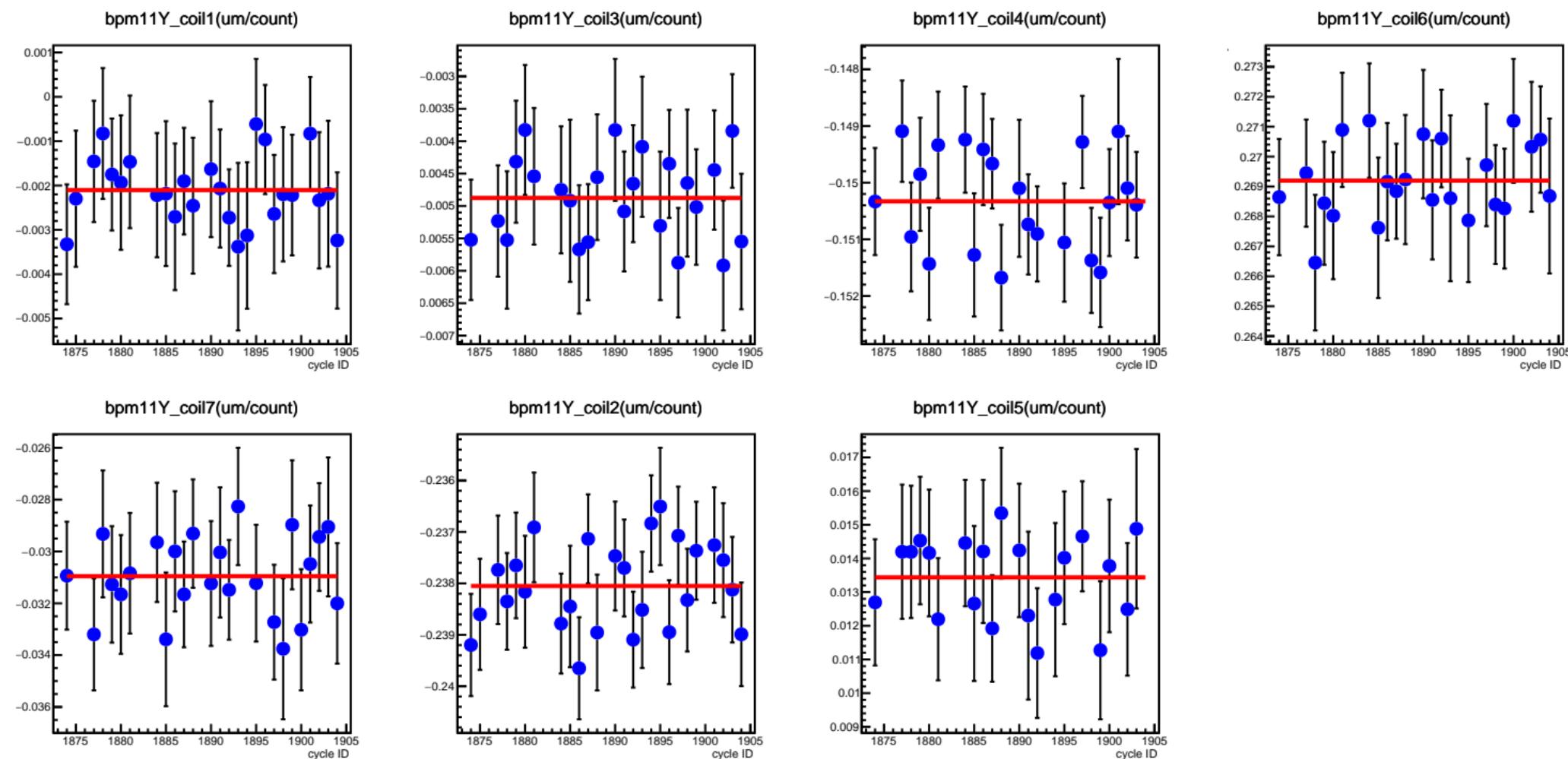


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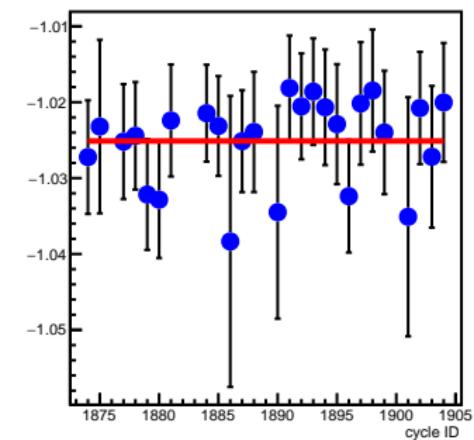


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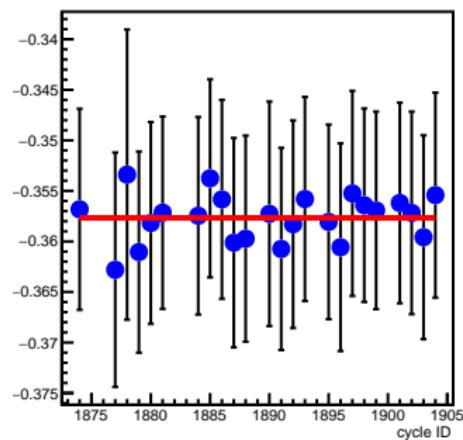




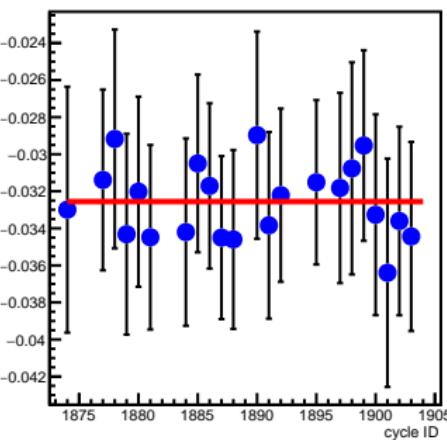
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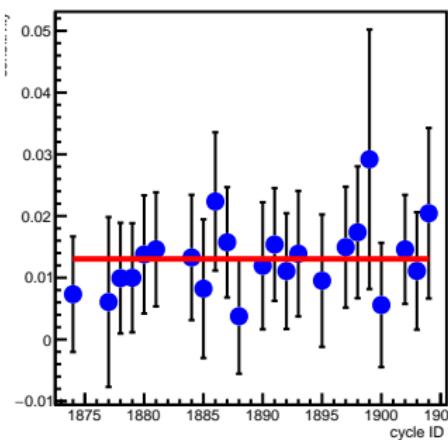
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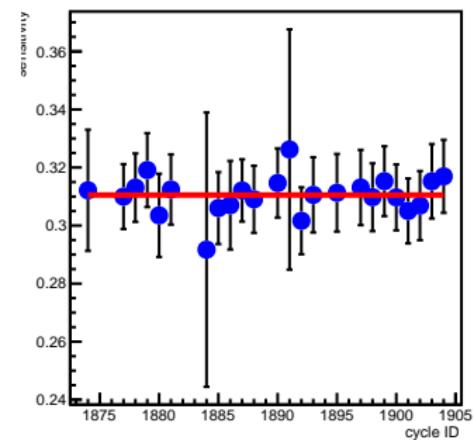
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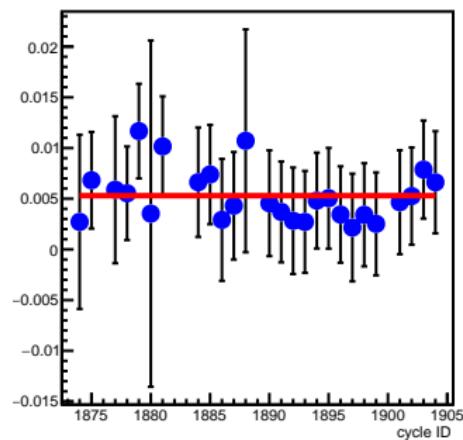
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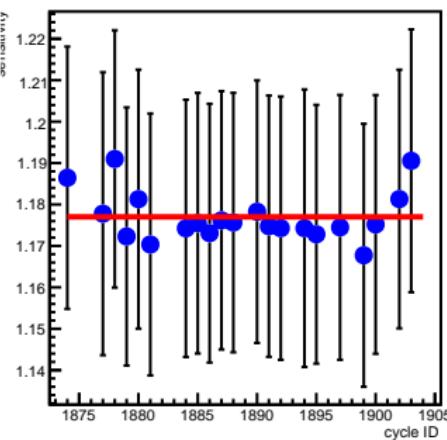
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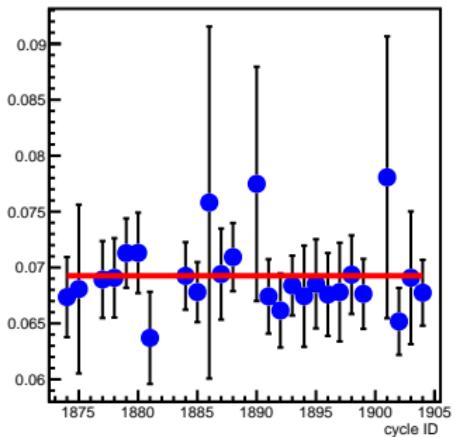
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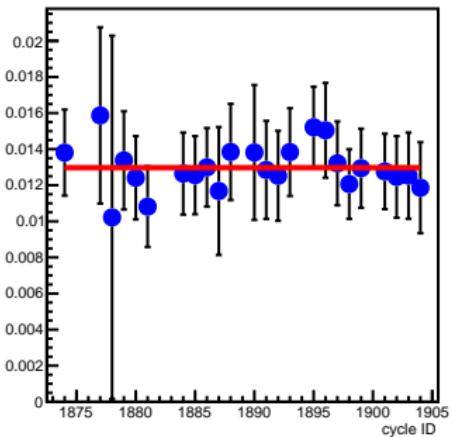
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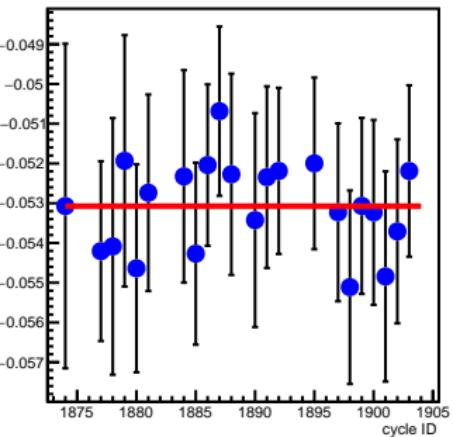
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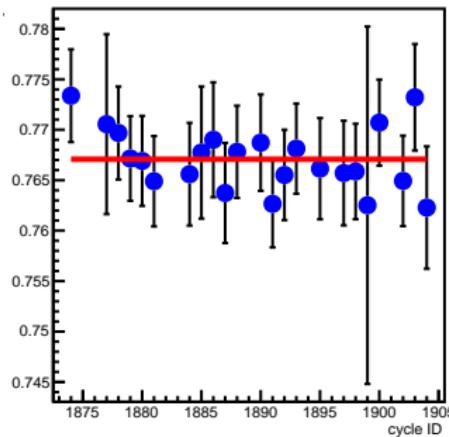
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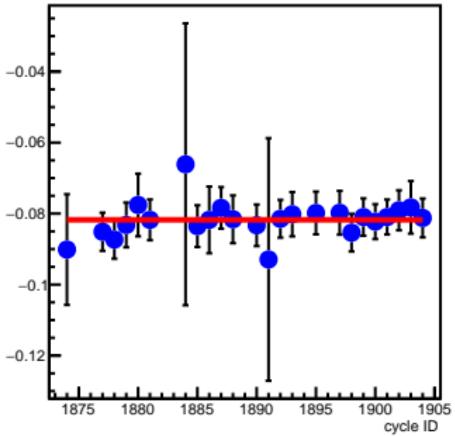
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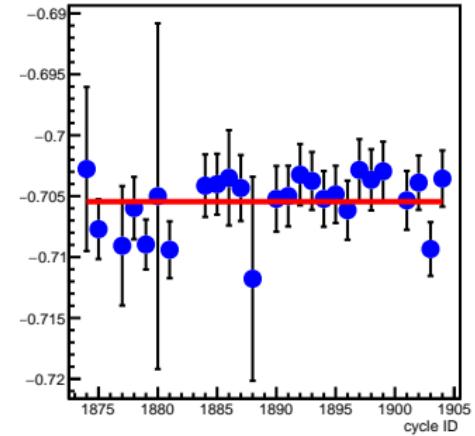
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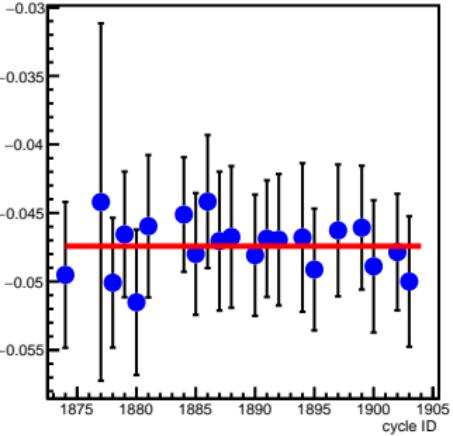
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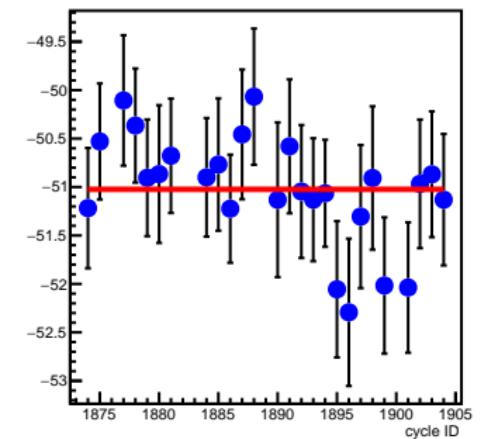
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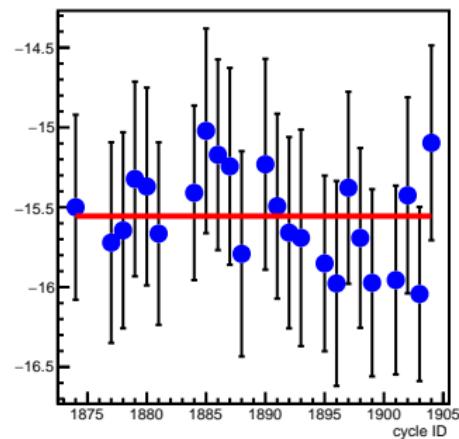
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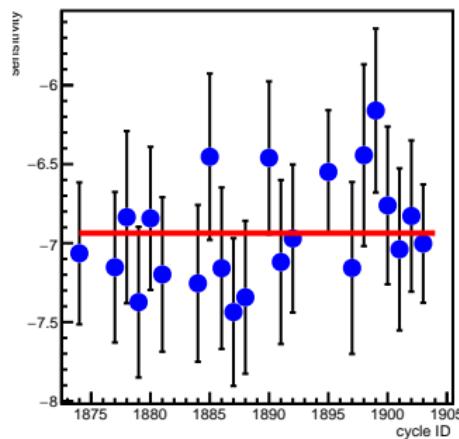
usl_coil1(ppm/count)



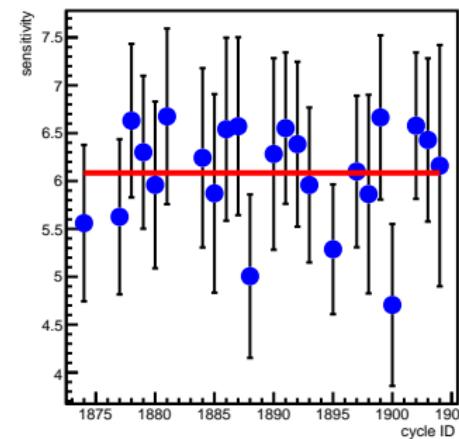
usl_coil3(ppm/count)



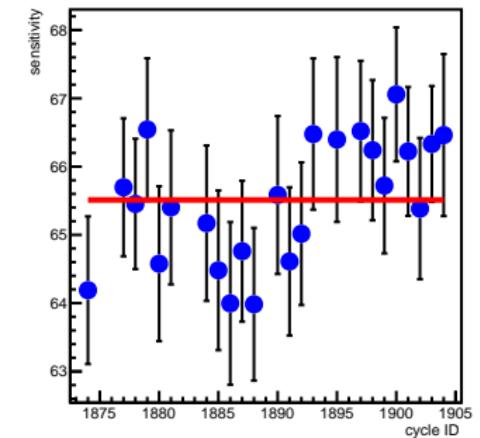
usl_coil4(ppm/count)



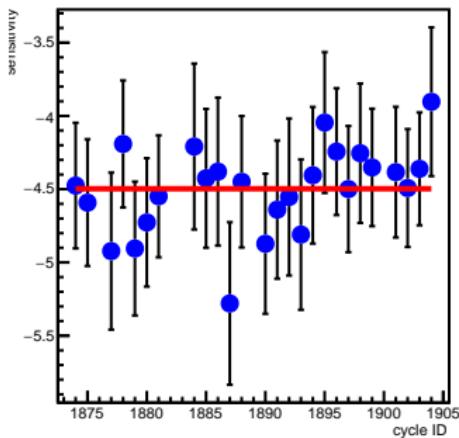
usl_coil6(ppm/count)



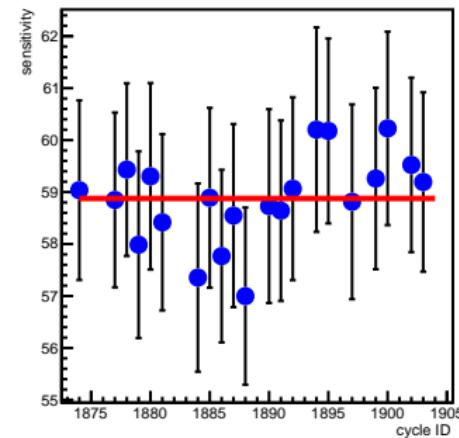
usl_coil7(ppm/count)

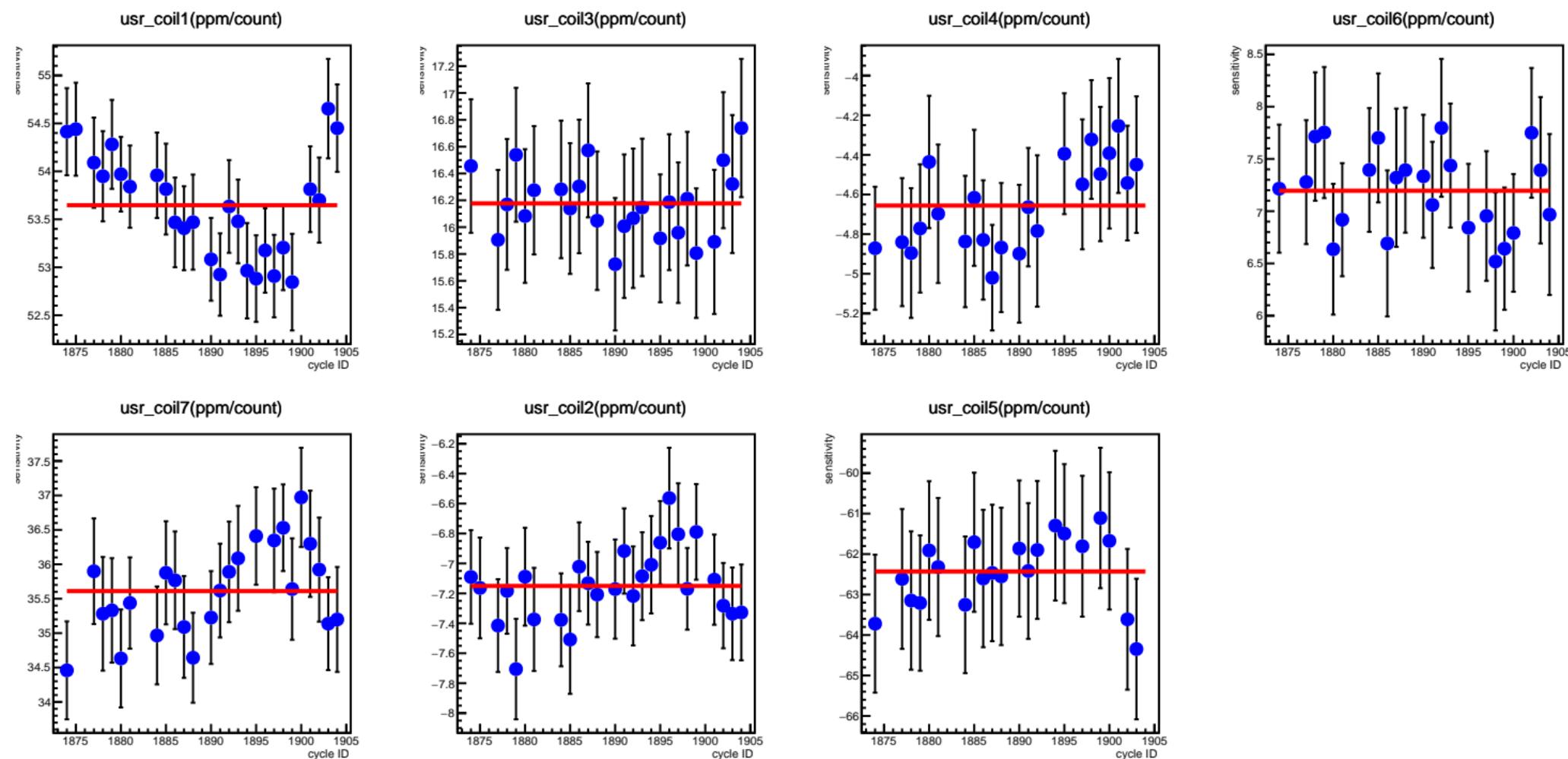


usl_coil2(ppm/count)

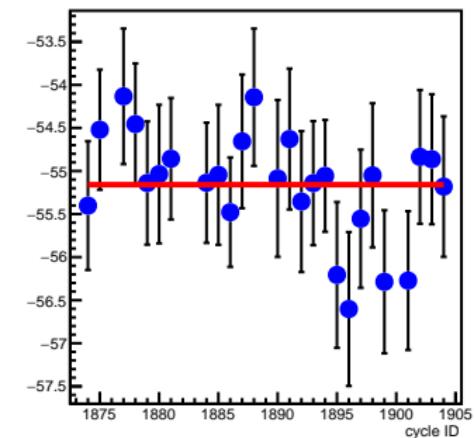


usl_coil5(ppm/count)

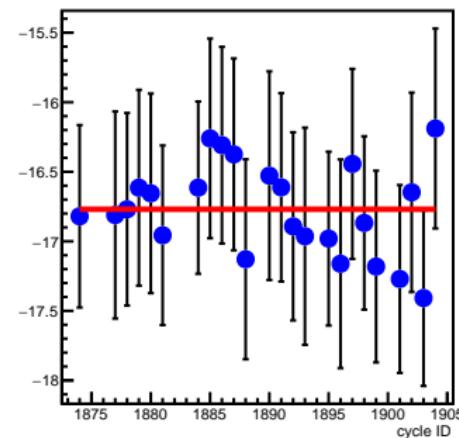




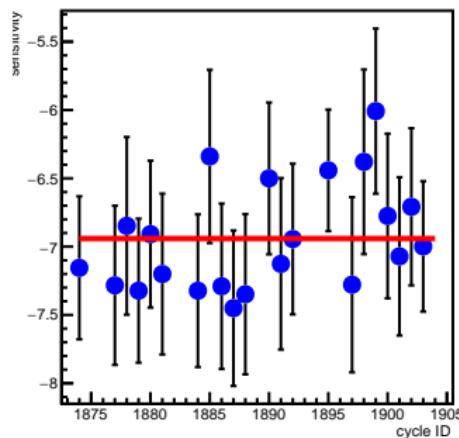
dsl_coil1(ppm/count)



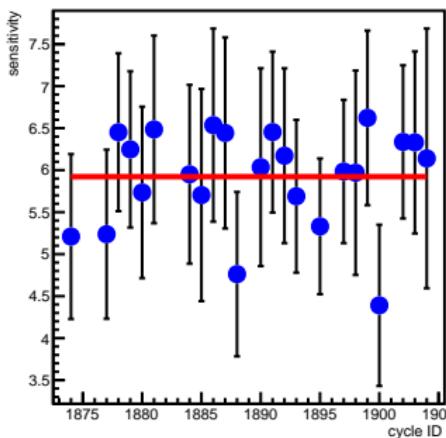
dsl_coil3(ppm/count)



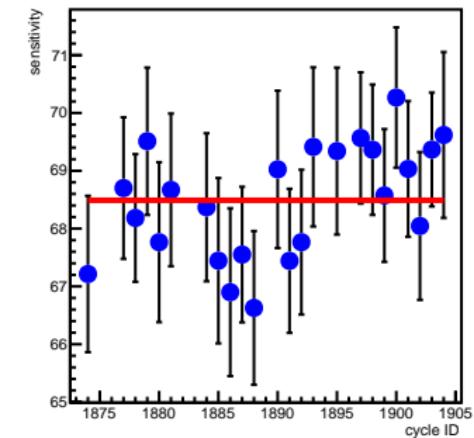
dsl_coil4(ppm/count)



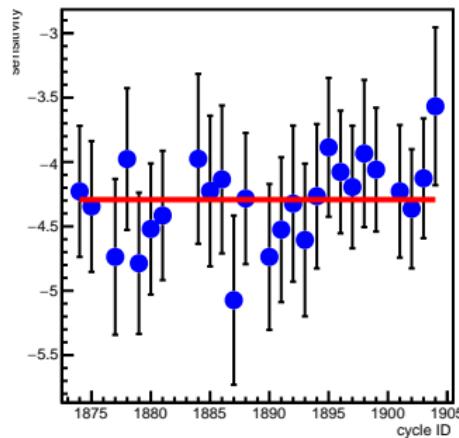
dsl_coil6(ppm/count)



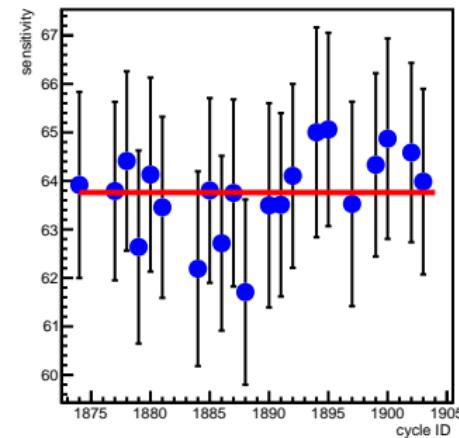
dsl_coil7(ppm/count)



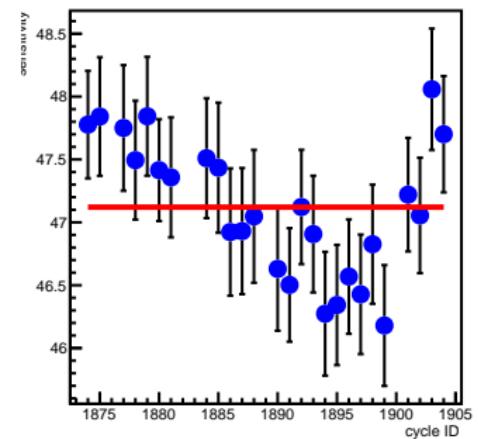
dsl_coil2(ppm/count)



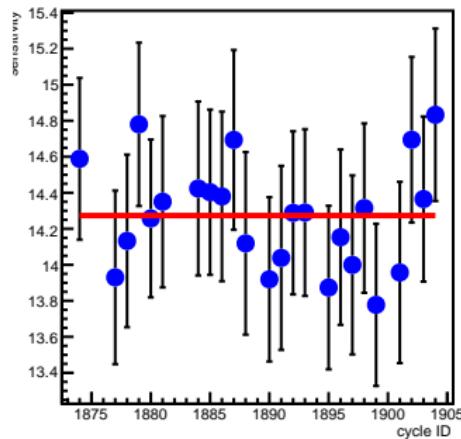
dsl_coil5(ppm/count)



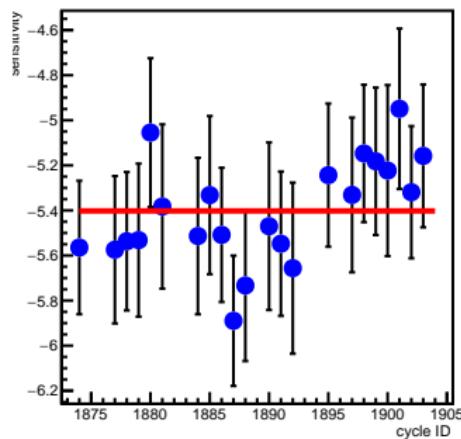
dsr_coil1(ppm/count)



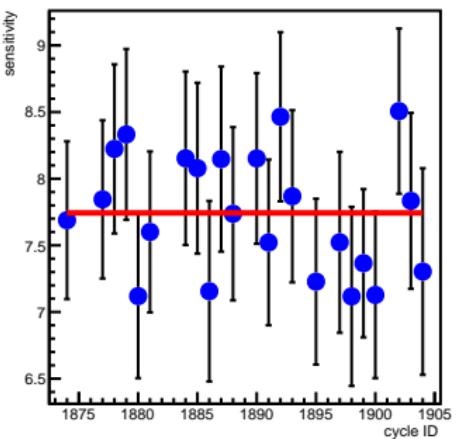
dsr_coil3(ppm/count)



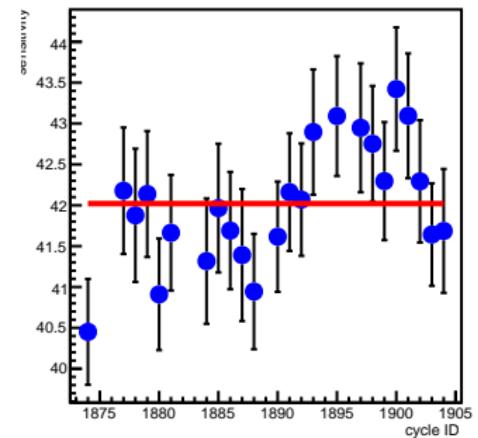
dsr_coil4(ppm/count)



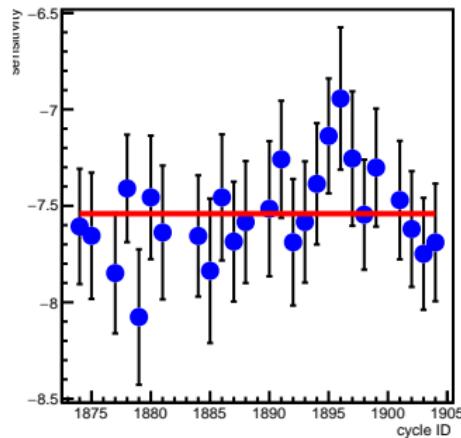
dsr_coil6(ppm/count)



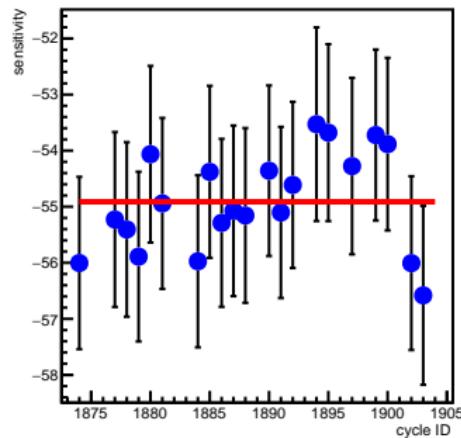
dsr_coil7(ppm/count)



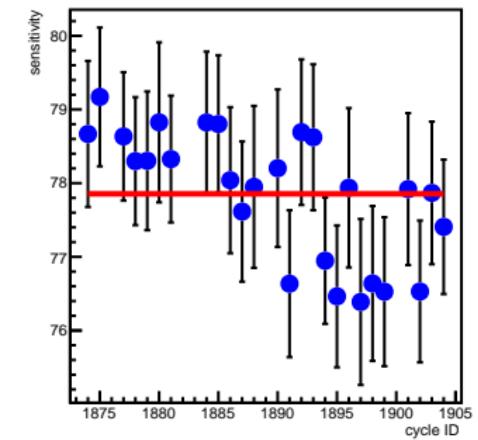
dsr_coil2(ppm/count)



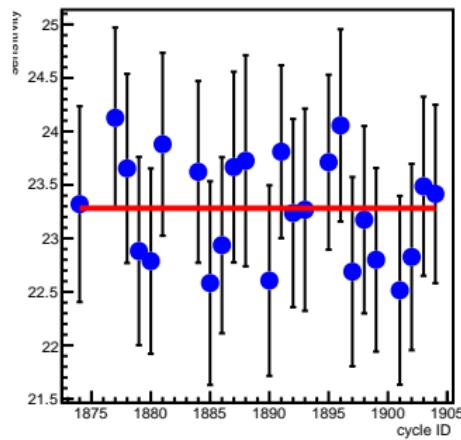
dsr_coil5(ppm/count)



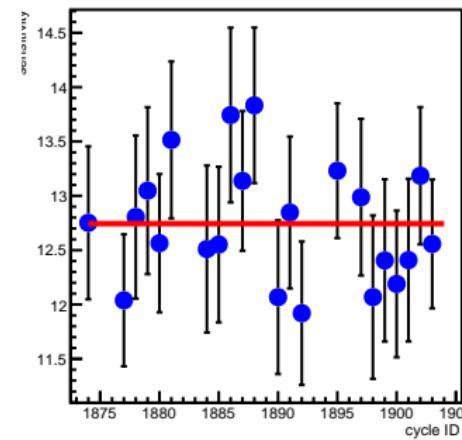
at1_coil1(ppm/count)



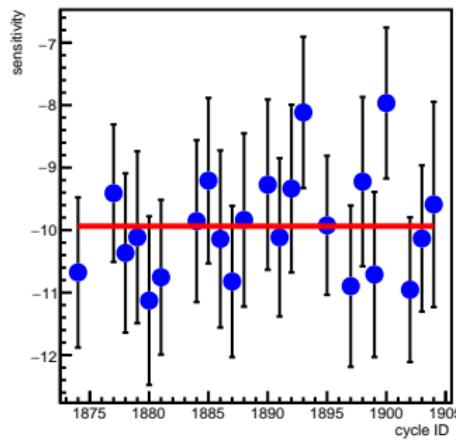
at1_coil3(ppm/count)



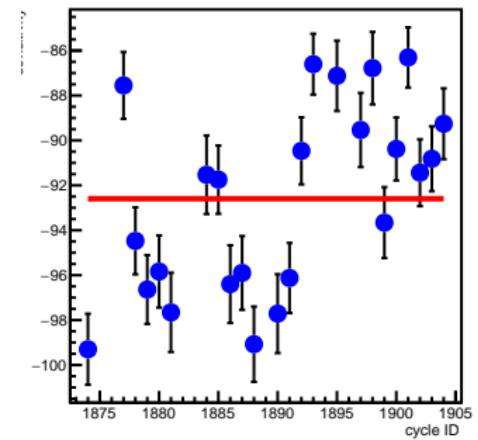
at1_coil4(ppm/count)



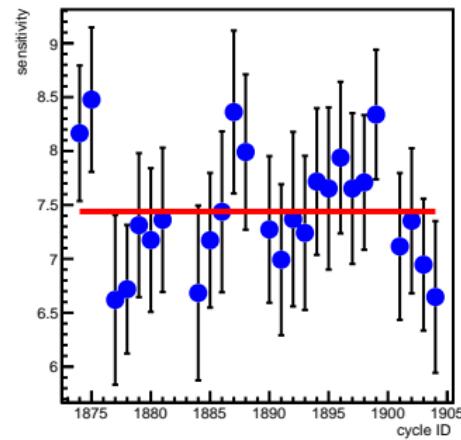
at1_coil6(ppm/count)



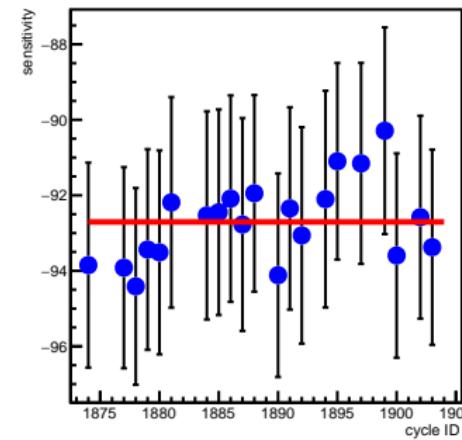
at1_coil7(ppm/count)



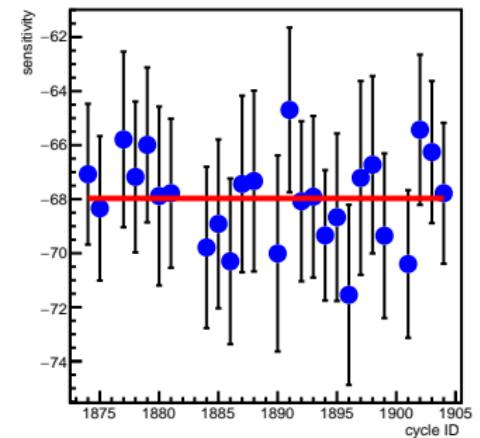
at1_coil2(ppm/count)



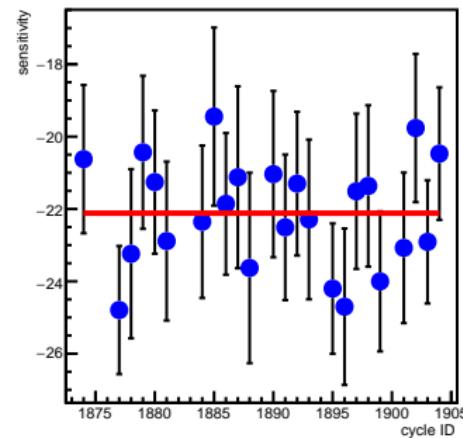
at1_coil5(ppm/count)



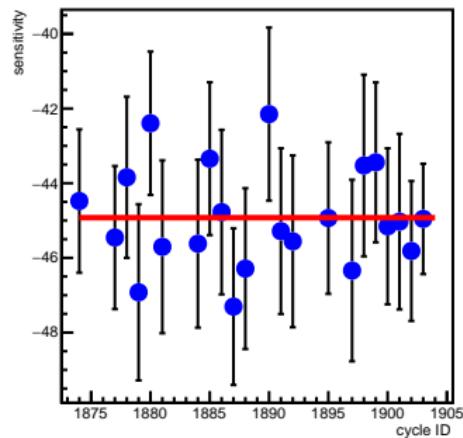
atl2_coil1(ppm/count)



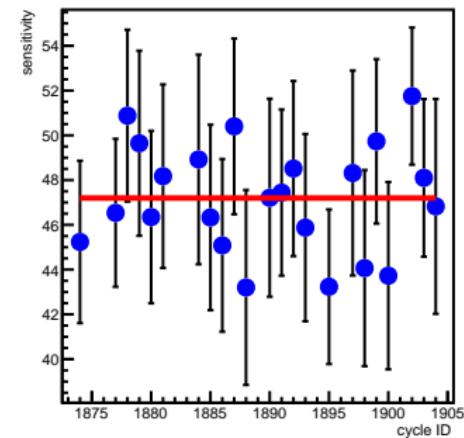
atl2_coil3(ppm/count)



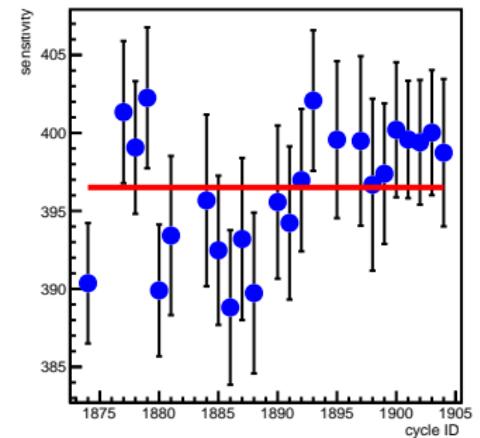
atl2_coil4(ppm/count)



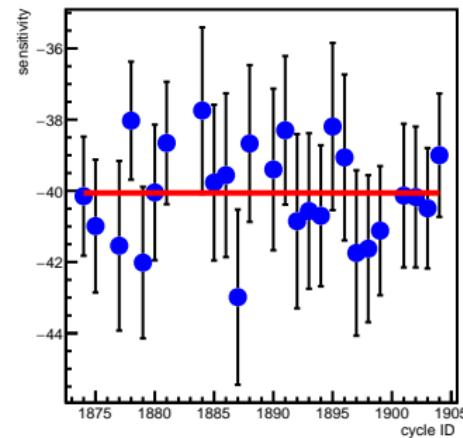
atl2_coil6(ppm/count)



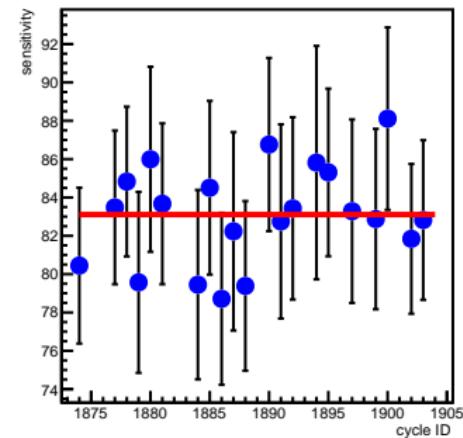
atl2_coil7(ppm/count)



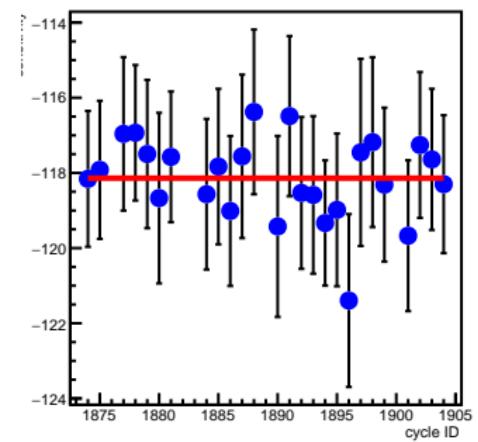
atl2_coil2(ppm/count)



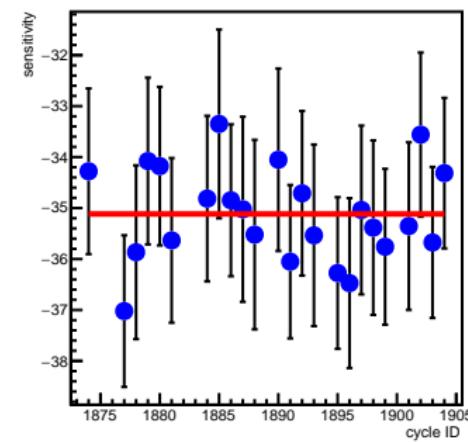
atl2_coil5(ppm/count)



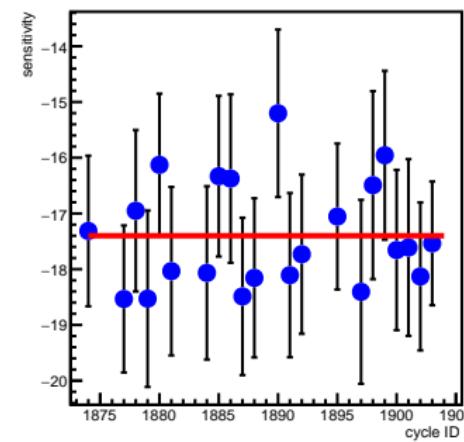
atr1_coil1(ppm/count)



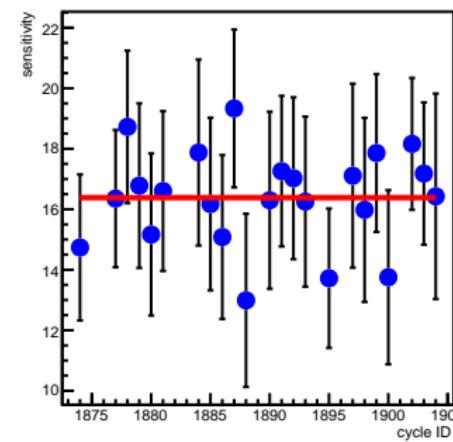
atr1_coil3(ppm/count)



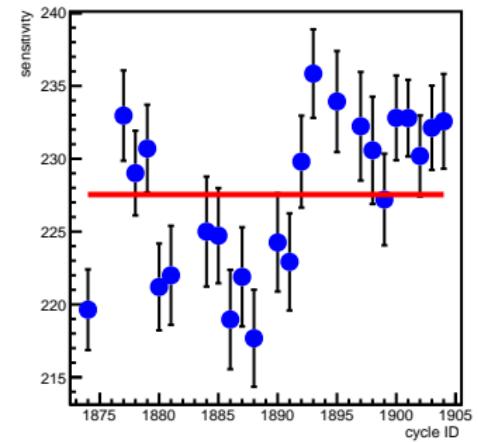
atr1_coil4(ppm/count)



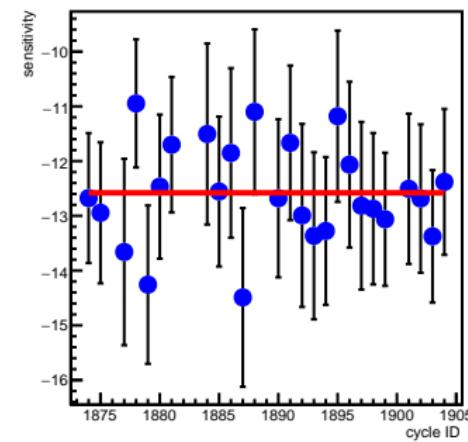
atr1_coil6(ppm/count)



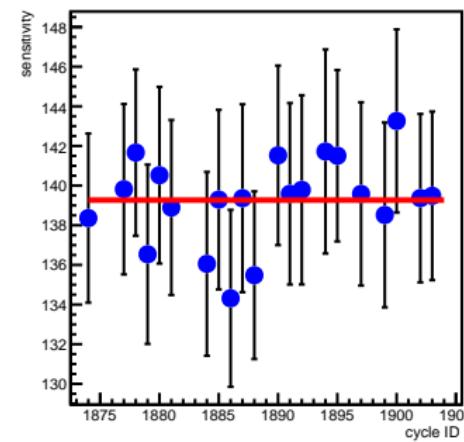
atr1_coil7(ppm/count)



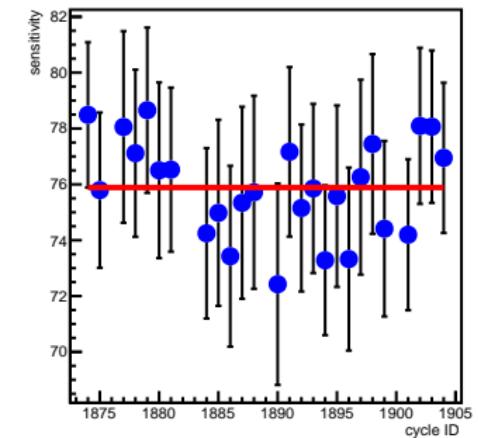
atr1_coil2(ppm/count)



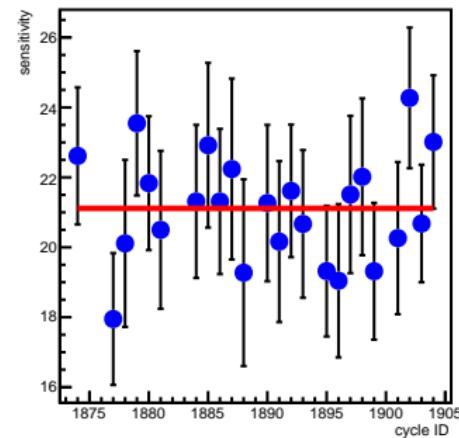
atr1_coil5(ppm/count)



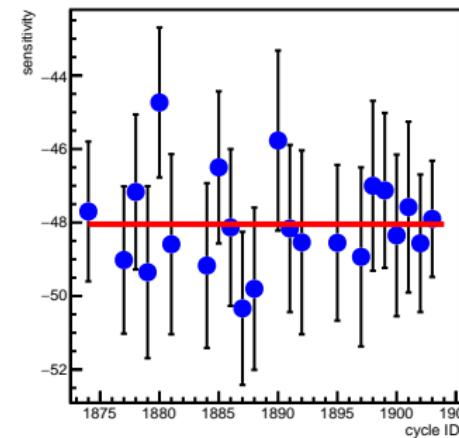
atr2_coil1(ppm/count)



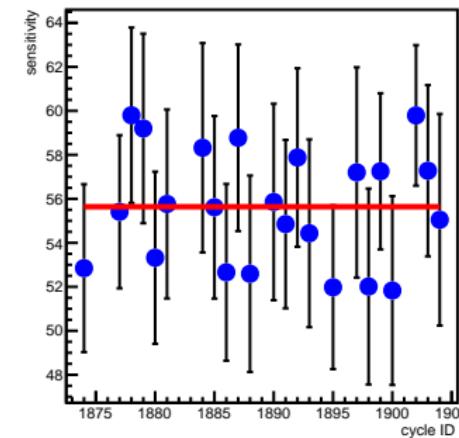
atr2_coil3(ppm/count)



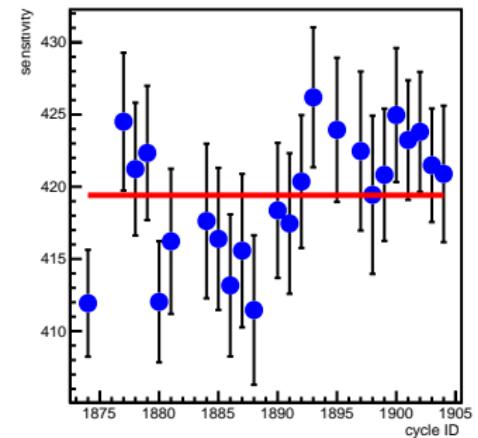
atr2_coil4(ppm/count)



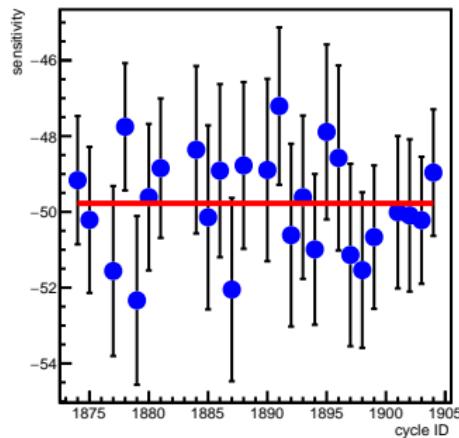
atr2_coil6(ppm/count)



atr2_coil7(ppm/count)



atr2_coil2(ppm/count)



atr2_coil5(ppm/count)

