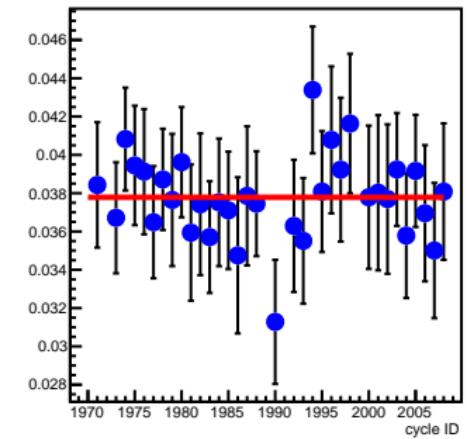
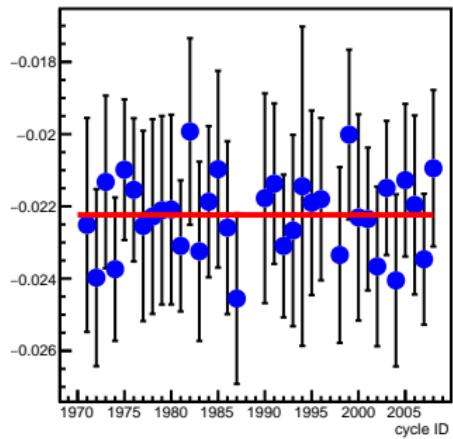


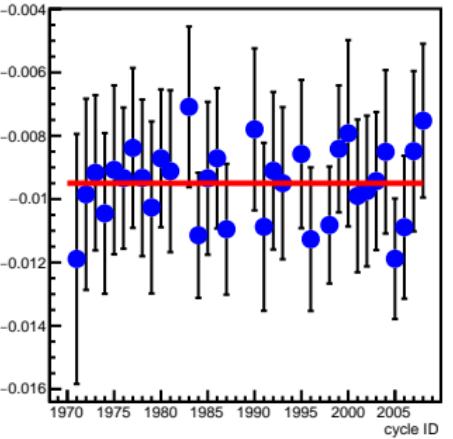
bpm4aX_coil1(um/count)



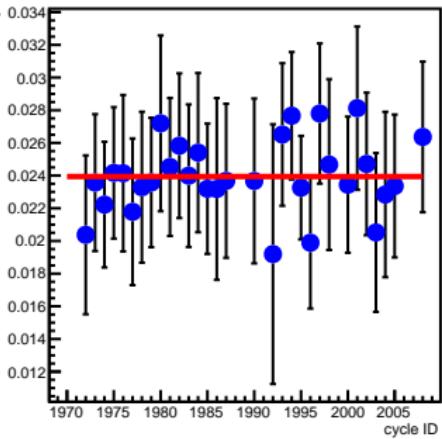
bpm4aX_coil3(um/count)



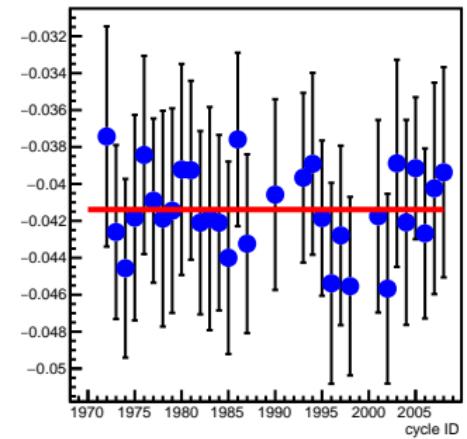
bpm4aX_coil4(um/count)



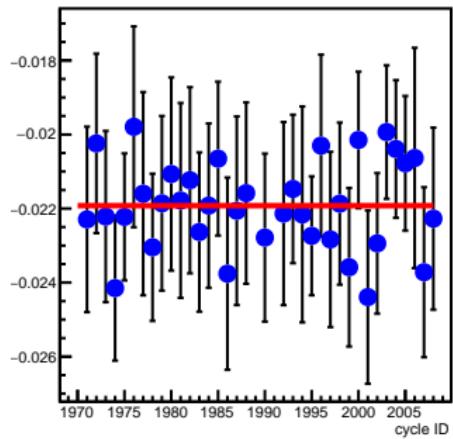
bpm4aX_coil6(um/count)



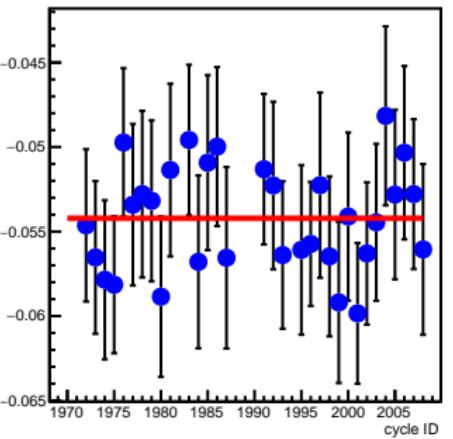
bpm4aX_coil7(um/count)



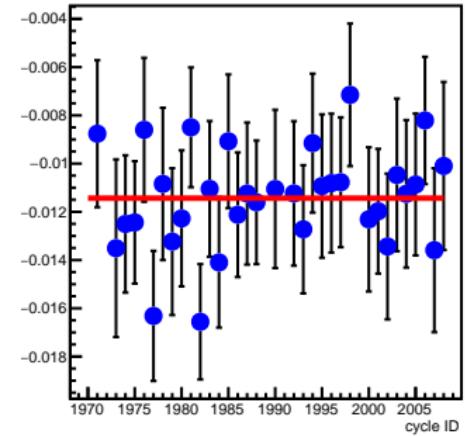
bpm4aX_coil2(um/count)



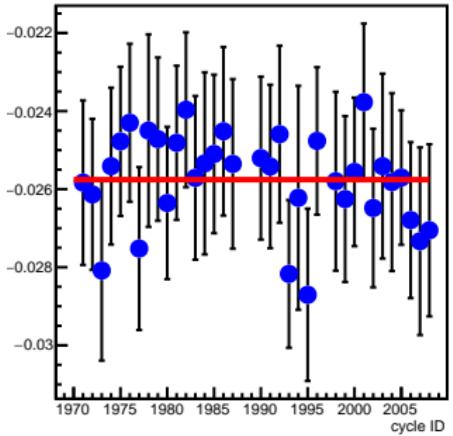
bpm4aX_coil5(um/count)



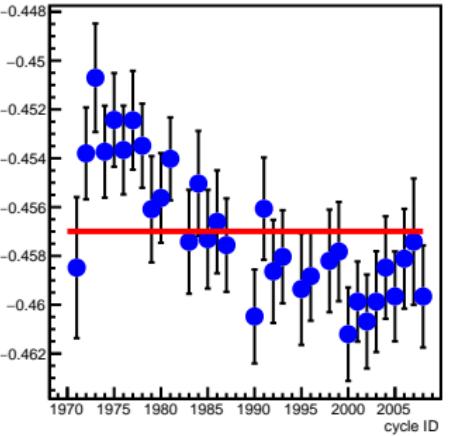
bpm4aY_coil1(um/count)



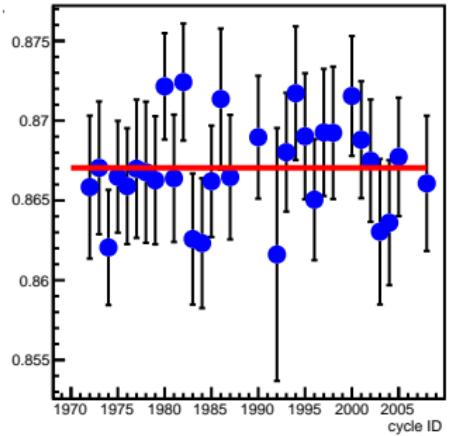
bpm4aY_coil3(um/count)



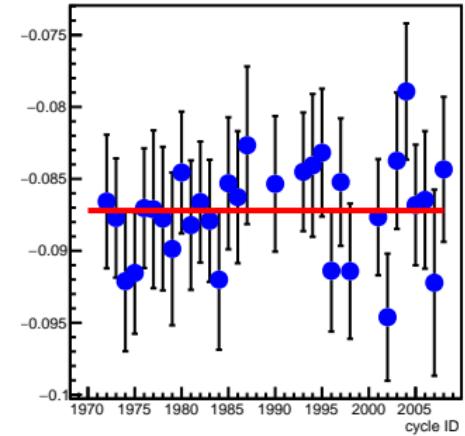
bpm4aY_coil4(um/count)



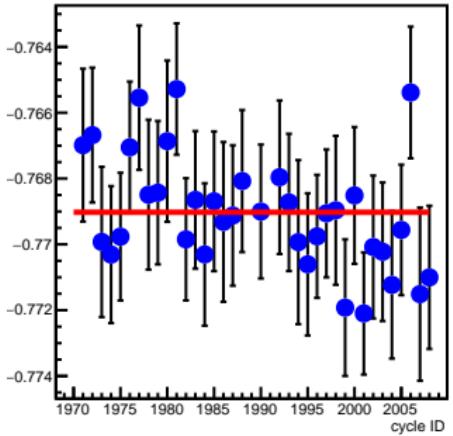
bpm4aY_coil6(um/count)



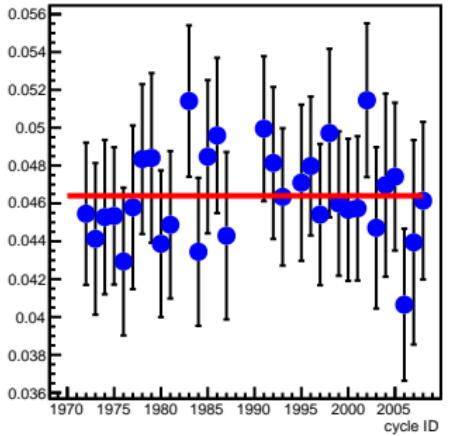
bpm4aY_coil7(um/count)



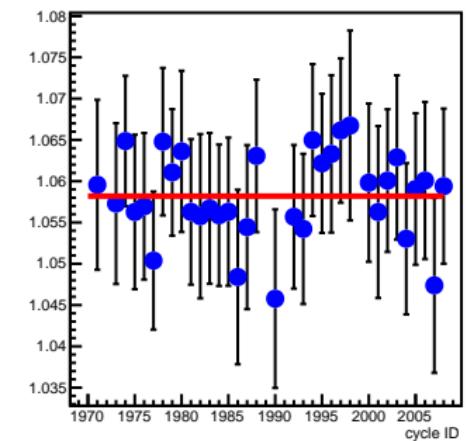
bpm4aY_coil2(um/count)



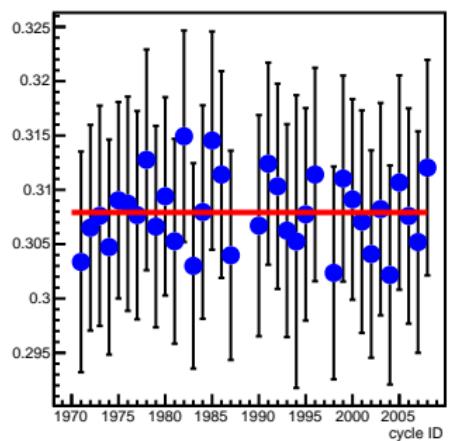
bpm4aY_coil5(um/count)



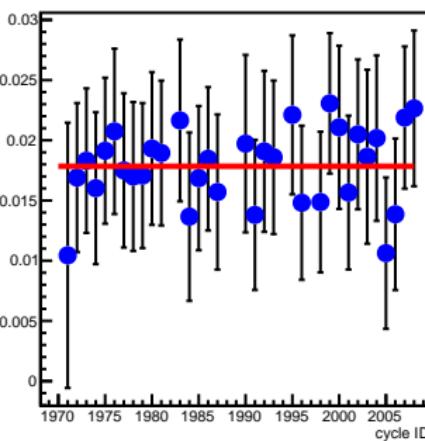
bpm4eX_coil1(um/count)



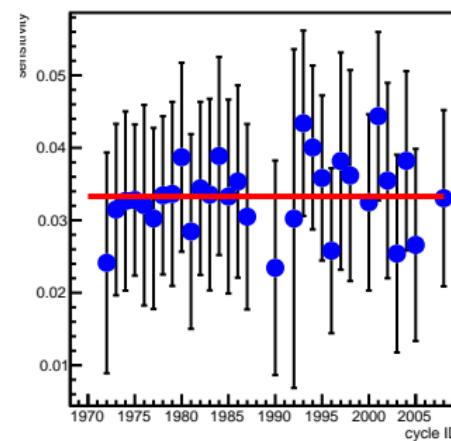
bpm4eX_coil3(um/count)



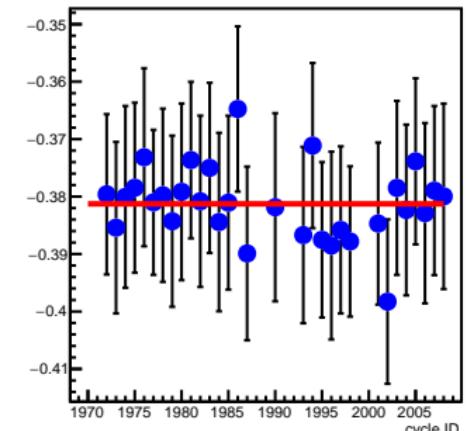
bpm4eX_coil4(um/count)



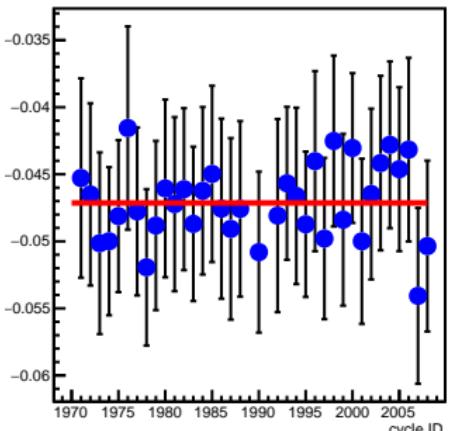
bpm4eX_coil6(um/count)



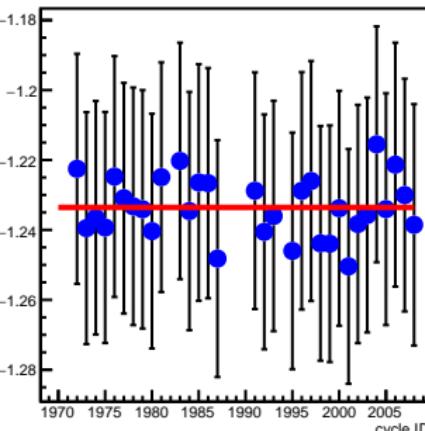
bpm4eX_coil7(um/count)



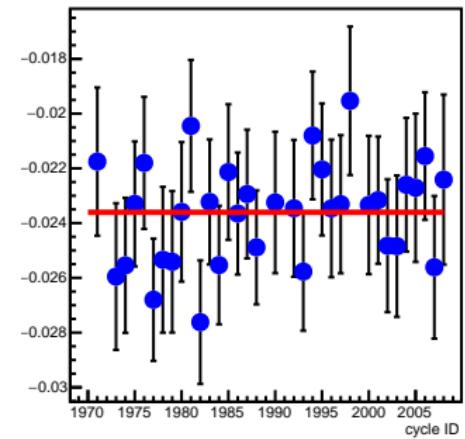
bpm4eX_coil2(um/count)



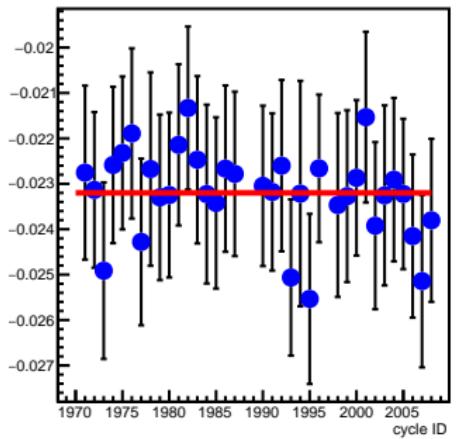
bpm4eX_coil5(um/count)



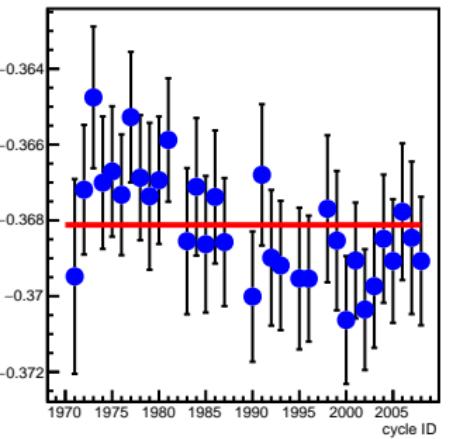
bpm4eY_coil1(um/count)



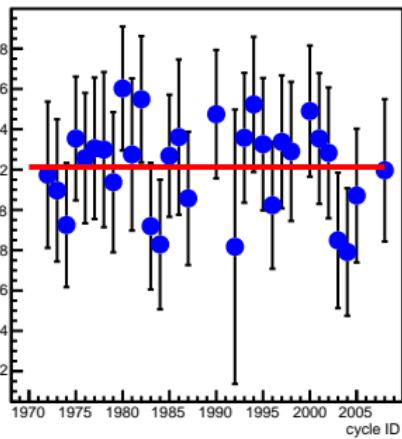
bpm4eY_coil3(um/count)



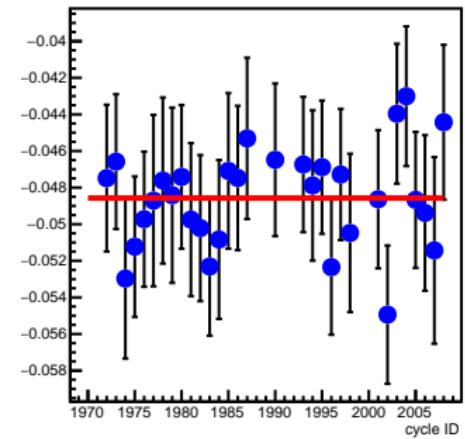
bpm4eY_coil4(um/count)



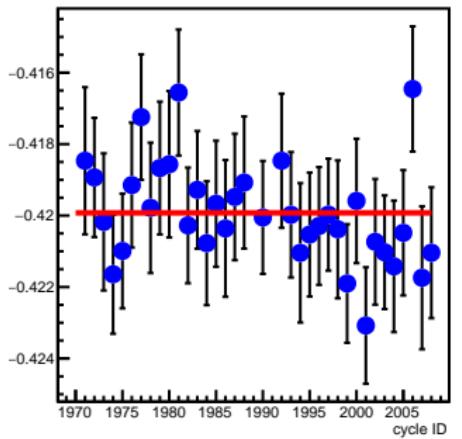
bpm4eY_coil6(um/count)



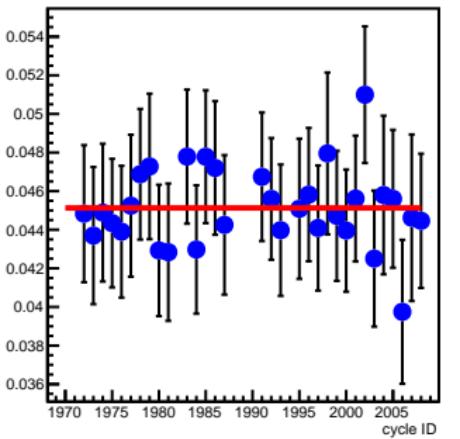
bpm4eY_coil7(um/count)

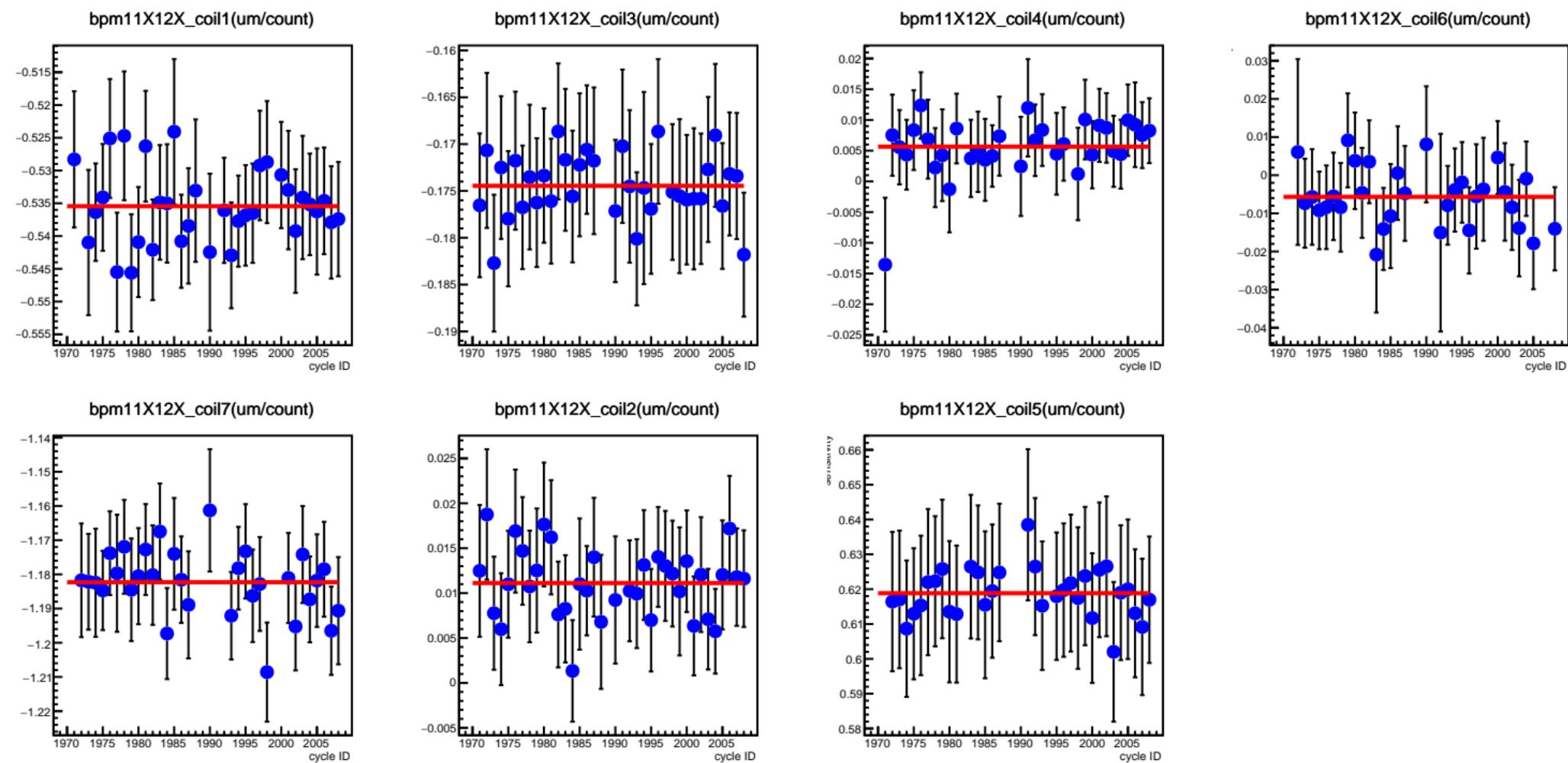


bpm4eY_coil2(um/count)

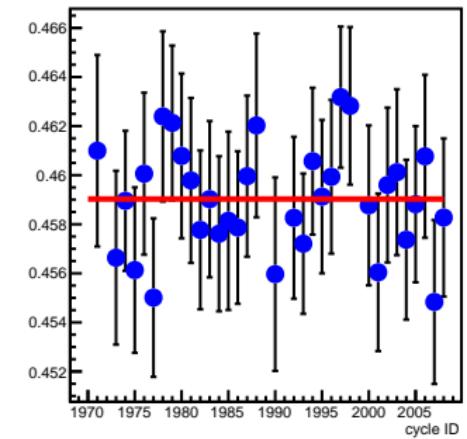


bpm4eY_coil5(um/count)

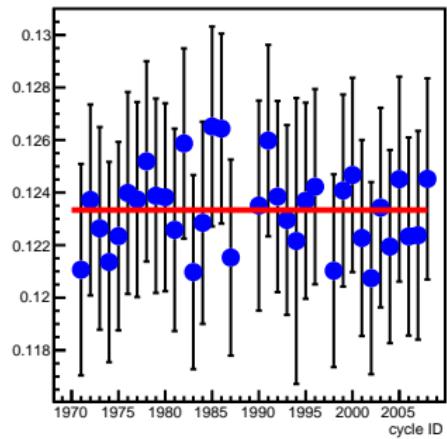




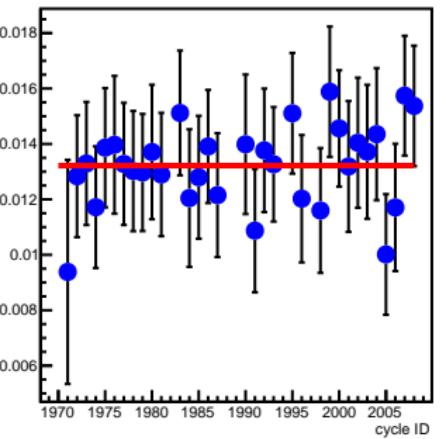
bpm16X_coil1(um/count)



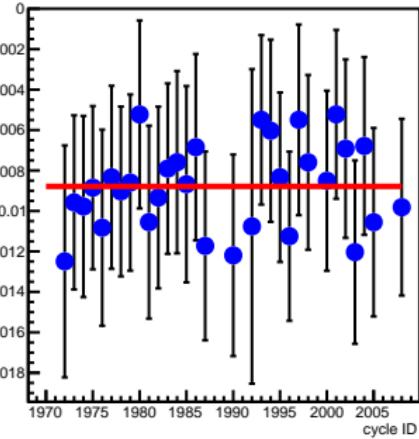
bpm16X_coil3(um/count)



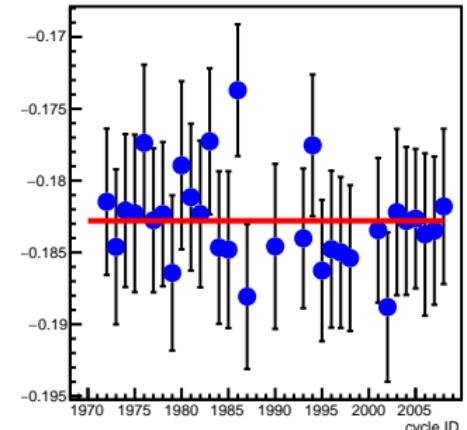
bpm16X_coil4(um/count)



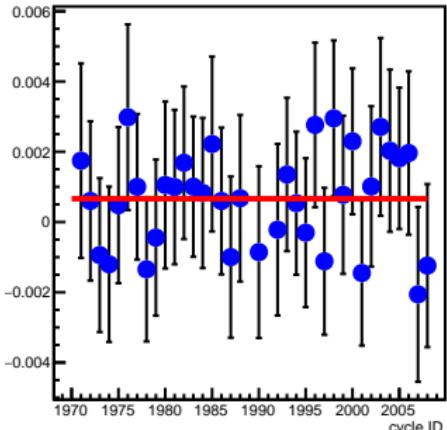
bpm16X_coil6(um/count)



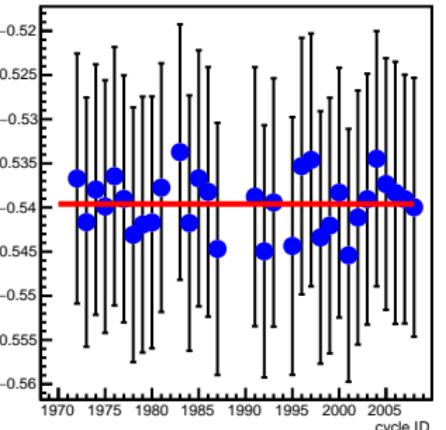
bpm16X_coil7(um/count)

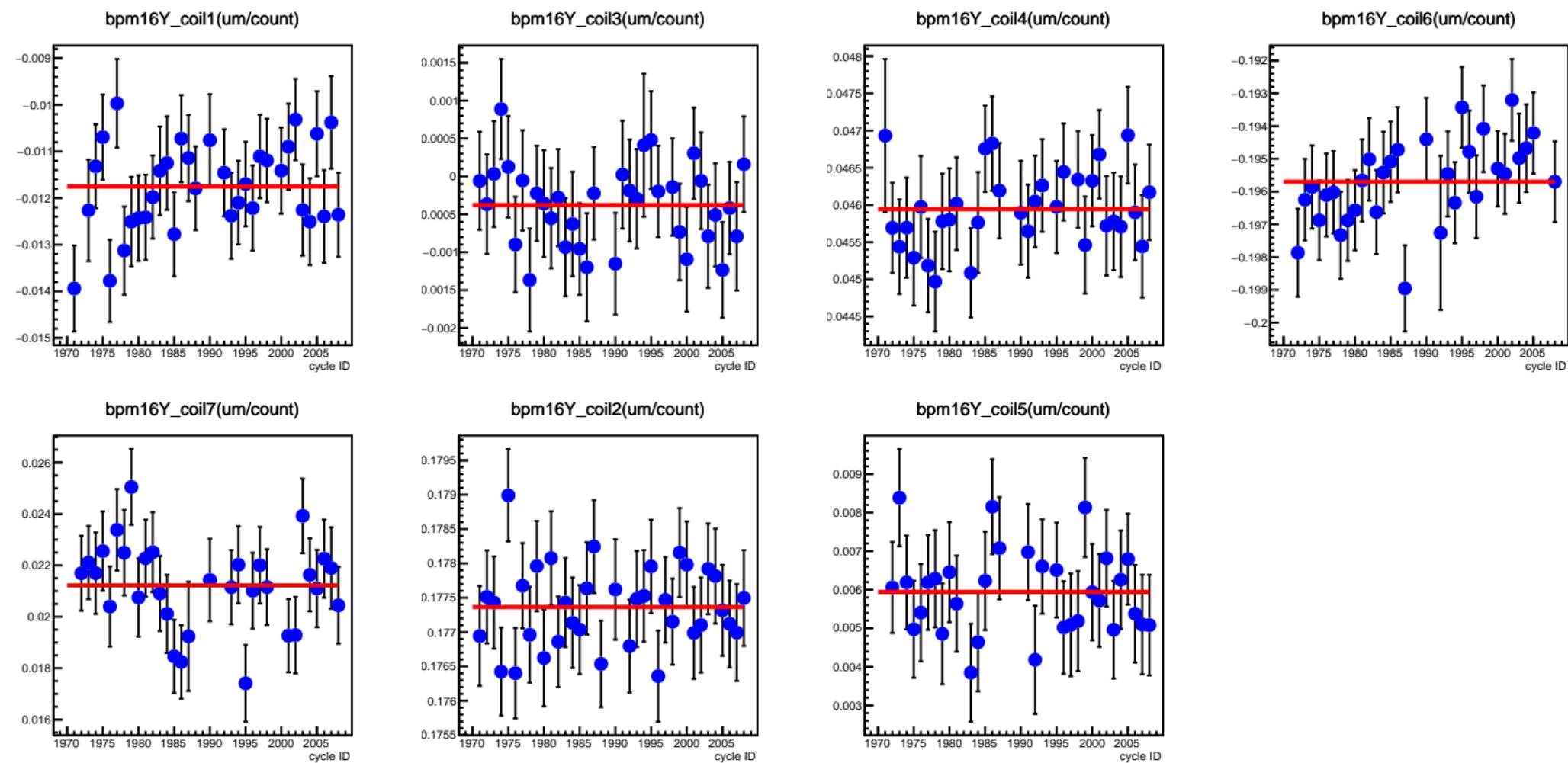


bpm16X_coil2(um/count)

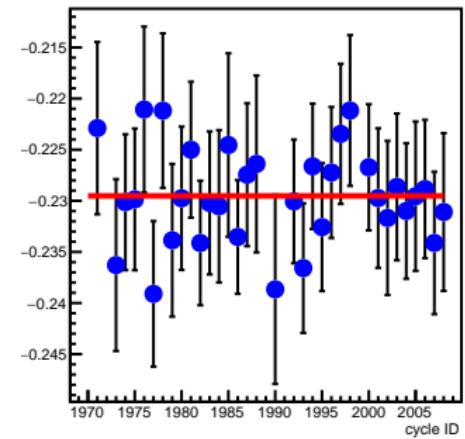


bpm16X_coil5(um/count)

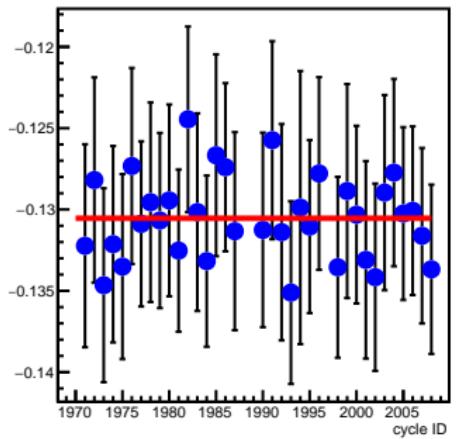




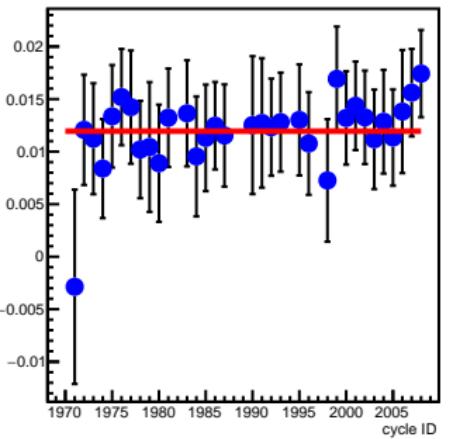
bpm12X_coil1(um/count)



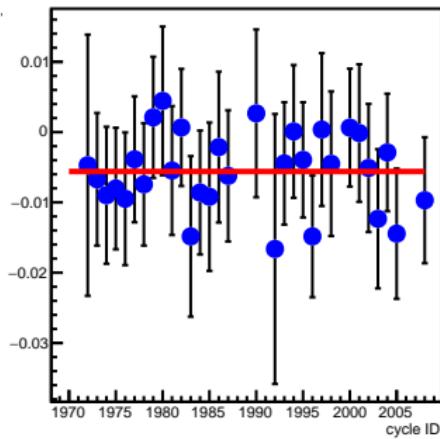
bpm12X_coil3(um/count)



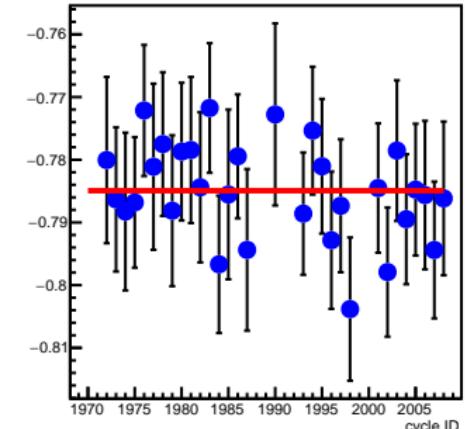
bpm12X_coil4(um/count)



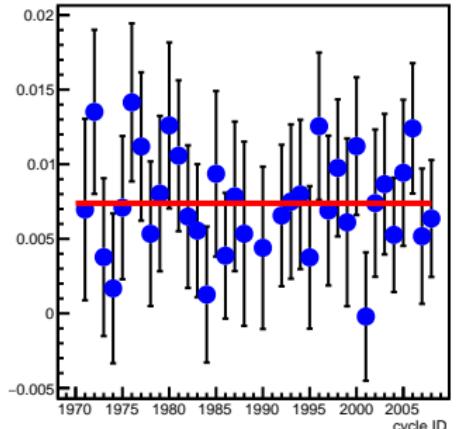
bpm12X_coil6(um/count)



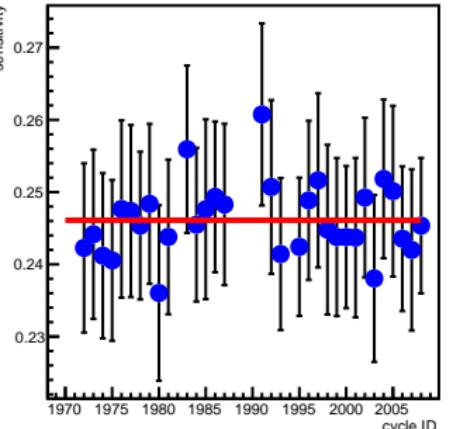
bpm12X_coil7(um/count)

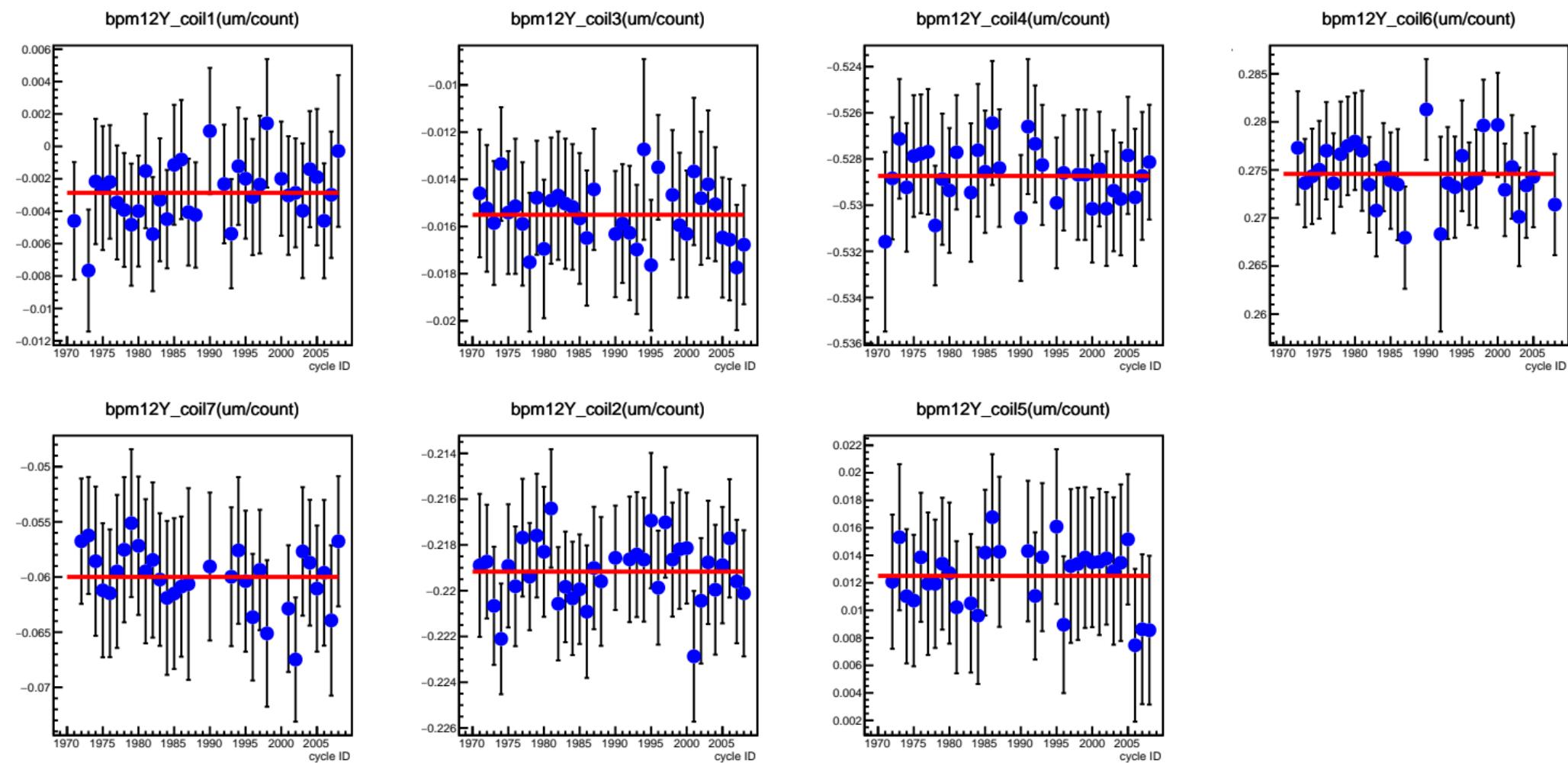


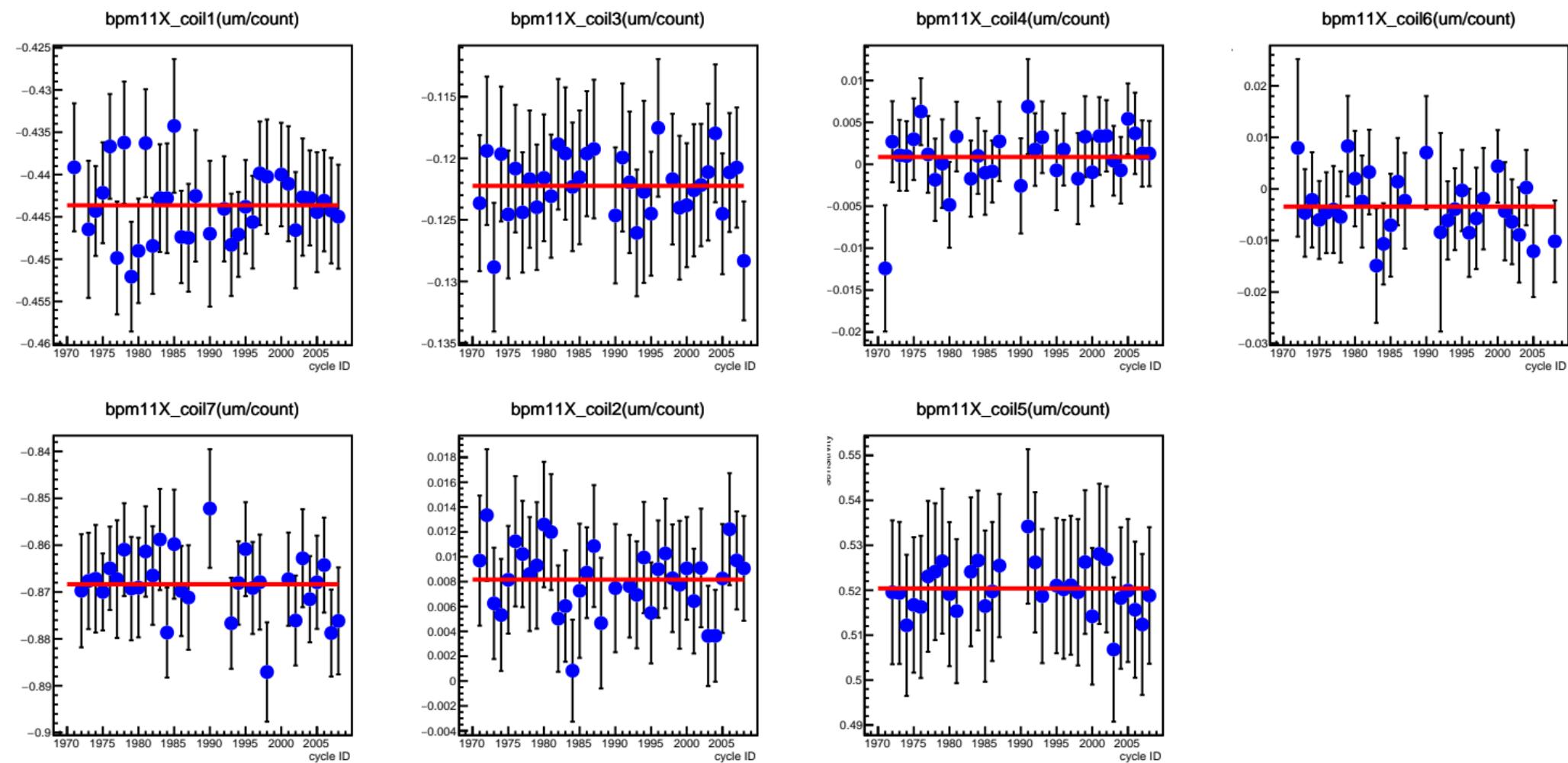
bpm12X_coil2(um/count)



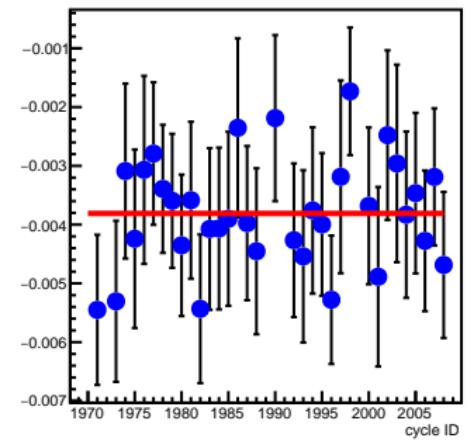
bpm12X_coil5(um/count)



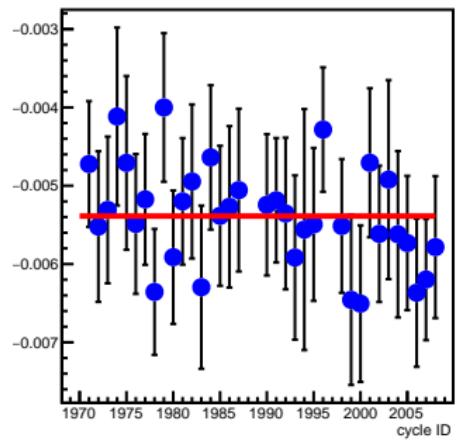




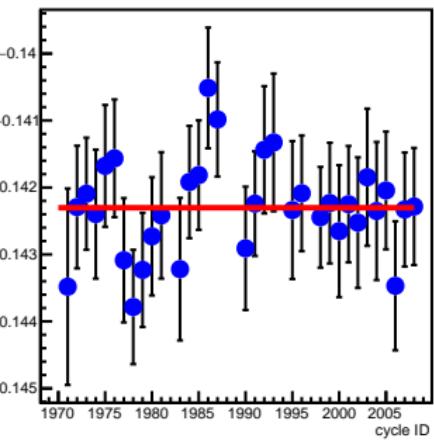
bpm11Y_coil1(um/count)



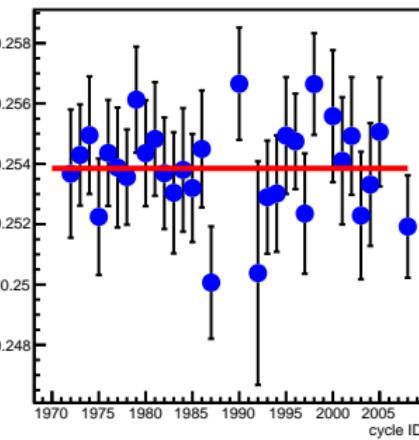
bpm11Y_coil3(um/count)



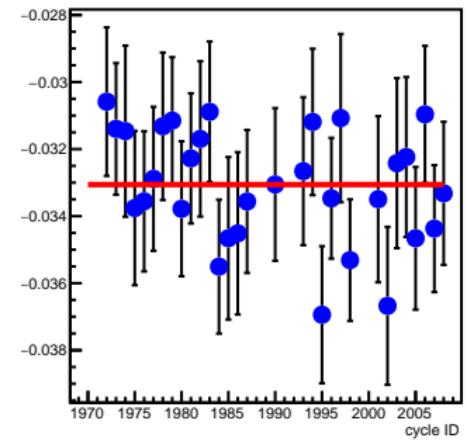
bpm11Y_coil4(um/count)



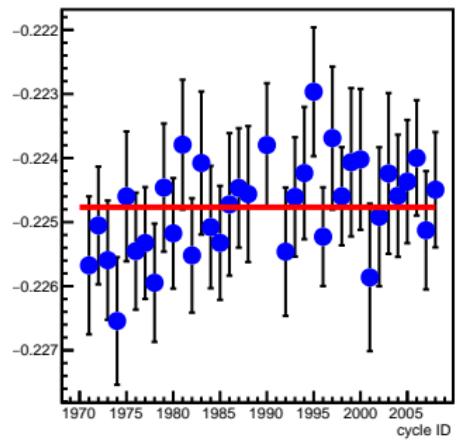
bpm11Y_coil6(um/count)



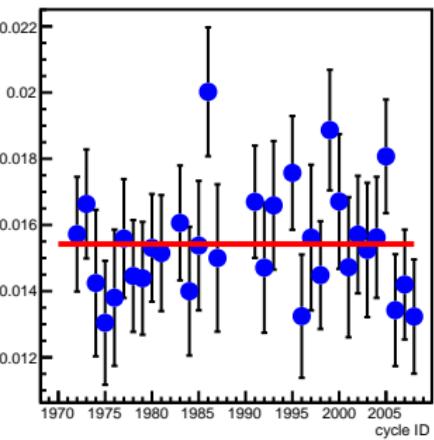
bpm11Y_coil7(um/count)



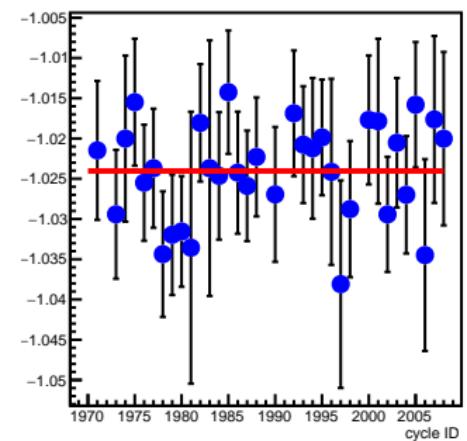
bpm11Y_coil2(um/count)



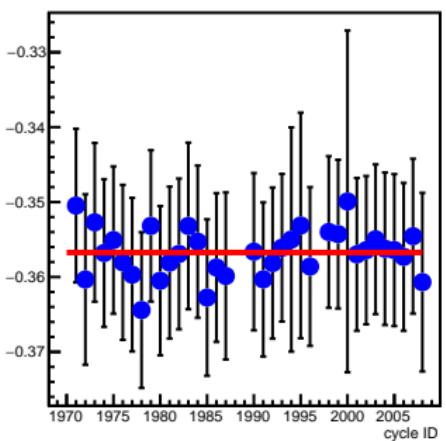
bpm11Y_coil5(um/count)



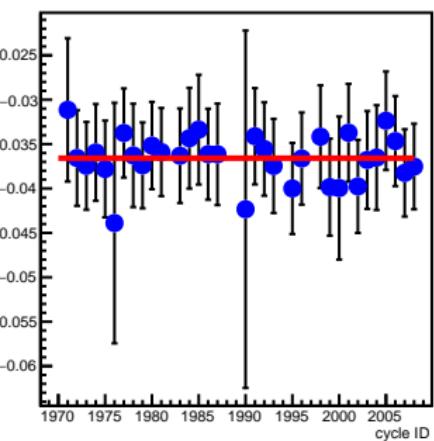
bpm1X_coil1(um/count)



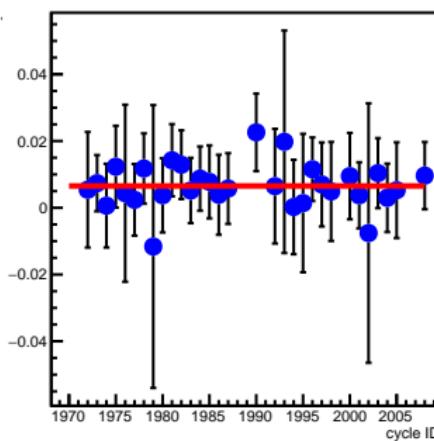
bpm1X_coil3(um/count)



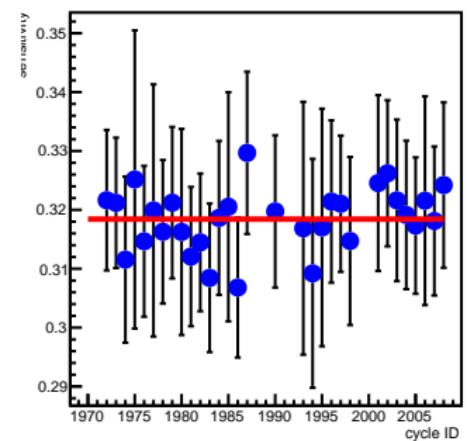
bpm1X_coil4(um/count)



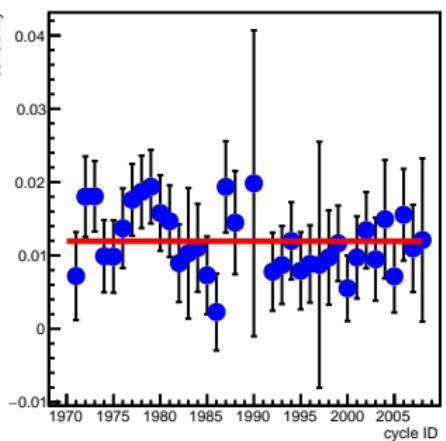
bpm1X_coil6(um/count)



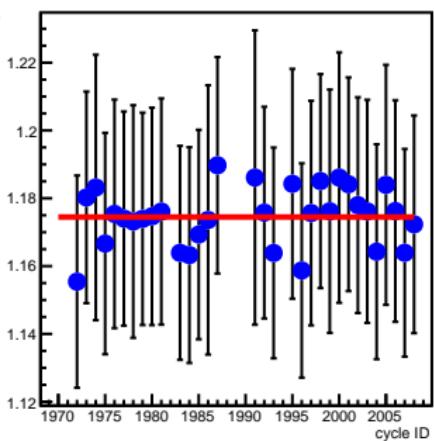
bpm1X_coil7(um/count)



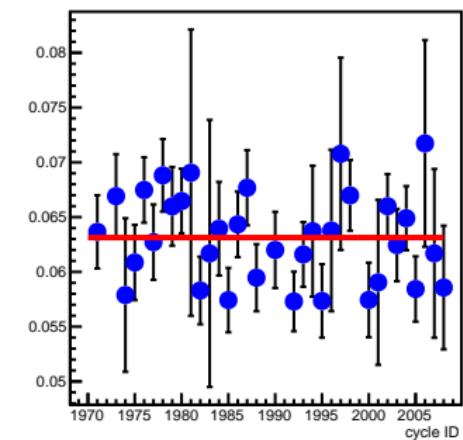
bpm1X_coil2(um/count)



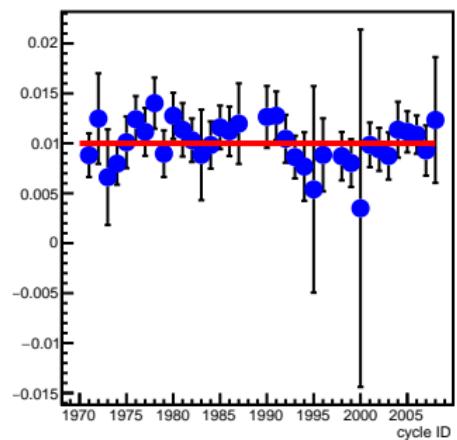
bpm1X_coil5(um/count)



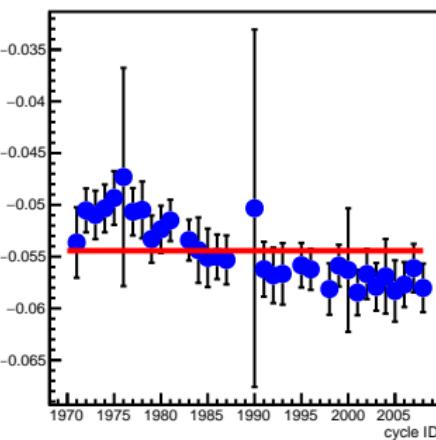
bpm1Y_coil1(um/count)



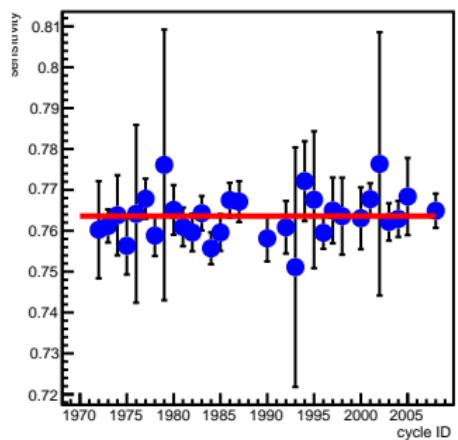
bpm1Y_coil3(um/count)



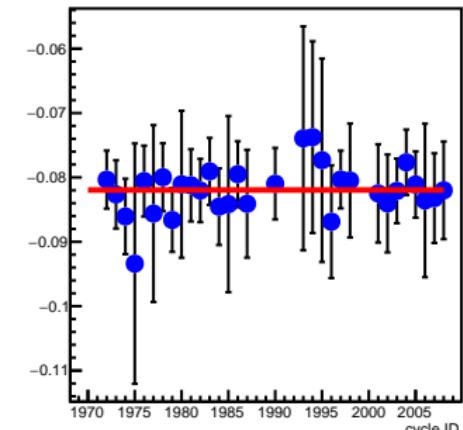
bpm1Y_coil4(um/count)



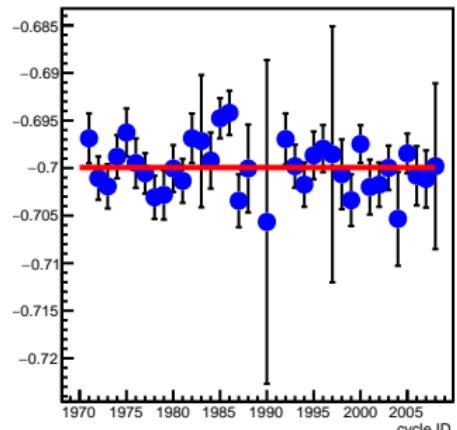
bpm1Y_coil6(um/count)



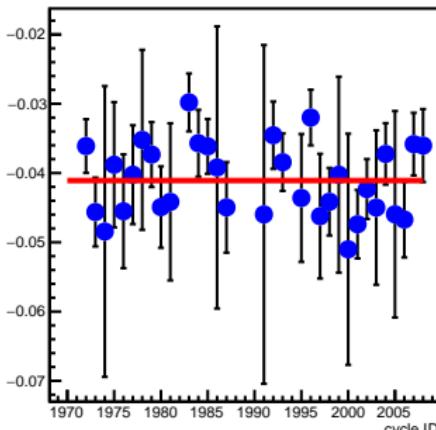
bpm1Y_coil7(um/count)

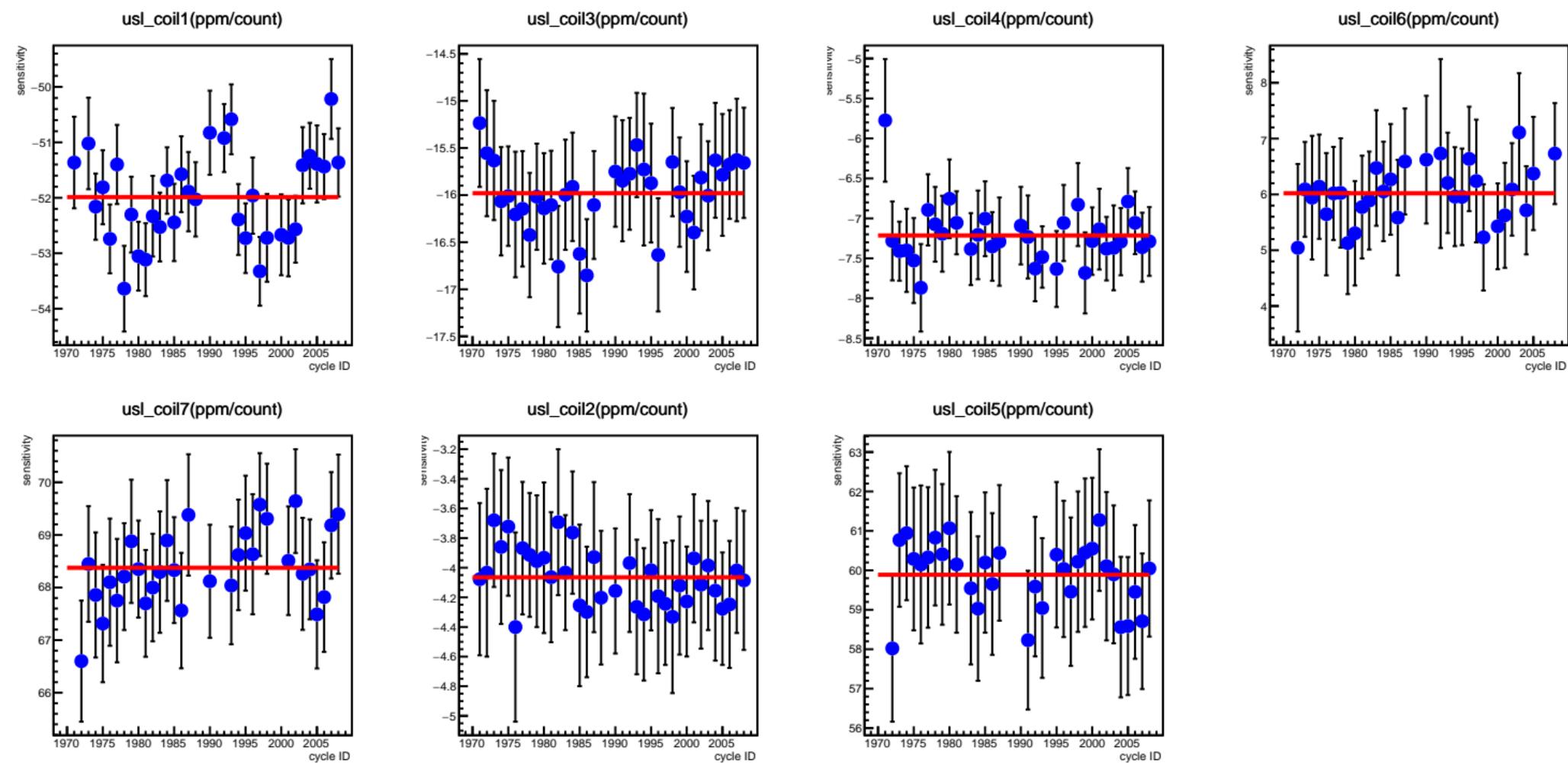


bpm1Y_coil2(um/count)

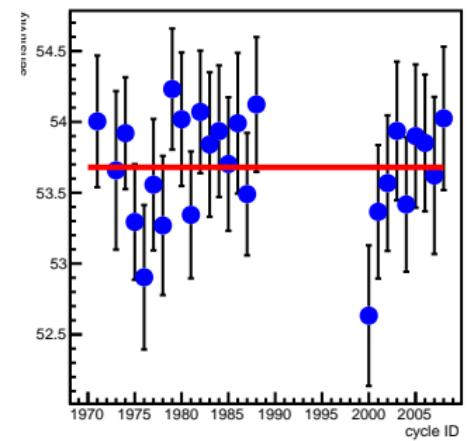


bpm1Y_coil5(um/count)

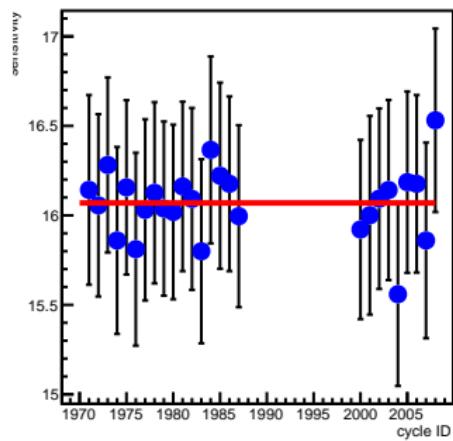




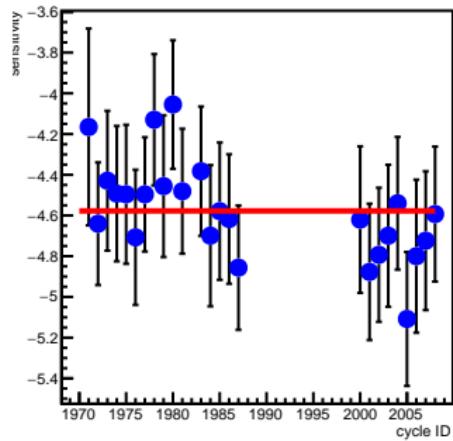
usr_coil1(ppm/count)



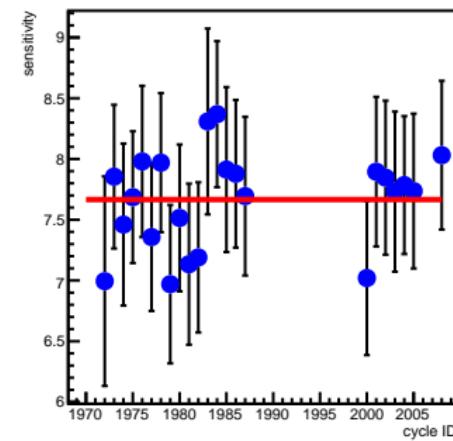
usr_coil3(ppm/count)



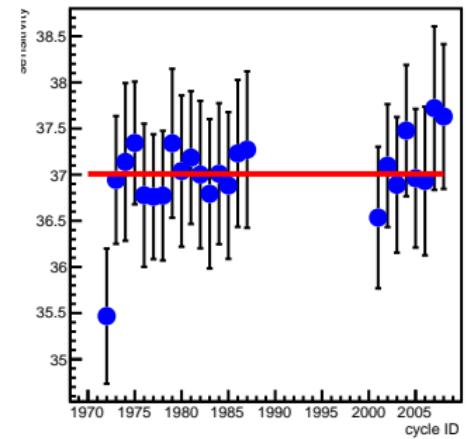
usr_coil4(ppm/count)



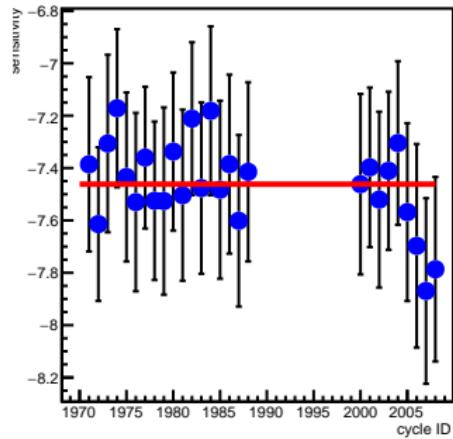
usr_coil6(ppm/count)



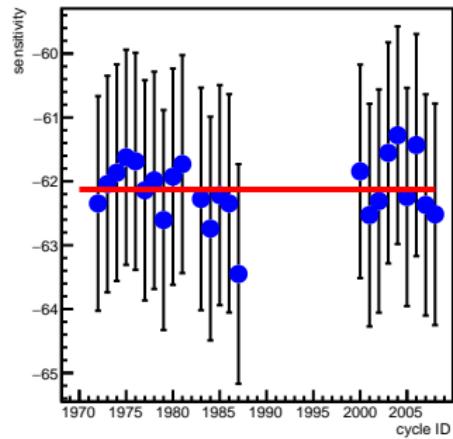
usr_coil7(ppm/count)

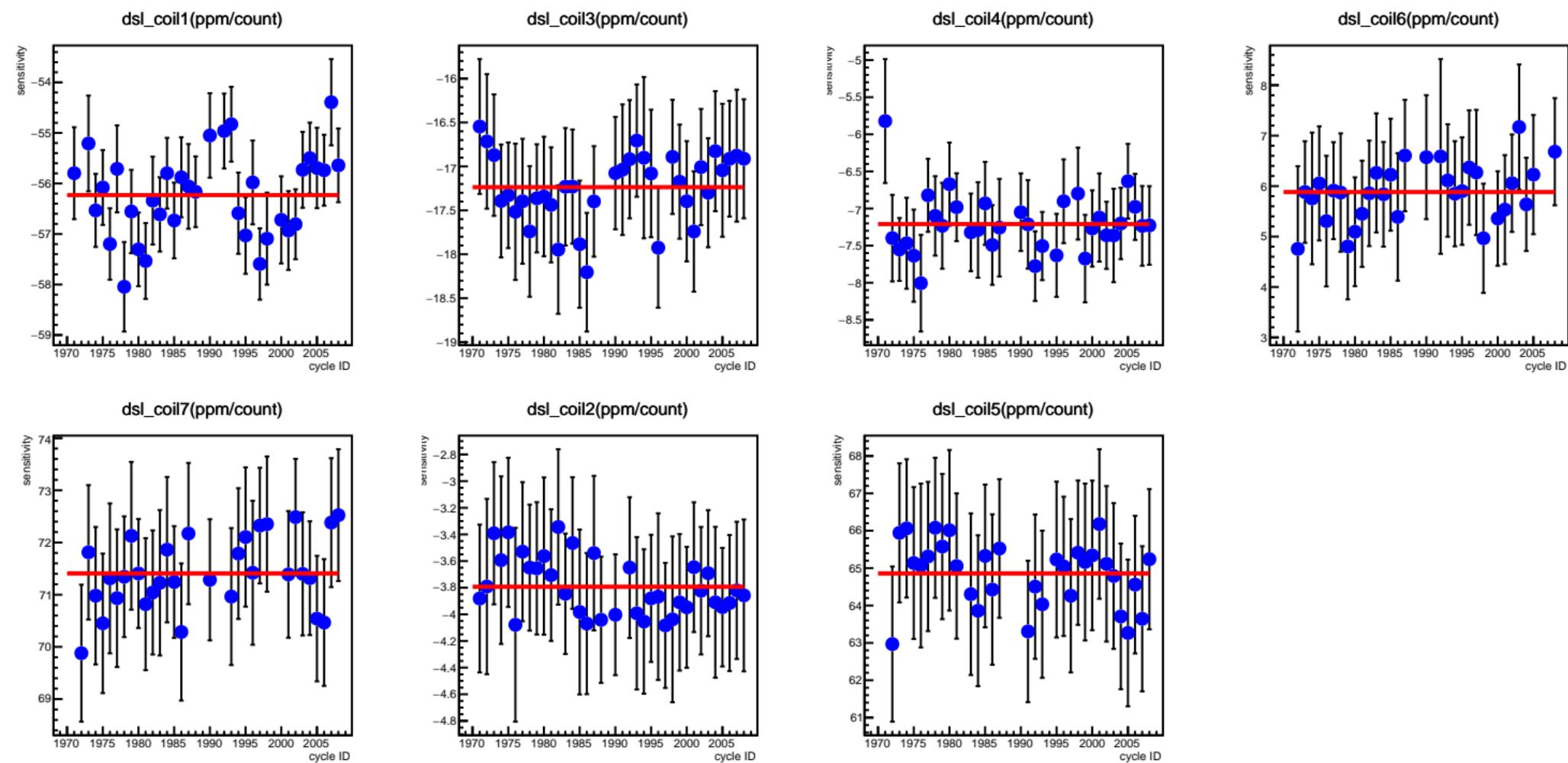


usr_coil2(ppm/count)

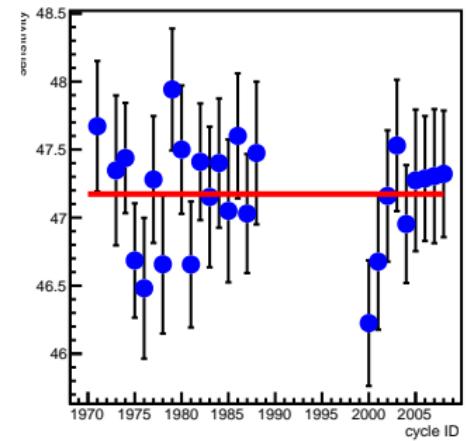


usr_coil5(ppm/count)

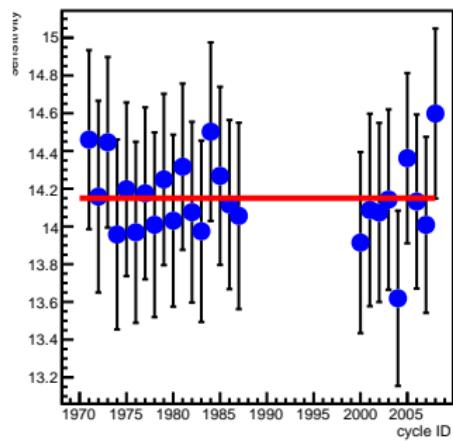




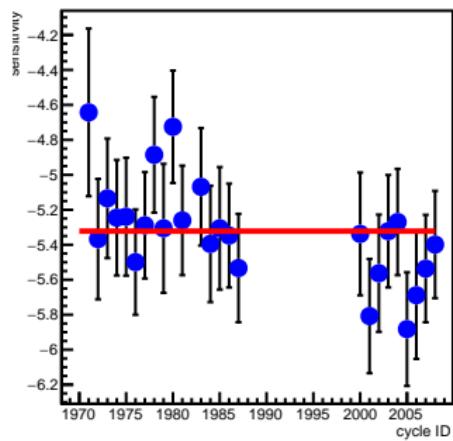
dsr_coil1(ppm/count)



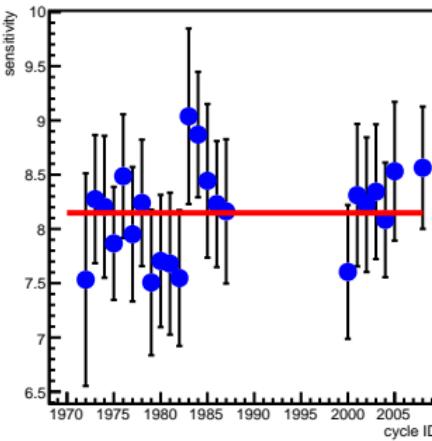
dsr_coil3(ppm/count)



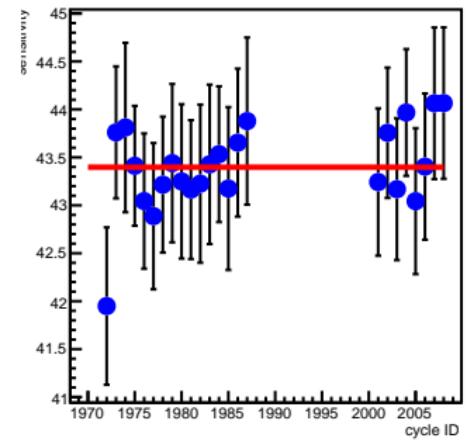
dsr_coil4(ppm/count)



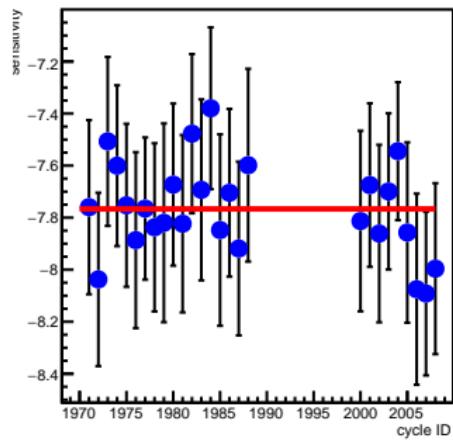
dsr_coil6(ppm/count)



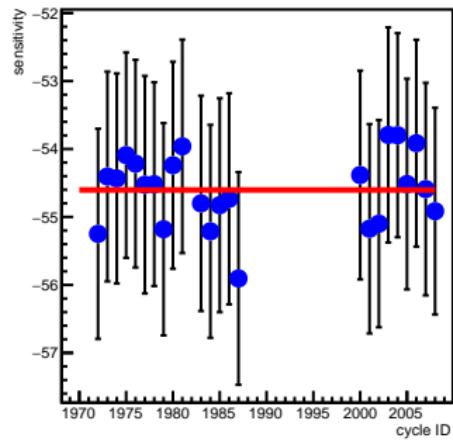
dsr_coil7(ppm/count)



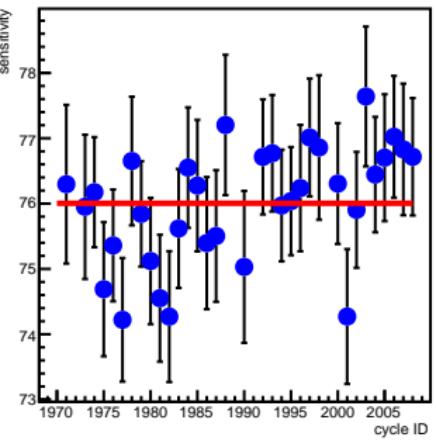
dsr_coil2(ppm/count)



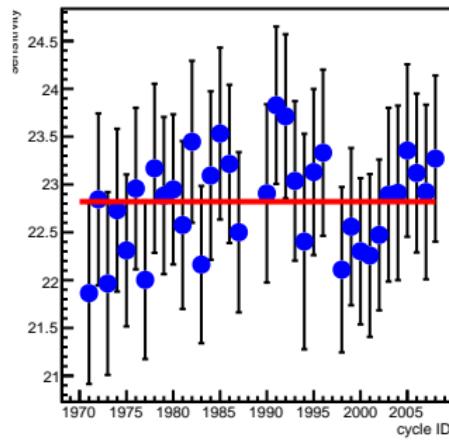
dsr_coil5(ppm/count)



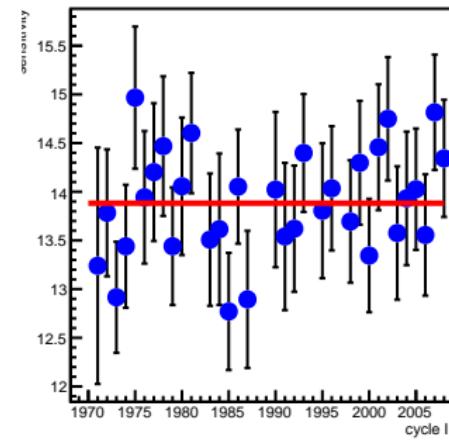
atl1_coil1(ppm/count)



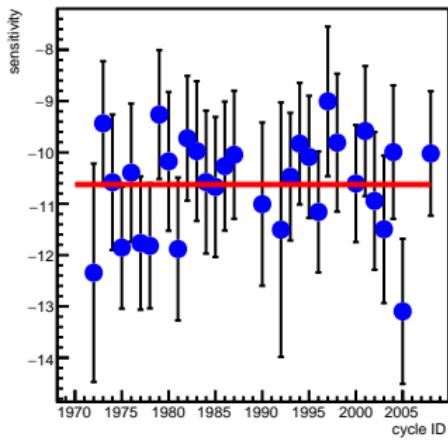
atl1_coil3(ppm/count)



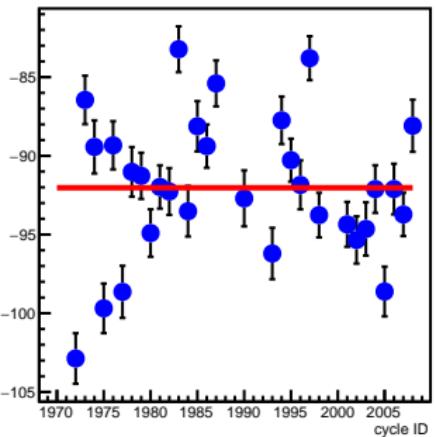
atl1_coil4(ppm/count)



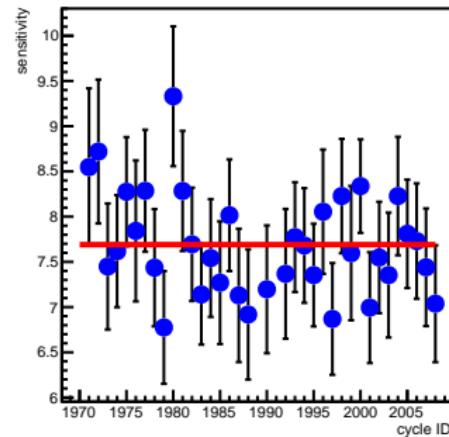
atl1_coil6(ppm/count)



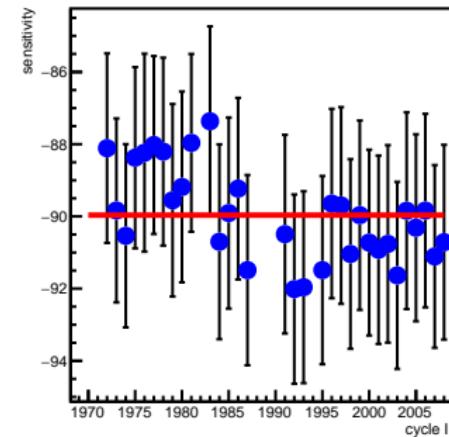
atl1_coil7(ppm/count)



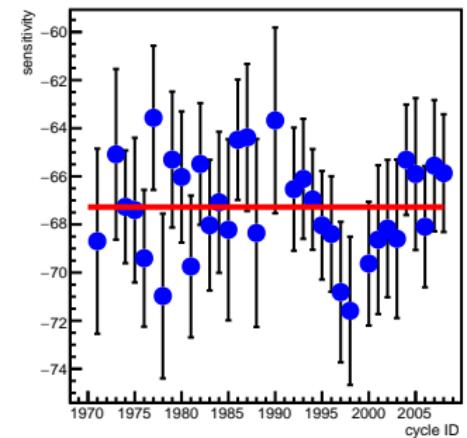
atl1_coil2(ppm/count)



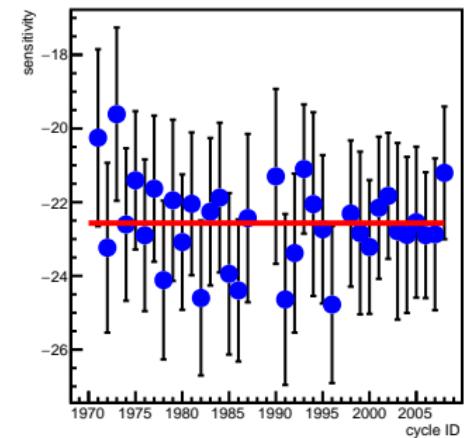
atl1_coil5(ppm/count)



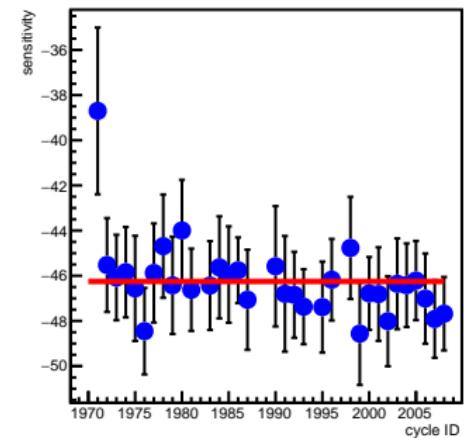
atl2_coil1(ppm/count)



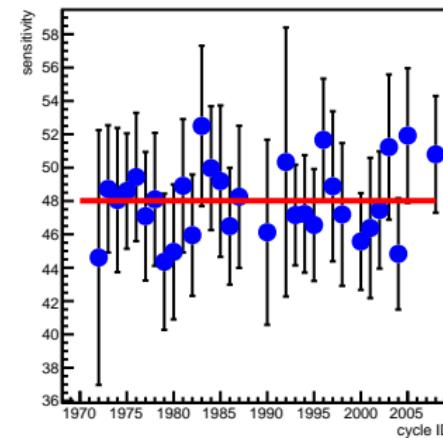
atl2_coil3(ppm/count)



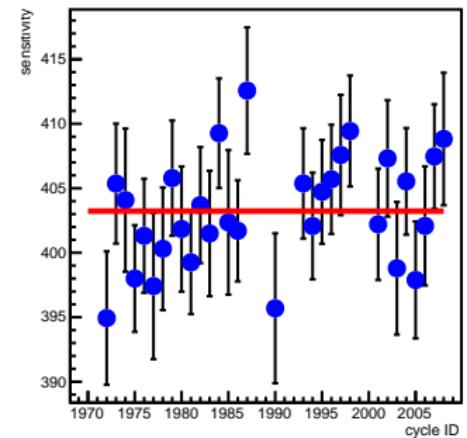
atl2_coil4(ppm/count)



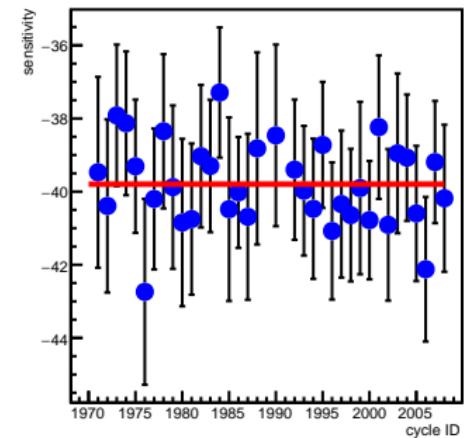
atl2_coil6(ppm/count)



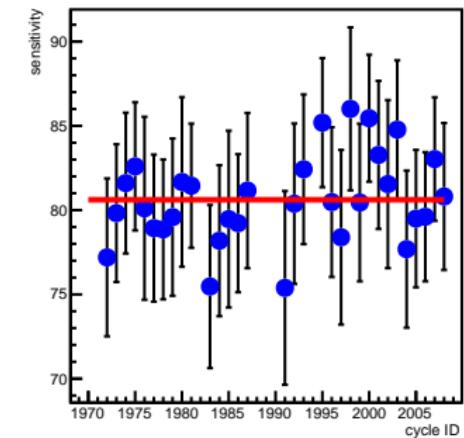
atl2_coil7(ppm/count)



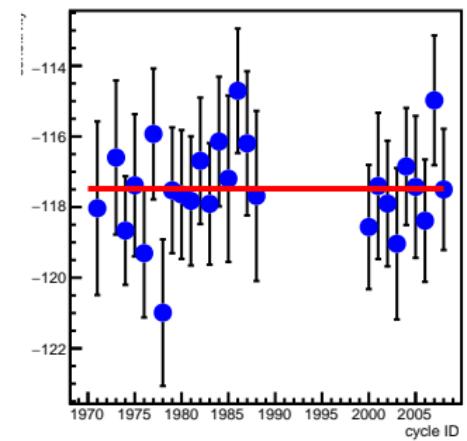
atl2_coil2(ppm/count)



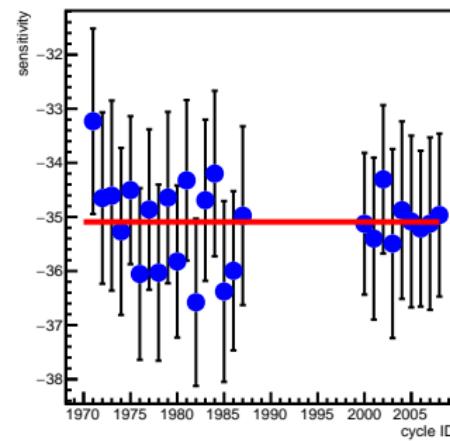
atl2_coil5(ppm/count)



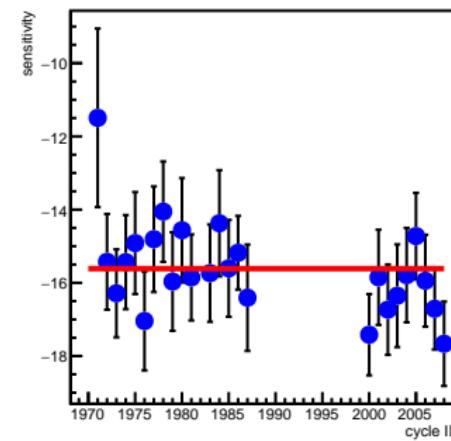
atr1_coil1(ppm/count)



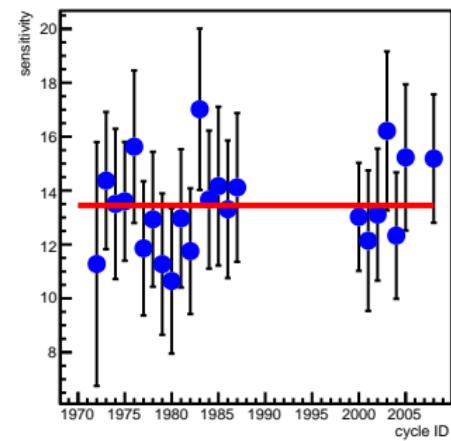
atr1_coil3(ppm/count)



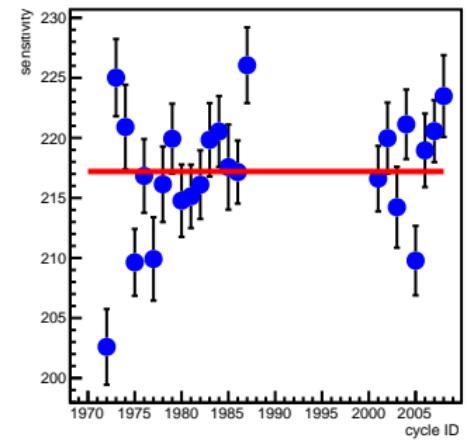
atr1_coil4(ppm/count)



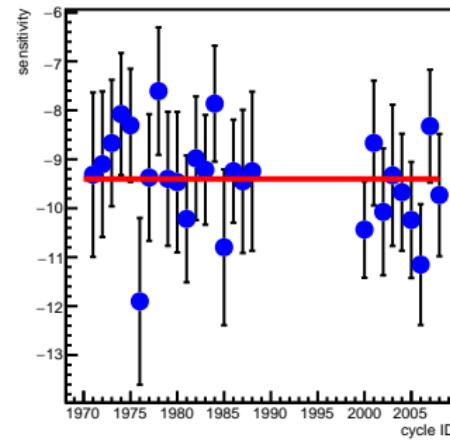
atr1_coil6(ppm/count)



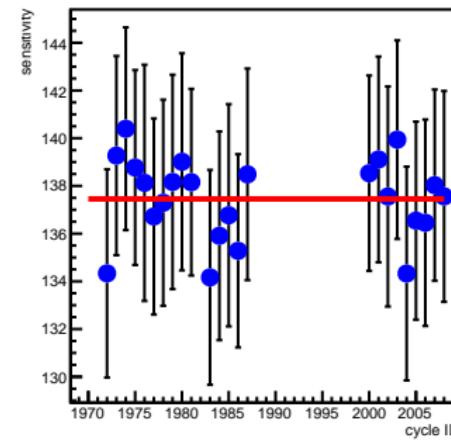
atr1_coil7(ppm/count)



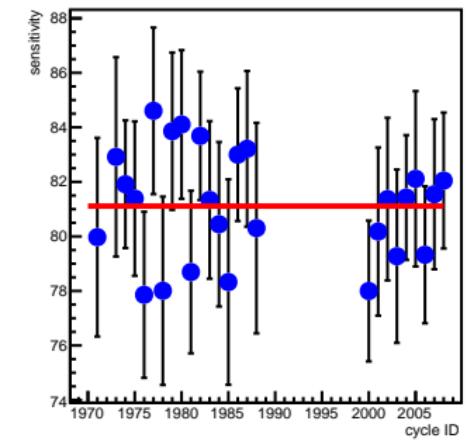
atr1_coil2(ppm/count)



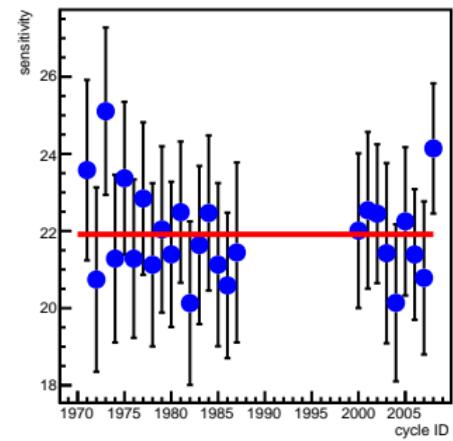
atr1_coil5(ppm/count)



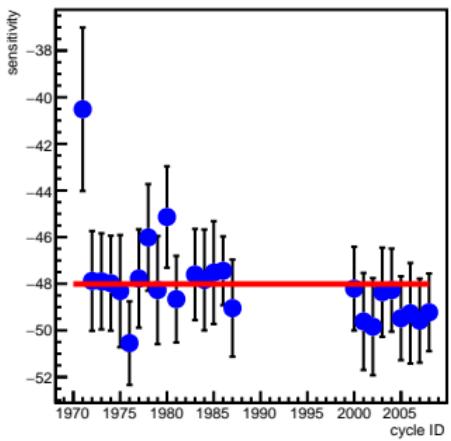
atr2_coil1(ppm/count)



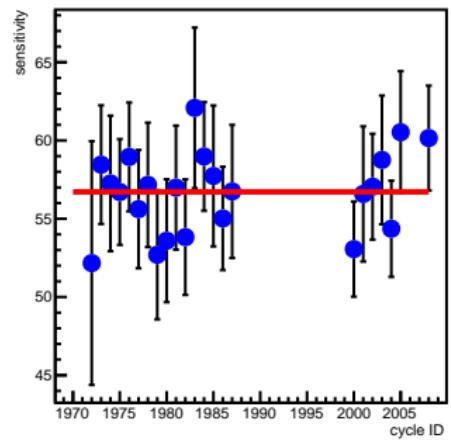
atr2_coil3(ppm/count)



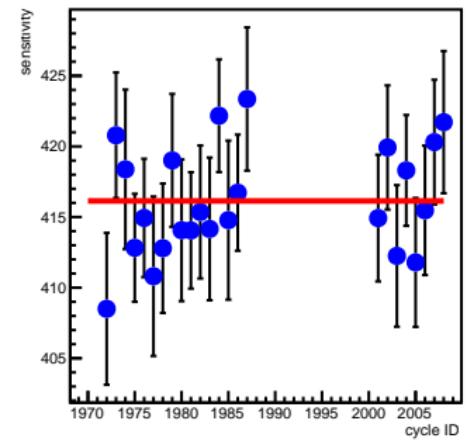
atr2_coil4(ppm/count)



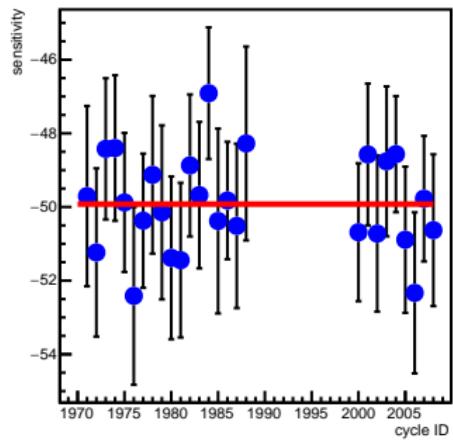
atr2_coil6(ppm/count)



atr2_coil7(ppm/count)



atr2_coil2(ppm/count)



atr2_coil5(ppm/count)

