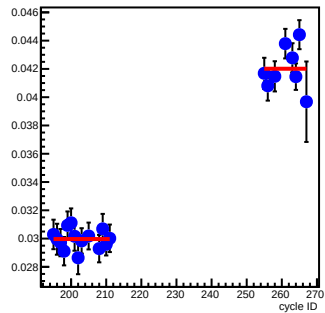
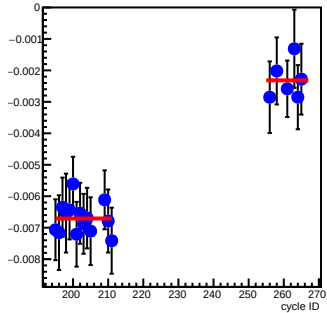


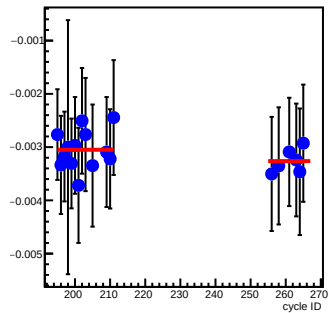
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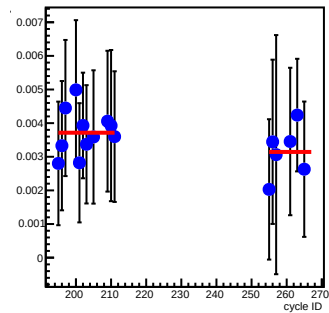
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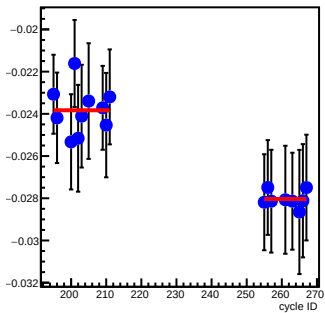
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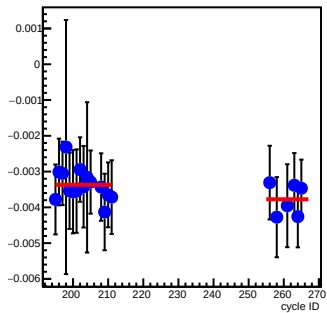
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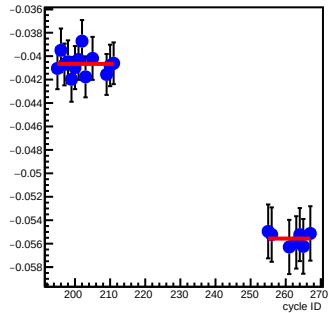
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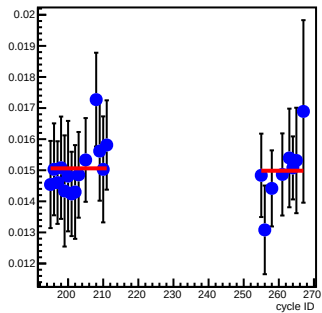
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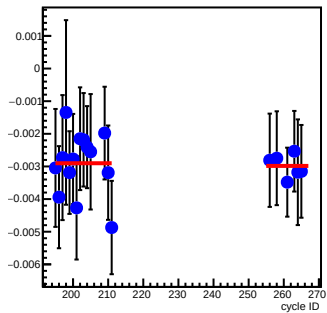
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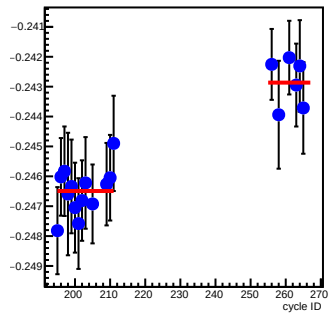
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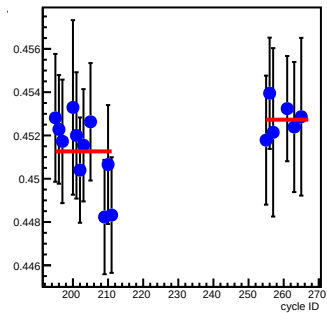
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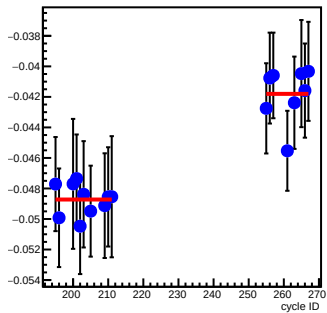
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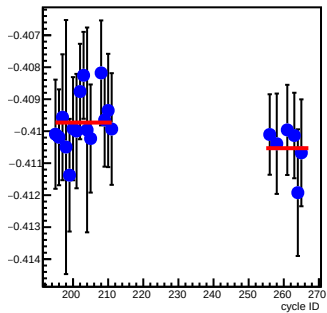
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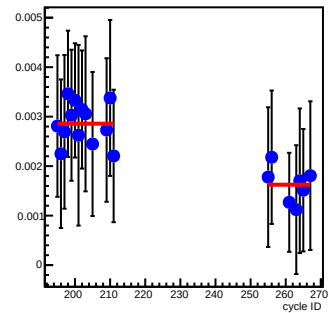
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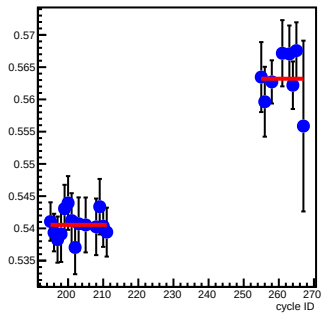
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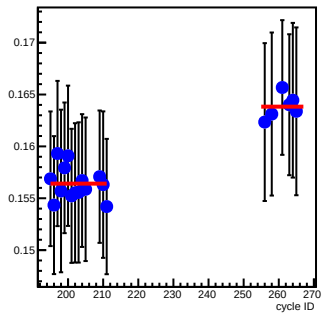
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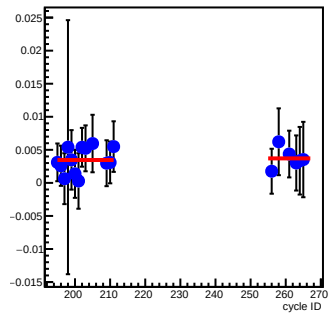
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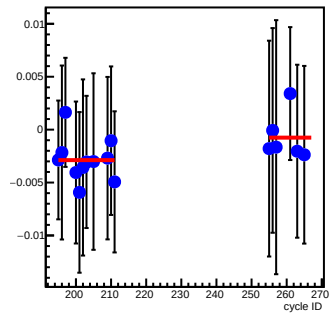
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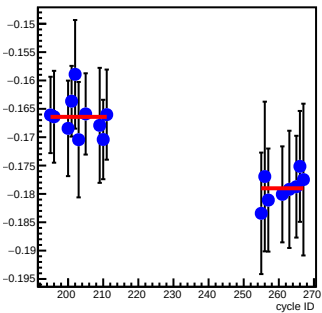
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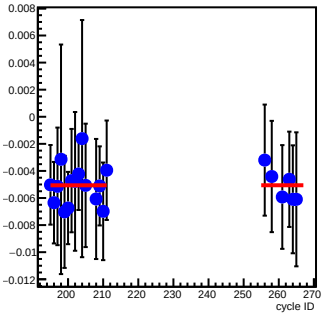
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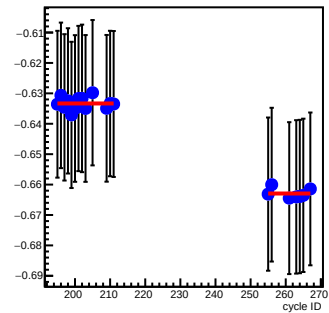
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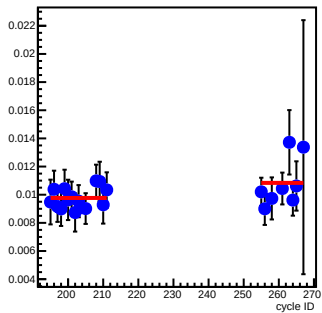
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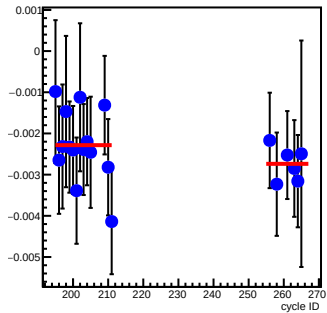
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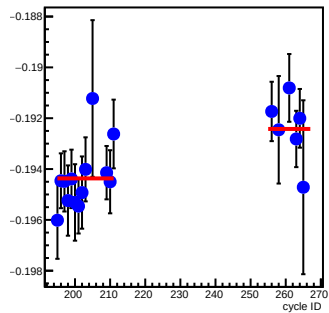
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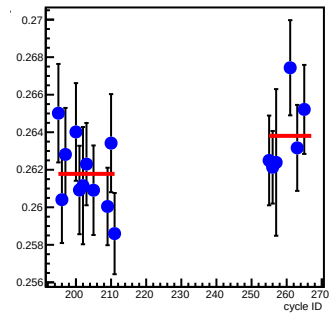
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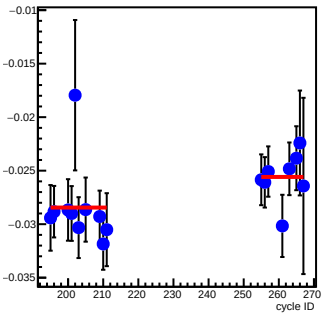
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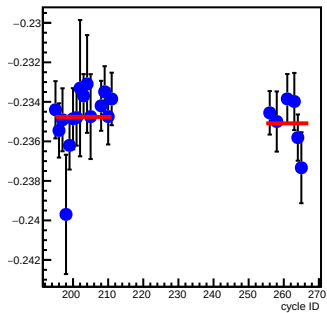
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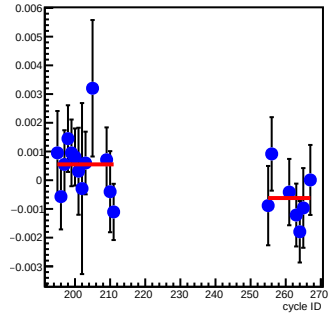
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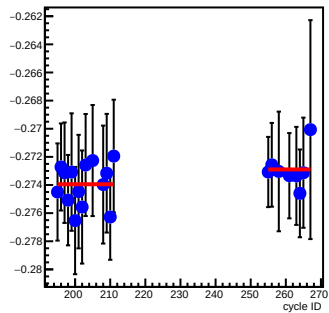
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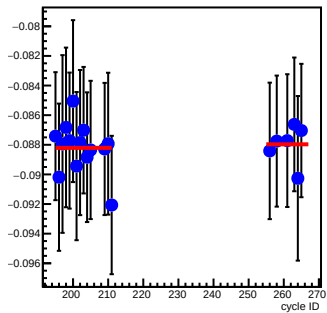
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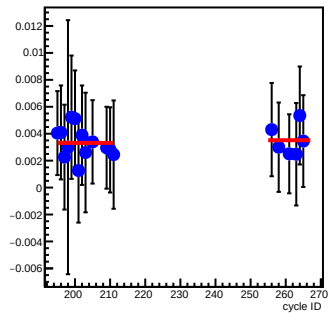
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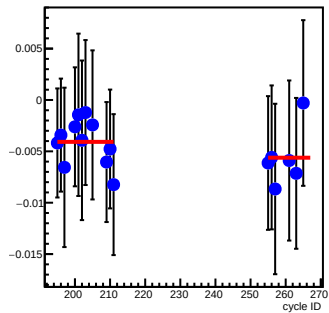
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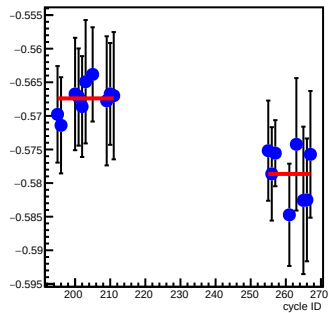
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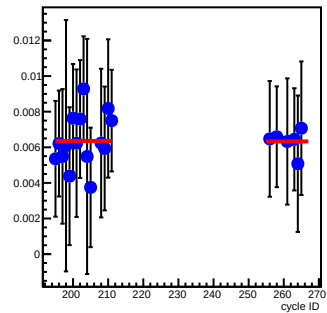
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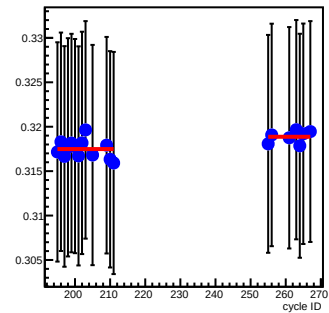
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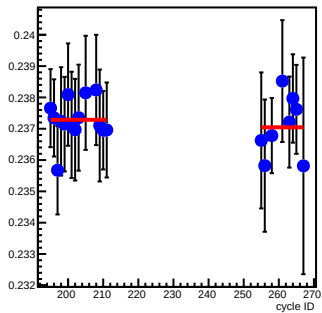
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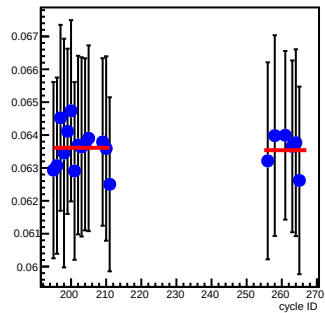
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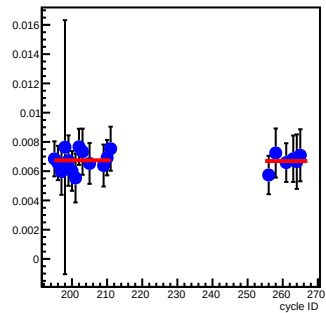
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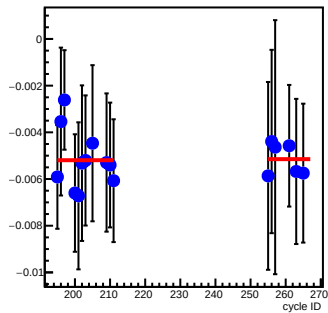
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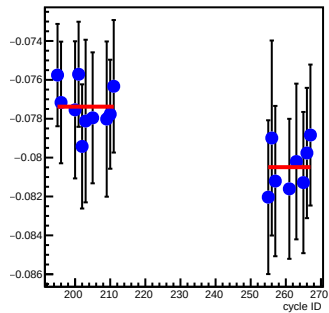
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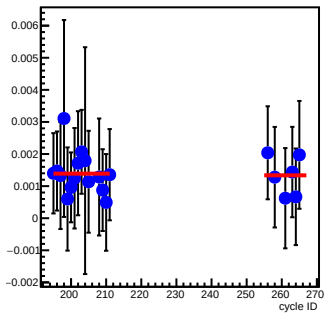
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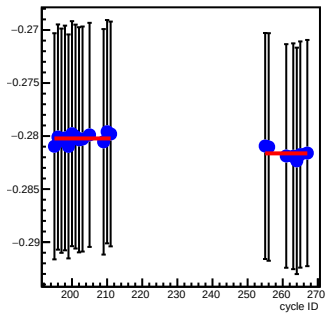
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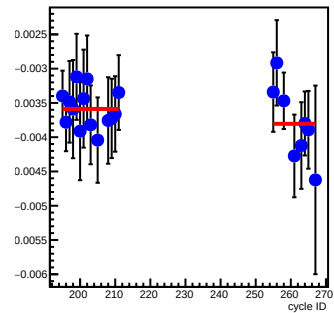
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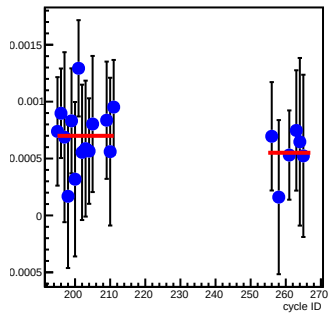
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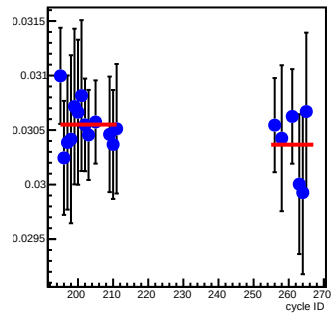
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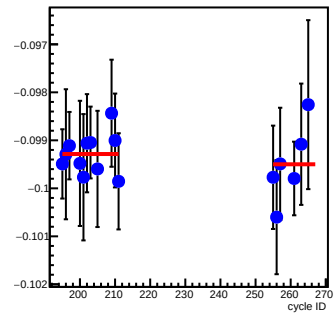
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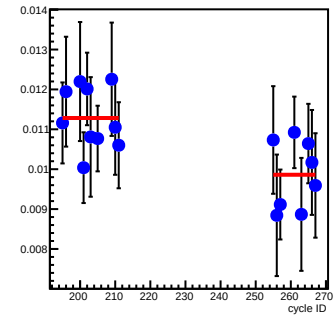
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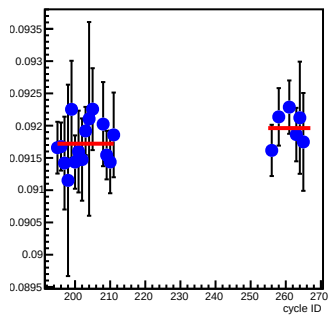
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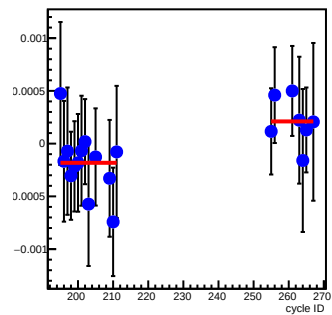
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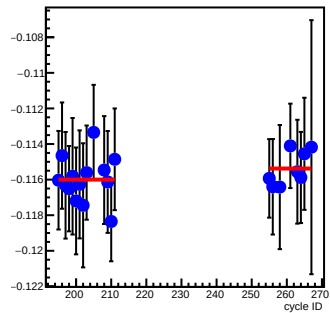
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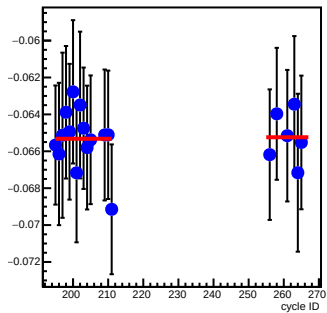
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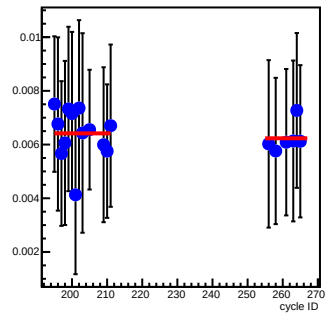
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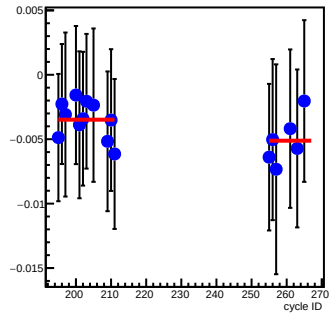
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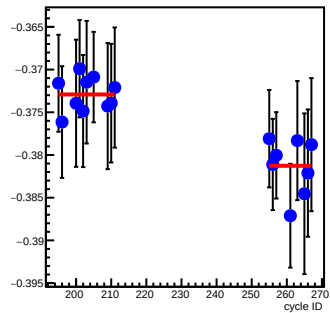
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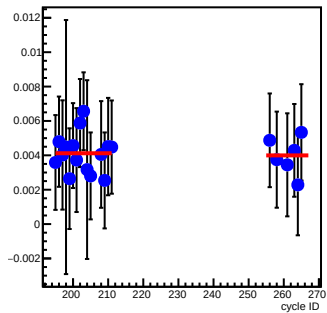
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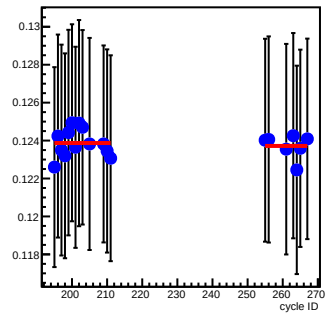
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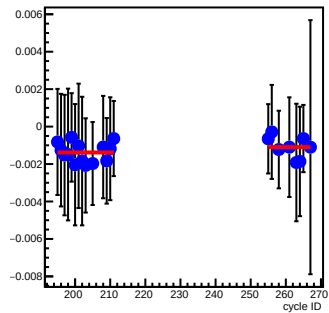
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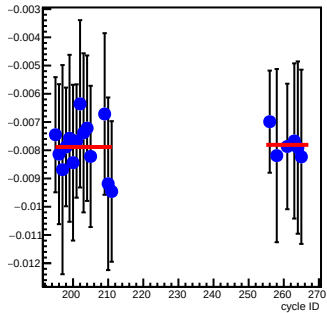
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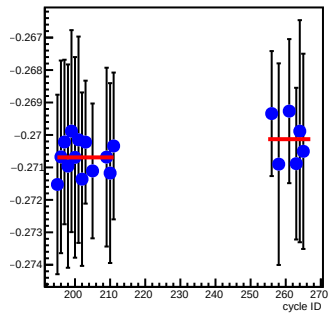
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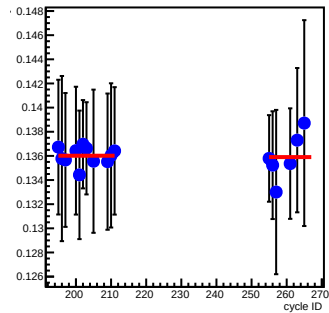
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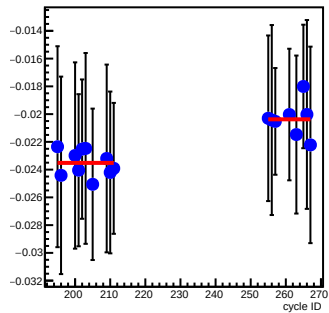
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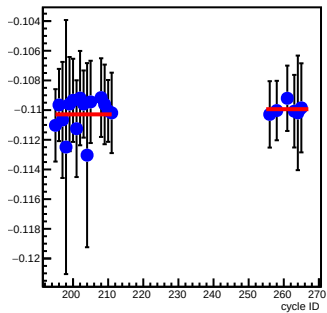
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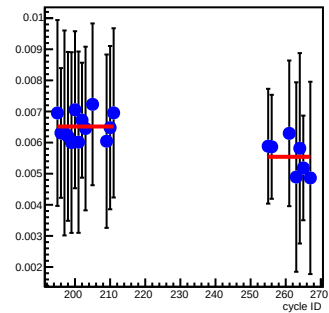
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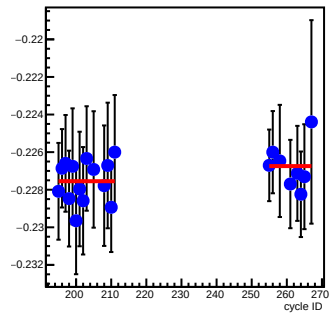
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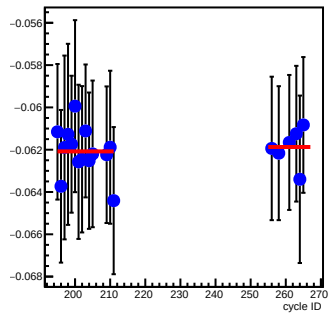
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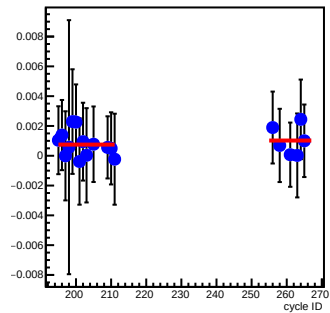
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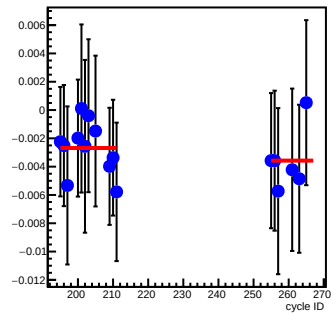
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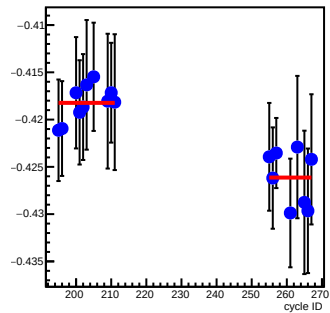
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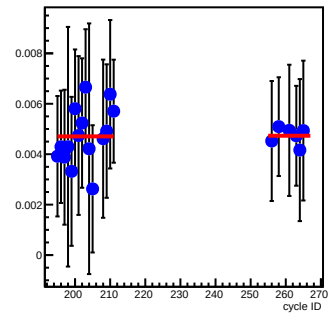
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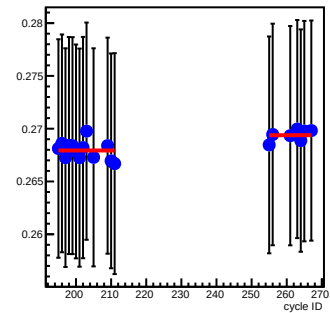
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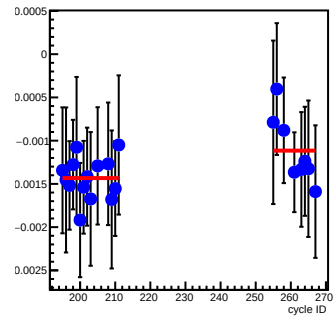
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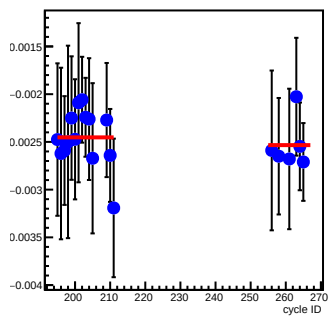
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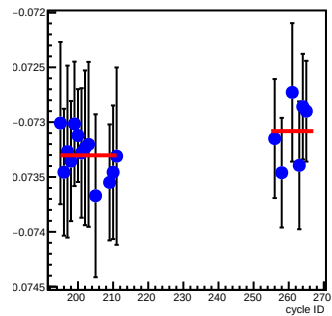
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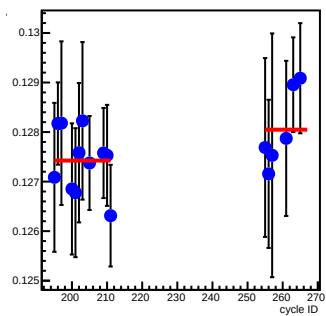
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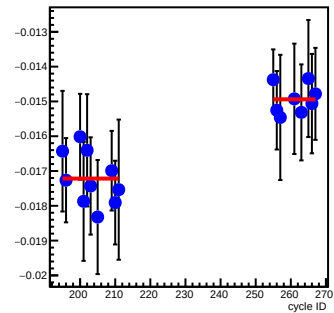
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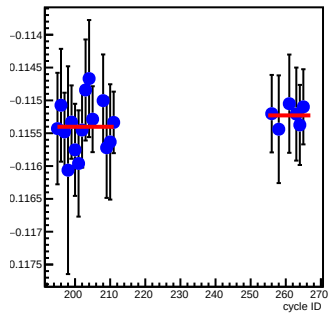
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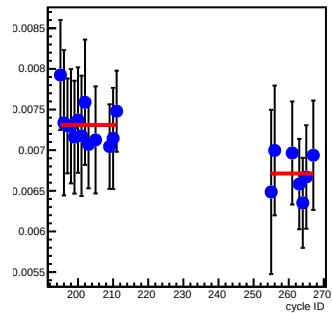
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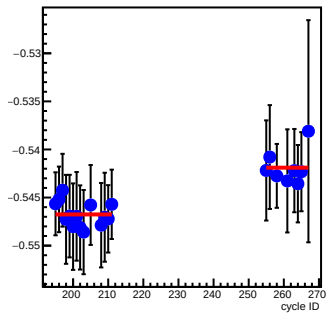
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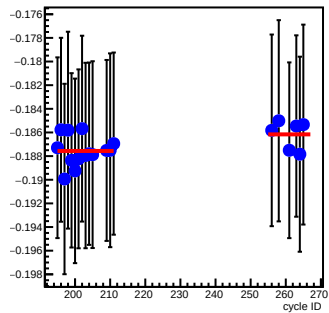
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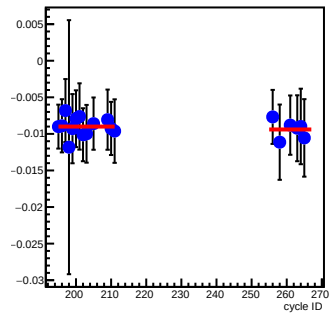
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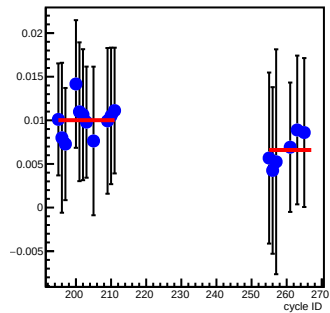
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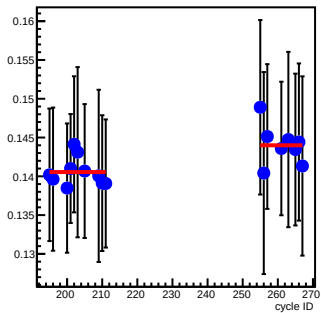
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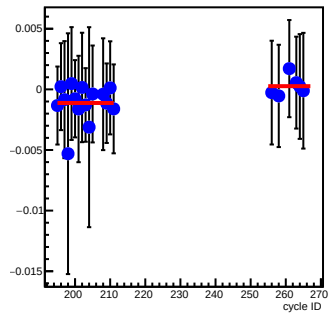
bpm1X_coil6(um/count)



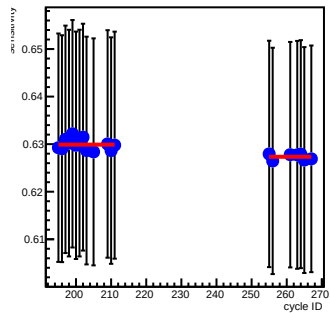
bpm1X_coil7(um/count)



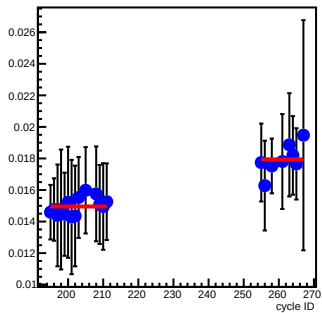
bpm1X_coil2(um/count)



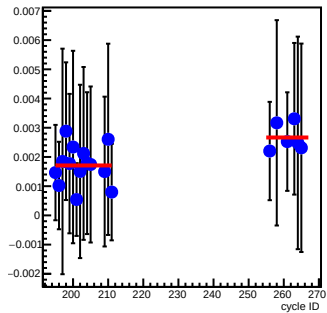
bpm1X_coil5(um/count)



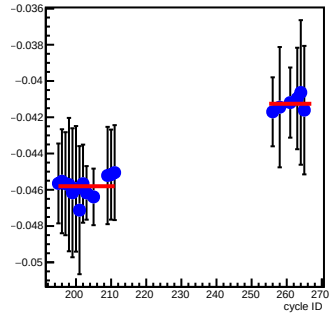
bpm1Y_coil1(um/count)



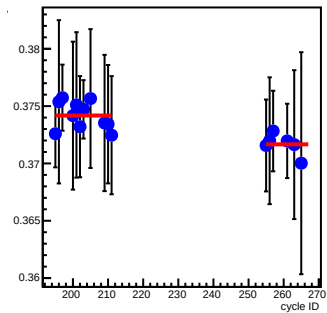
bpm1Y_coil3(um/count)



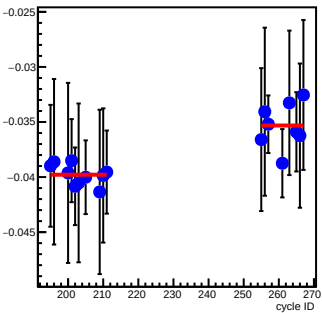
bpm1Y_coil4(um/count)



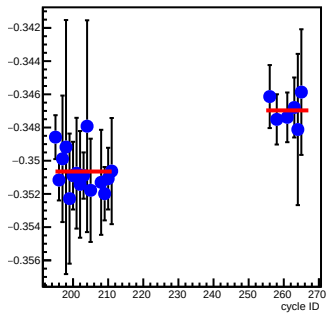
bpm1Y_coil6(um/count)



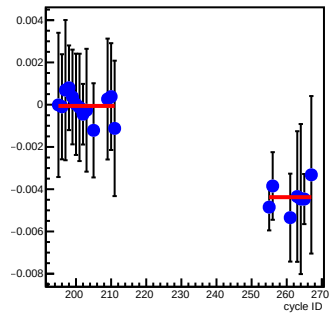
bpm1Y_coil7(um/count)



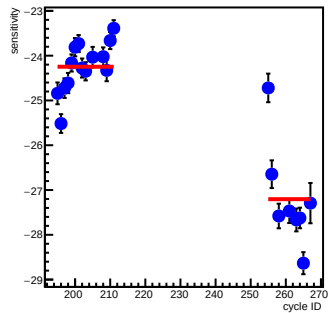
bpm1Y_coil2(um/count)



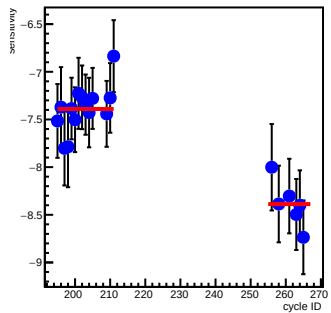
bpm1Y_coil5(um/count)



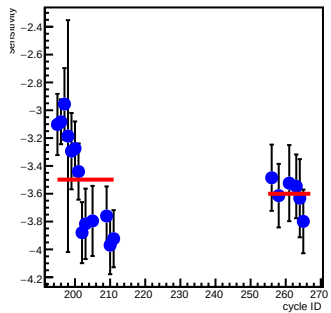
usl_coil1(ppm/count)



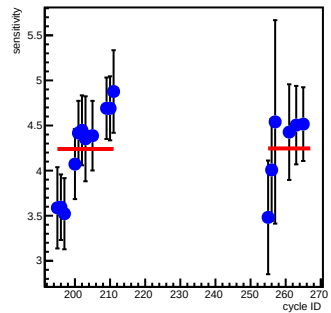
usl_coil3(ppm/count)



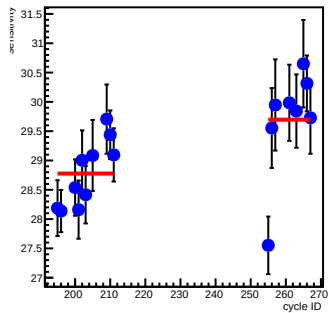
usl_coil4(ppm/count)



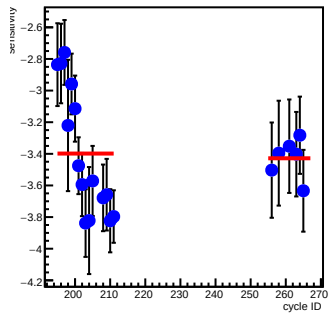
usl_coil6(ppm/count)



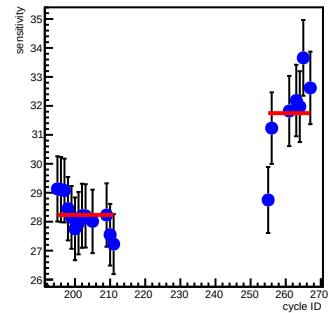
usl_coil7(ppm/count)



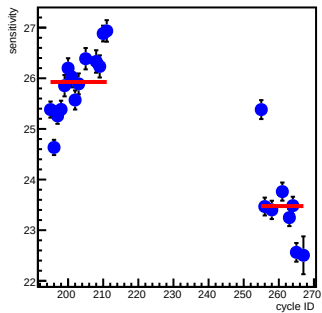
usl_coil2(ppm/count)



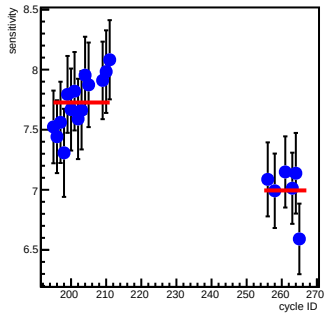
usl_coil5(ppm/count)



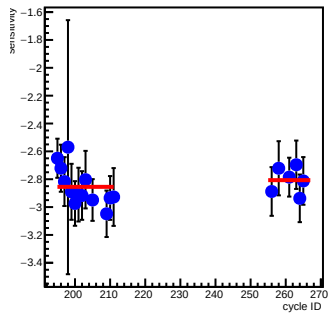
usr_coil1(ppm/count)



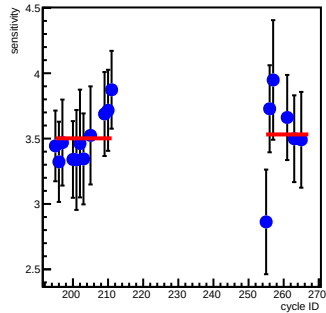
usr_coil3(ppm/count)



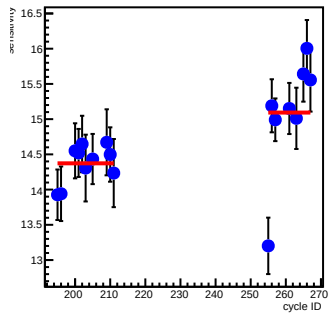
usr_coil4(ppm/count)



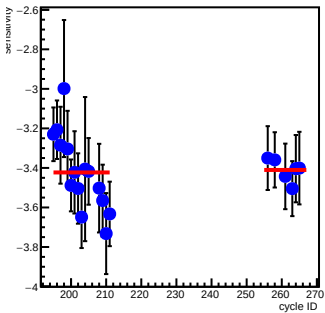
usr_coil6(ppm/count)



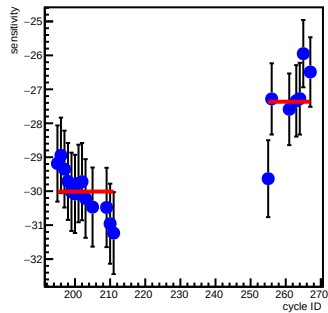
usr_coil7(ppm/count)



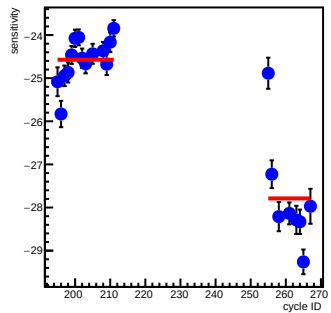
usr_coil2(ppm/count)



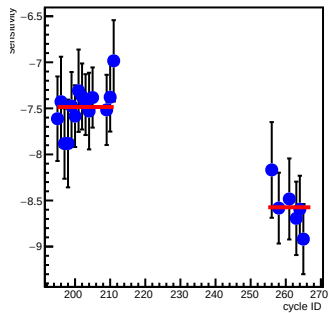
usr_coil5(ppm/count)



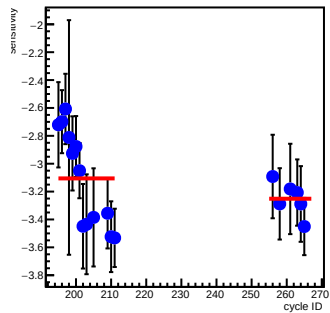
dsl_coil1(ppm/count)



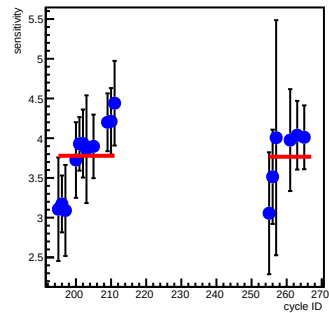
dsl_coil3(ppm/count)



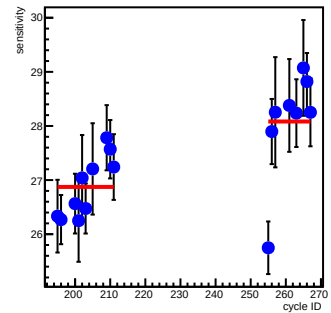
dsl_coil4(ppm/count)



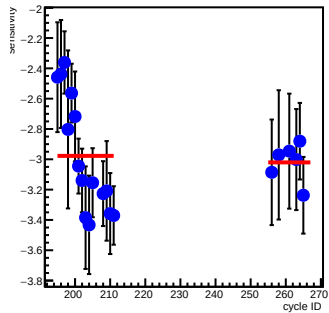
dsl_coil6(ppm/count)



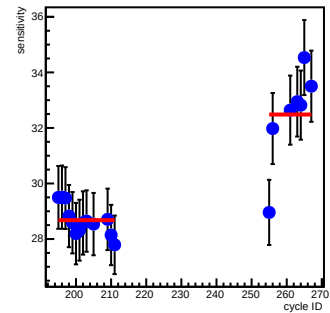
dsl_coil7(ppm/count)



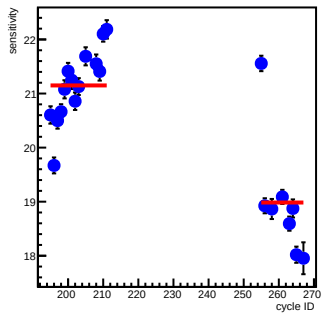
dsl_coil2(ppm/count)



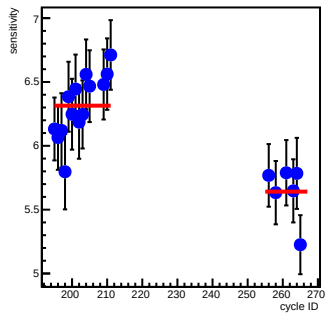
dsl_coil5(ppm/count)



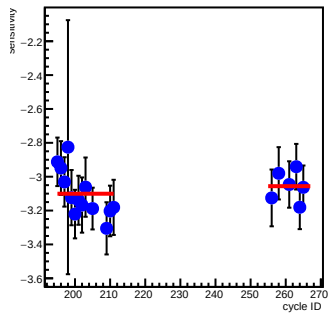
dsr_coil1(ppm/count)



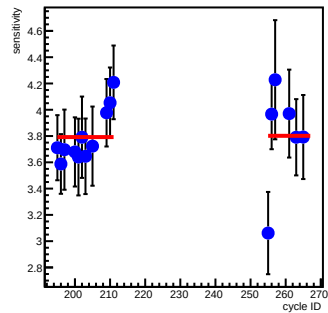
dsr_coil3(ppm/count)



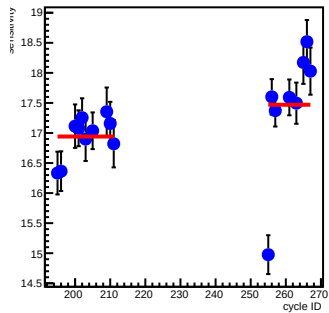
dsr_coil4(ppm/count)



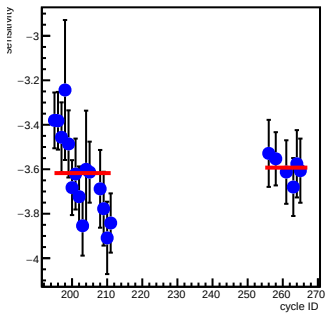
dsr_coil6(ppm/count)



dsr_coil7(ppm/count)



dsr_coil2(ppm/count)



dsr_coil5(ppm/count)

