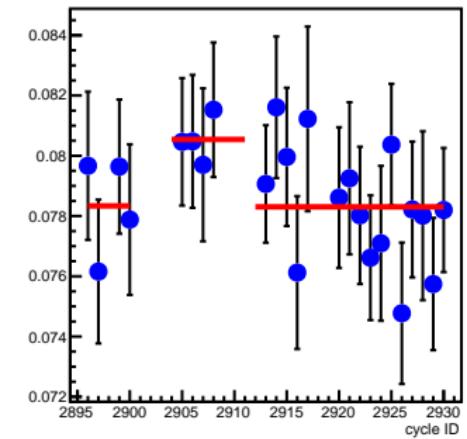
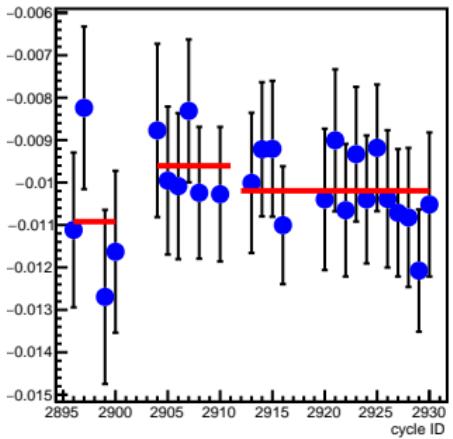


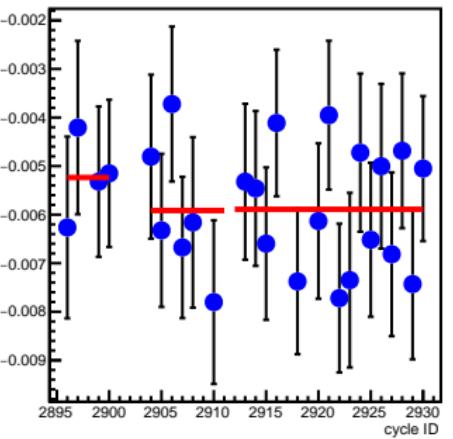
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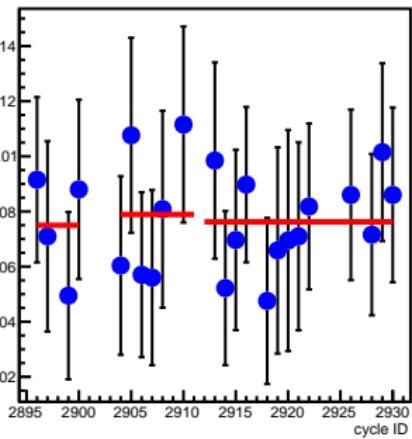
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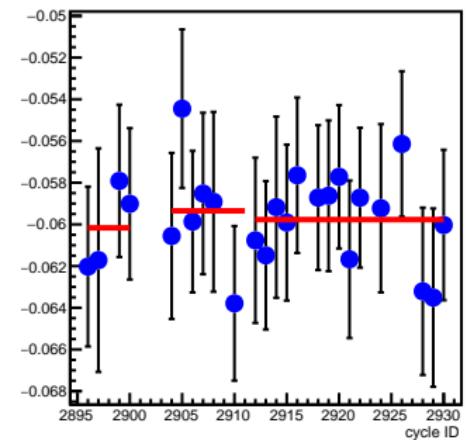
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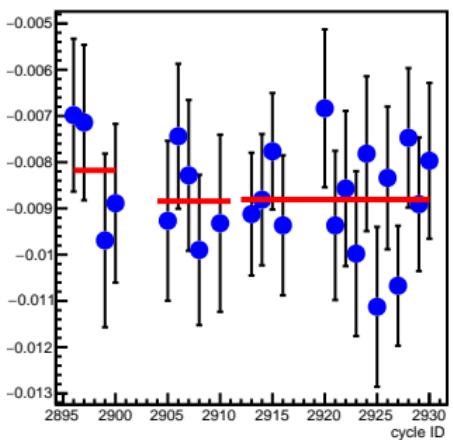
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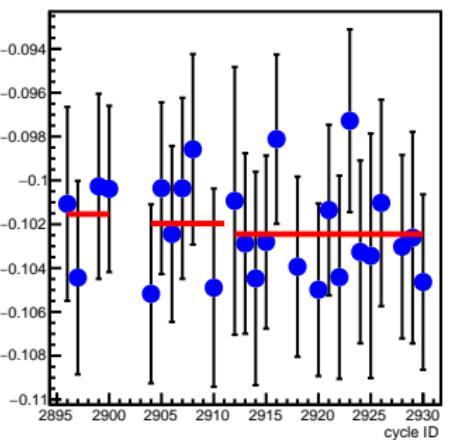
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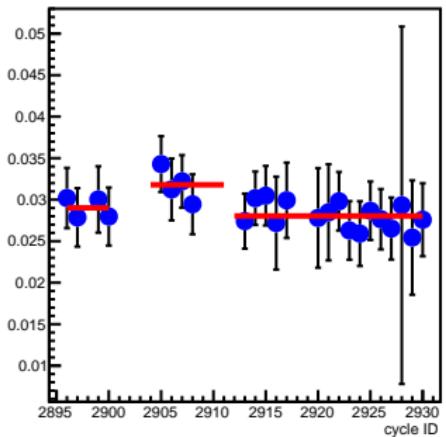
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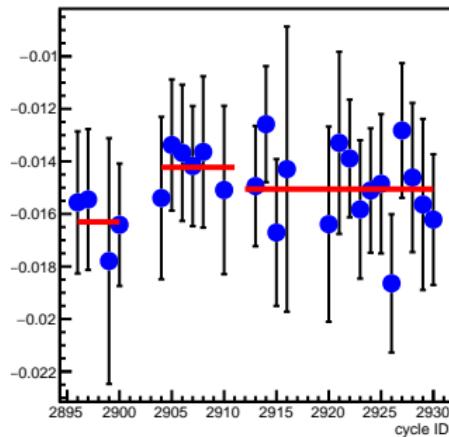
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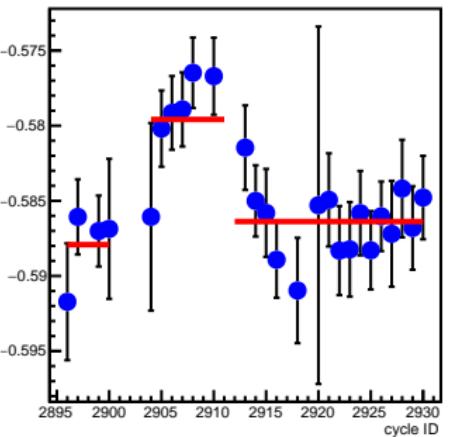
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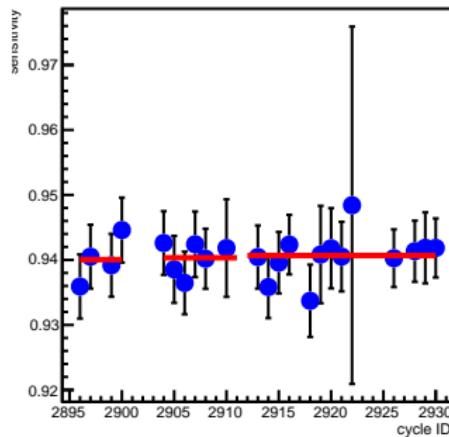
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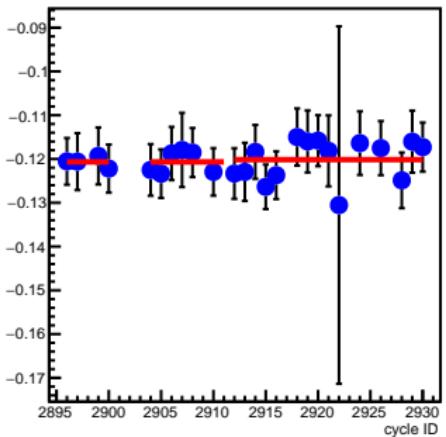
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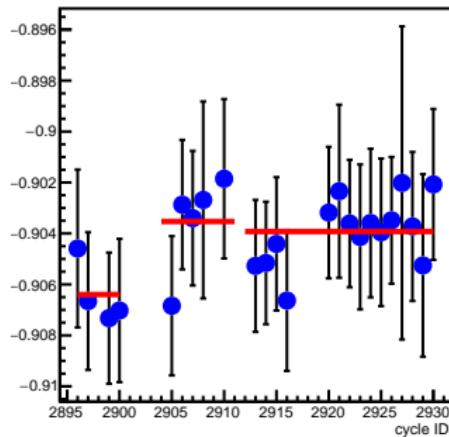
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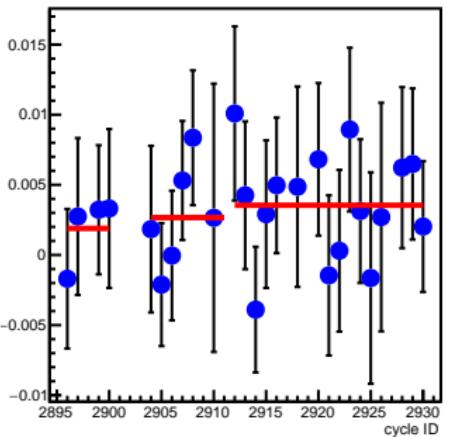
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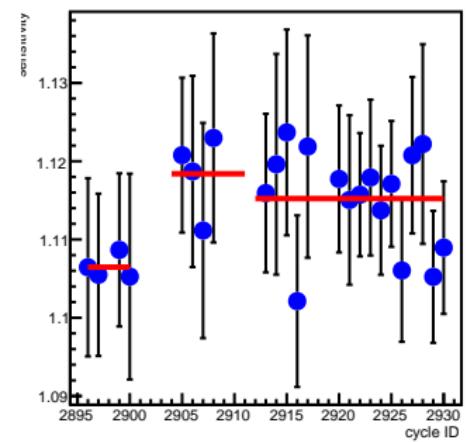
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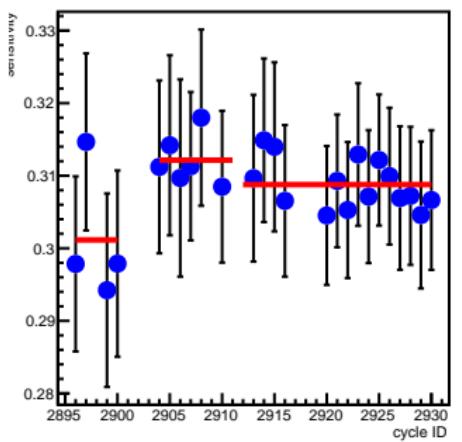
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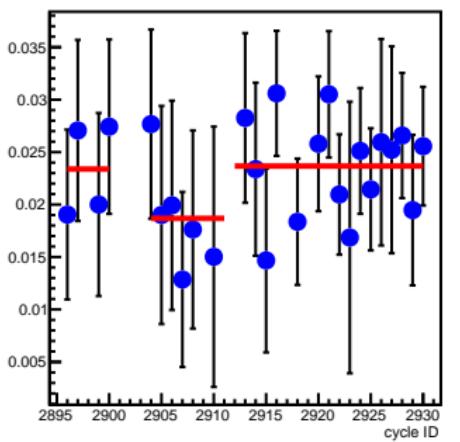
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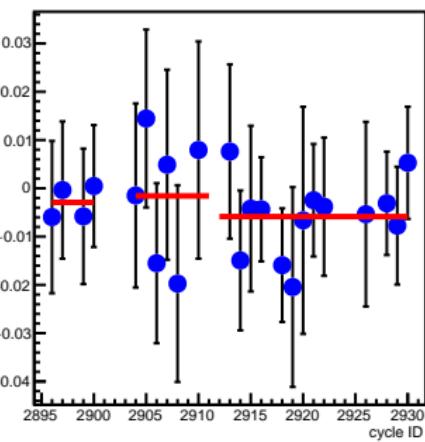
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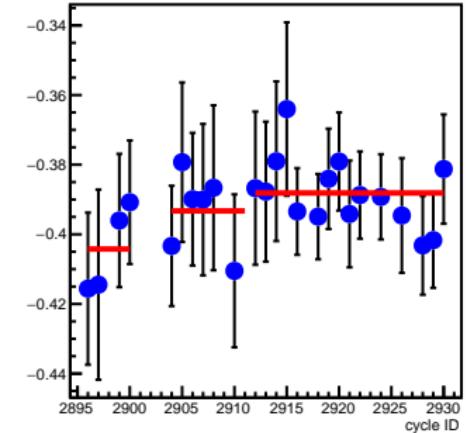
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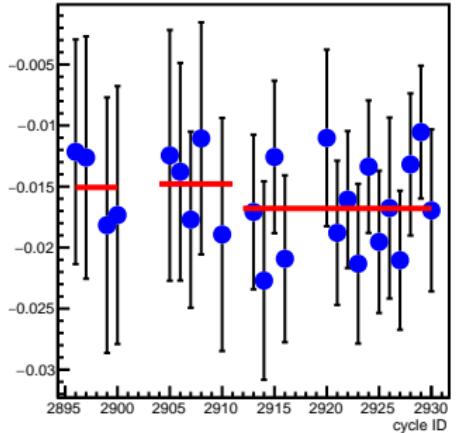
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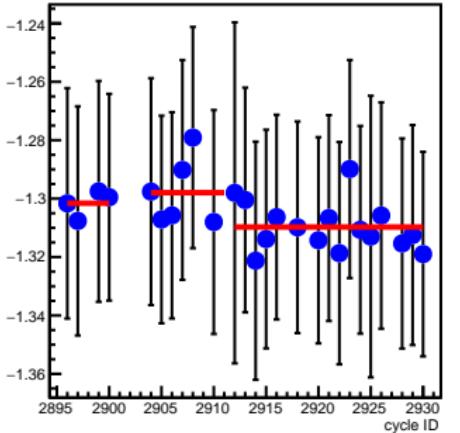
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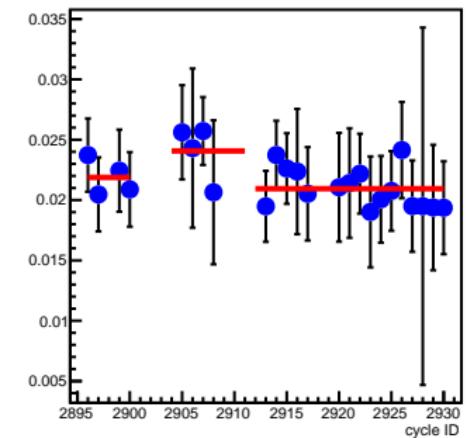
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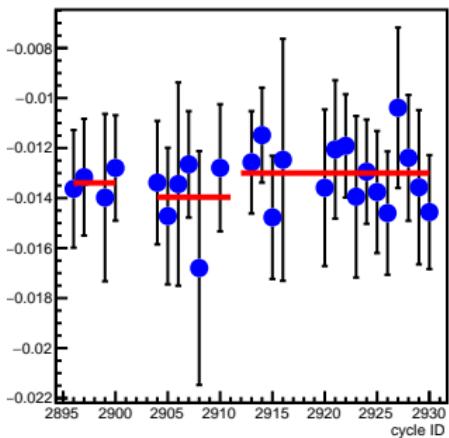
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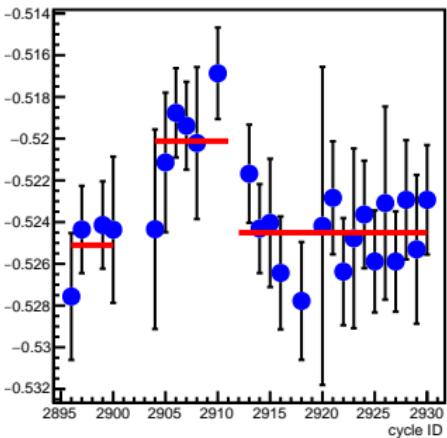
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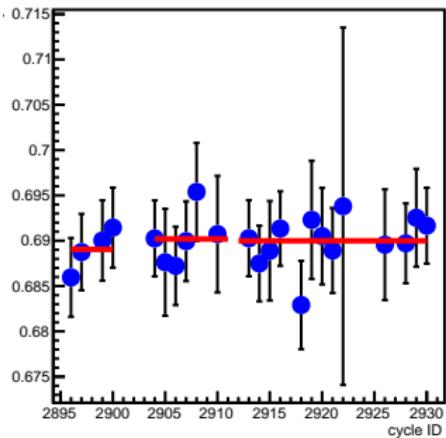
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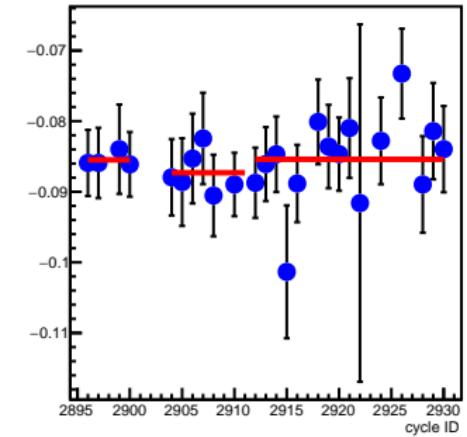
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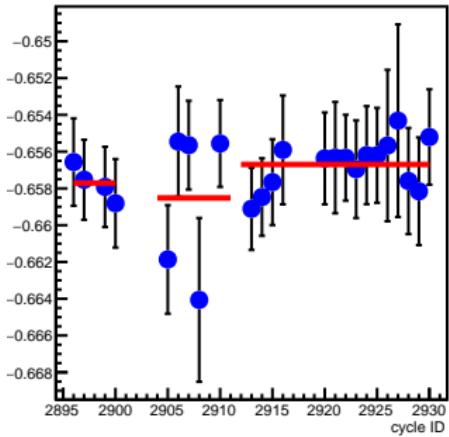
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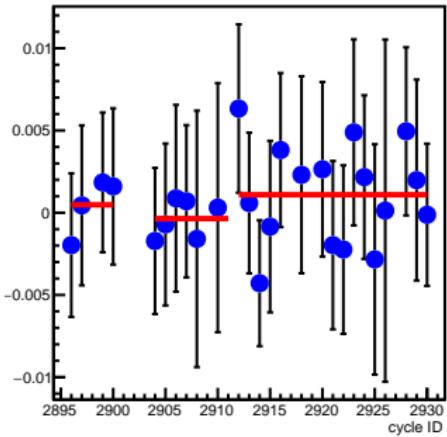
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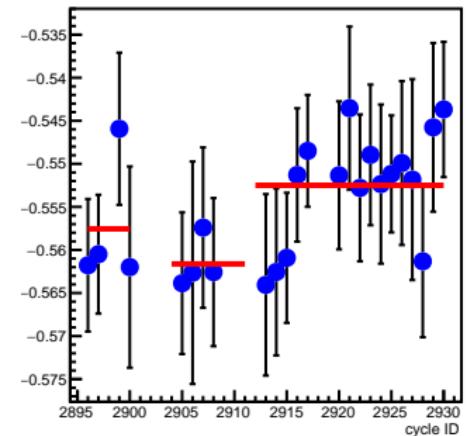
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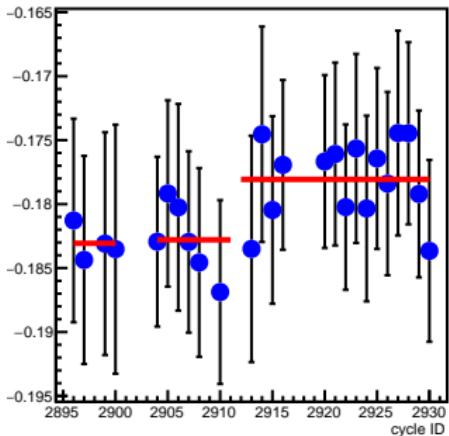
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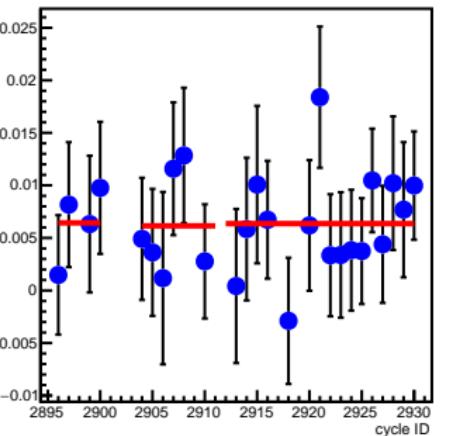
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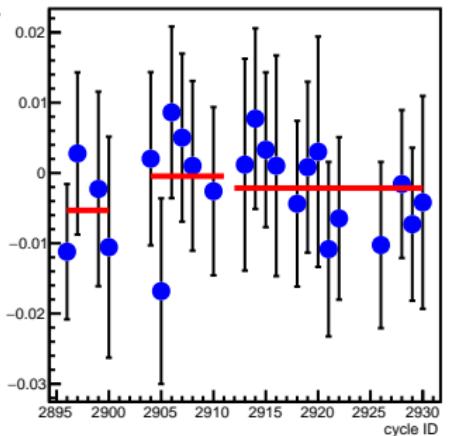
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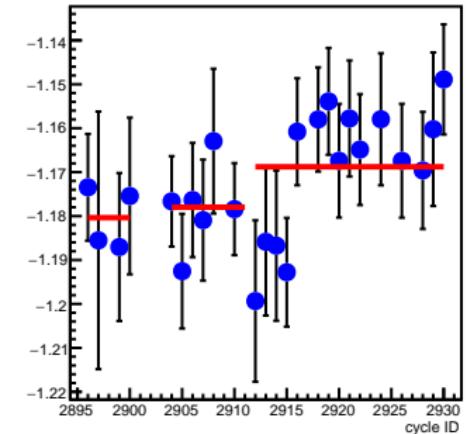
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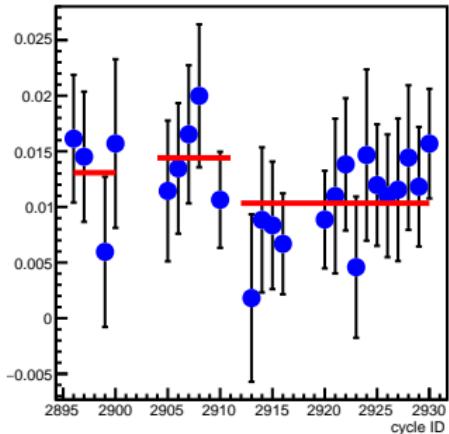
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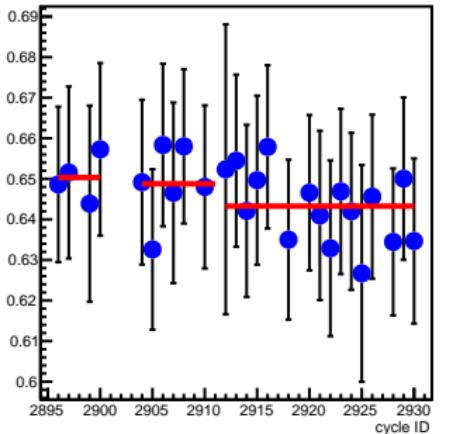
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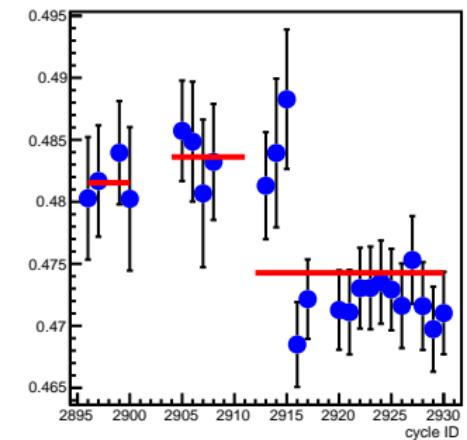
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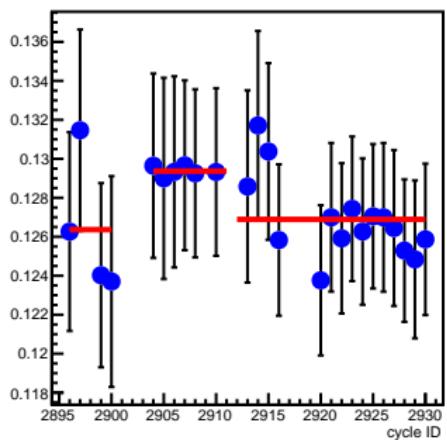
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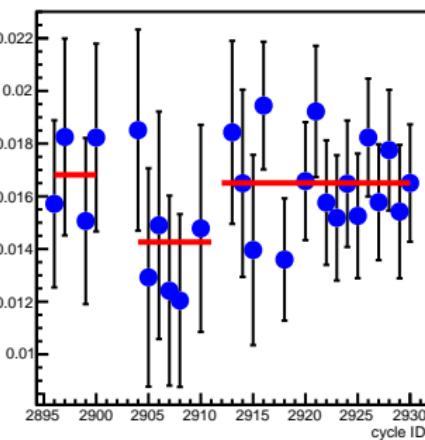
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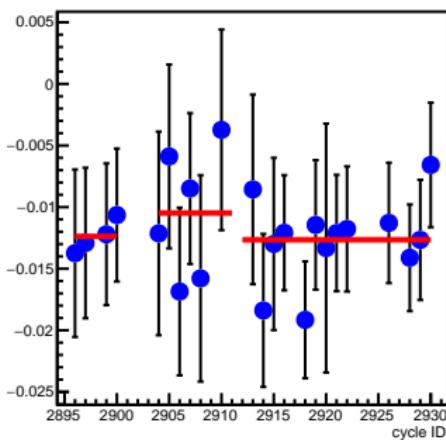
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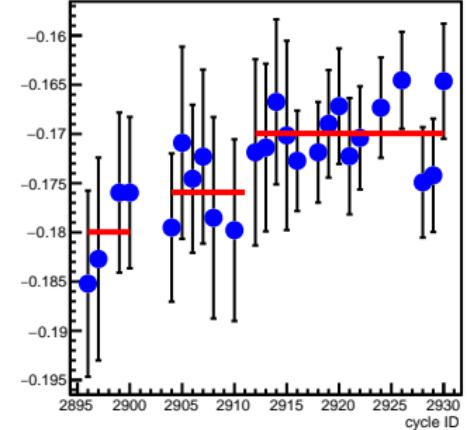
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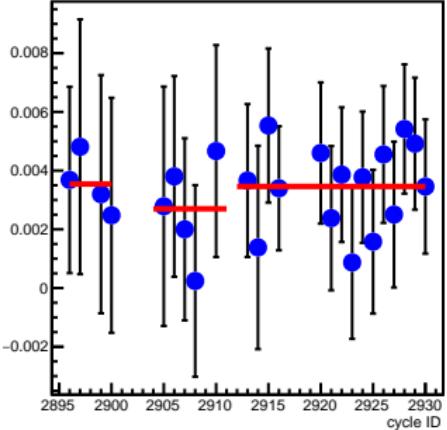
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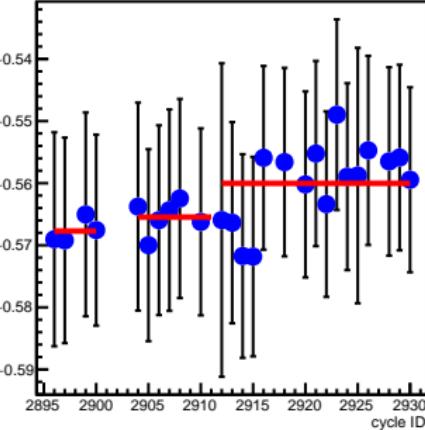
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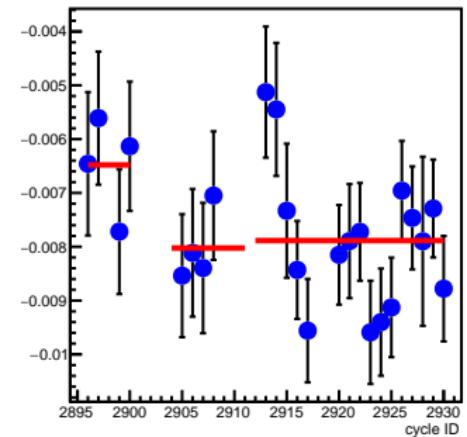
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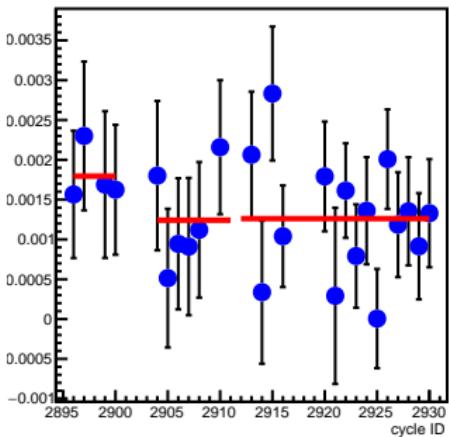
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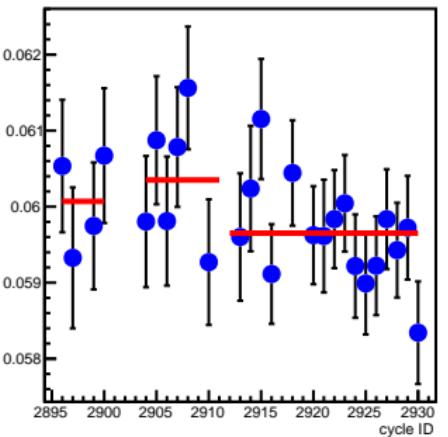
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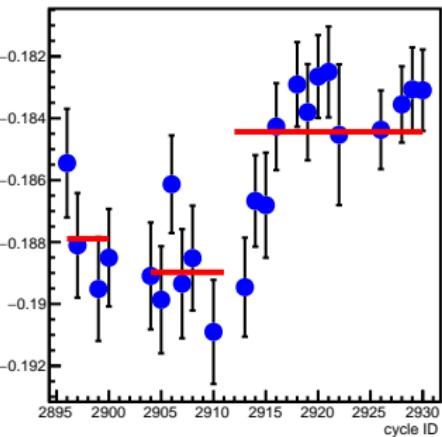
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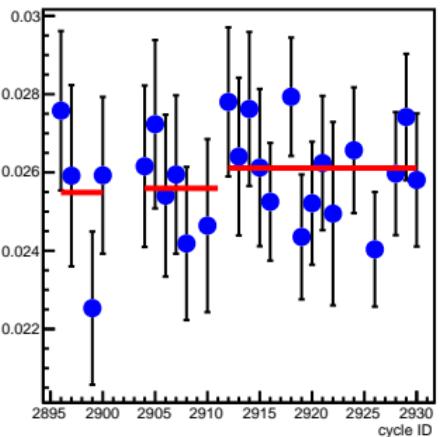
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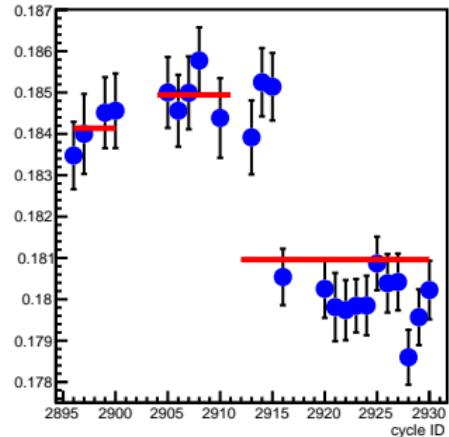
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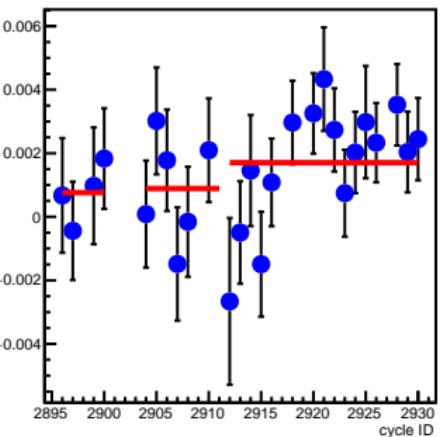
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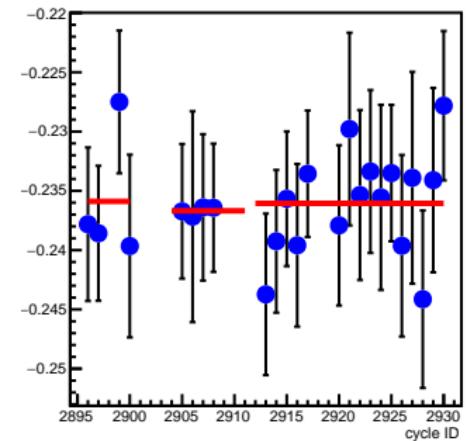
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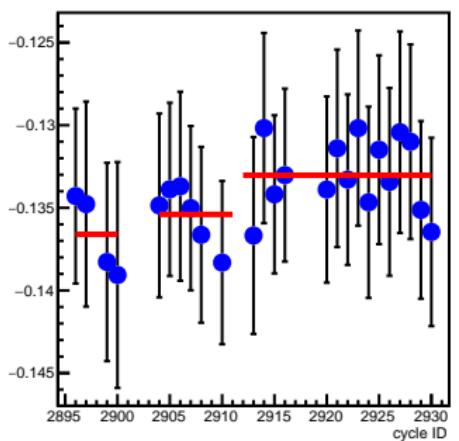
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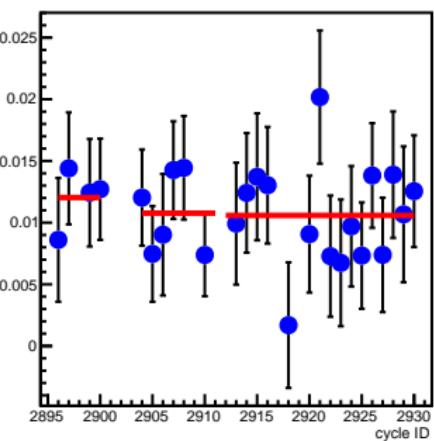
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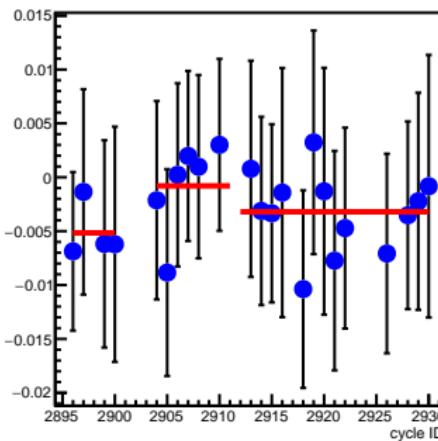
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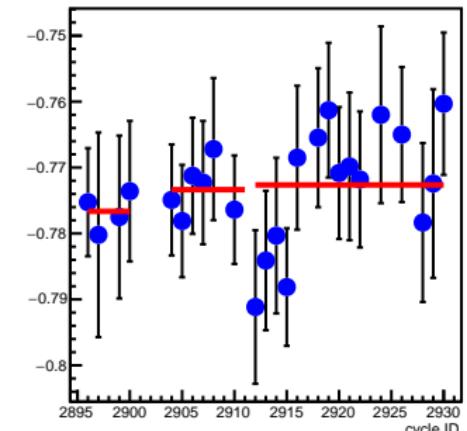
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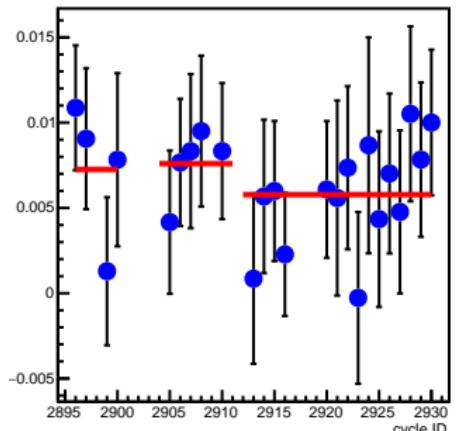
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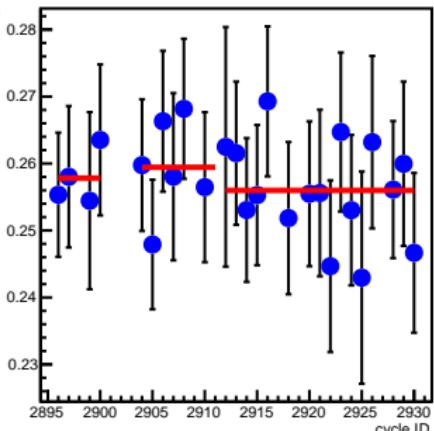
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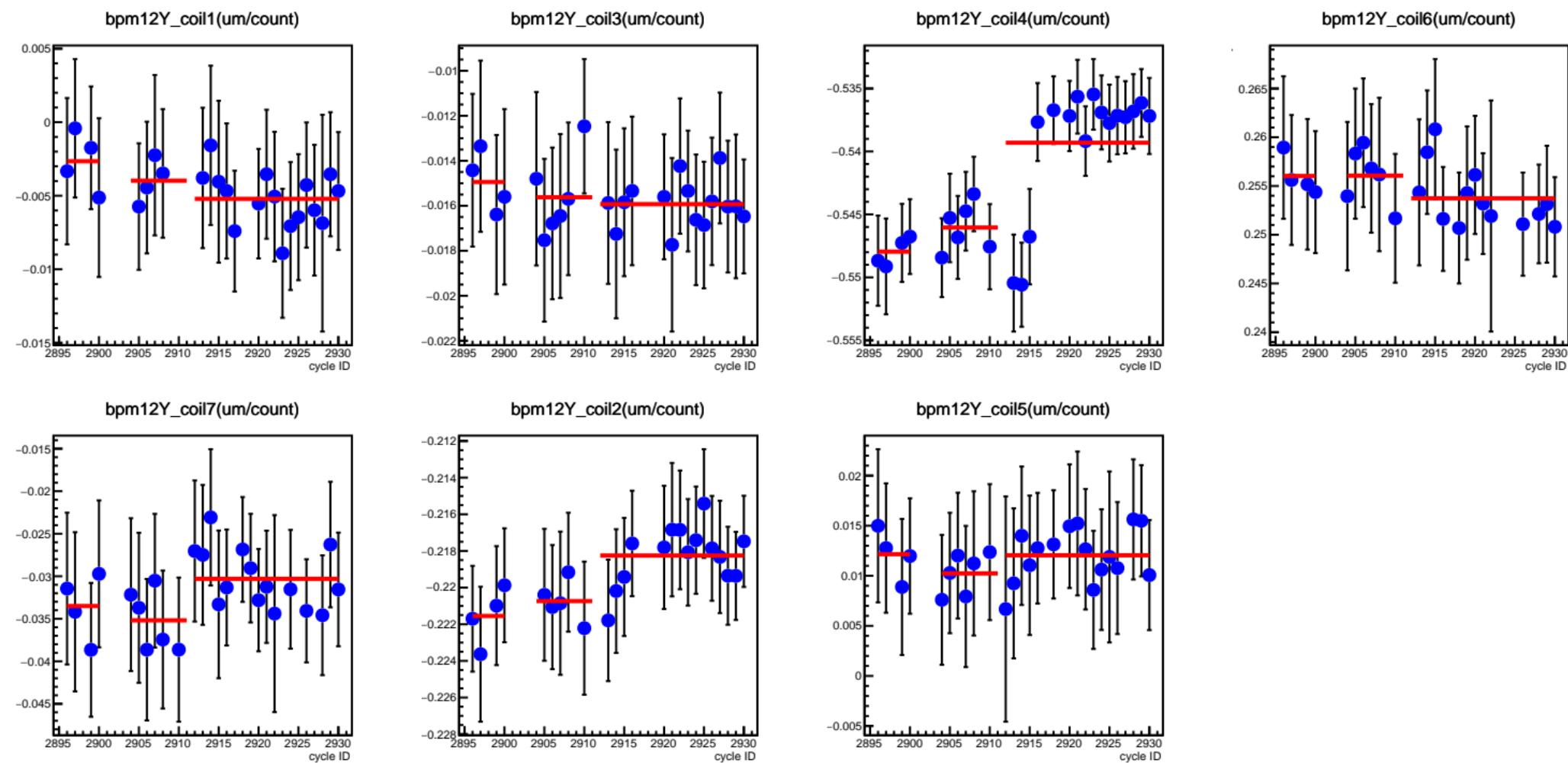


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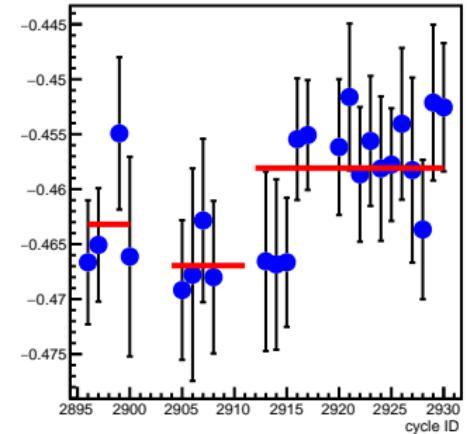


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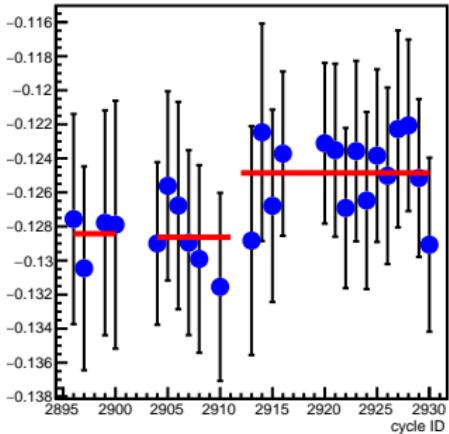




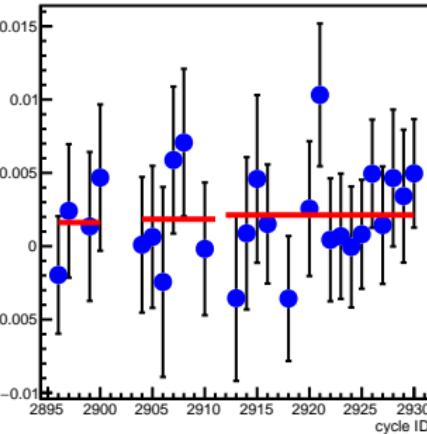
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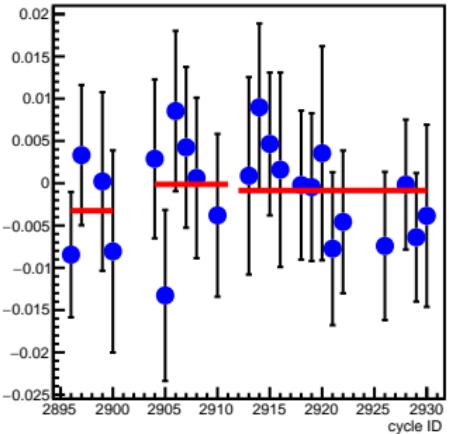
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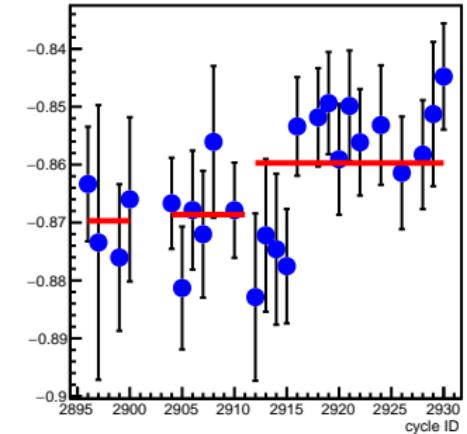
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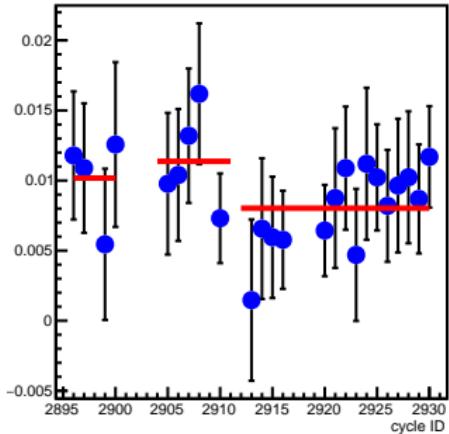
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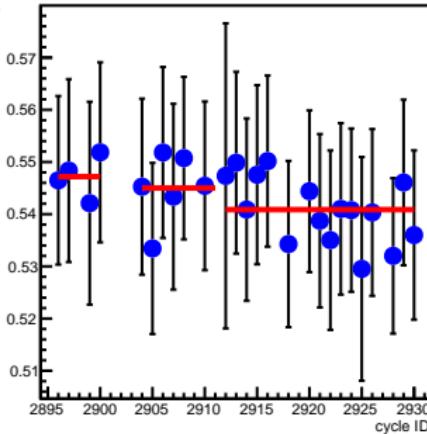
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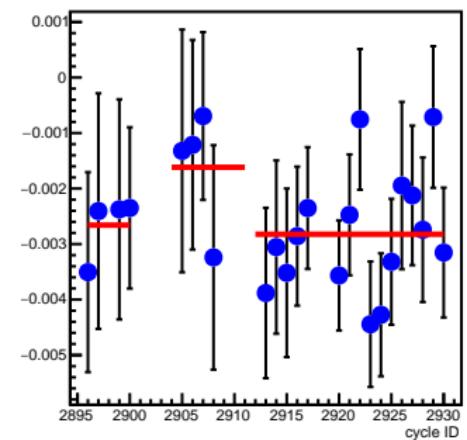
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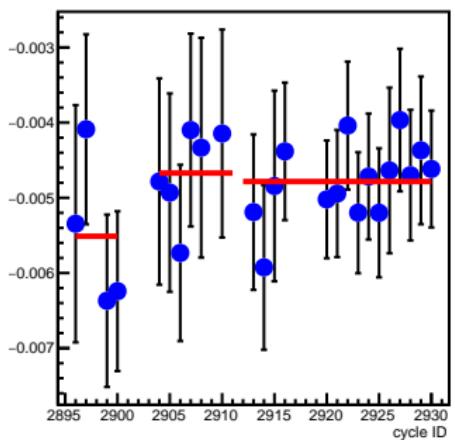
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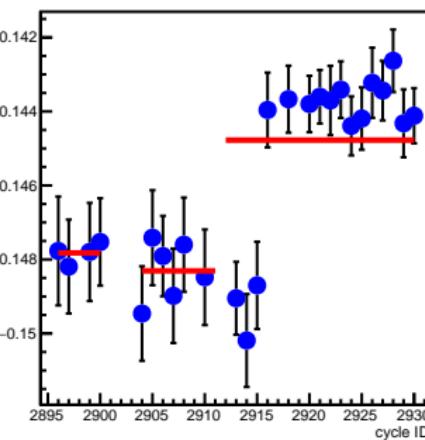
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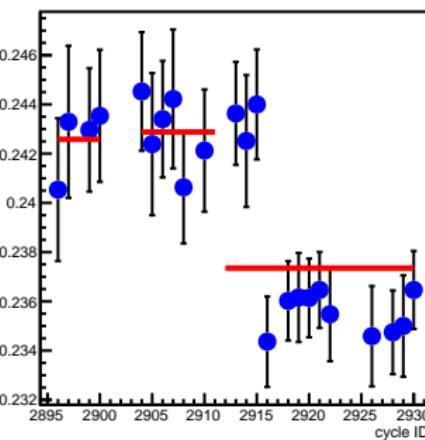
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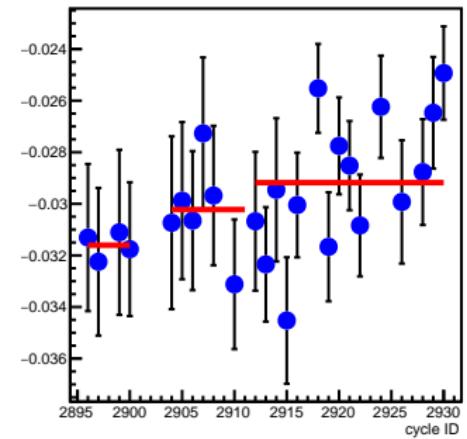
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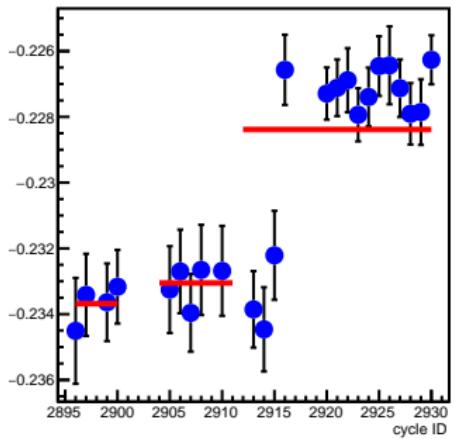
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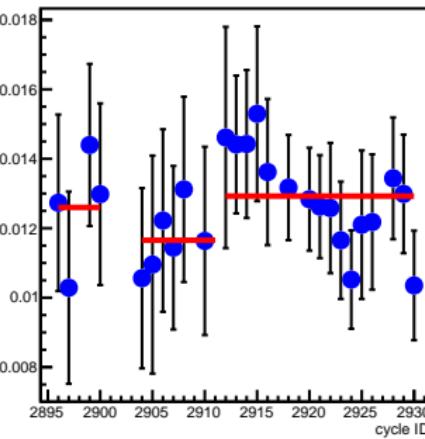
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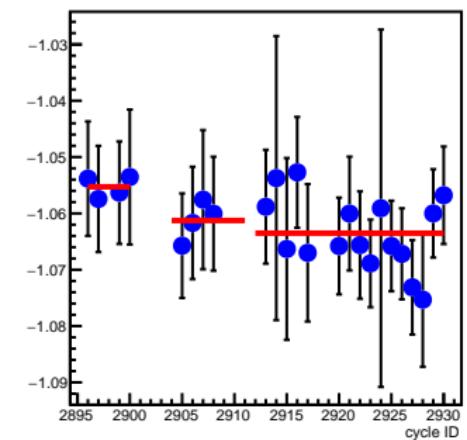
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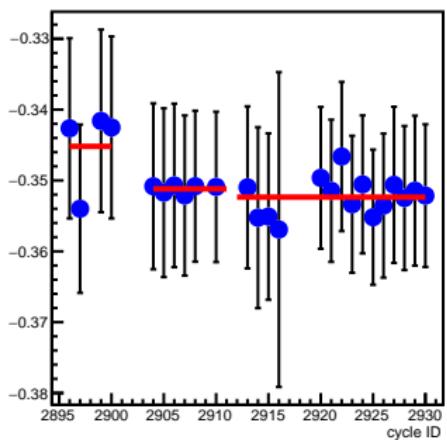
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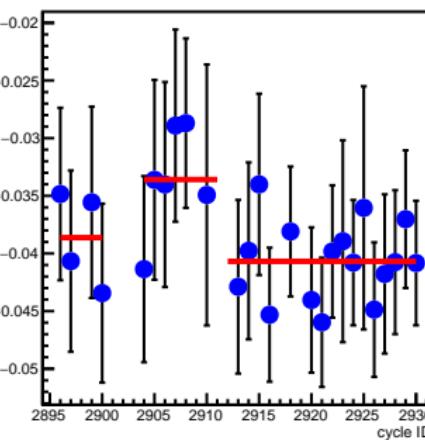
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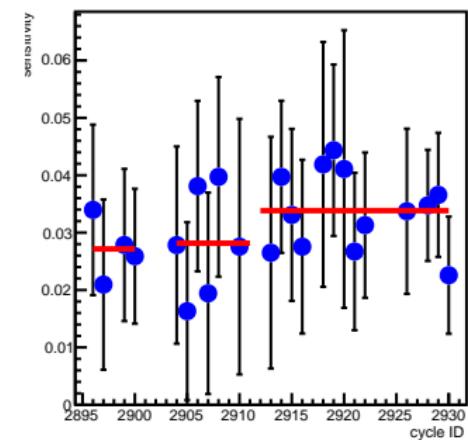
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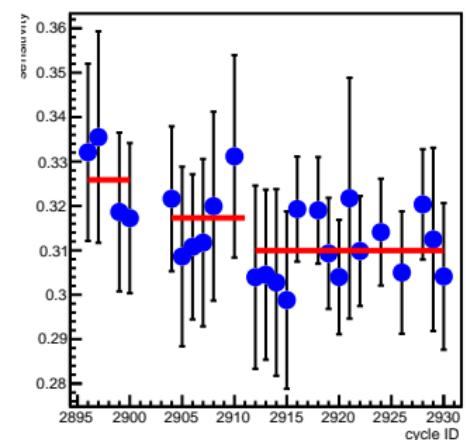
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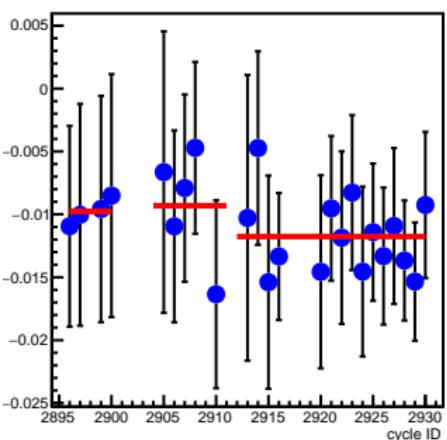
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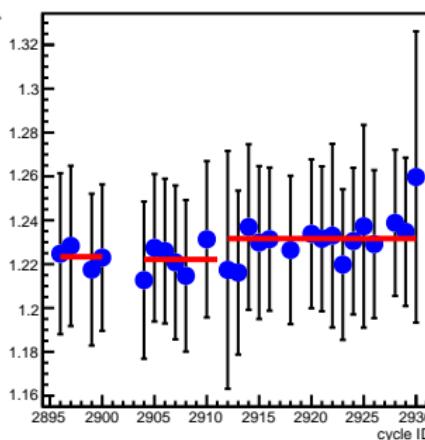
bpm1X_coil7(um/count)



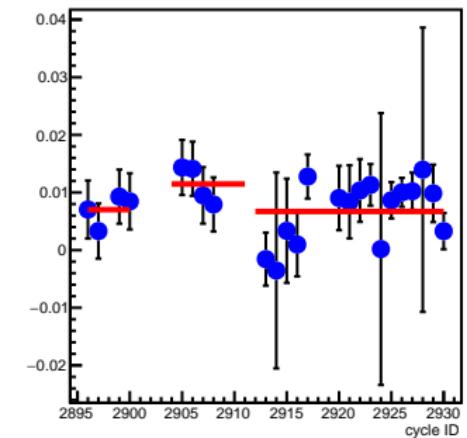
bpm1X_coil2(um/count)



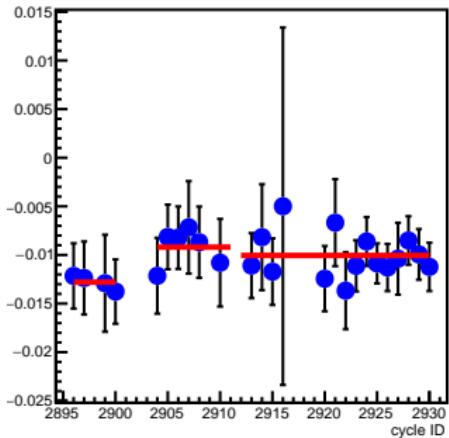
bpm1X_coil5(um/count)



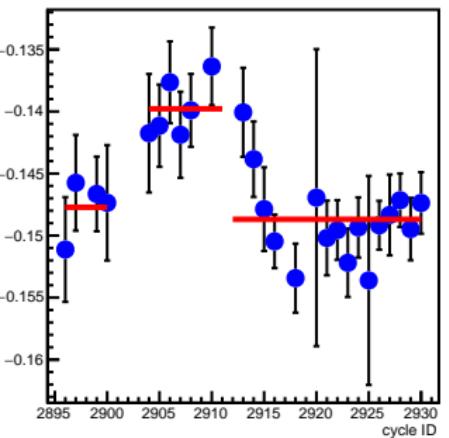
bpm1Y_coil1(um/count)



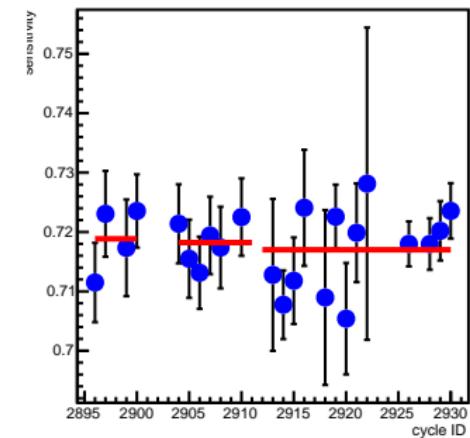
bpm1Y_coil3(um/count)



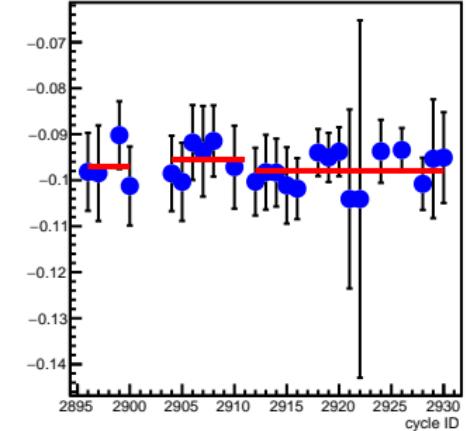
bpm1Y_coil4(um/count)



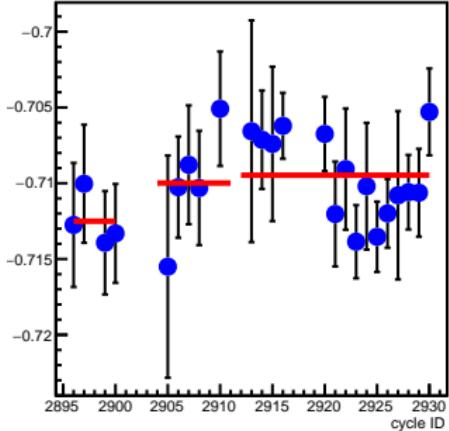
bpm1Y_coil6(um/count)



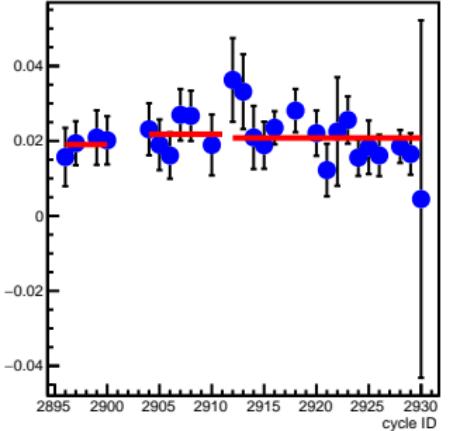
bpm1Y_coil7(um/count)

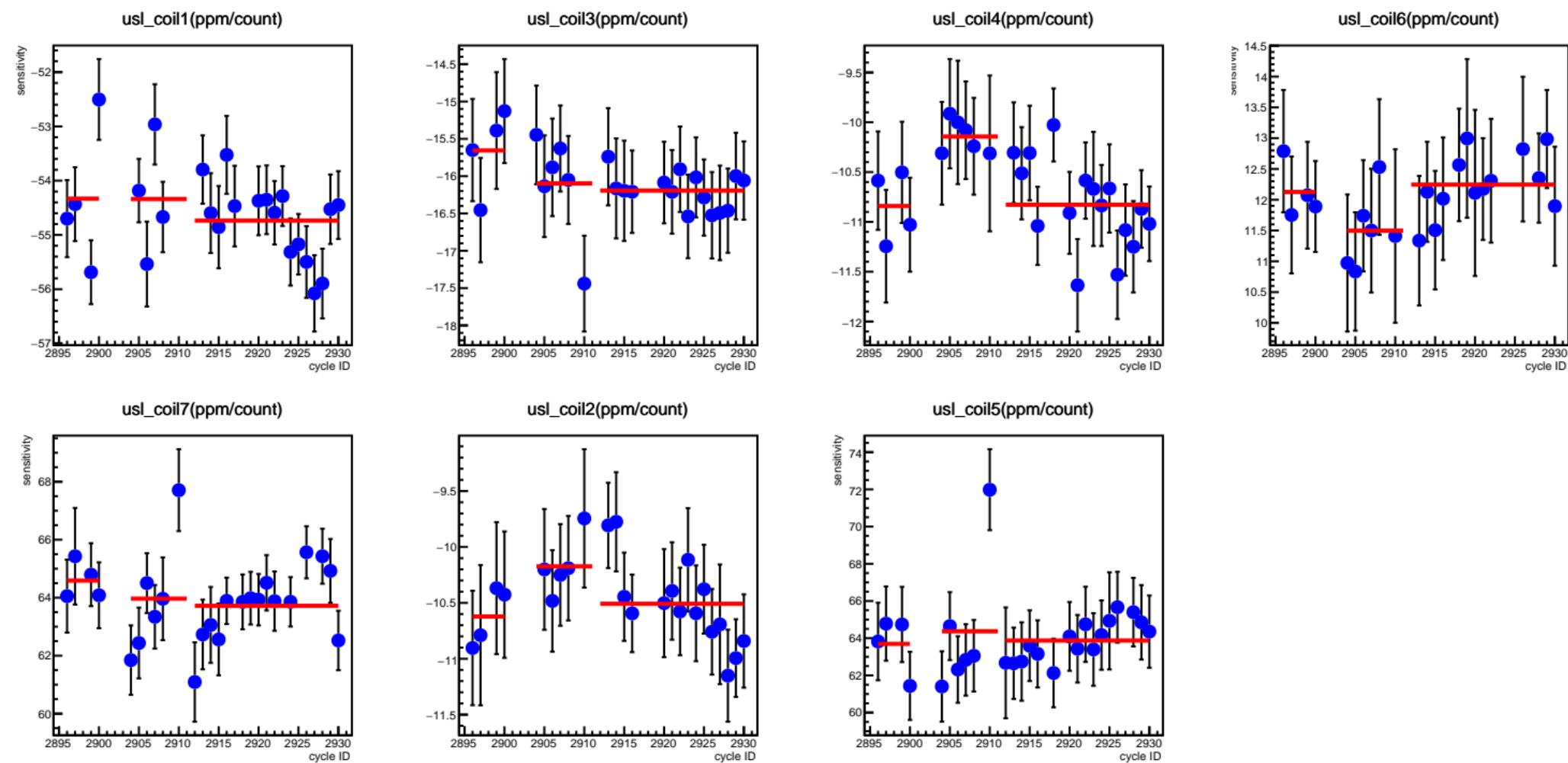


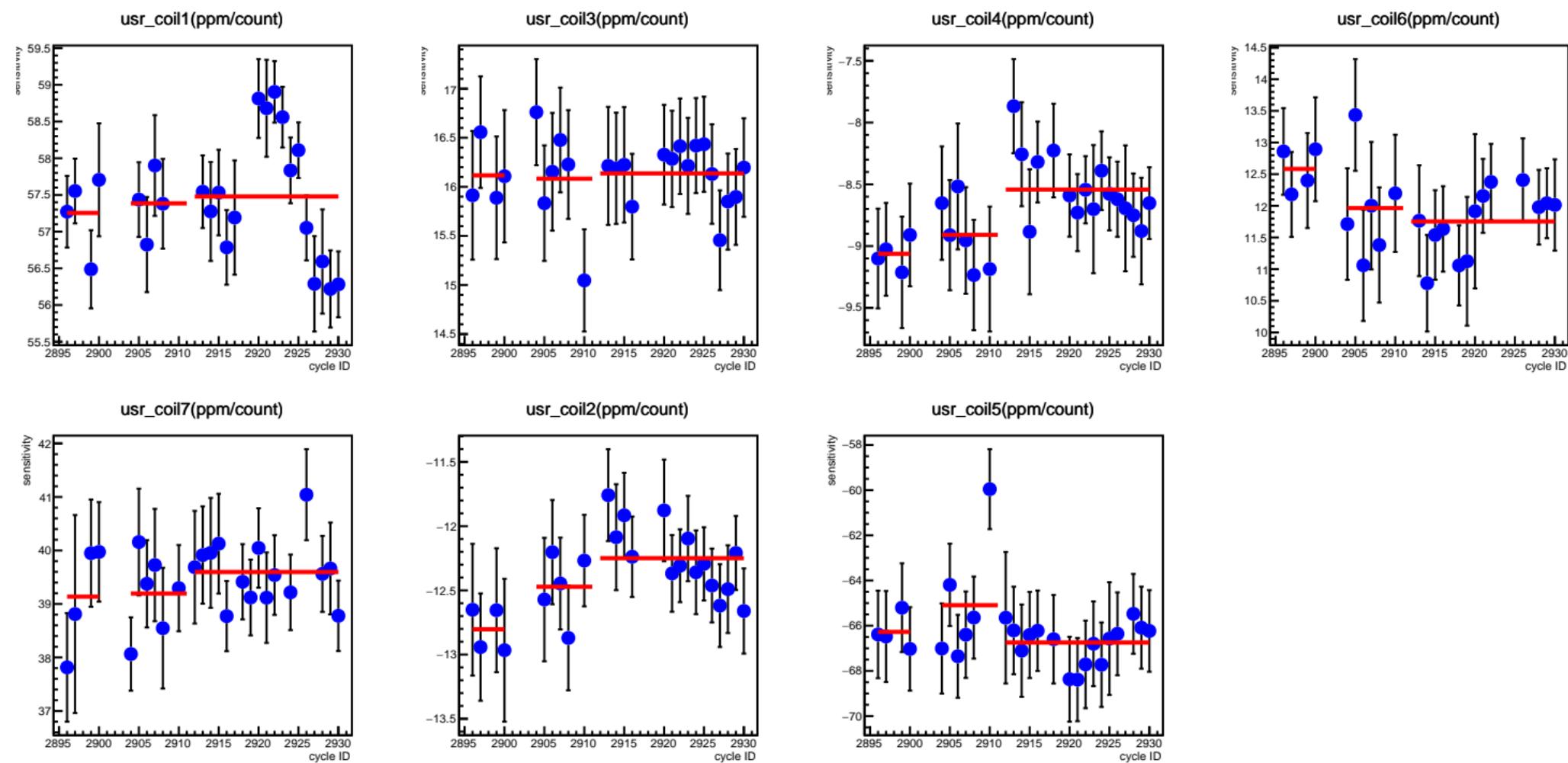
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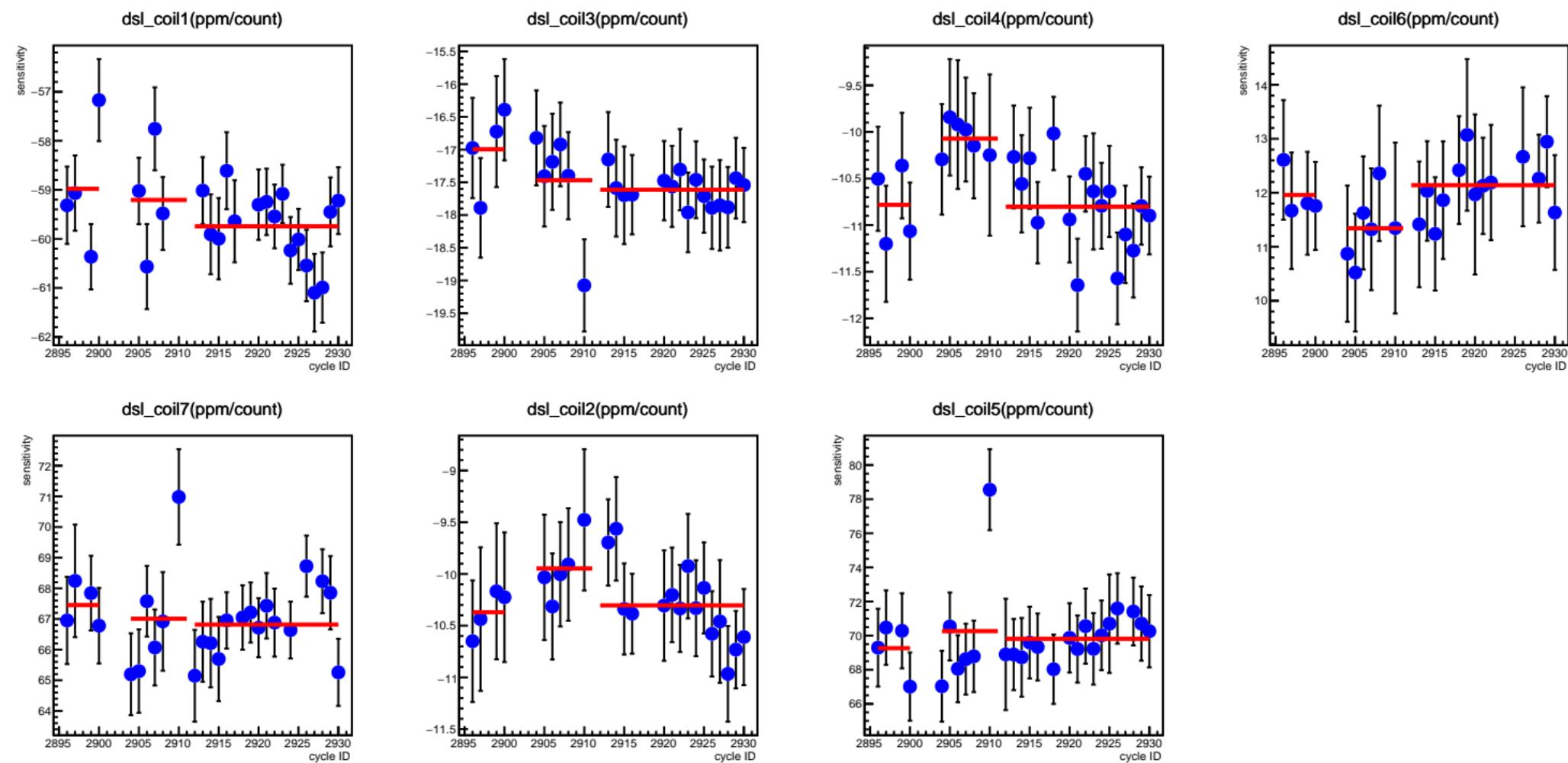


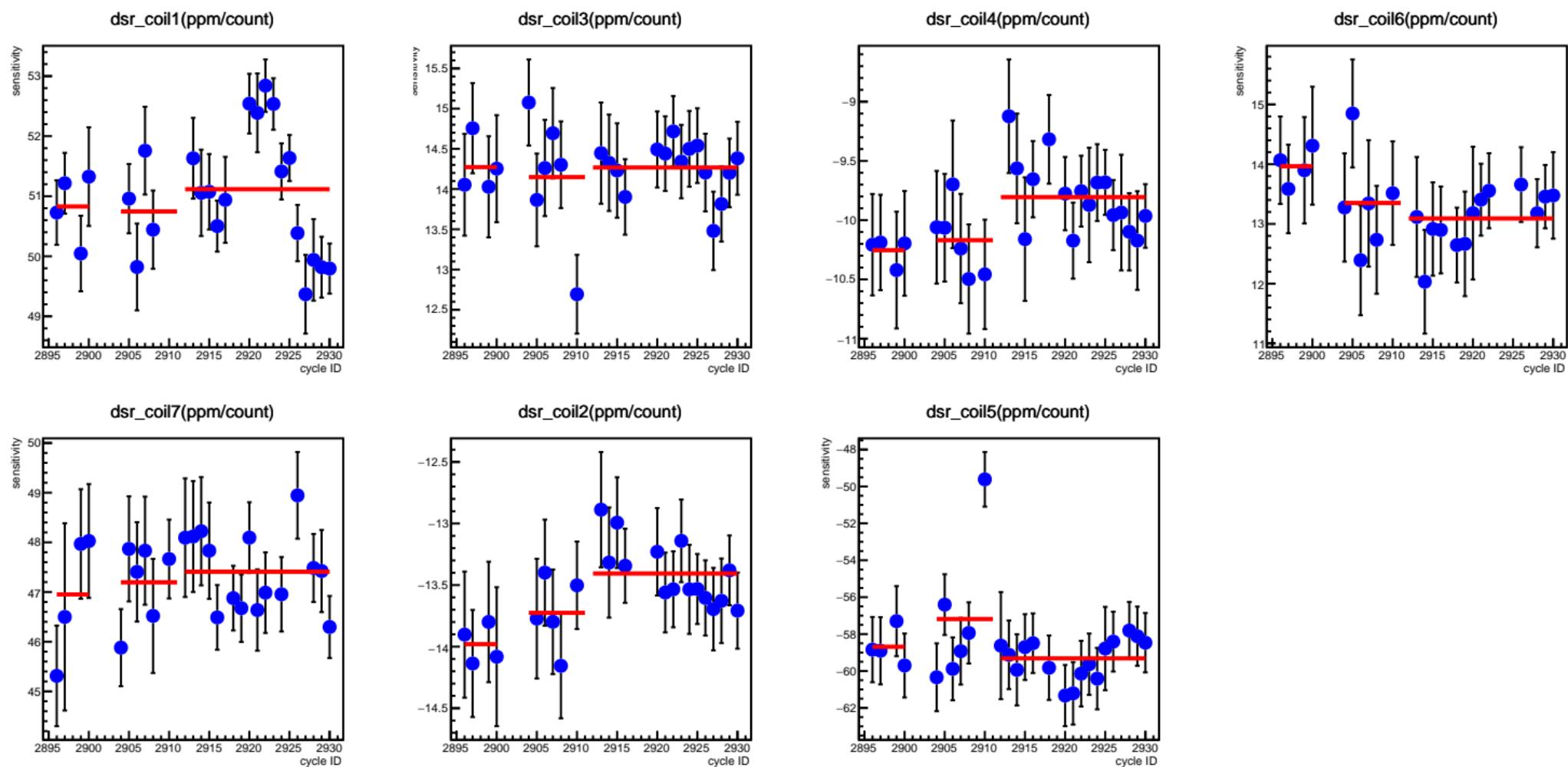
bpm1Y_coil5(um/count)



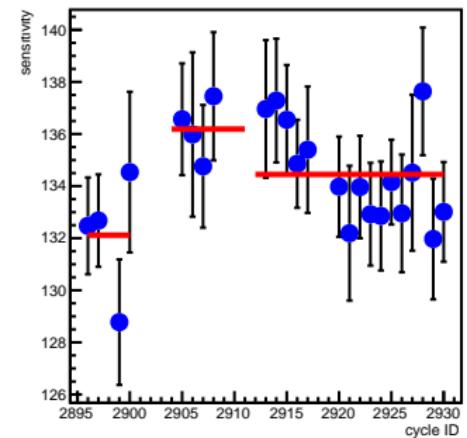




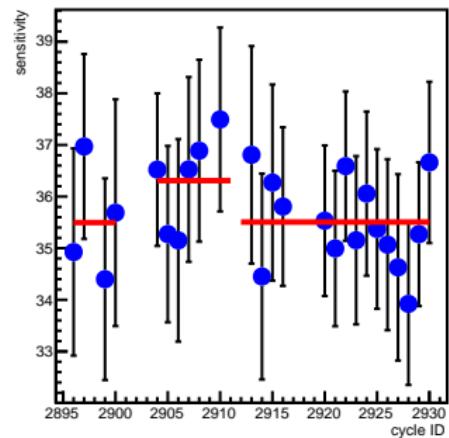




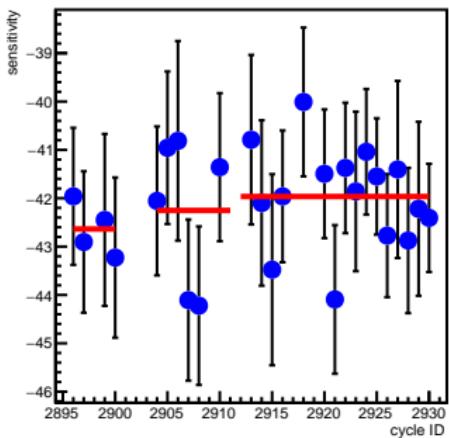
atl1_coil1(ppm/count)



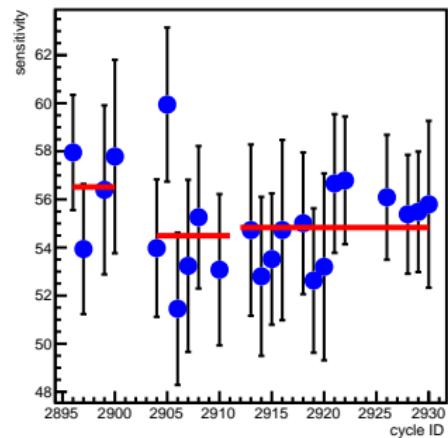
atl1_coil3(ppm/count)



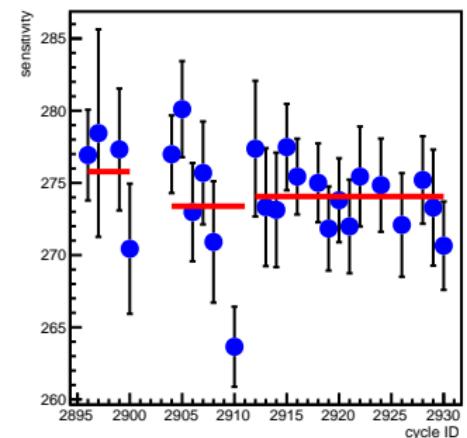
atl1_coil4(ppm/count)



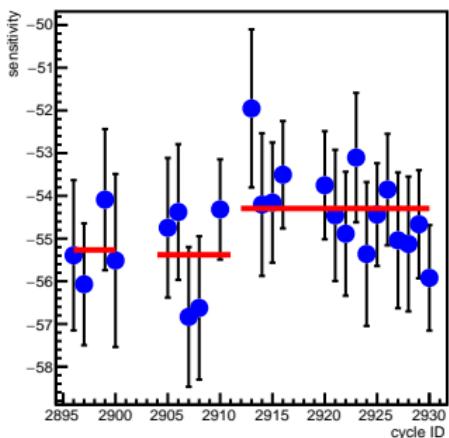
atl1_coil6(ppm/count)



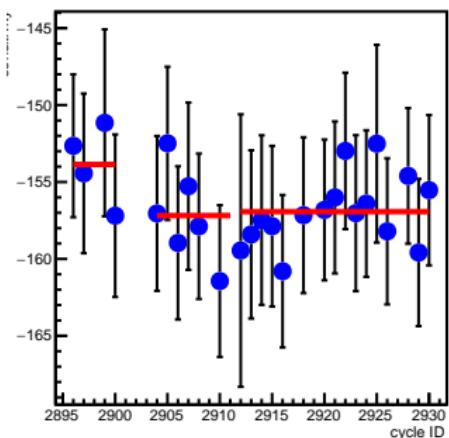
atl1_coil7(ppm/count)



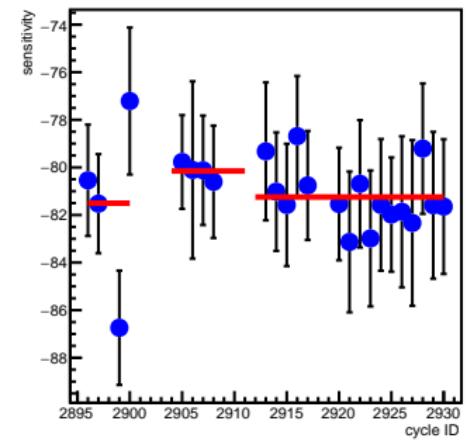
atl1_coil2(ppm/count)



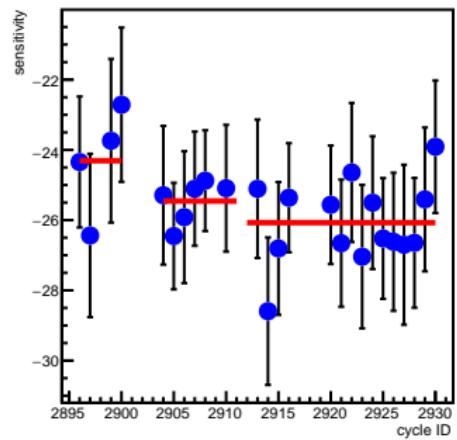
atl1_coil5(ppm/count)



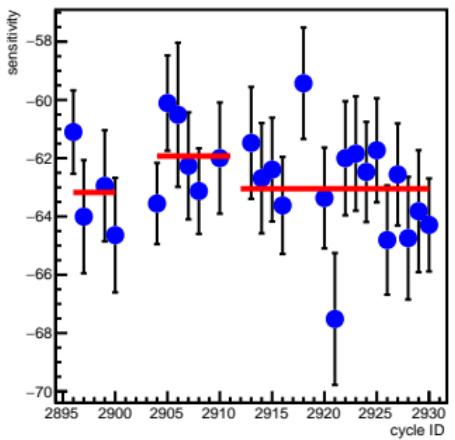
atl2_coil1(ppm/count)



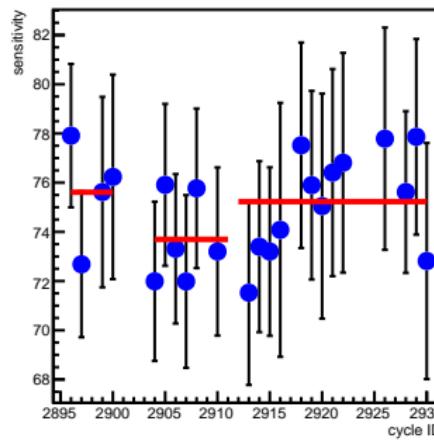
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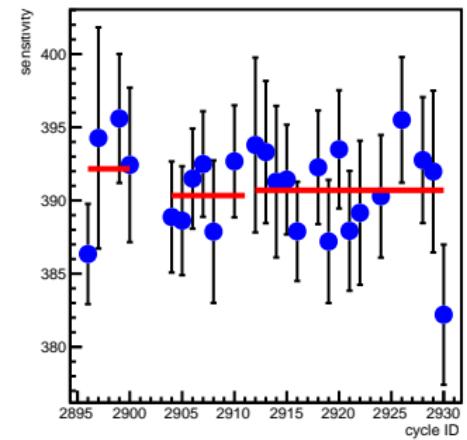
atl2_coil4(ppm/count)



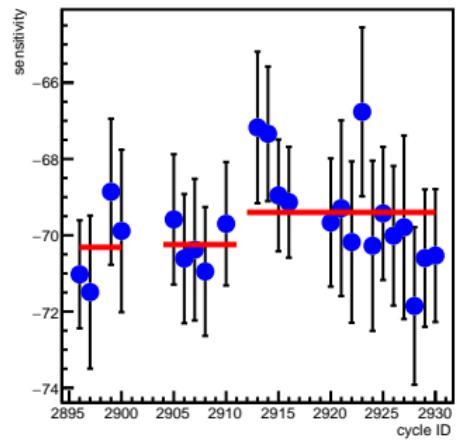
atl2_coil6(ppm/count)



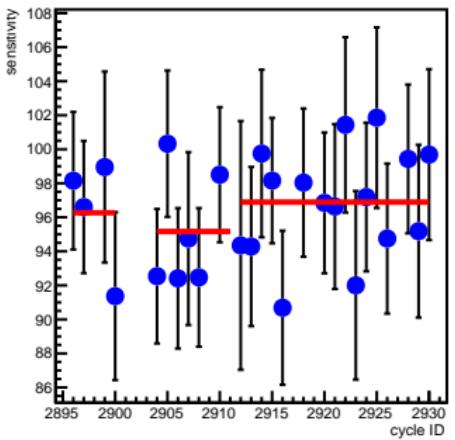
atl2_coil7(ppm/count)



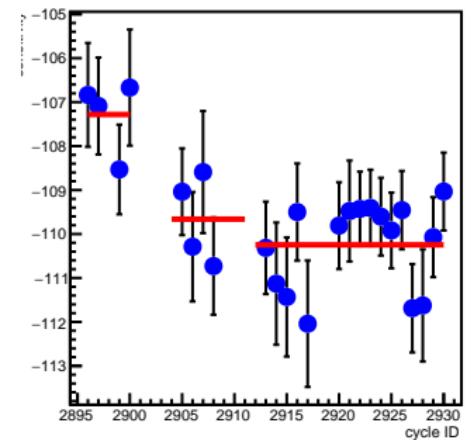
atl2_coil2(ppm/count)



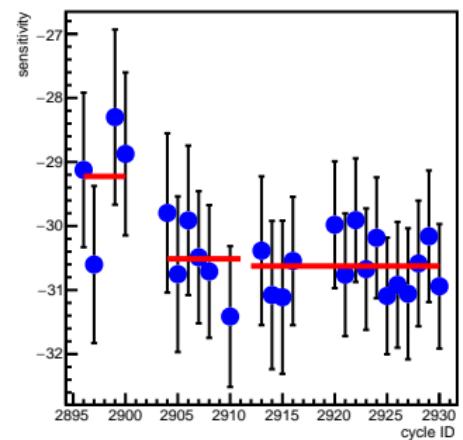
atl2_coil5(ppm/count)



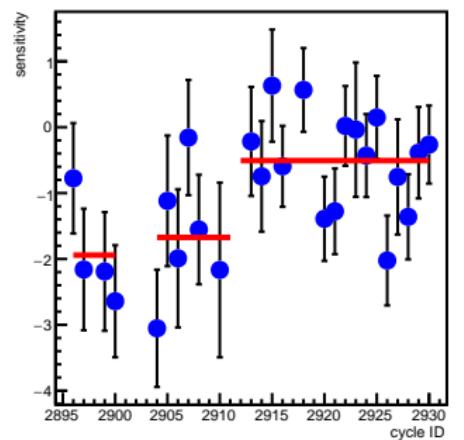
atr1_coil1(ppm/count)



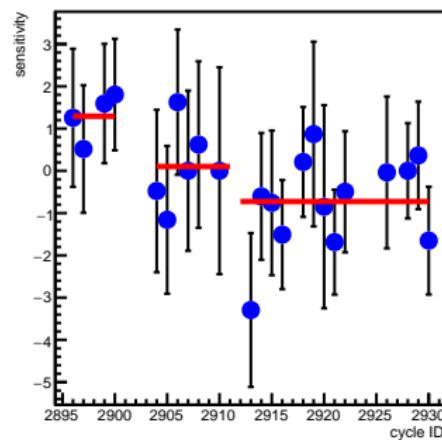
atr1_coil3(ppm/count)



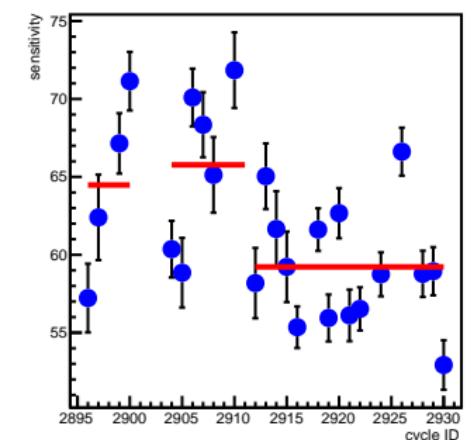
atr1_coil4(ppm/count)



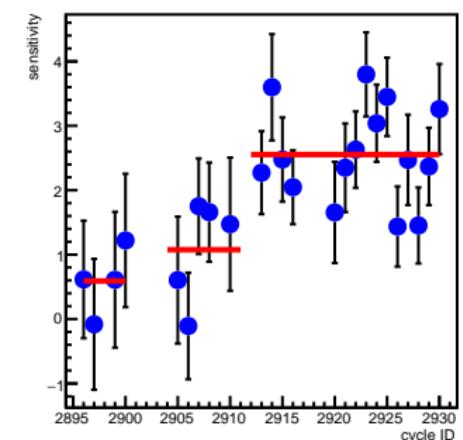
atr1_coil6(ppm/count)



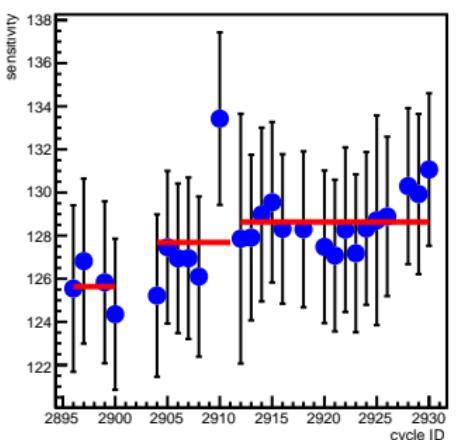
atr1_coil7(ppm/count)



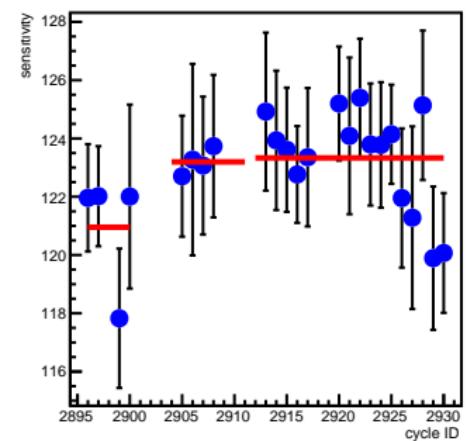
atr1_coil2(ppm/count)



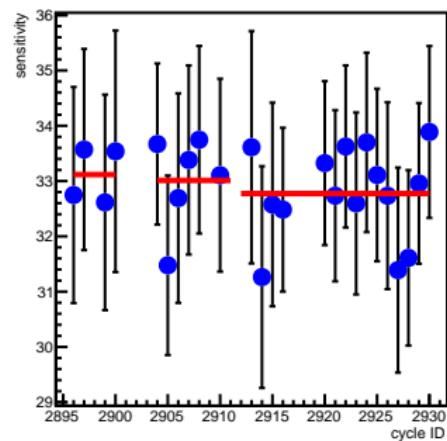
atr1_coil5(ppm/count)



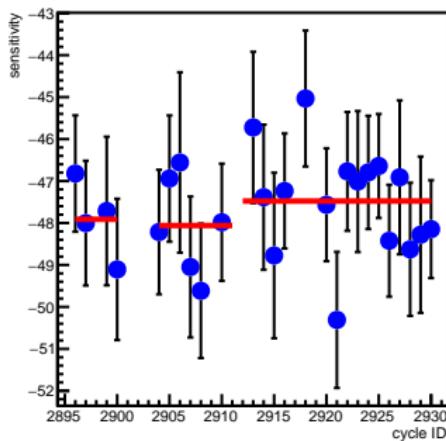
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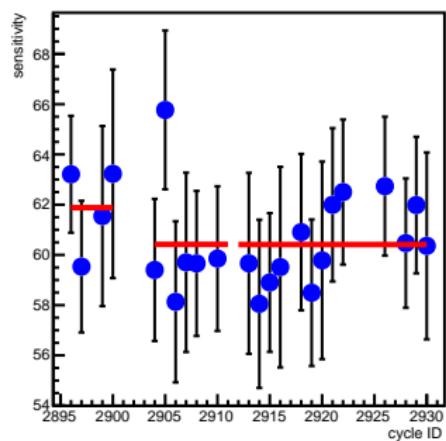
atr2_coil3(ppm/count)



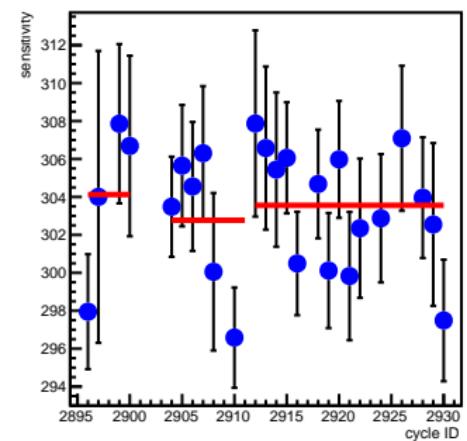
atr2_coil4(ppm/count)



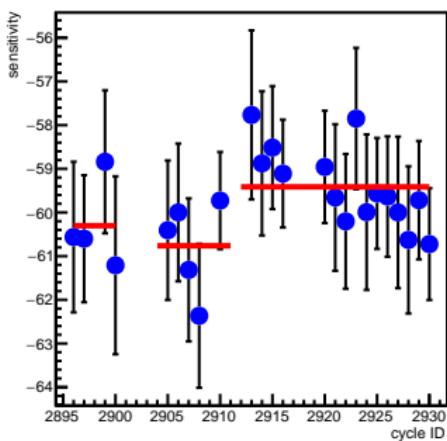
atr2_coil6(ppm/count)



atr2_coil7(ppm/count)



atr2_coil2(ppm/count)



atr2_coil5(ppm/count)

