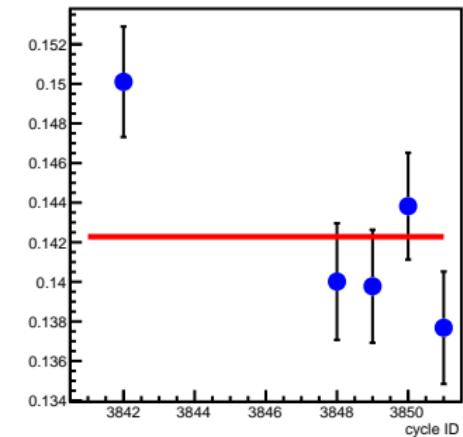
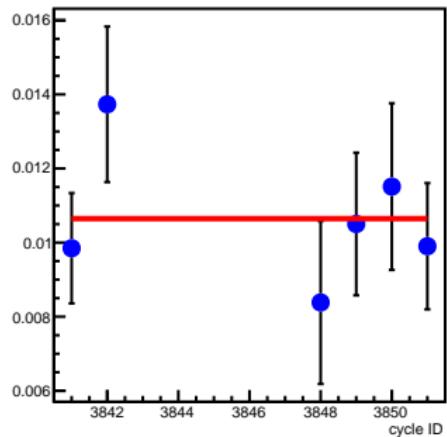


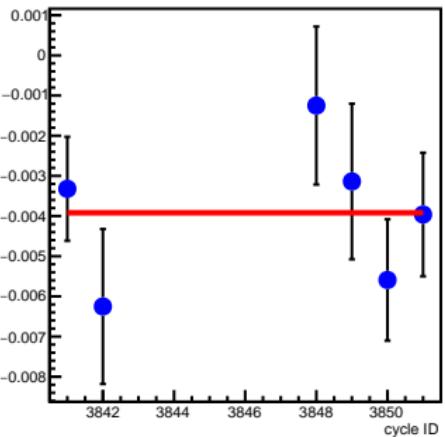
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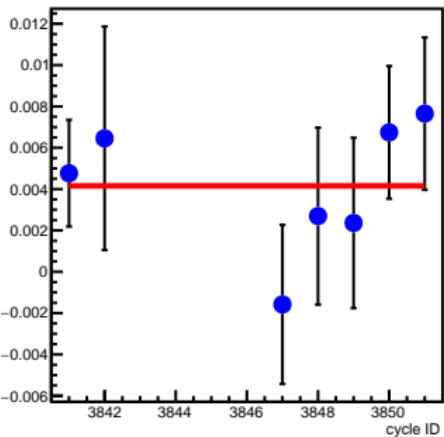
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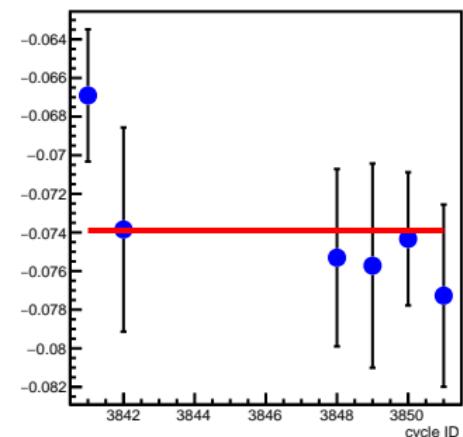
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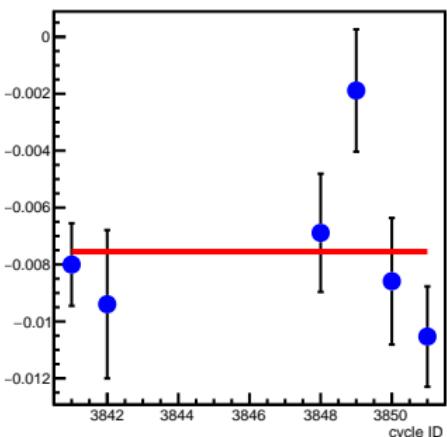
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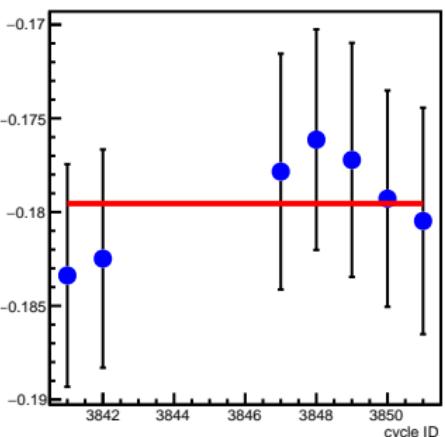
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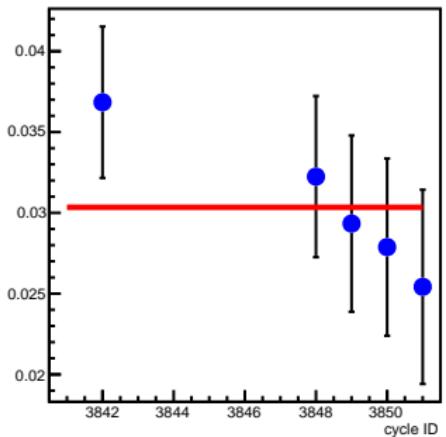
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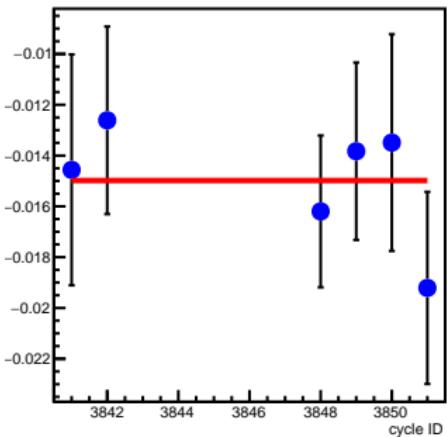
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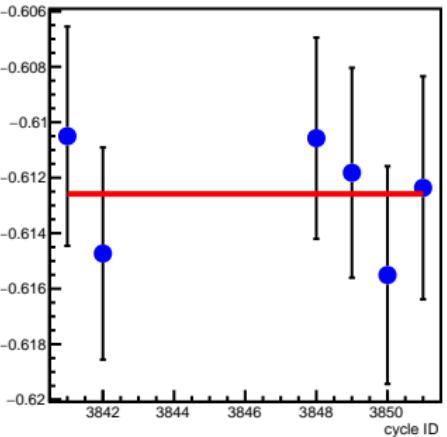
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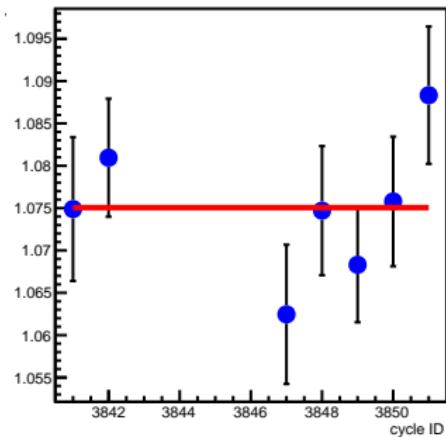
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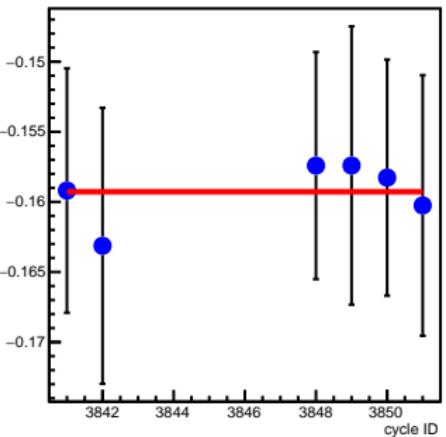
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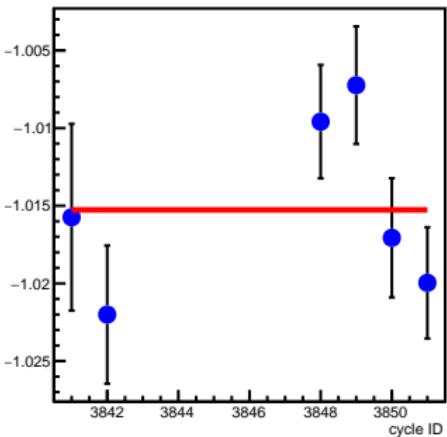
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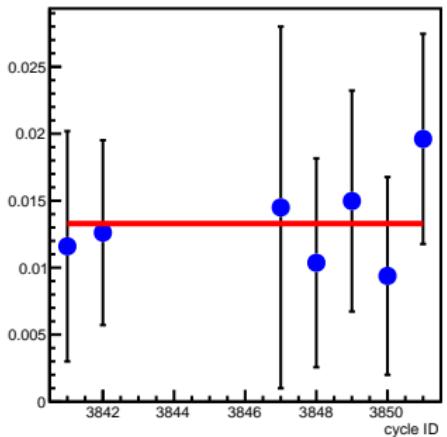
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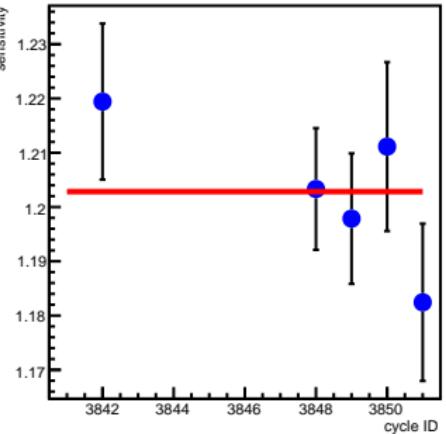
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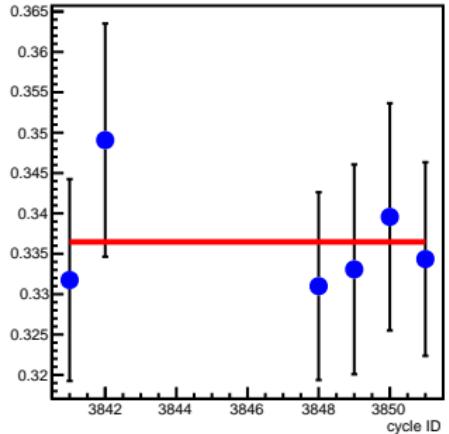
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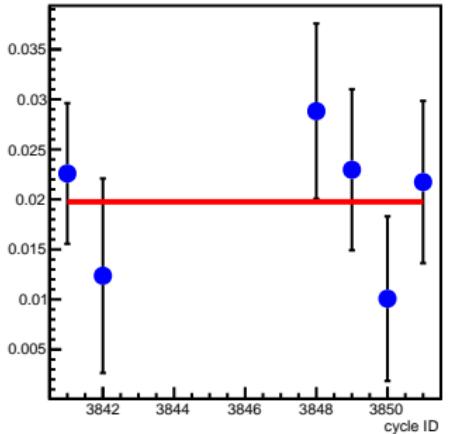
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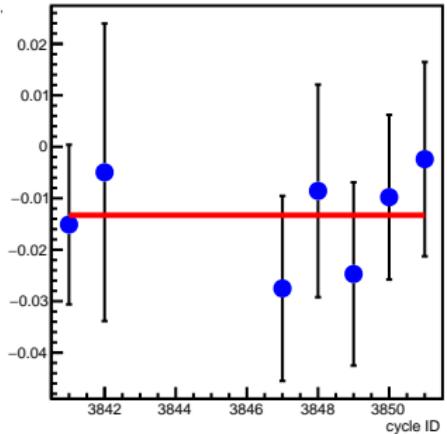
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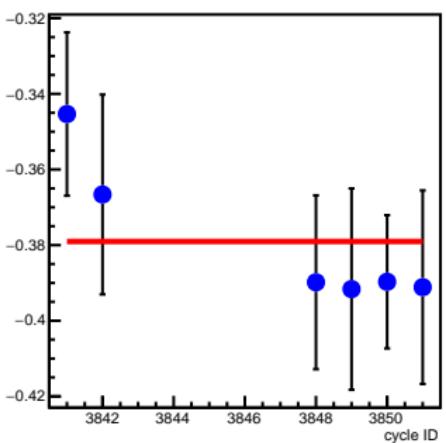
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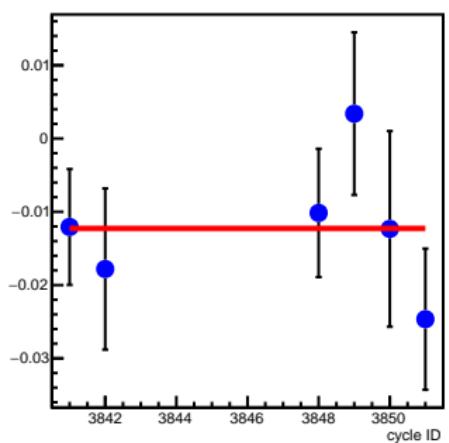
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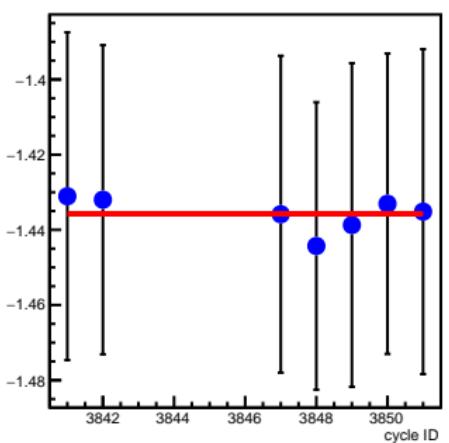
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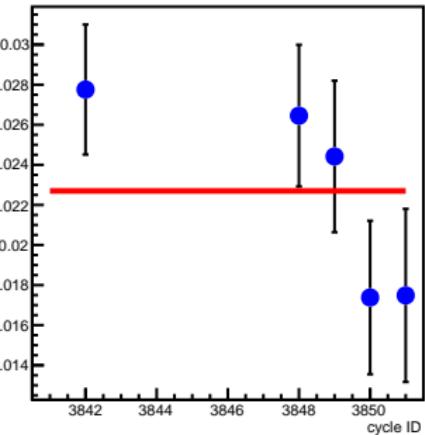
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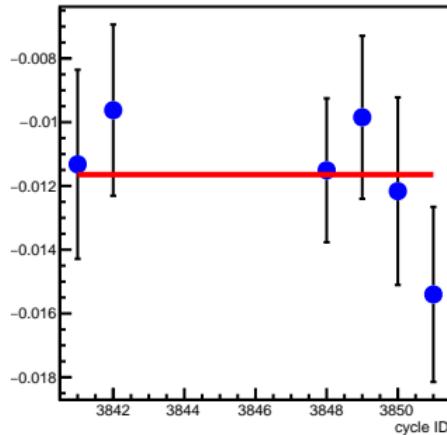
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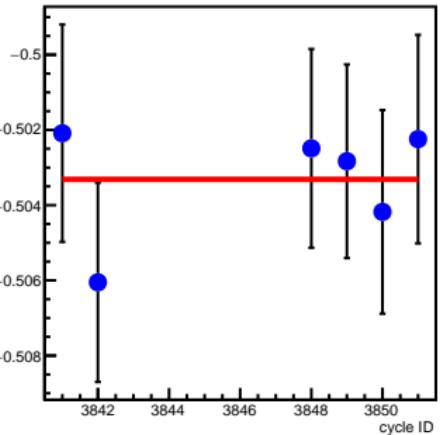
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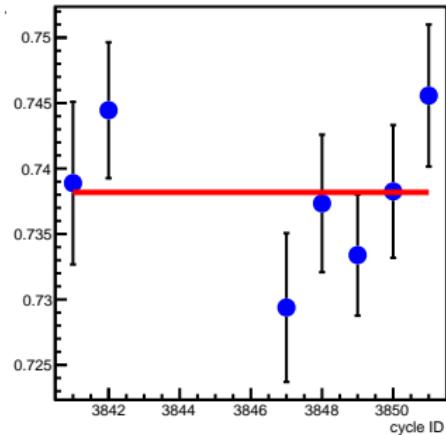
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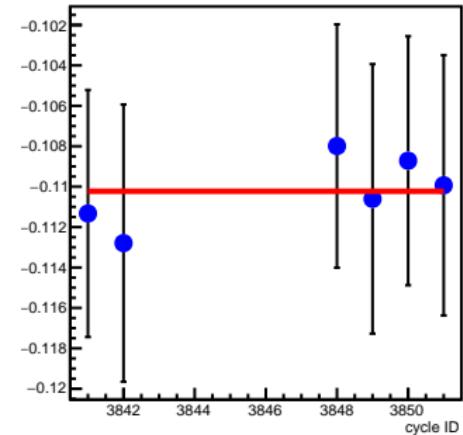
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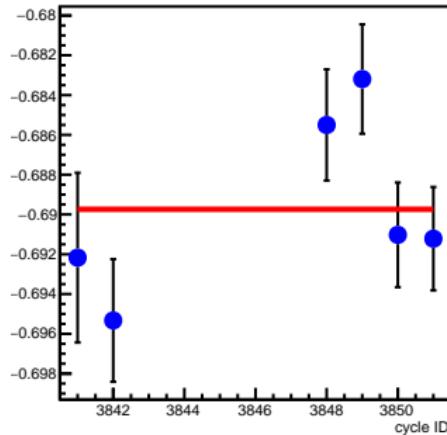
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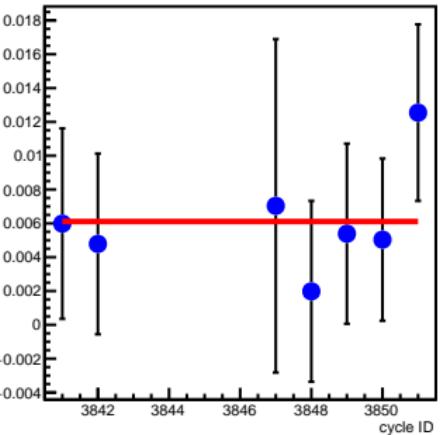
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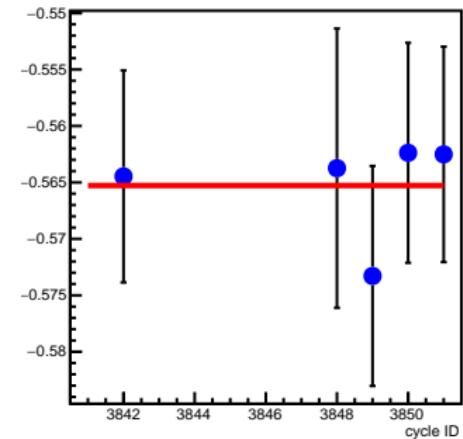
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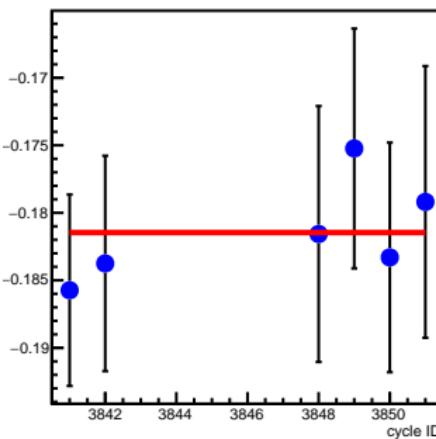
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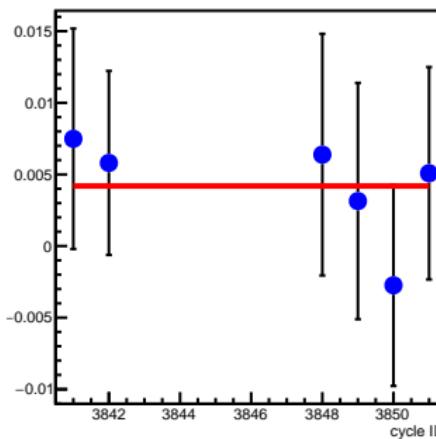
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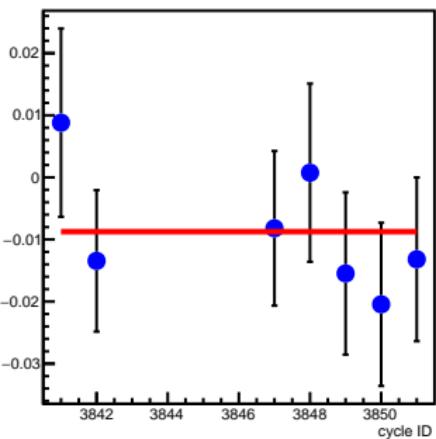
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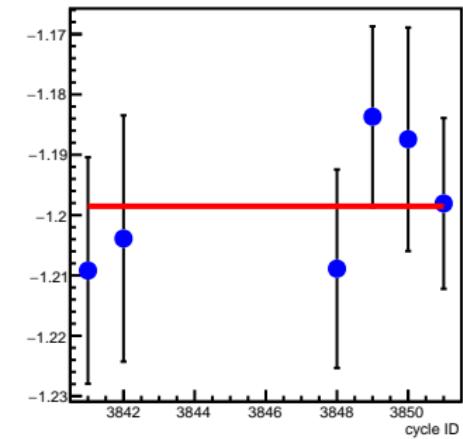
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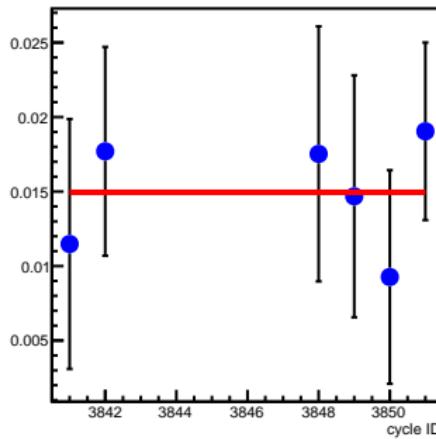
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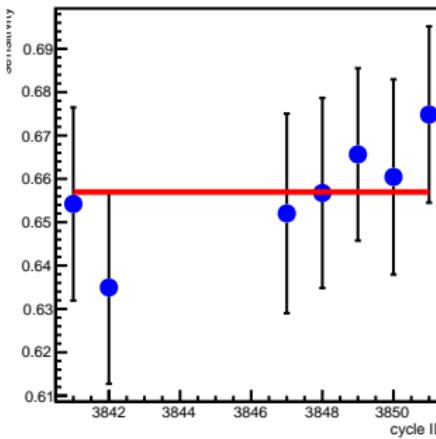
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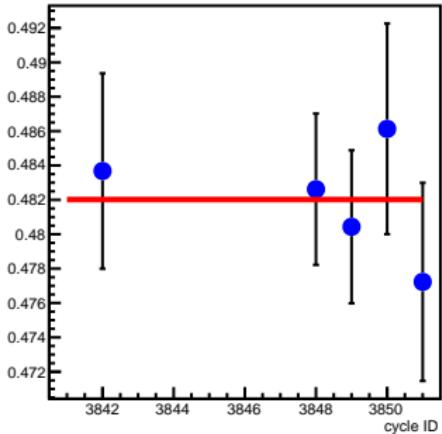
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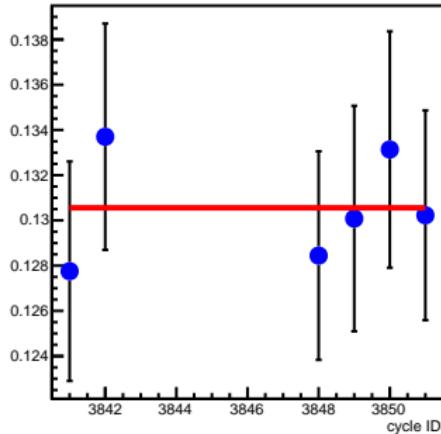
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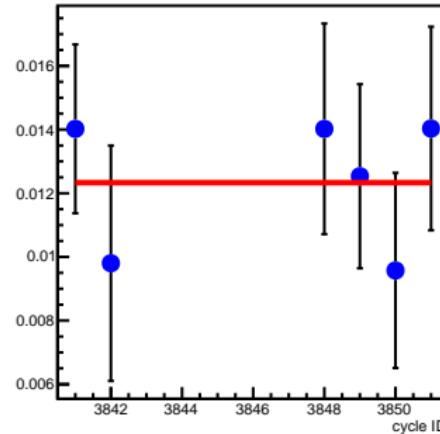
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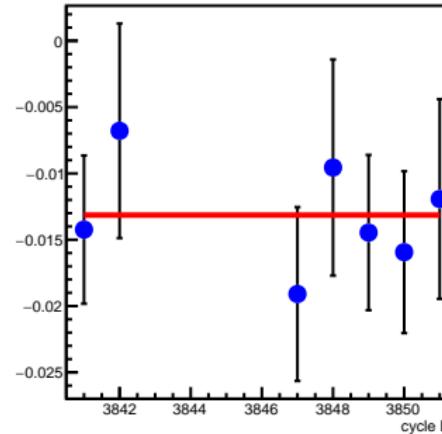
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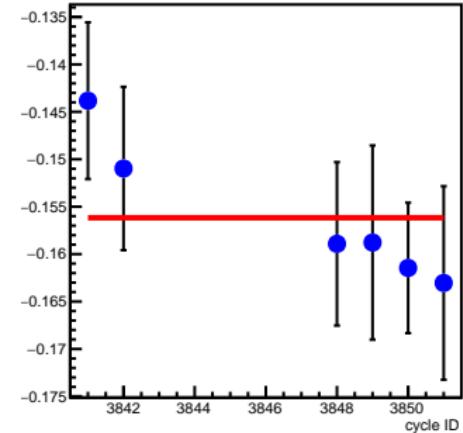
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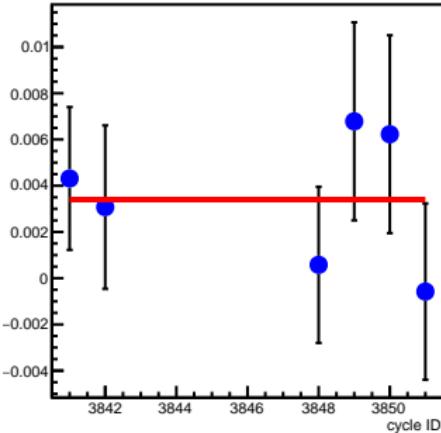
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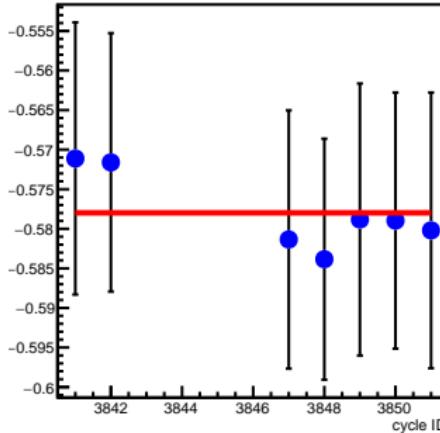
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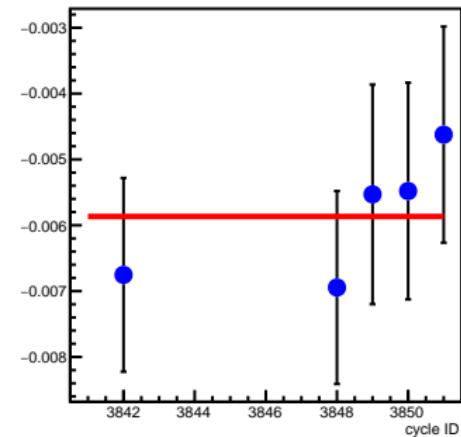
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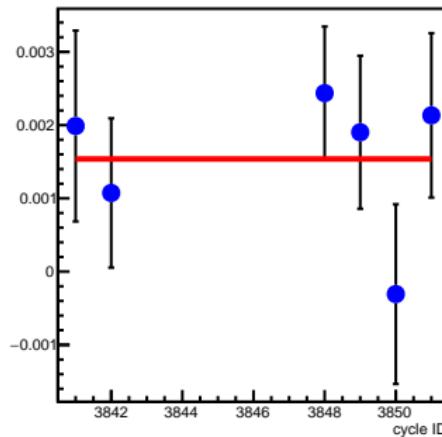
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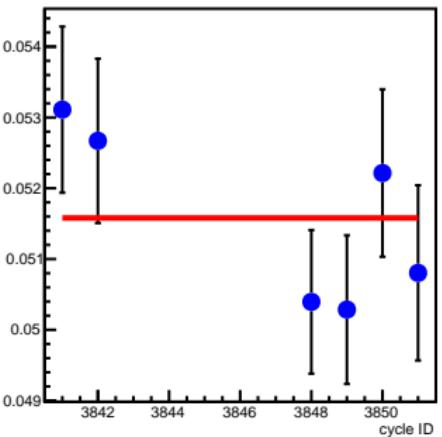
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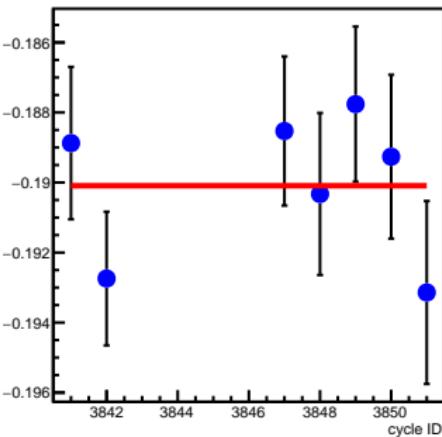
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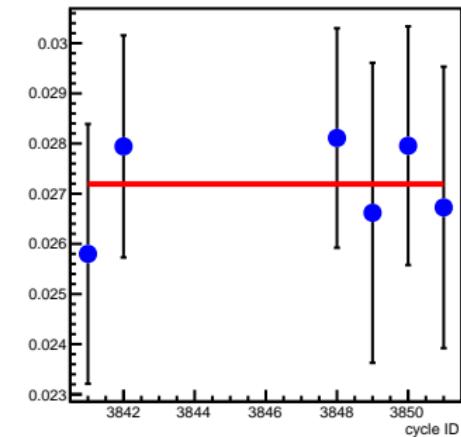
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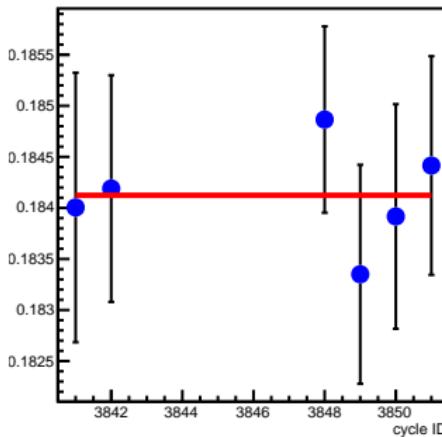
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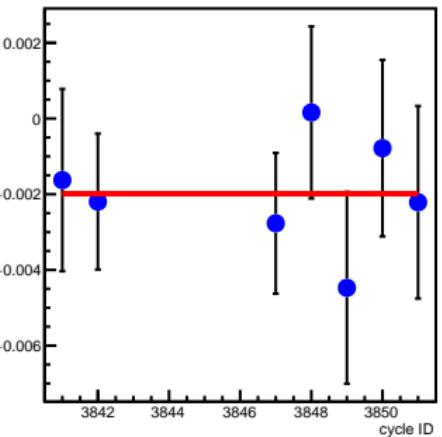
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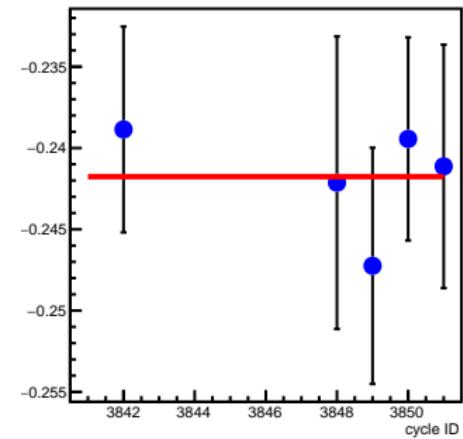
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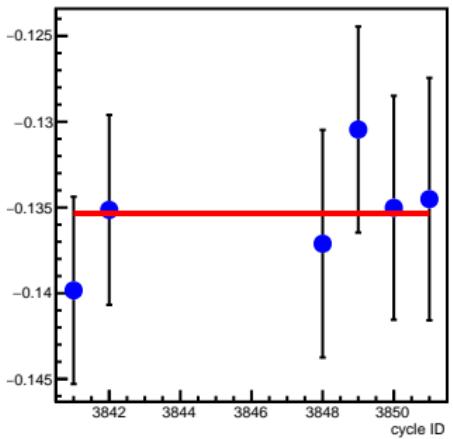
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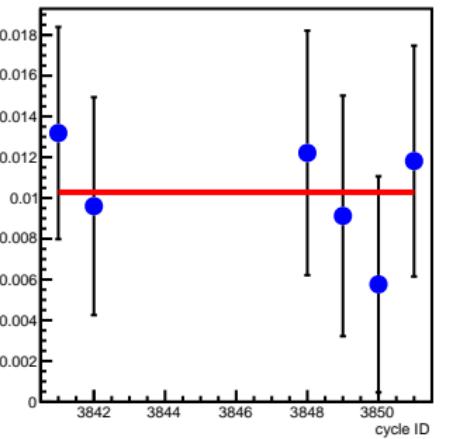
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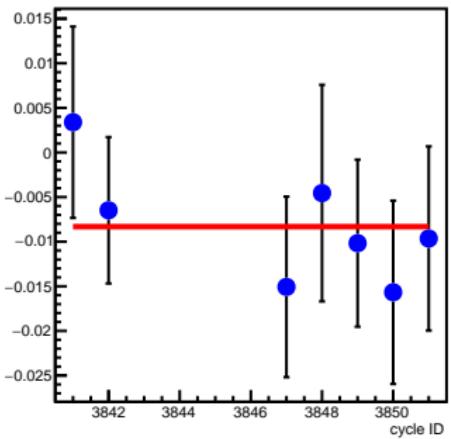
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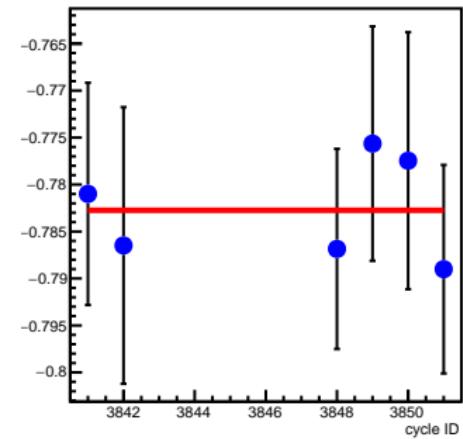
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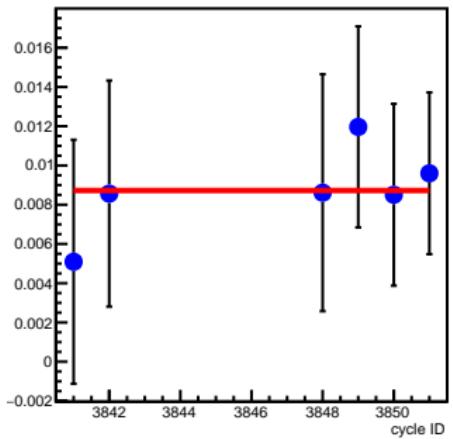
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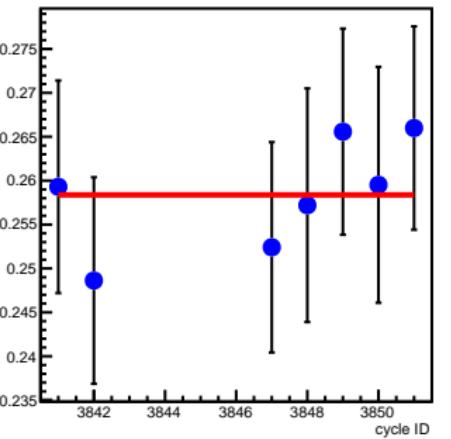
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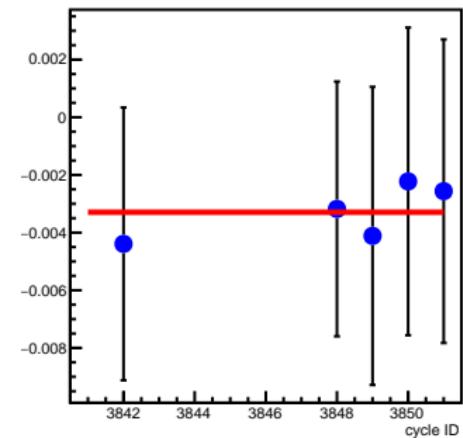
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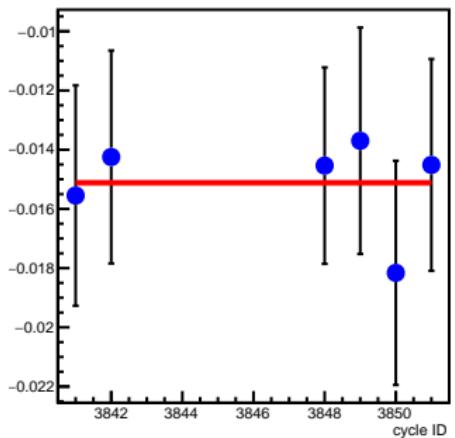
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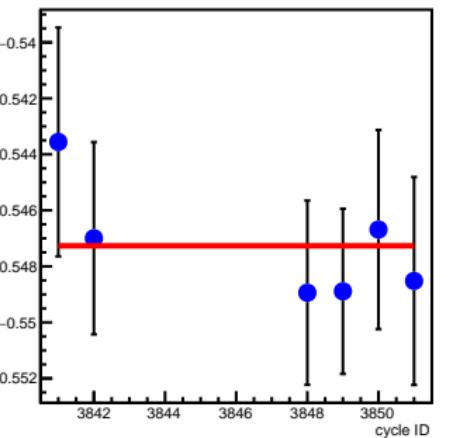
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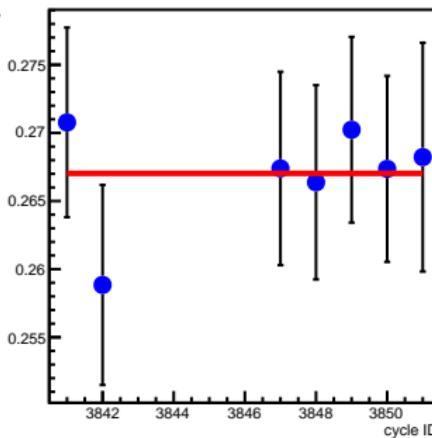
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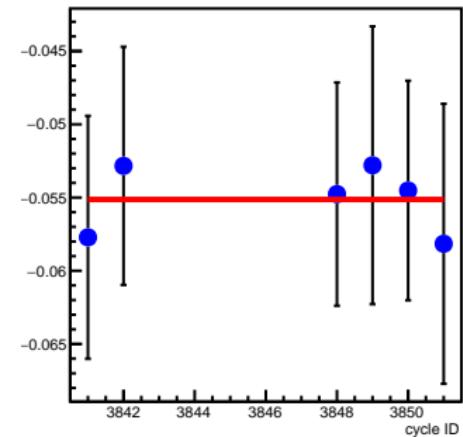
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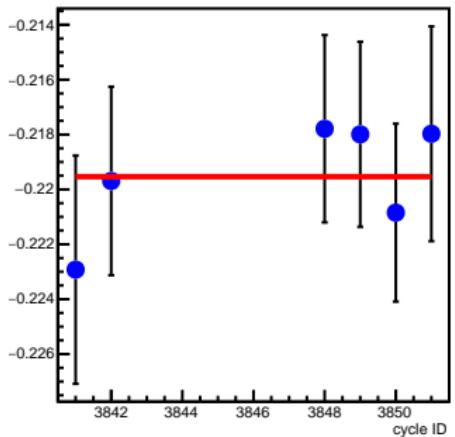
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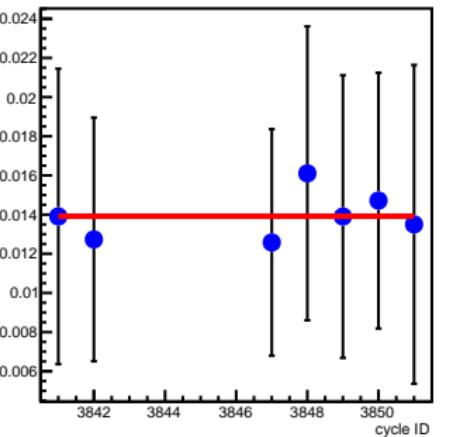
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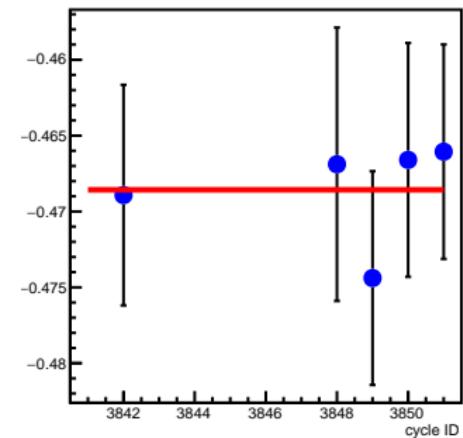
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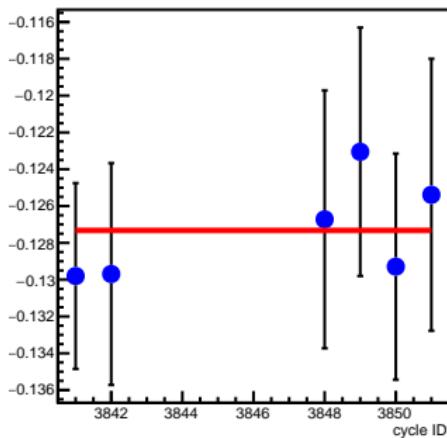
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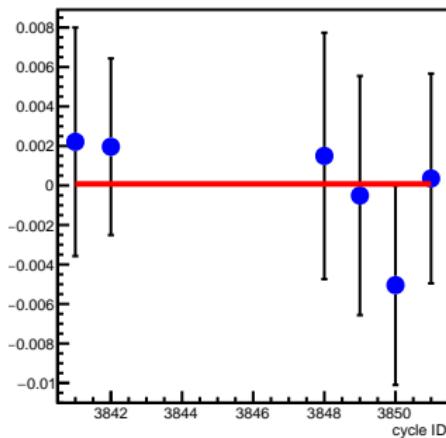
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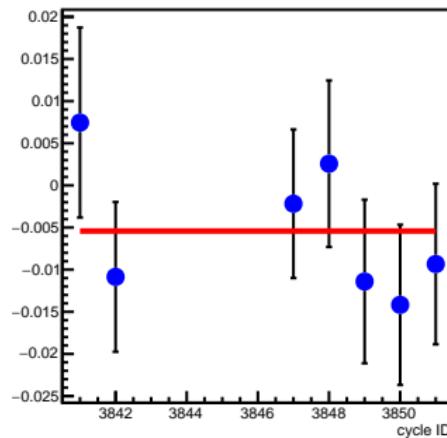
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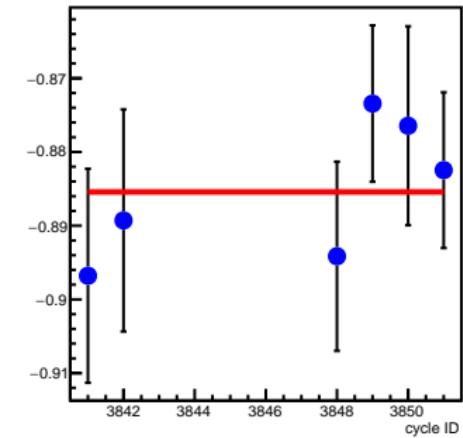
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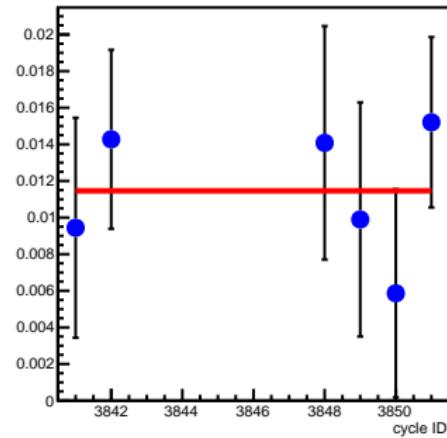
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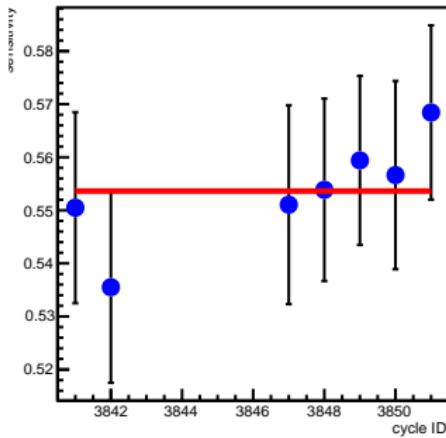
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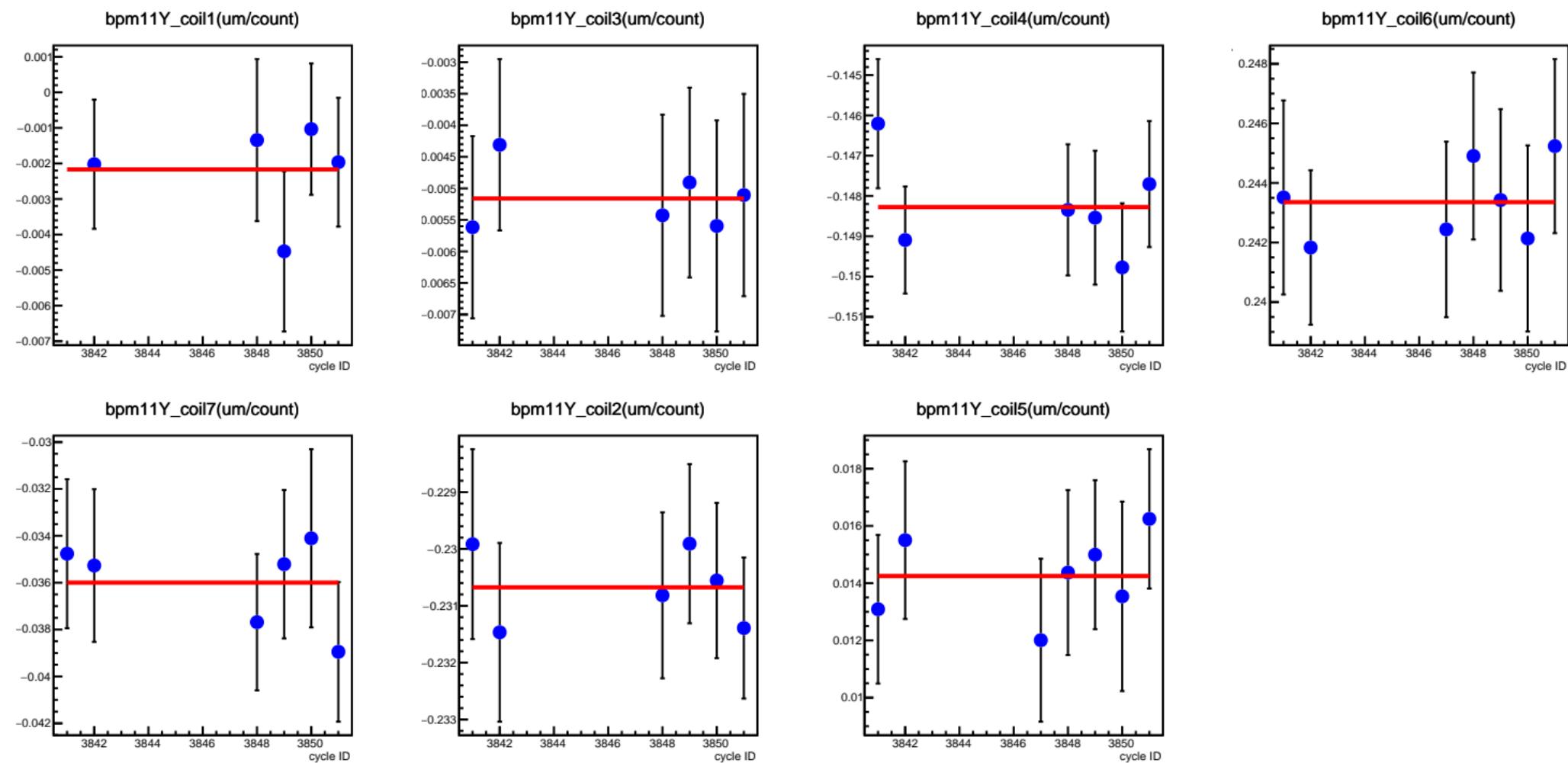


bpm11X\_coil2(um/count)

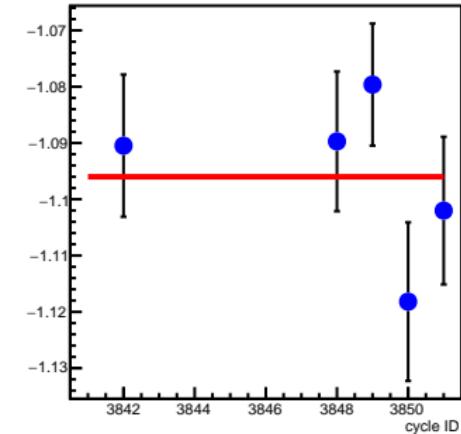


bpm11X\_coil5(um/count)

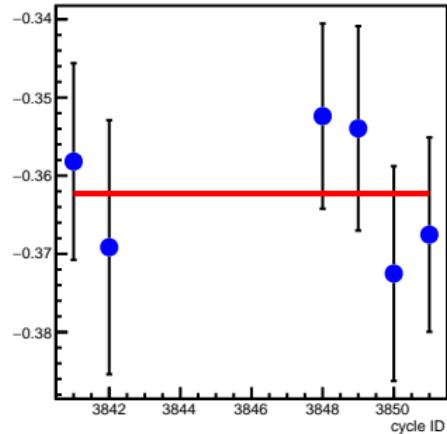




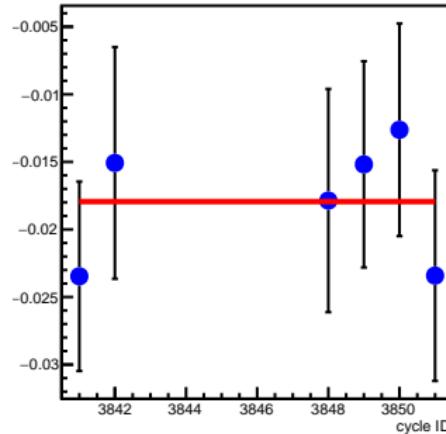
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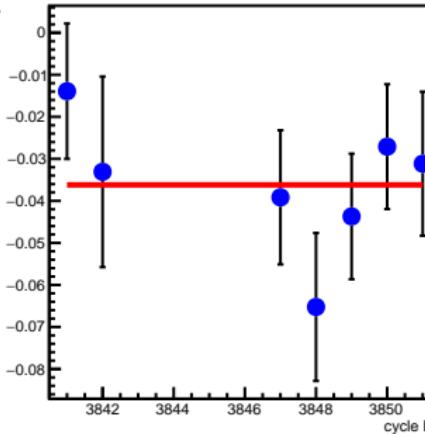
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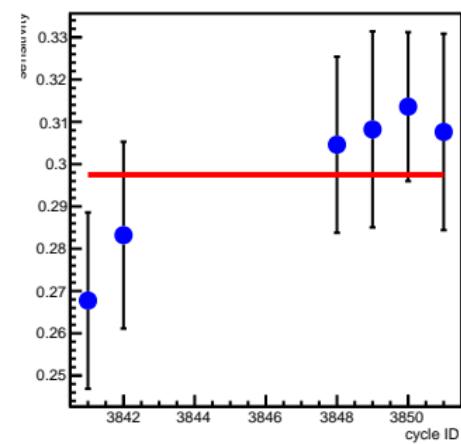
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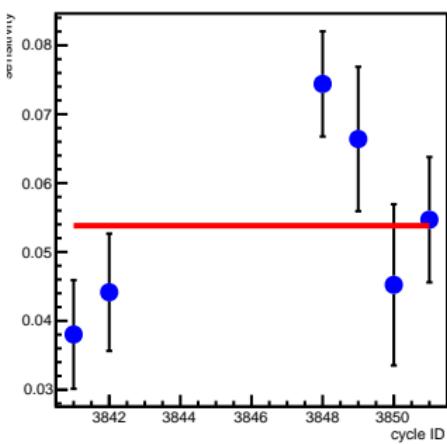
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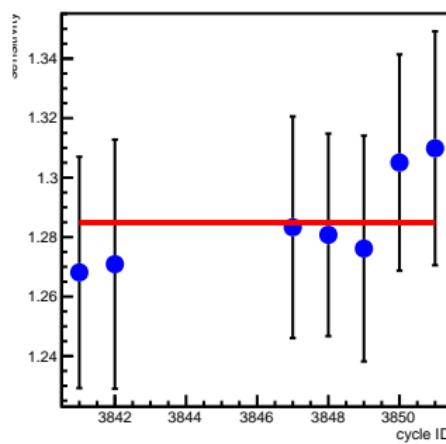
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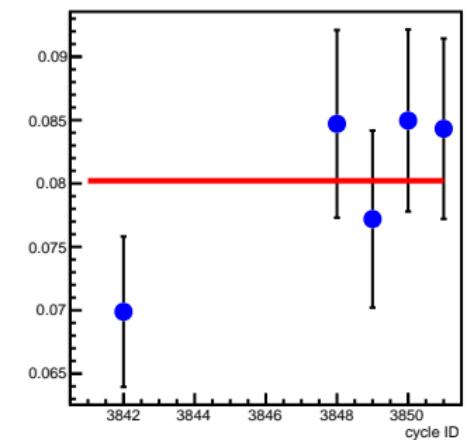
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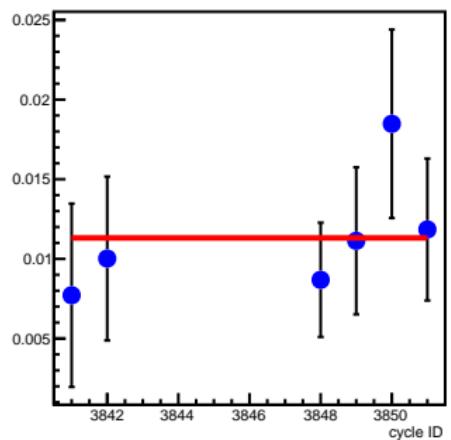
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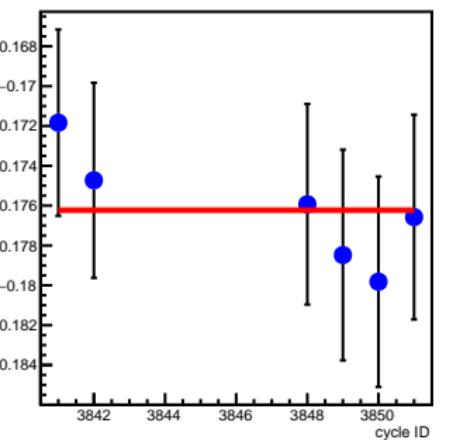
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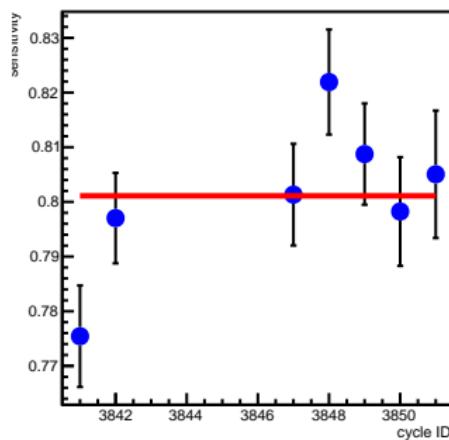
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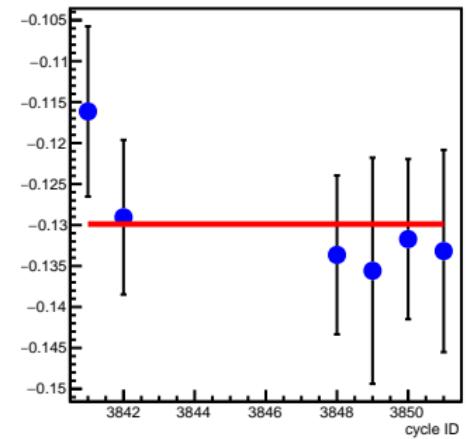
bpm1Y\_coil4(um/count)



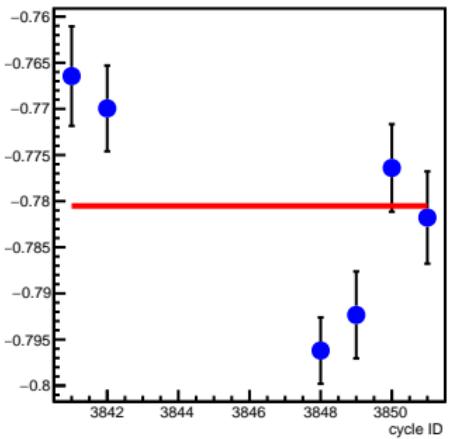
bpm1Y\_coil6(um/count)



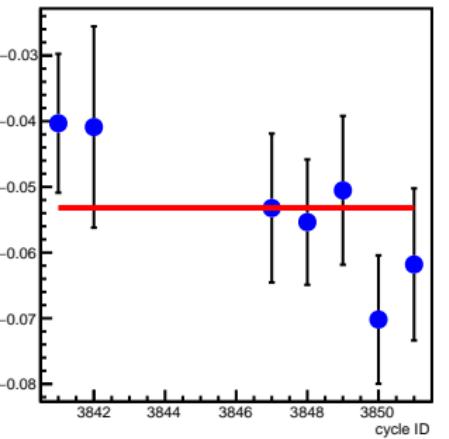
bpm1Y\_coil7(um/count)



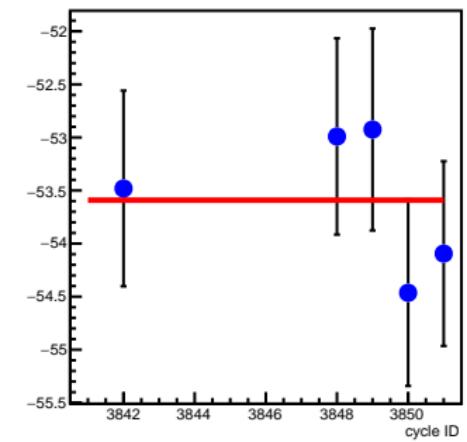
bpm1Y\_coil2(um/count)



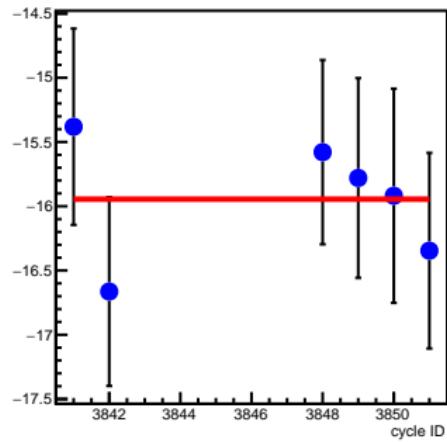
bpm1Y\_coil5(um/count)



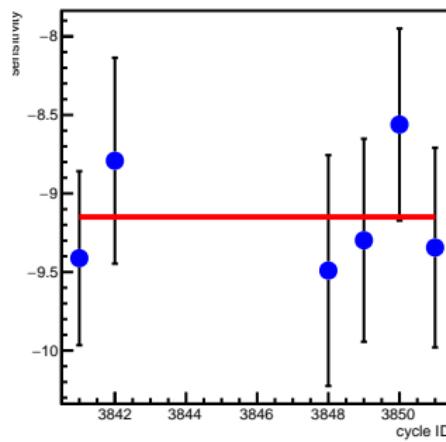
usl\_coil1(ppm/count)



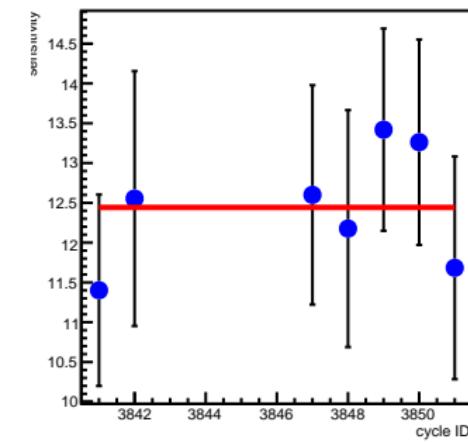
usl\_coil3(ppm/count)



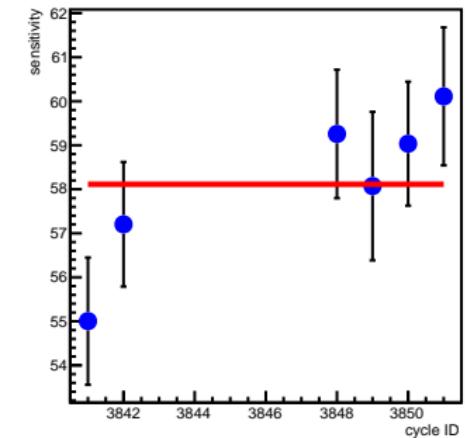
usl\_coil4(ppm/count)



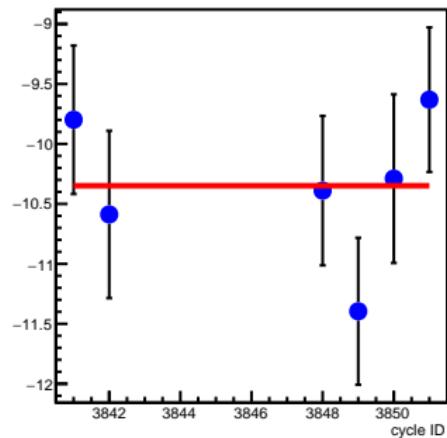
usl\_coil6(ppm/count)



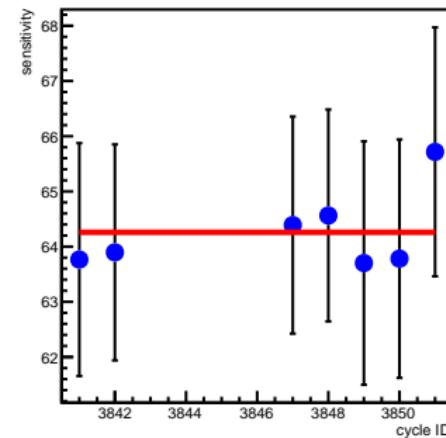
usl\_coil7(ppm/count)

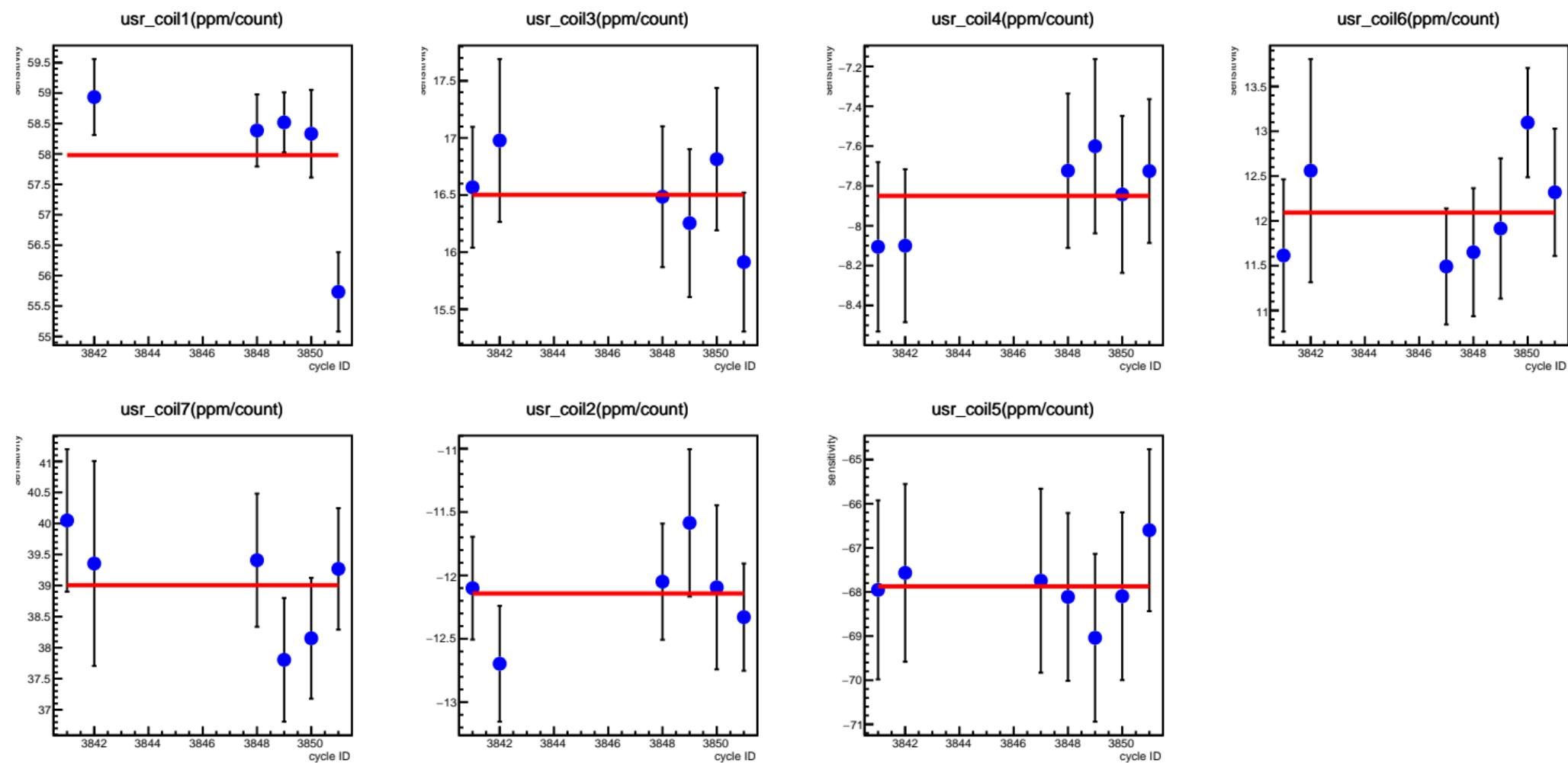


usl\_coil2(ppm/count)

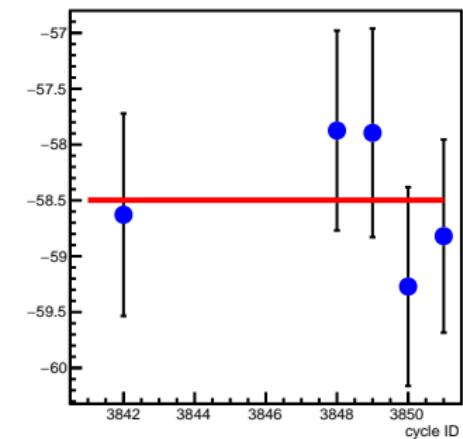


usl\_coil5(ppm/count)

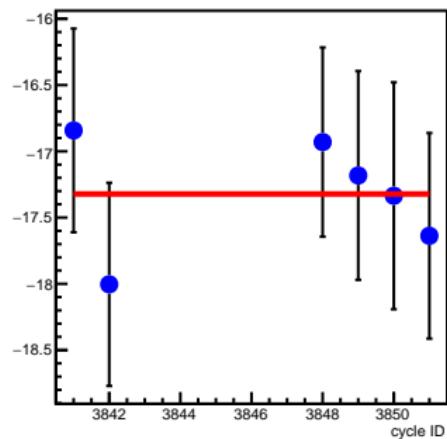




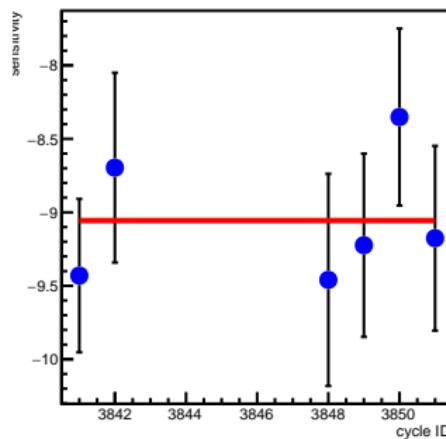
dsl\_coil1(ppm/count)



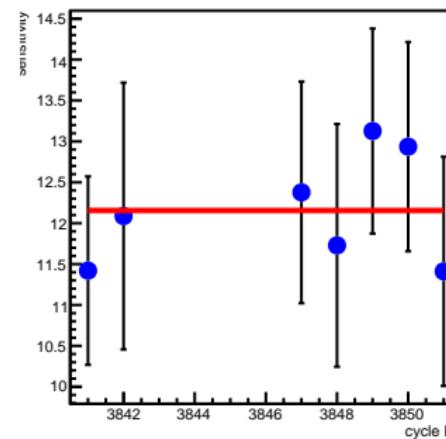
dsl\_coil3(ppm/count)



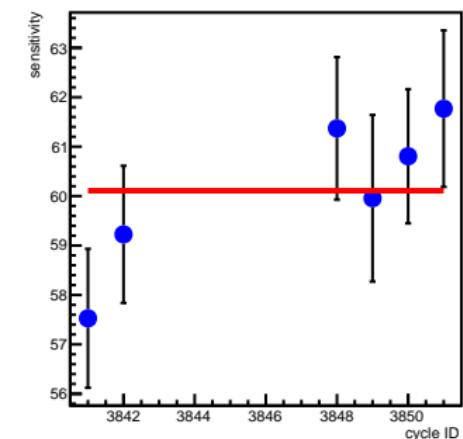
dsl\_coil4(ppm/count)



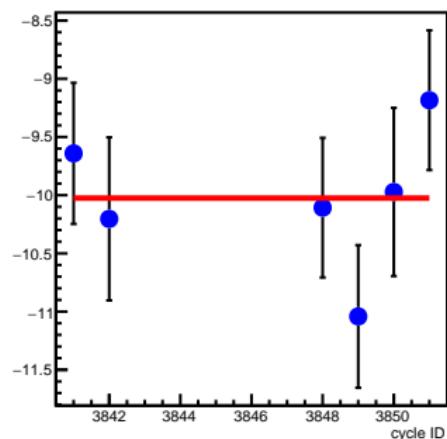
dsl\_coil6(ppm/count)



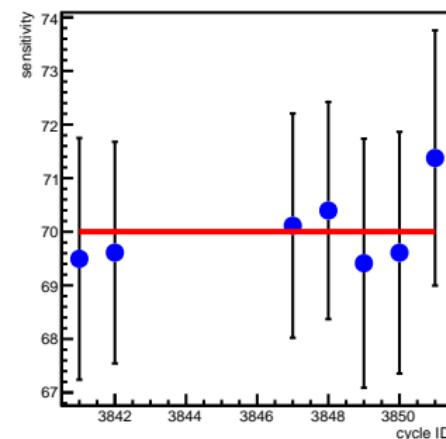
dsl\_coil7(ppm/count)

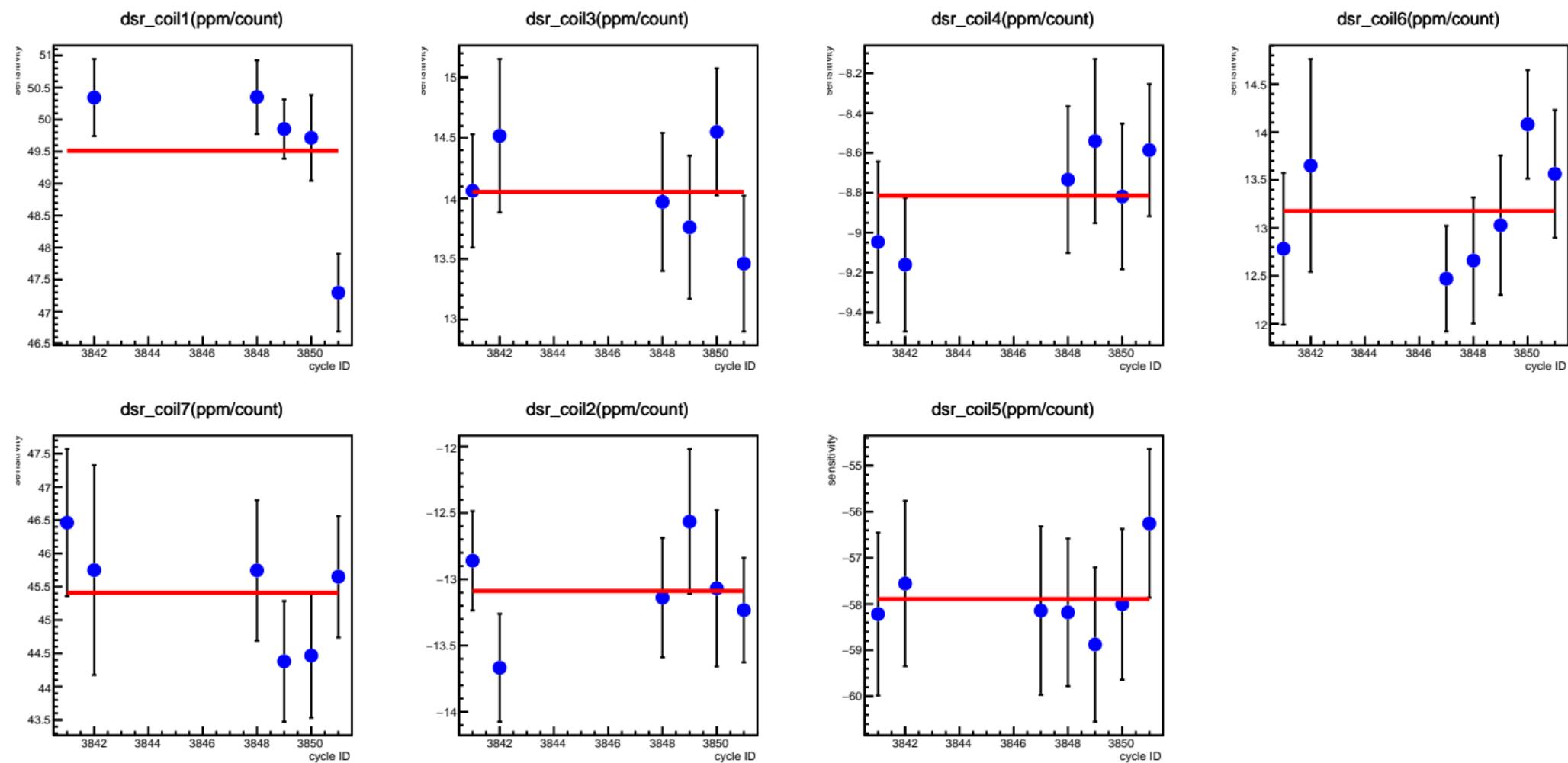


dsl\_coil2(ppm/count)

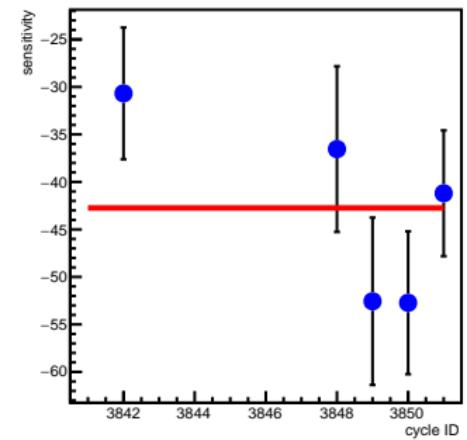


dsl\_coil5(ppm/count)

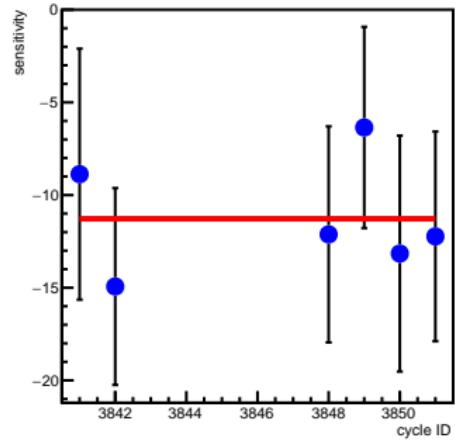




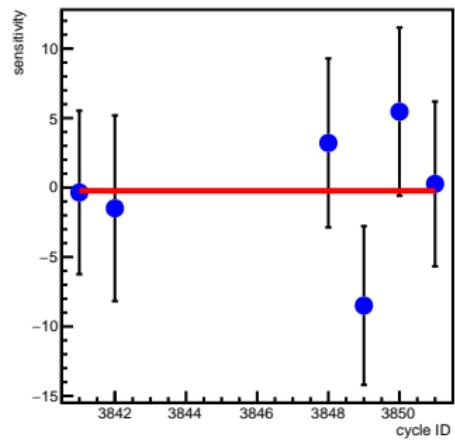
at1\_coil1(ppm/count)



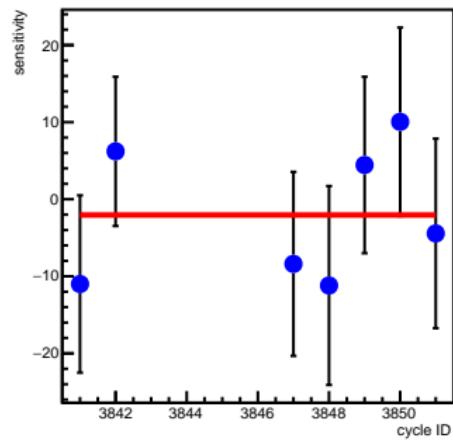
at1\_coil3(ppm/count)



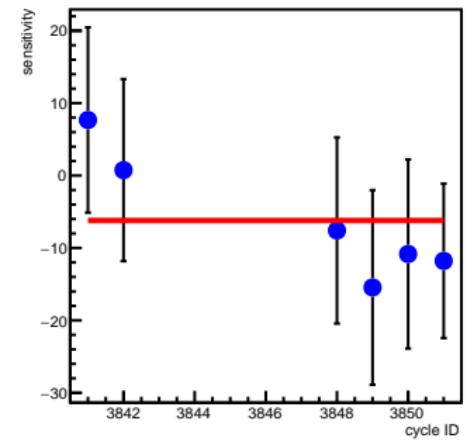
at1\_coil4(ppm/count)



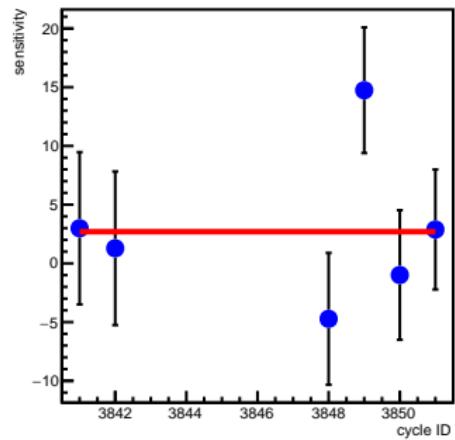
at1\_coil6(ppm/count)



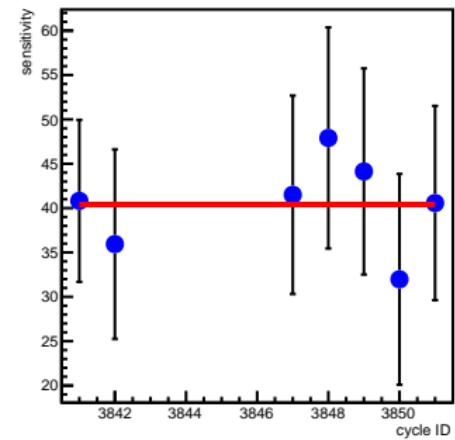
at1\_coil7(ppm/count)



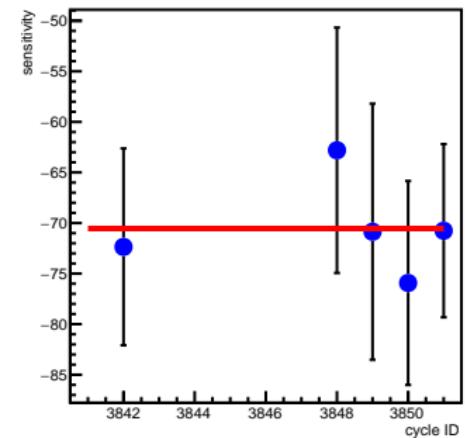
at1\_coil2(ppm/count)



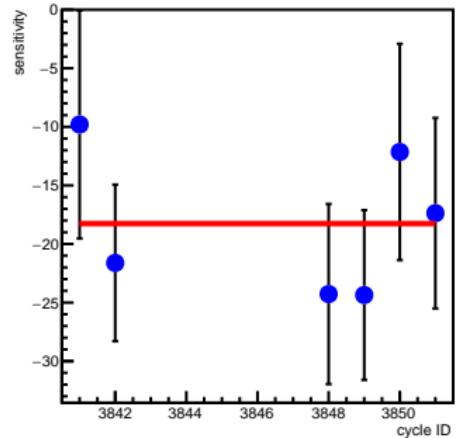
at1\_coil5(ppm/count)



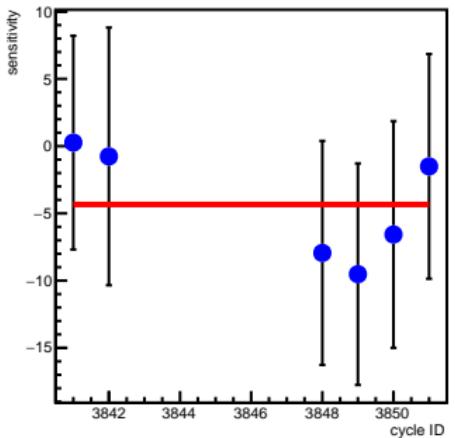
atl2\_coil1(ppm/count)



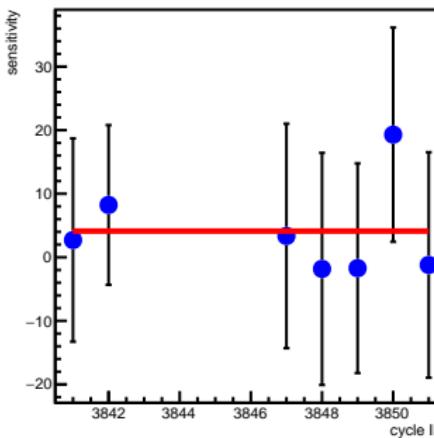
atl2\_coil3(ppm/count)



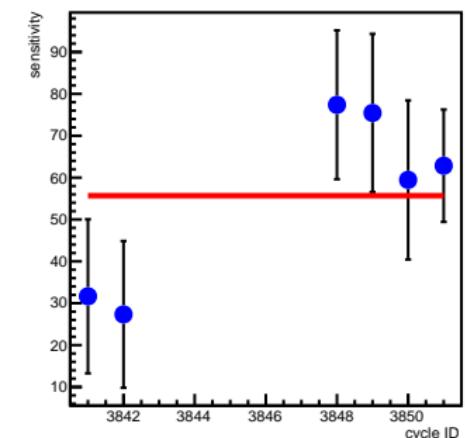
atl2\_coil4(ppm/count)



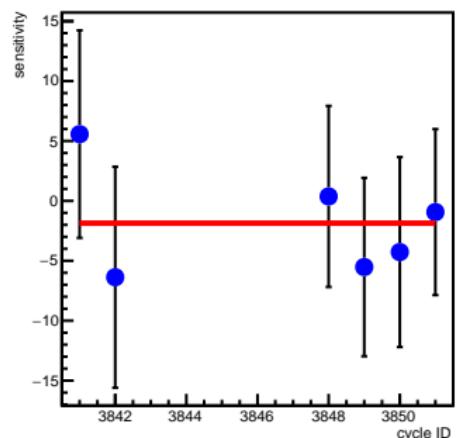
atl2\_coil6(ppm/count)



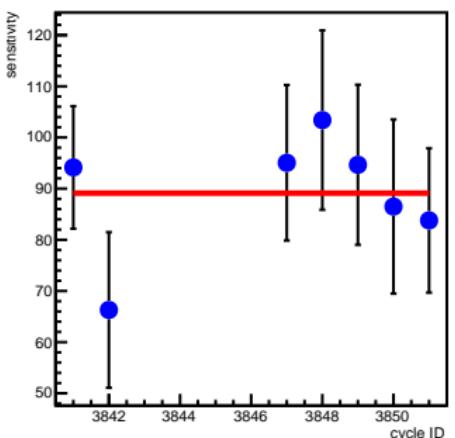
atl2\_coil7(ppm/count)



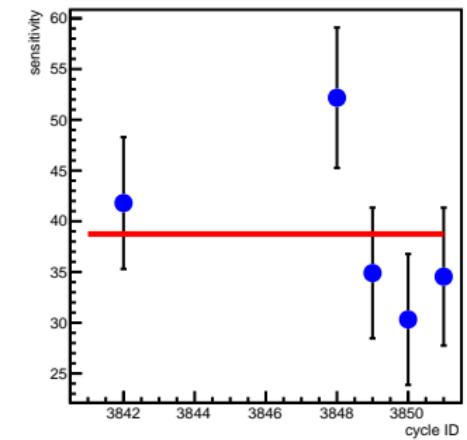
atl2\_coil2(ppm/count)



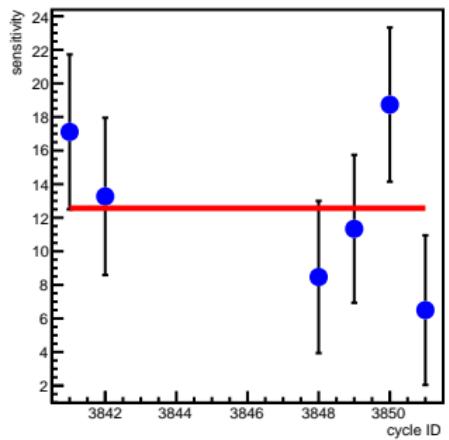
atl2\_coil5(ppm/count)



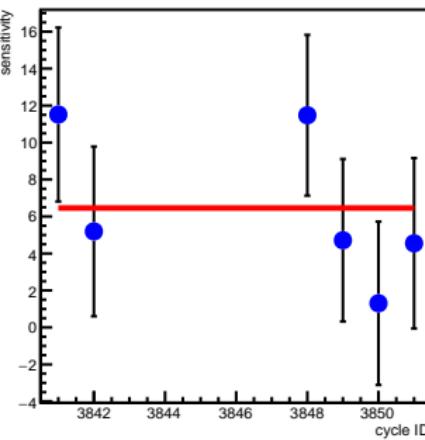
atr1\_coil1(ppm/count)



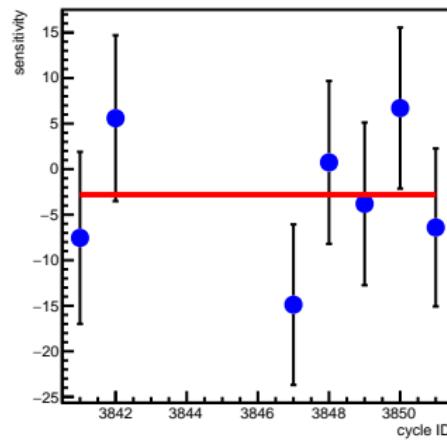
atr1\_coil3(ppm/count)



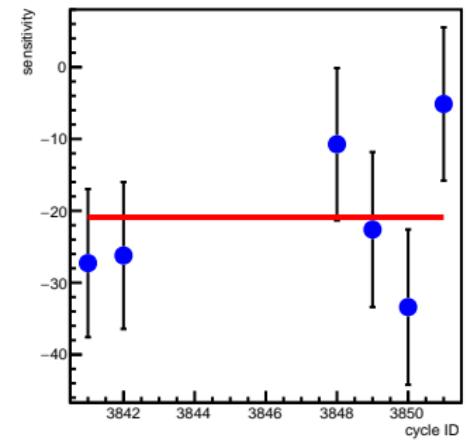
atr1\_coil4(ppm/count)



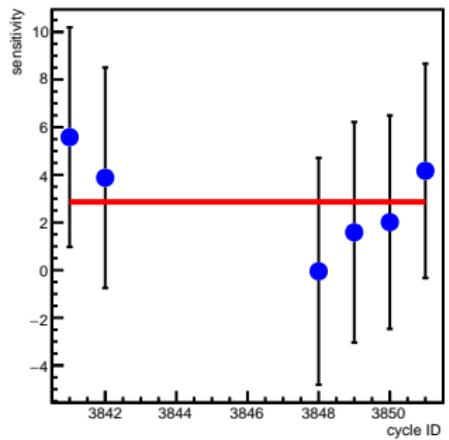
atr1\_coil6(ppm/count)



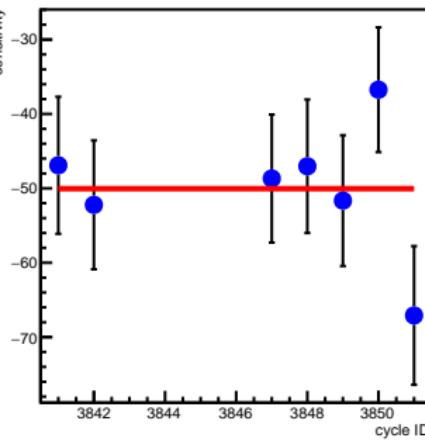
atr1\_coil7(ppm/count)



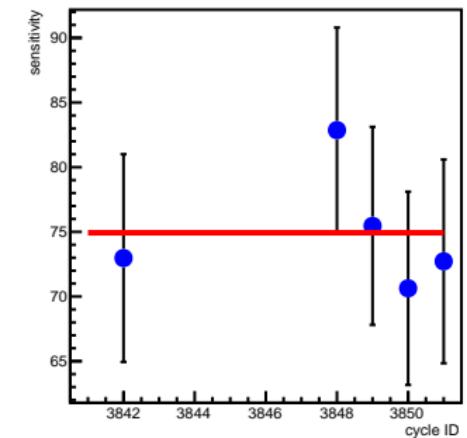
atr1\_coil2(ppm/count)



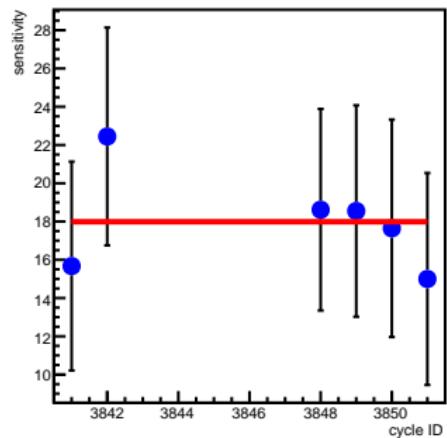
atr1\_coil5(ppm/count)



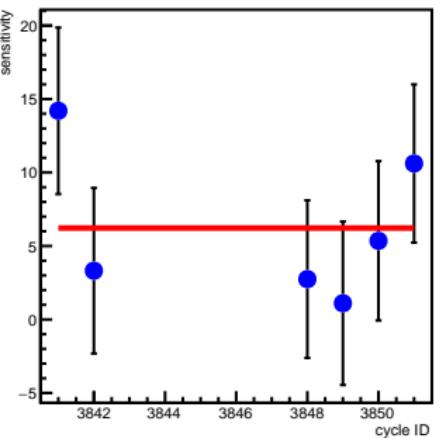
atr2\_coil1(ppm/count)



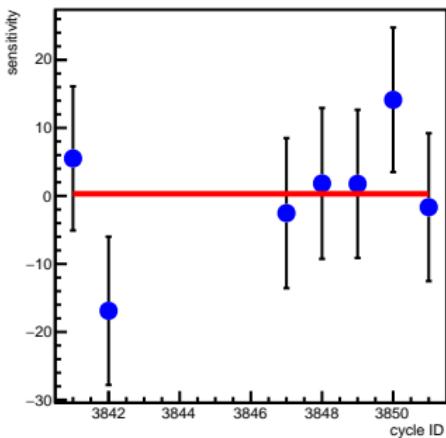
atr2\_coil3(ppm/count)



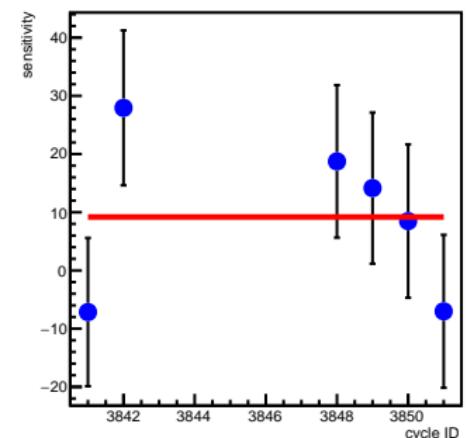
atr2\_coil4(ppm/count)



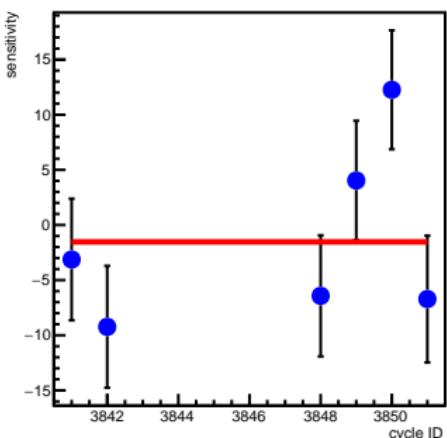
atr2\_coil6(ppm/count)



atr2\_coil7(ppm/count)



atr2\_coil2(ppm/count)



atr2\_coil5(ppm/count)

