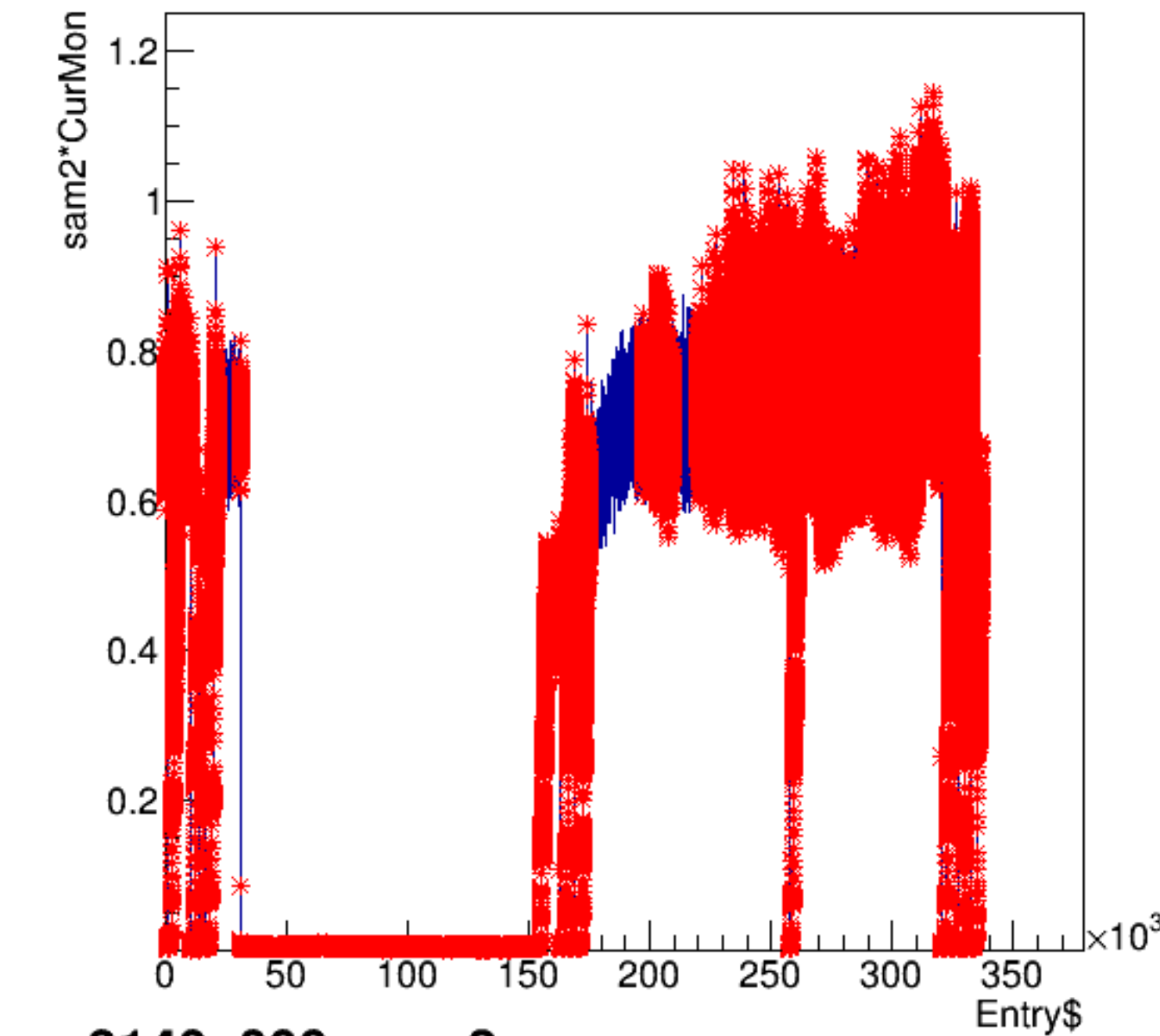
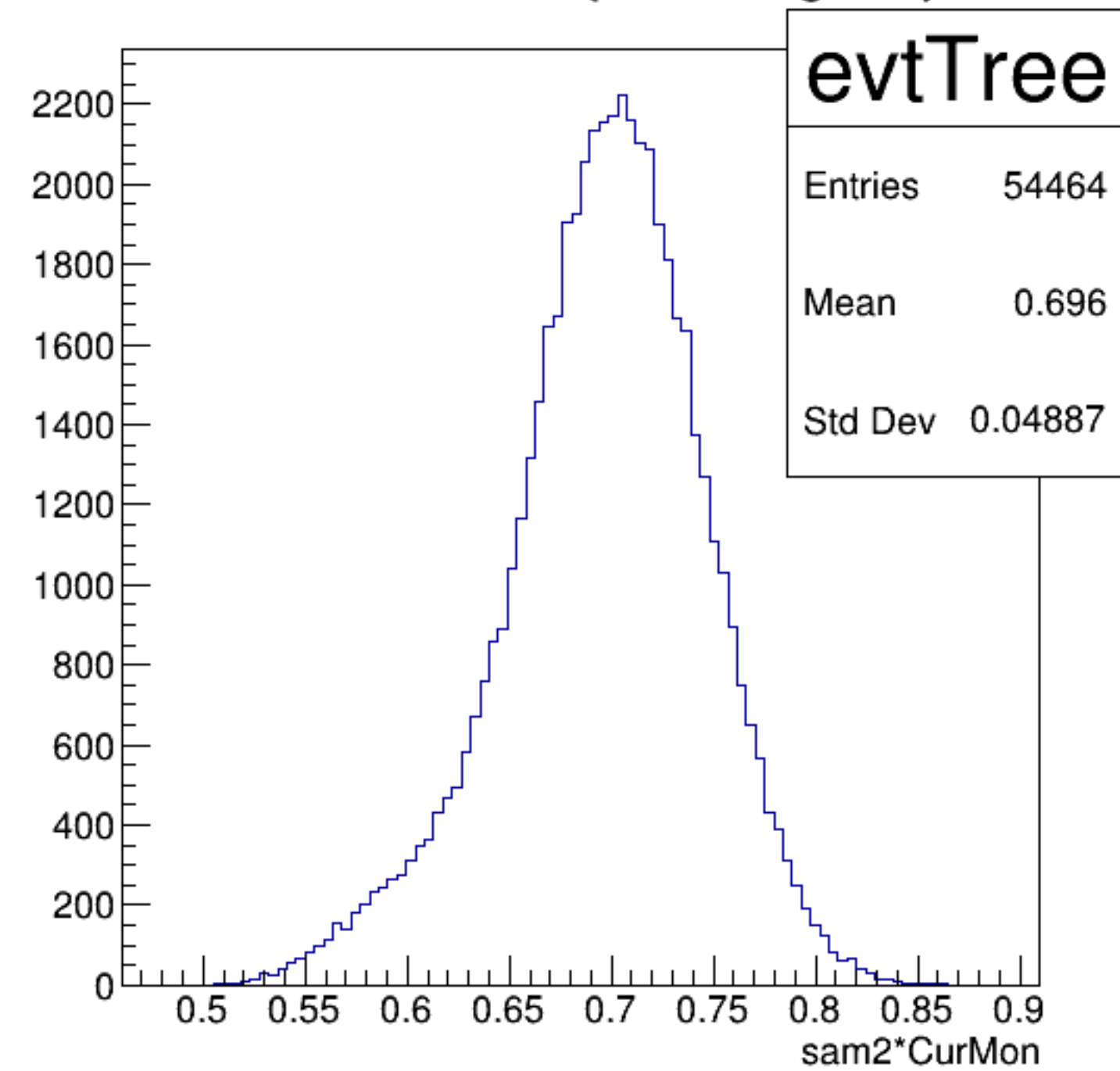


sam2*CurMon:Entry\$

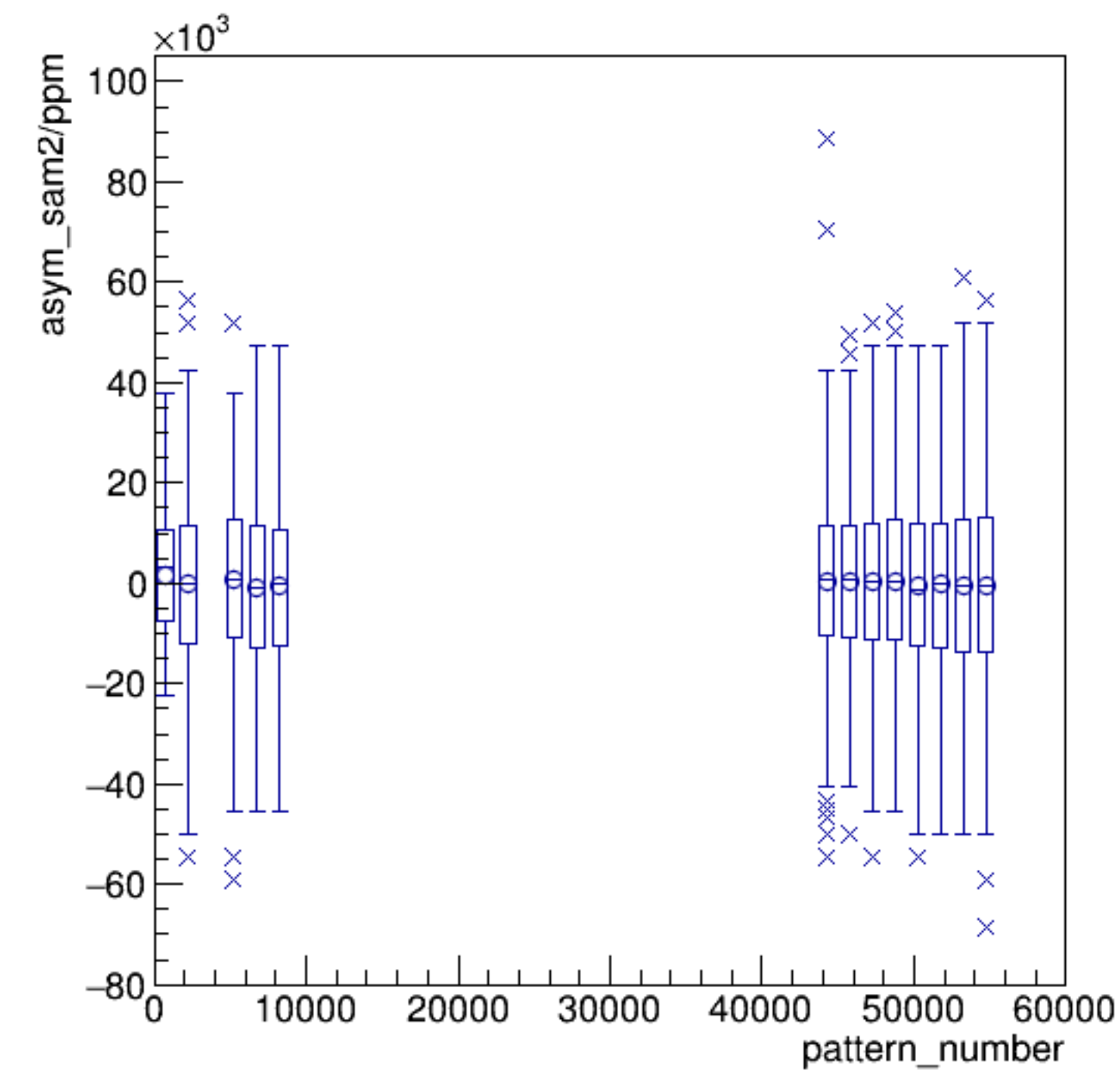


run3140_000_sam2.png

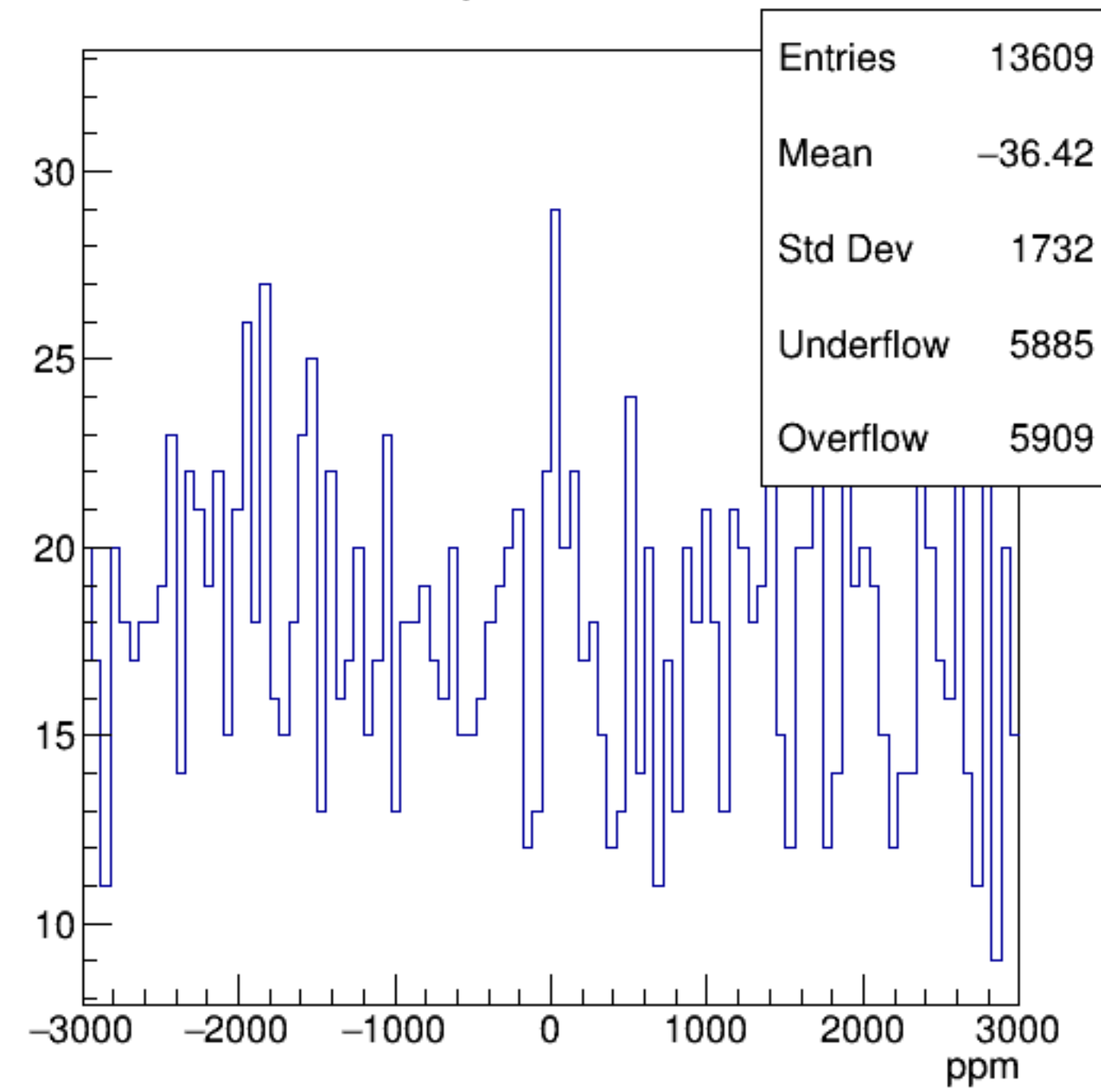
sam2*CurMon {ErrorFlag==0}



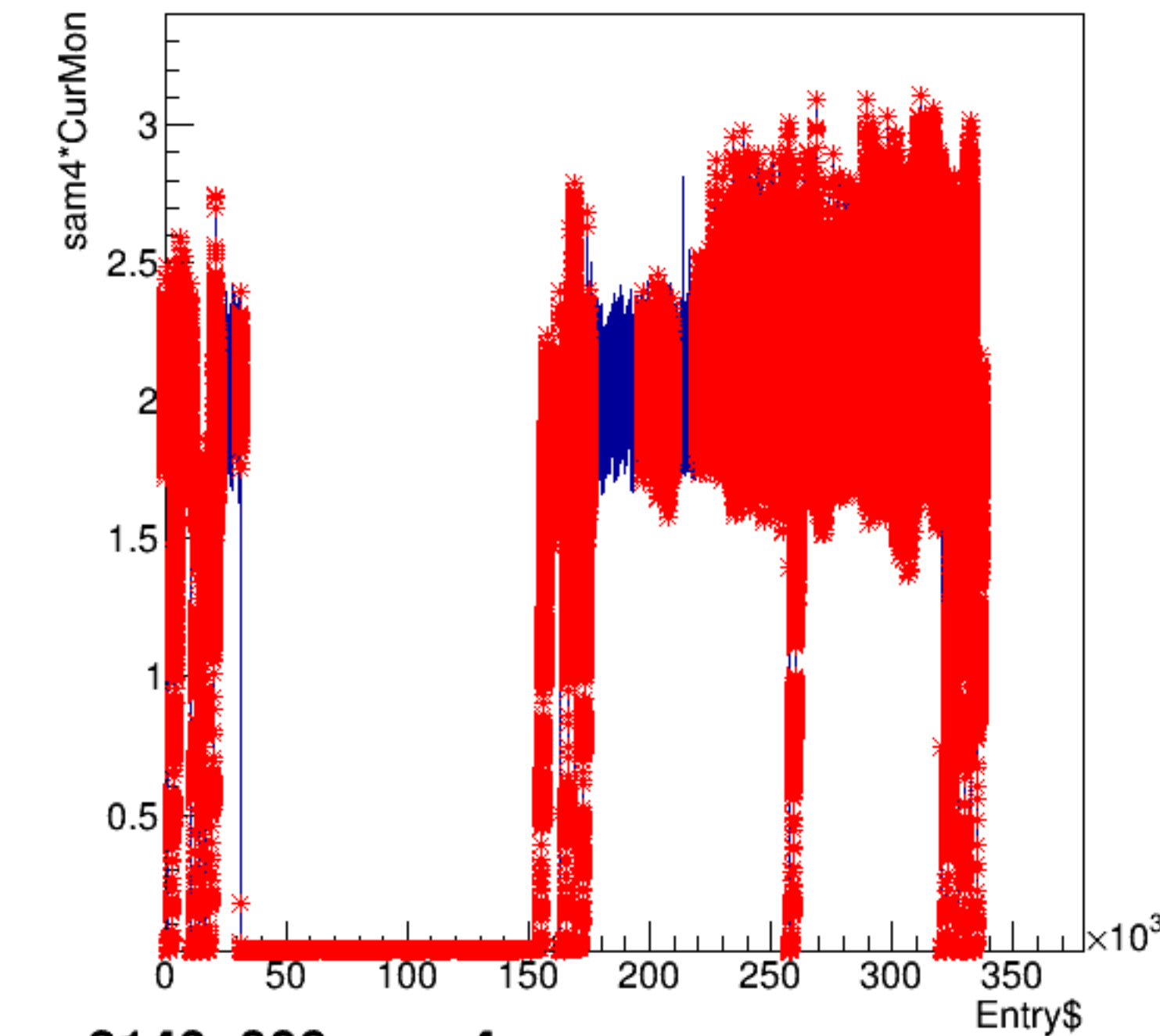
asym_sam2/ppm:pattern_number {ErrorFlag==0}



asym_sam2

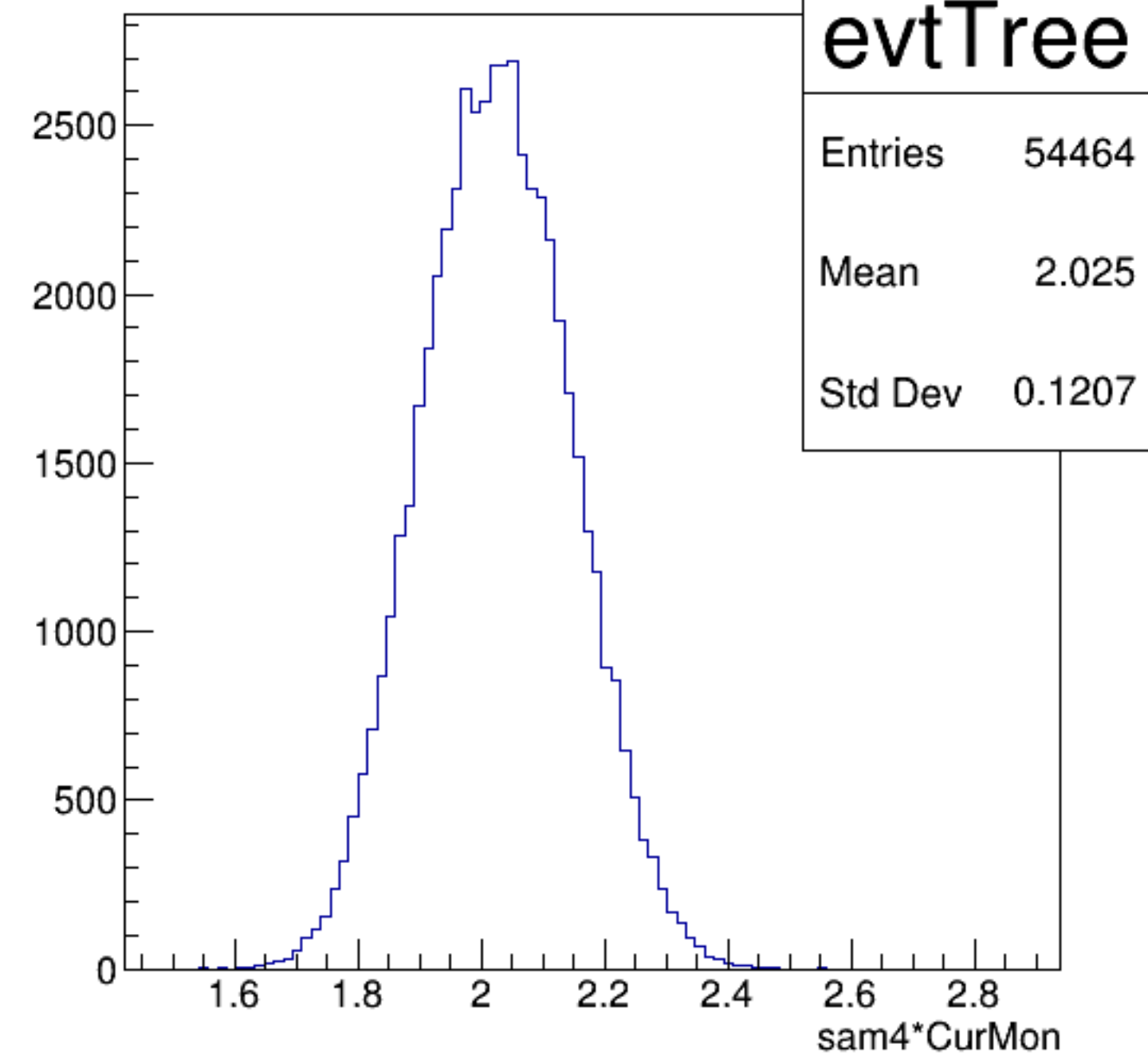


sam4*CurMon:Entry\$

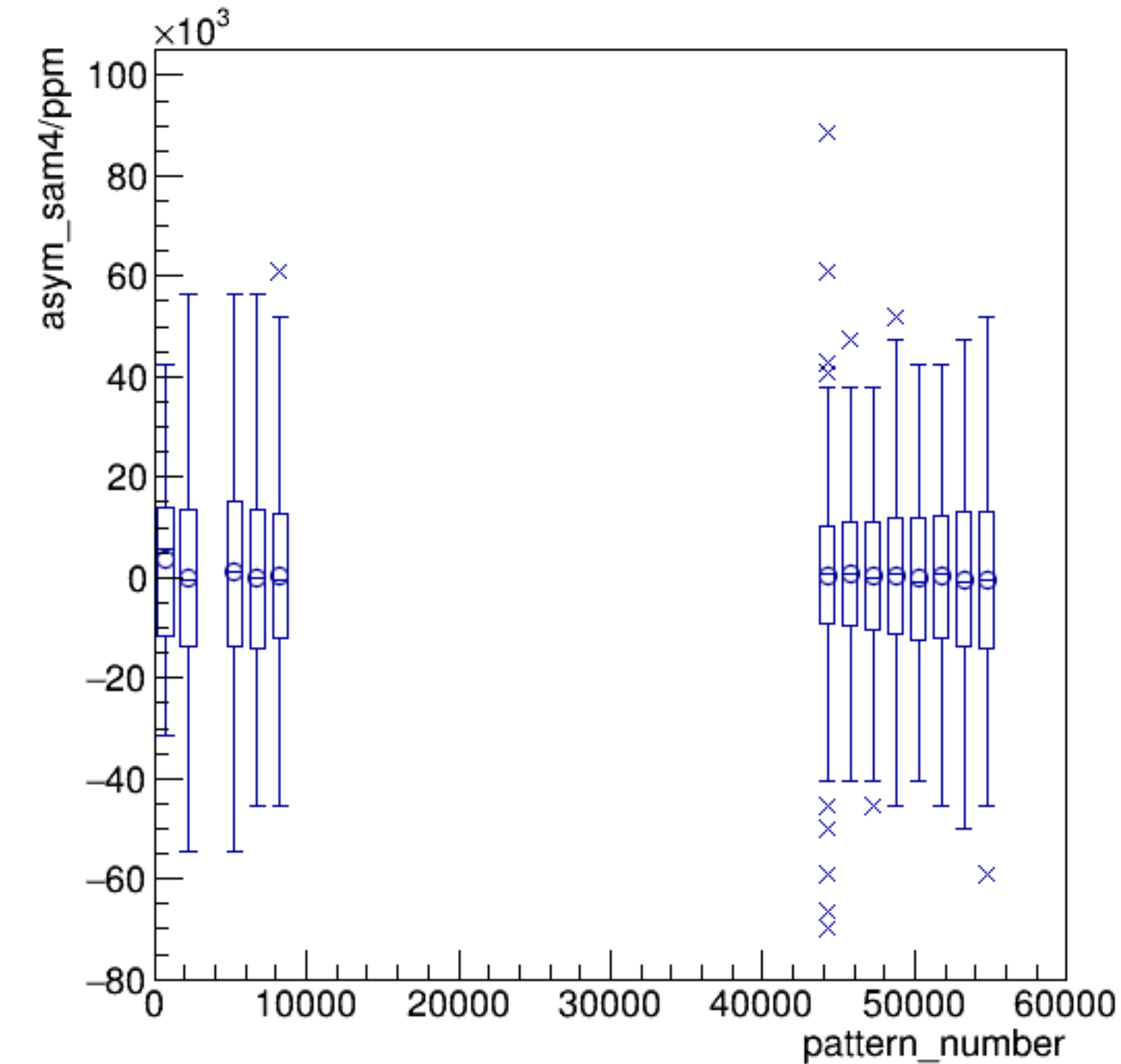


run3140_000_sam4.png

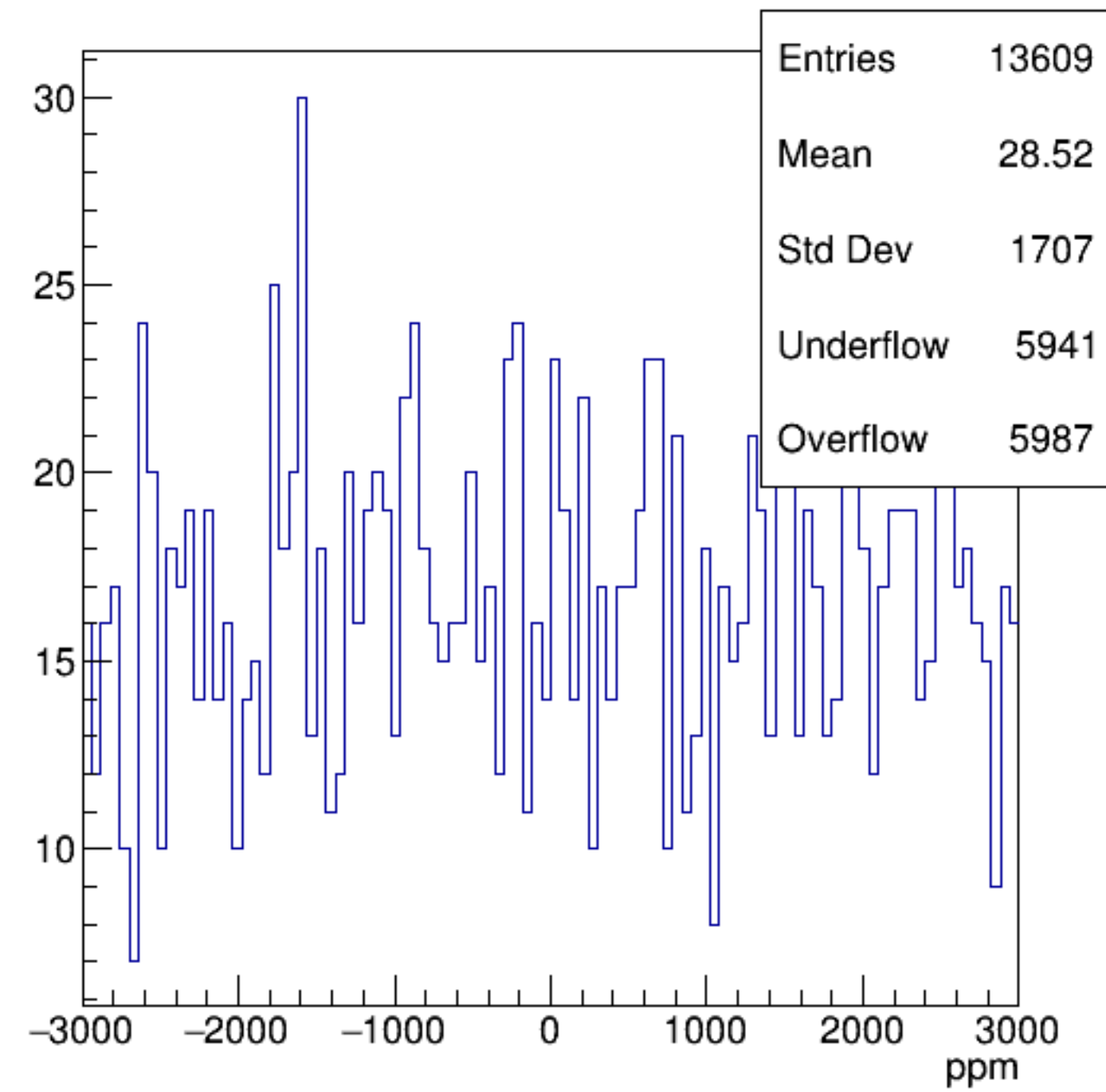
sam4*CurMon {ErrorFlag==0}



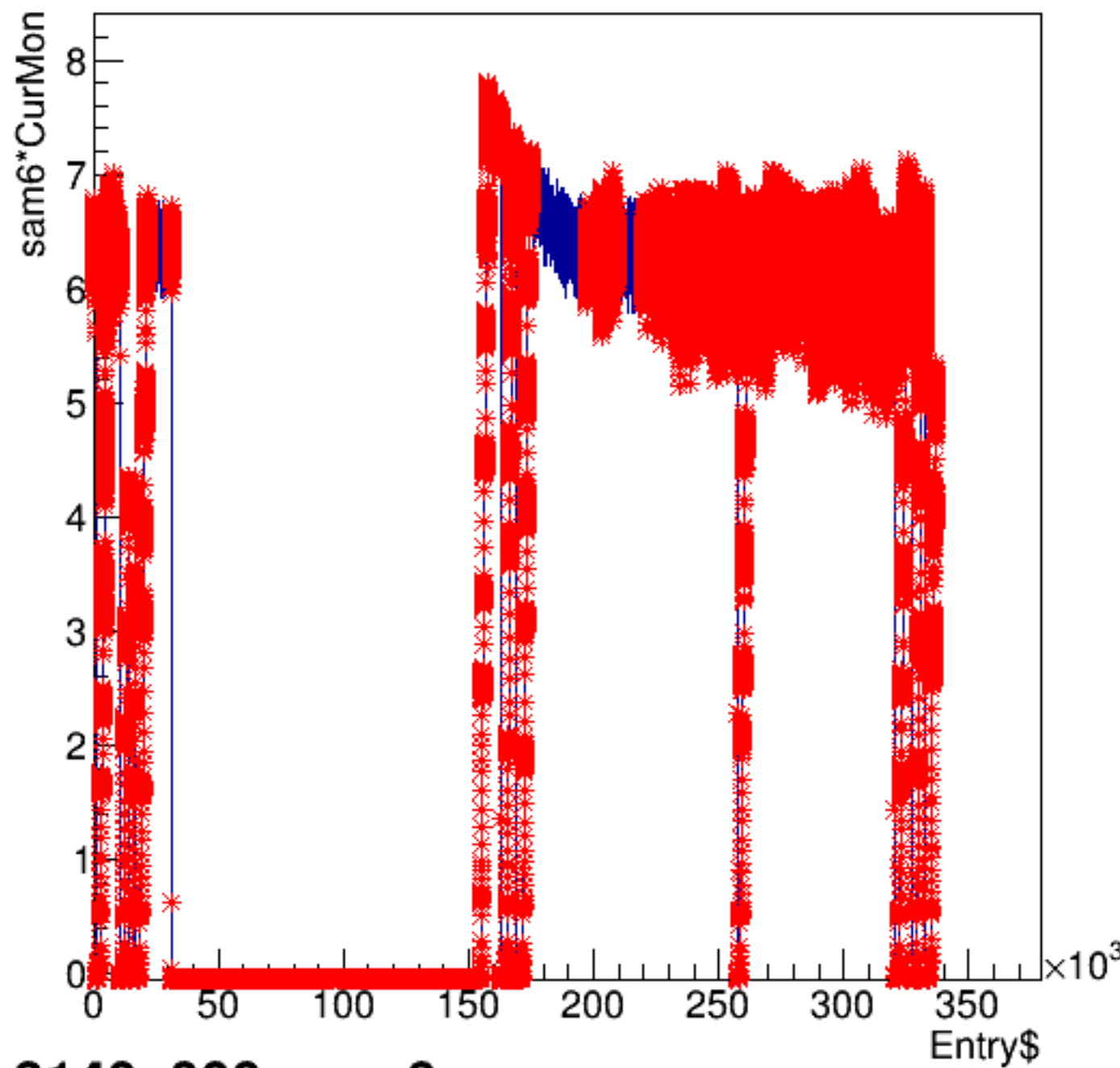
asym_sam4/ppm:pattern_number {ErrorFlag==0}



asym_sam4

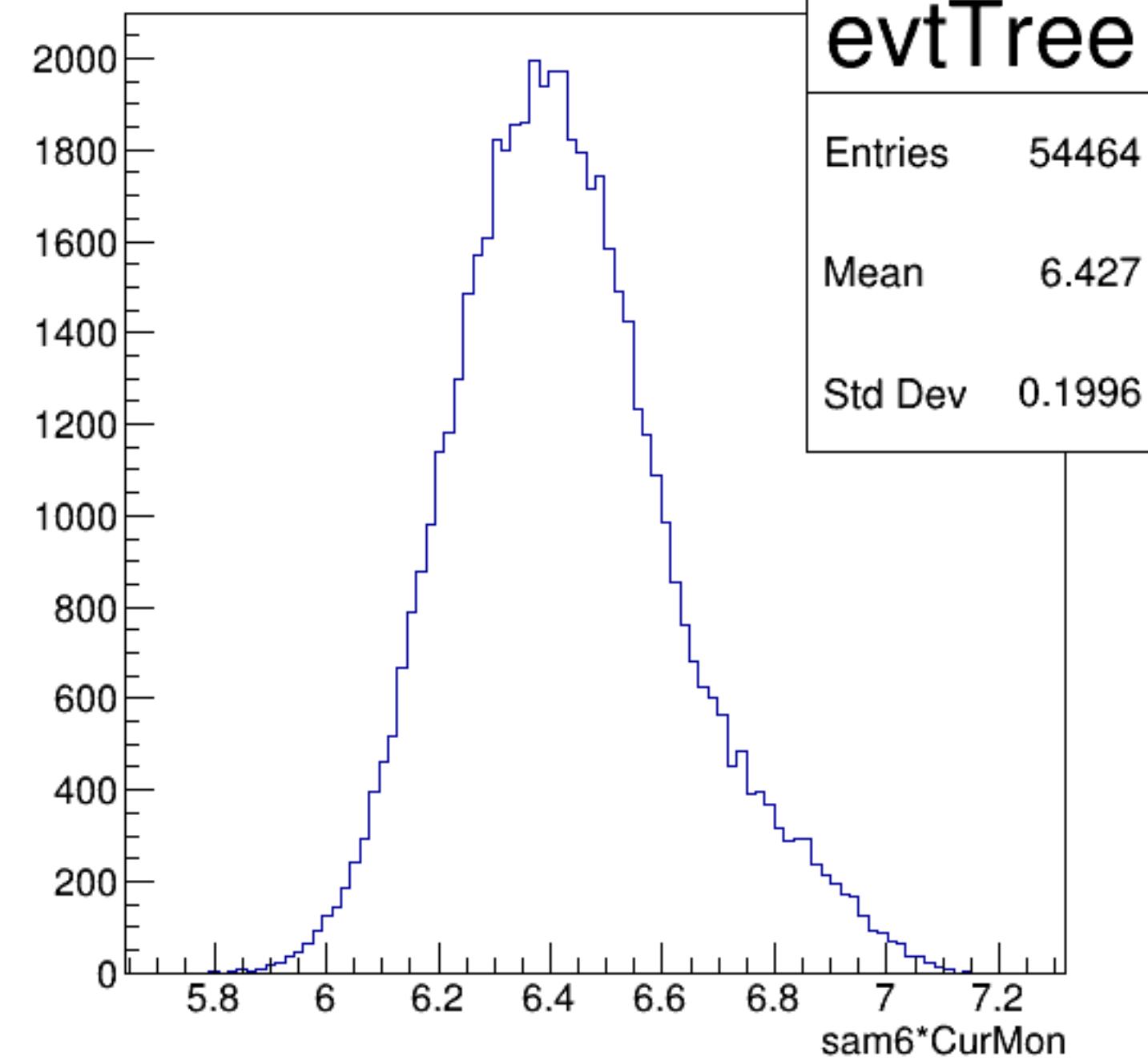


sam6*CurMon:Entry\$



run3140_000_sam6.png

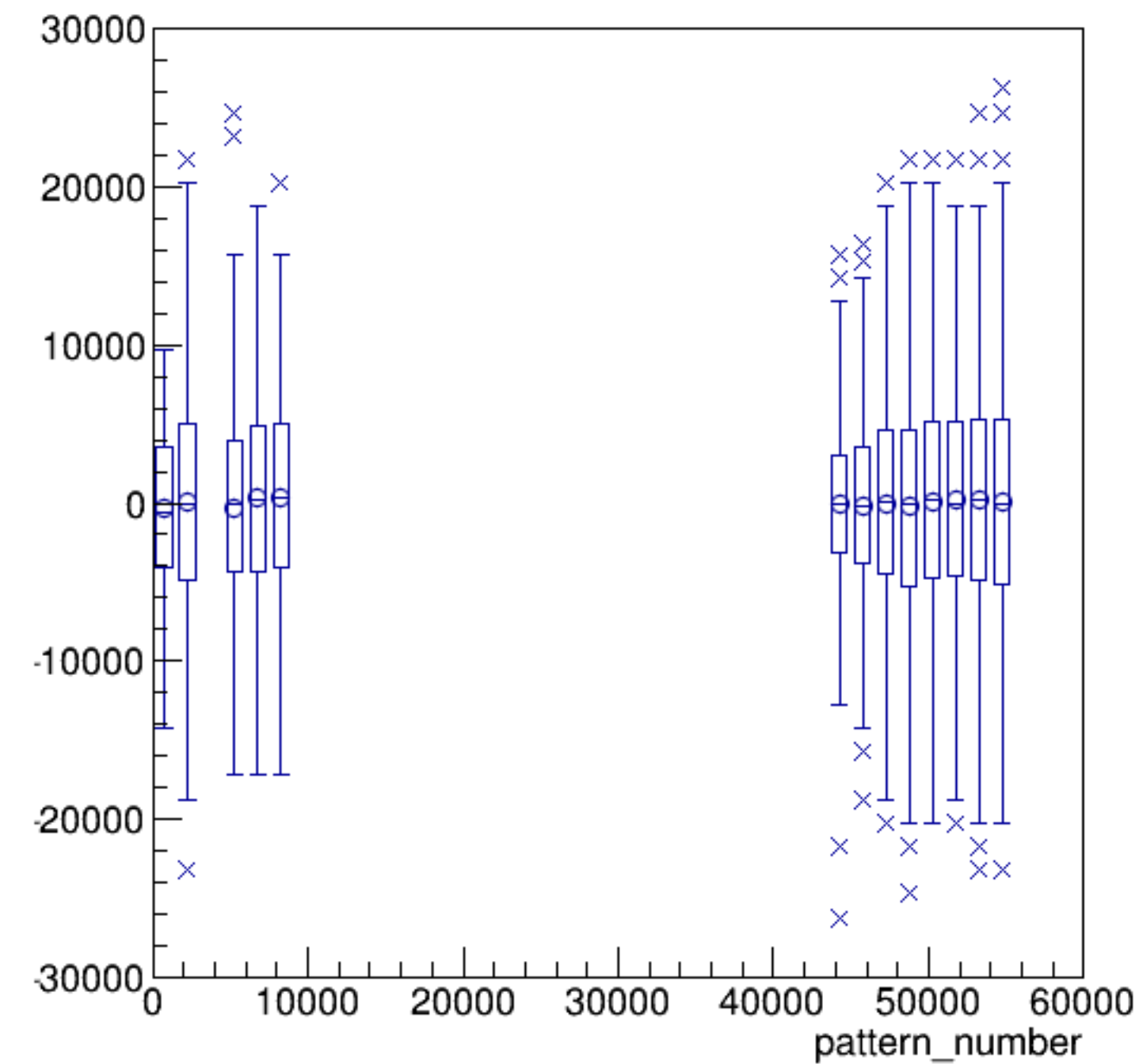
sam6*CurMon {ErrorFlag==0}



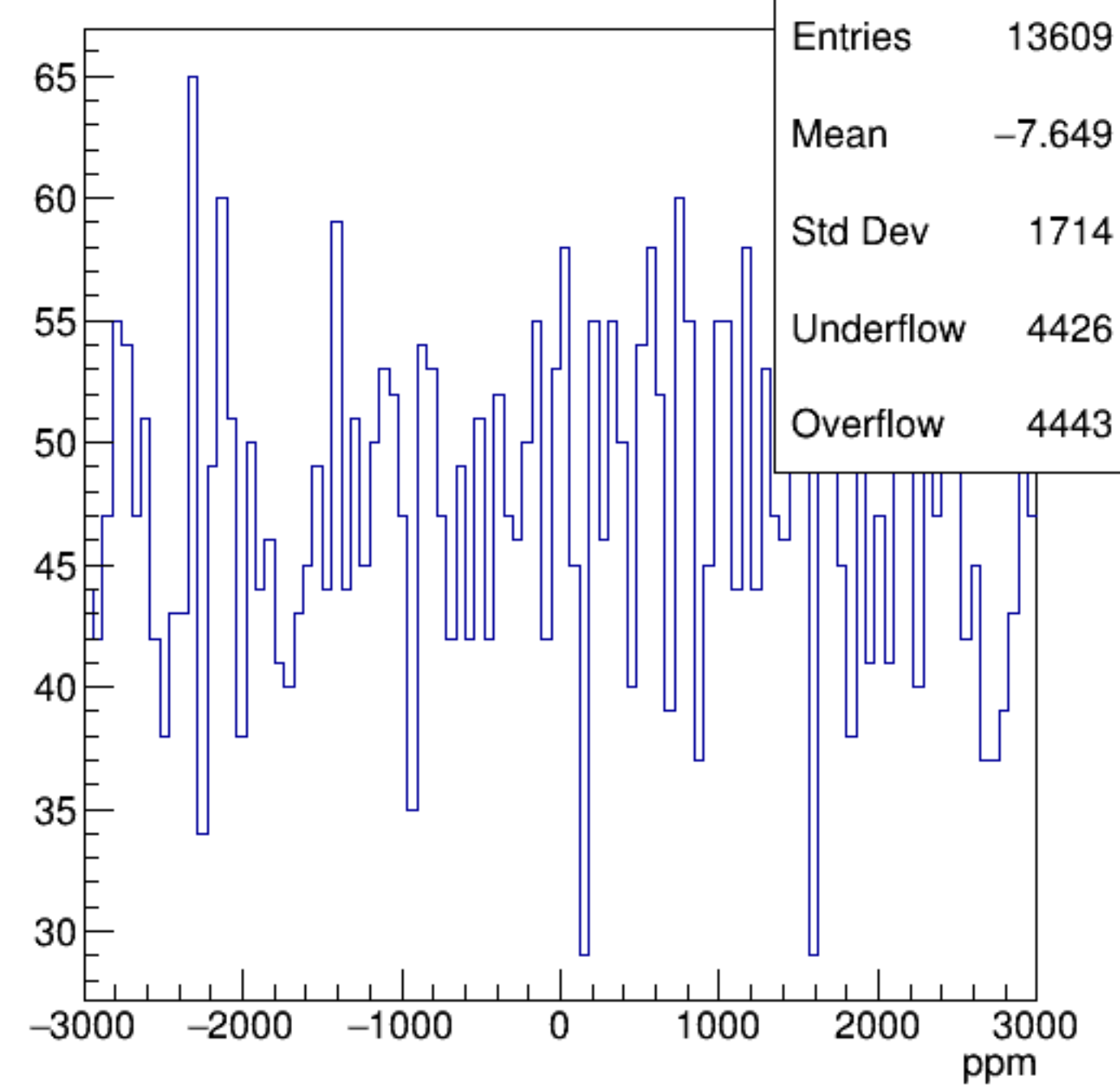
evtTree

Entries	54464
Mean	6.427
Std Dev	0.1996

asym_sam6/ppm:pattern_number {ErrorFlag==0}

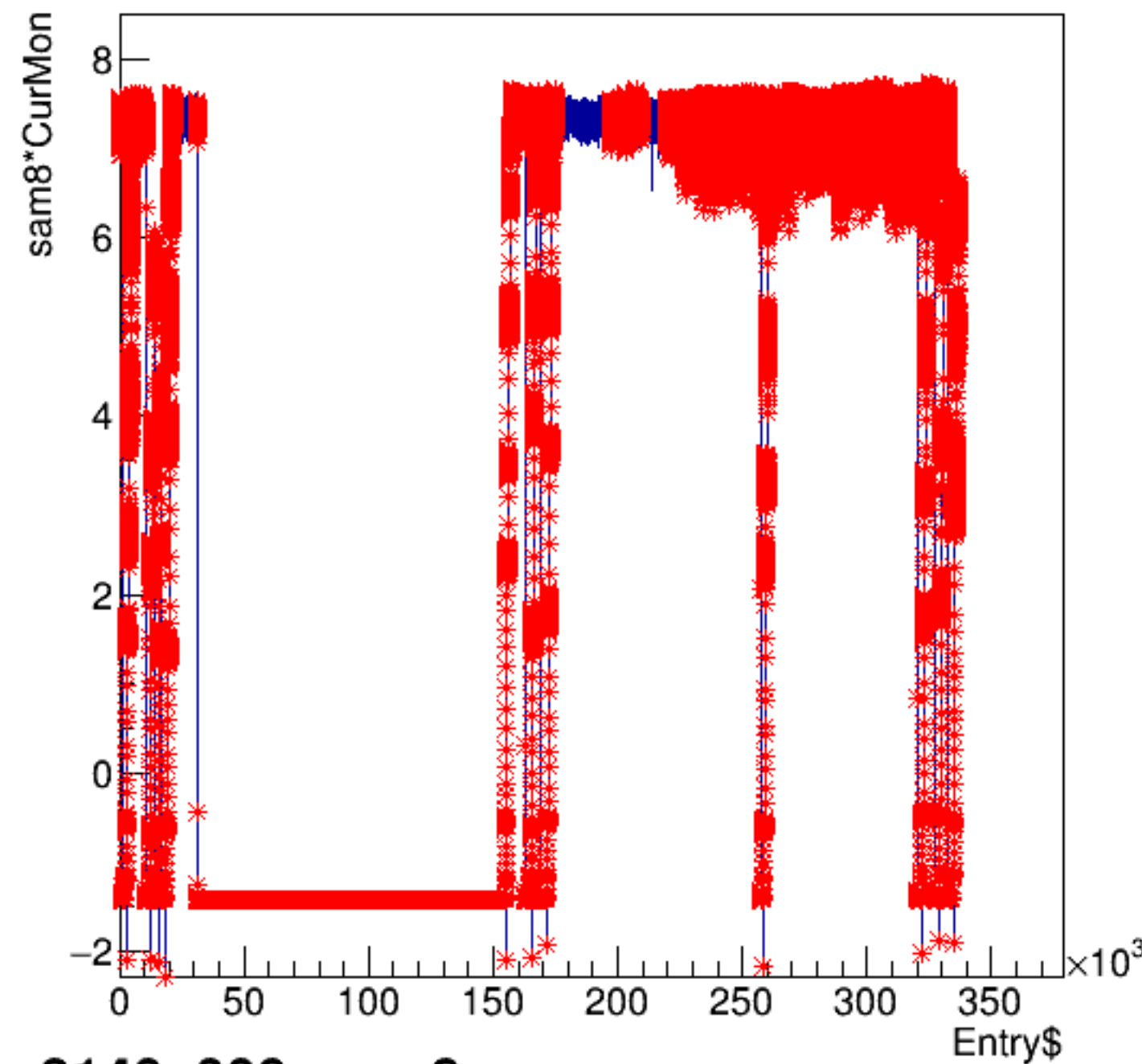


asym_sam6



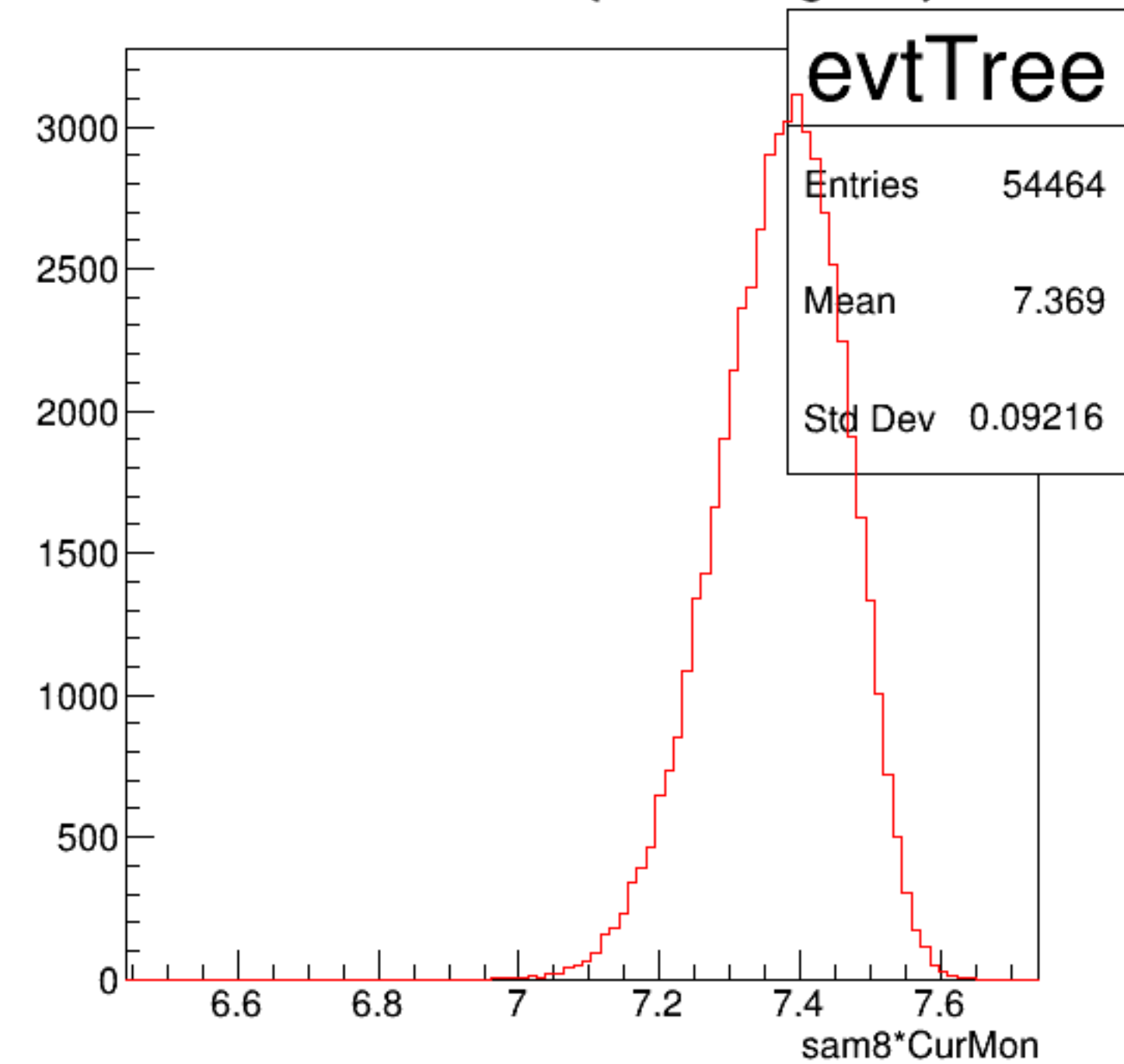
Entries	13609
Mean	-7.649
Std Dev	1714
Underflow	4426
Overflow	4443

sam8*CurMon:Entry\$

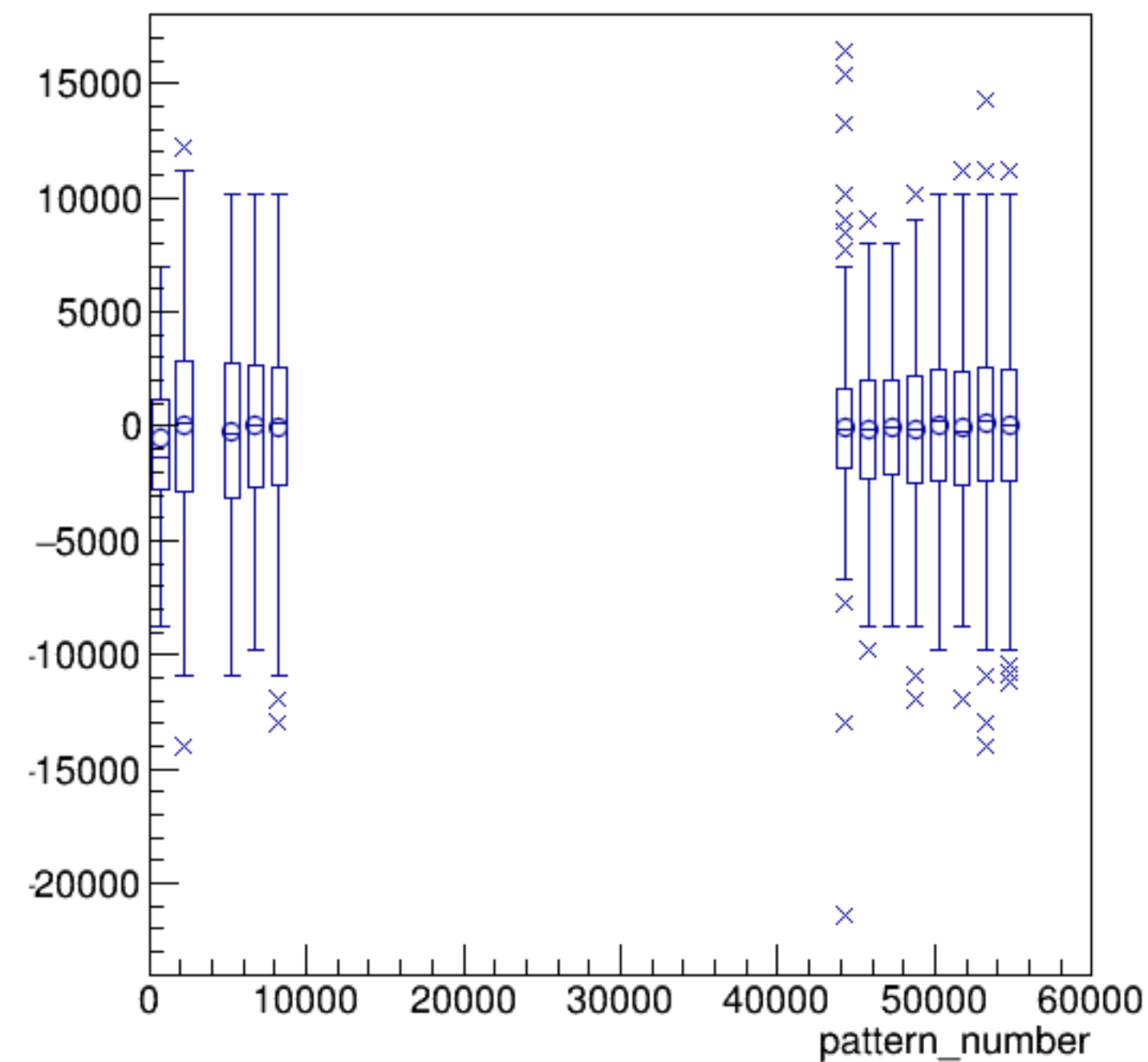


run3140_000_sam8.png

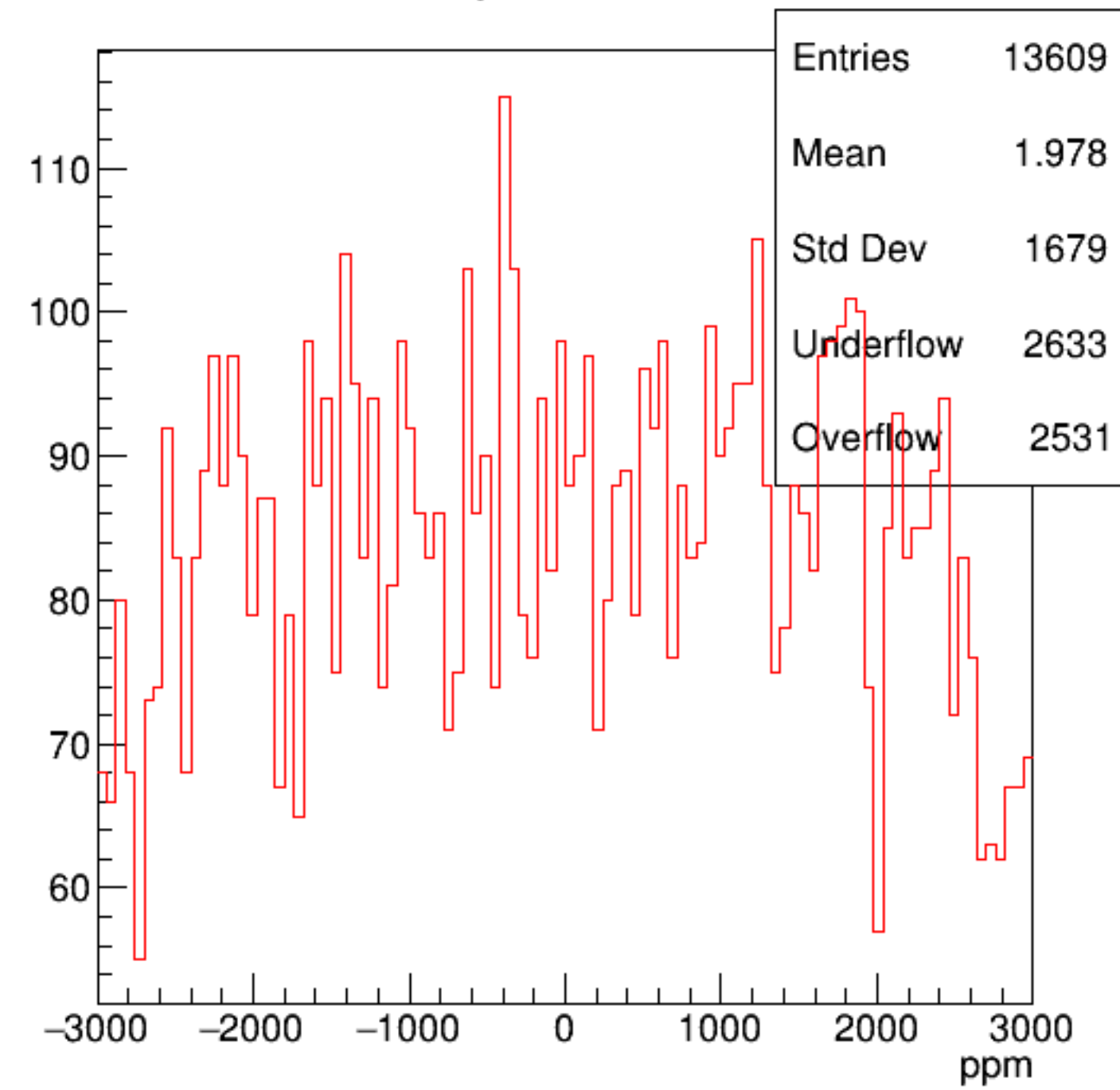
sam8*CurMon {ErrorFlag==0}



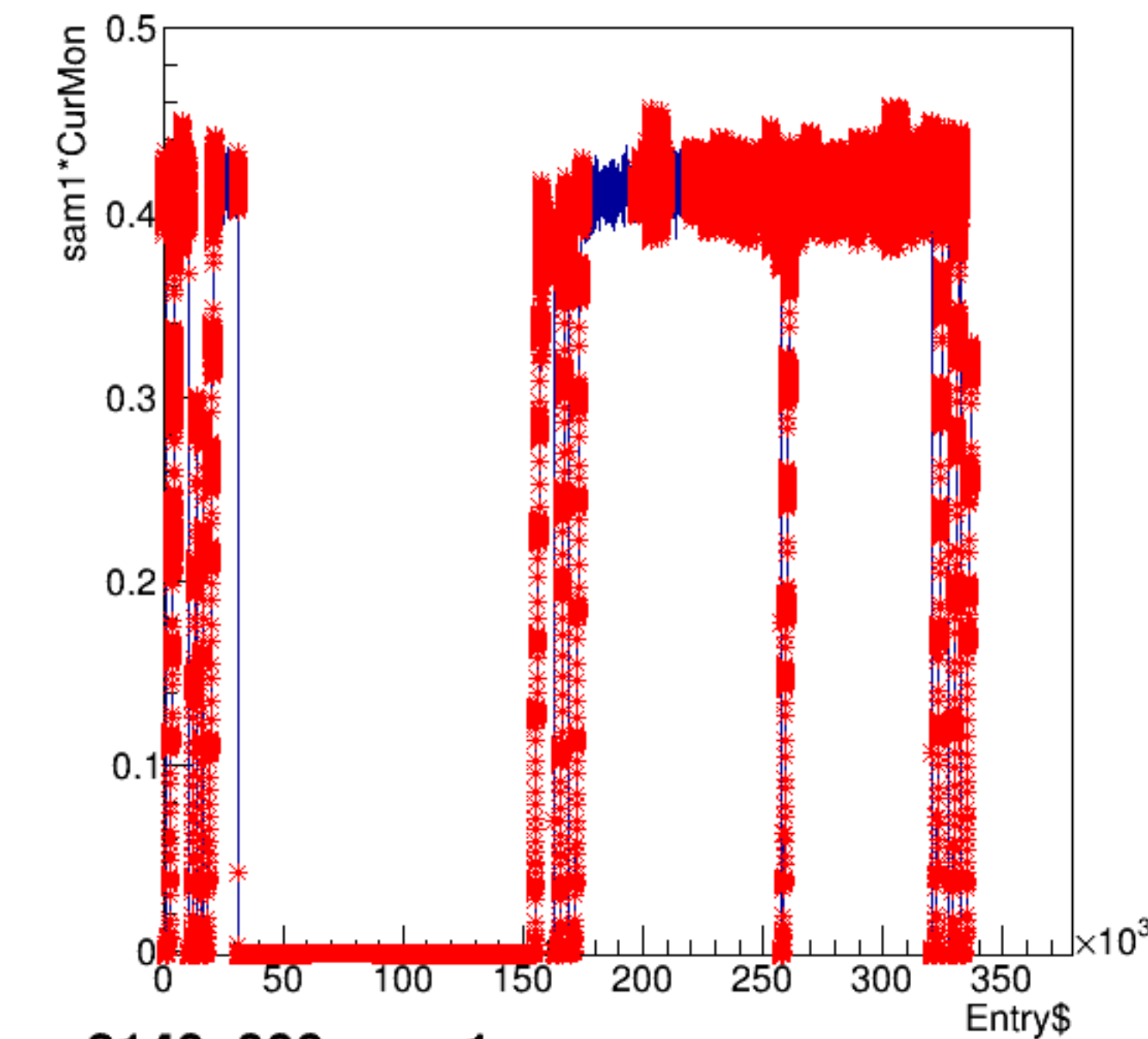
asym_sam8/ppm:pattern_number {ErrorFlag==0}



asym_sam8

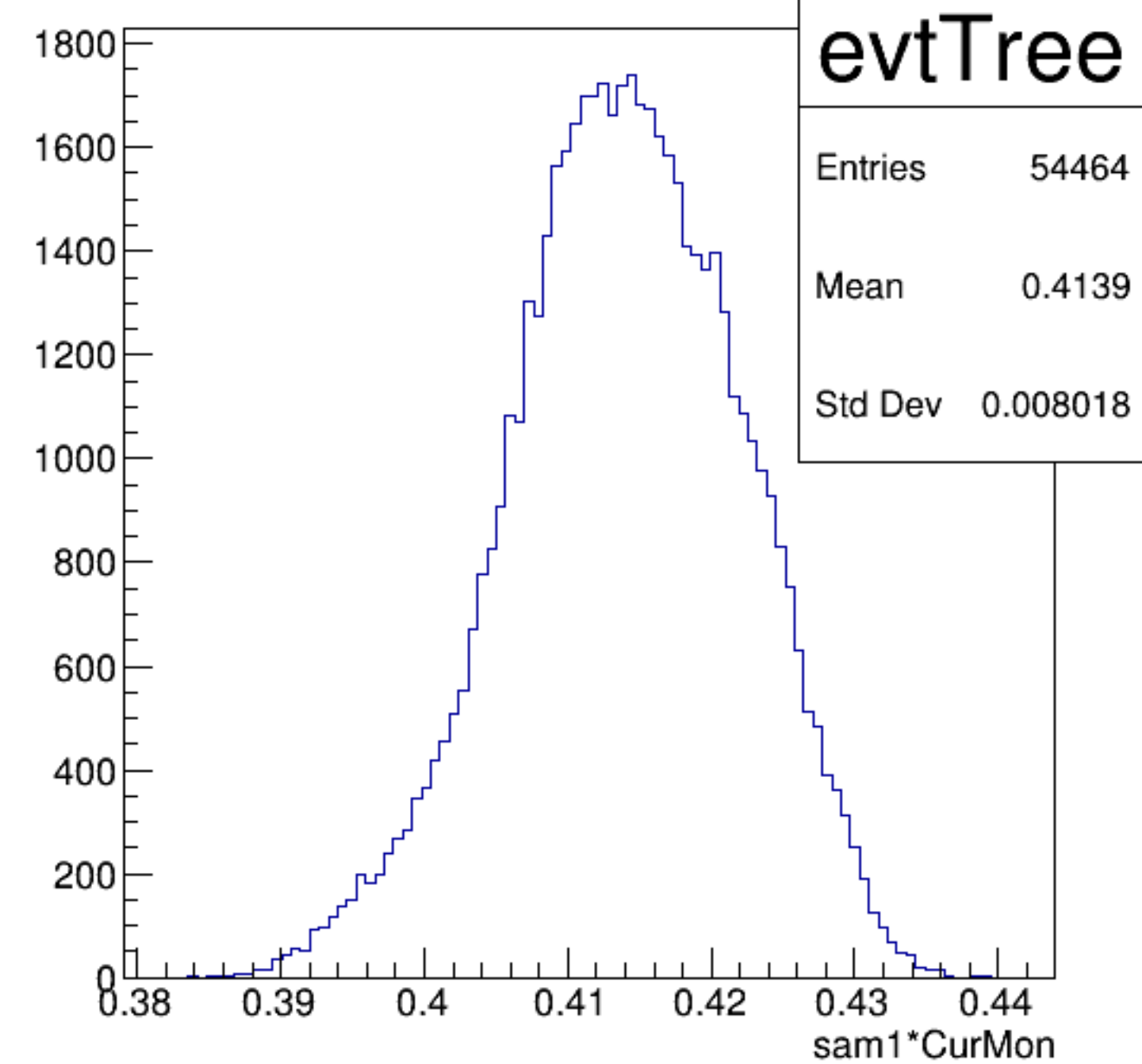


sam1*CurMon:Entry\$



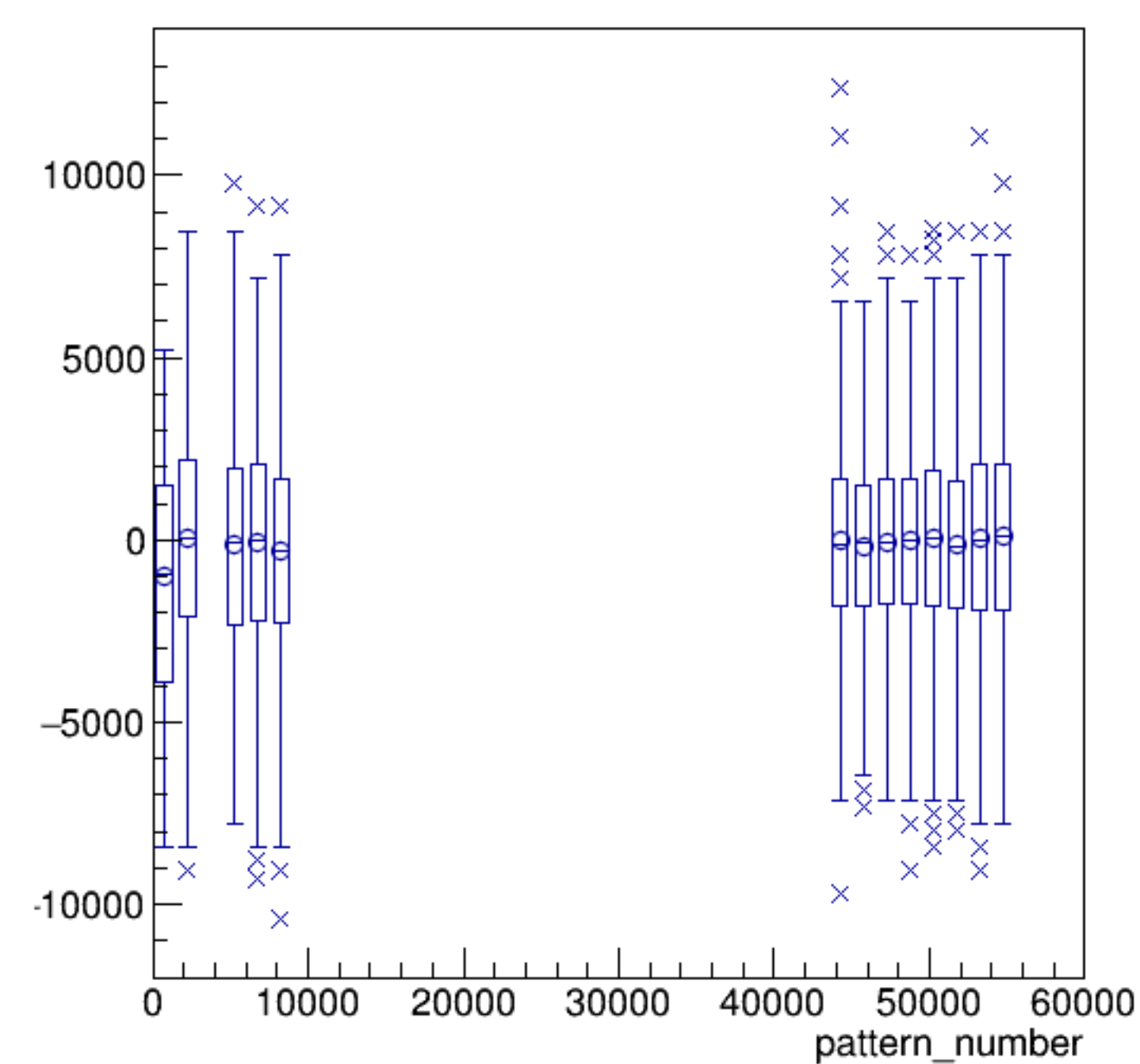
run3140_000_sam1.png

sam1*CurMon {ErrorFlag==0}

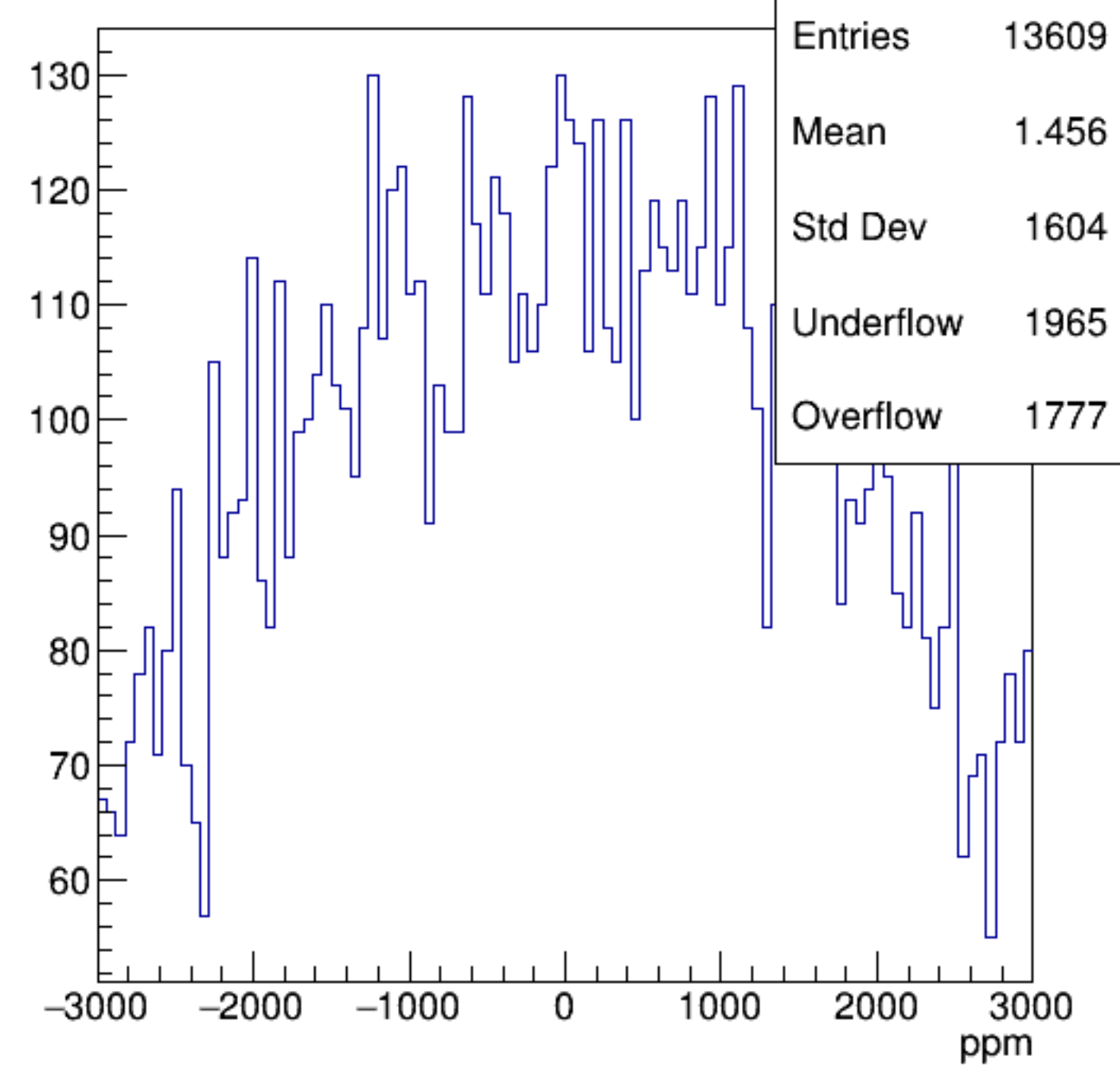


evtTree	
Entries	54464
Mean	0.4139
Std Dev	0.008018

asym_sam1/ppm:pattern_number {ErrorFlag==0}

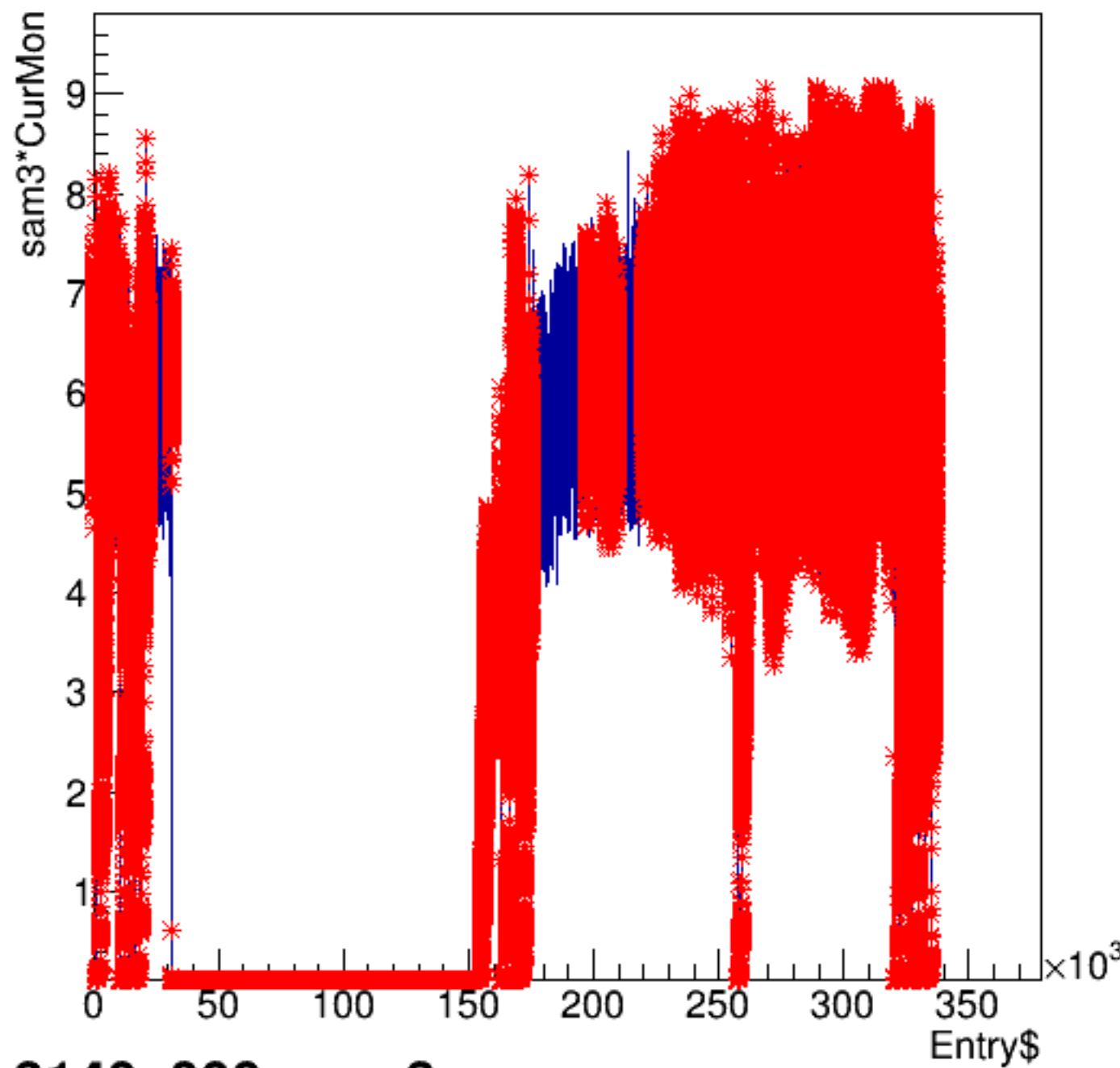


asym_sam1



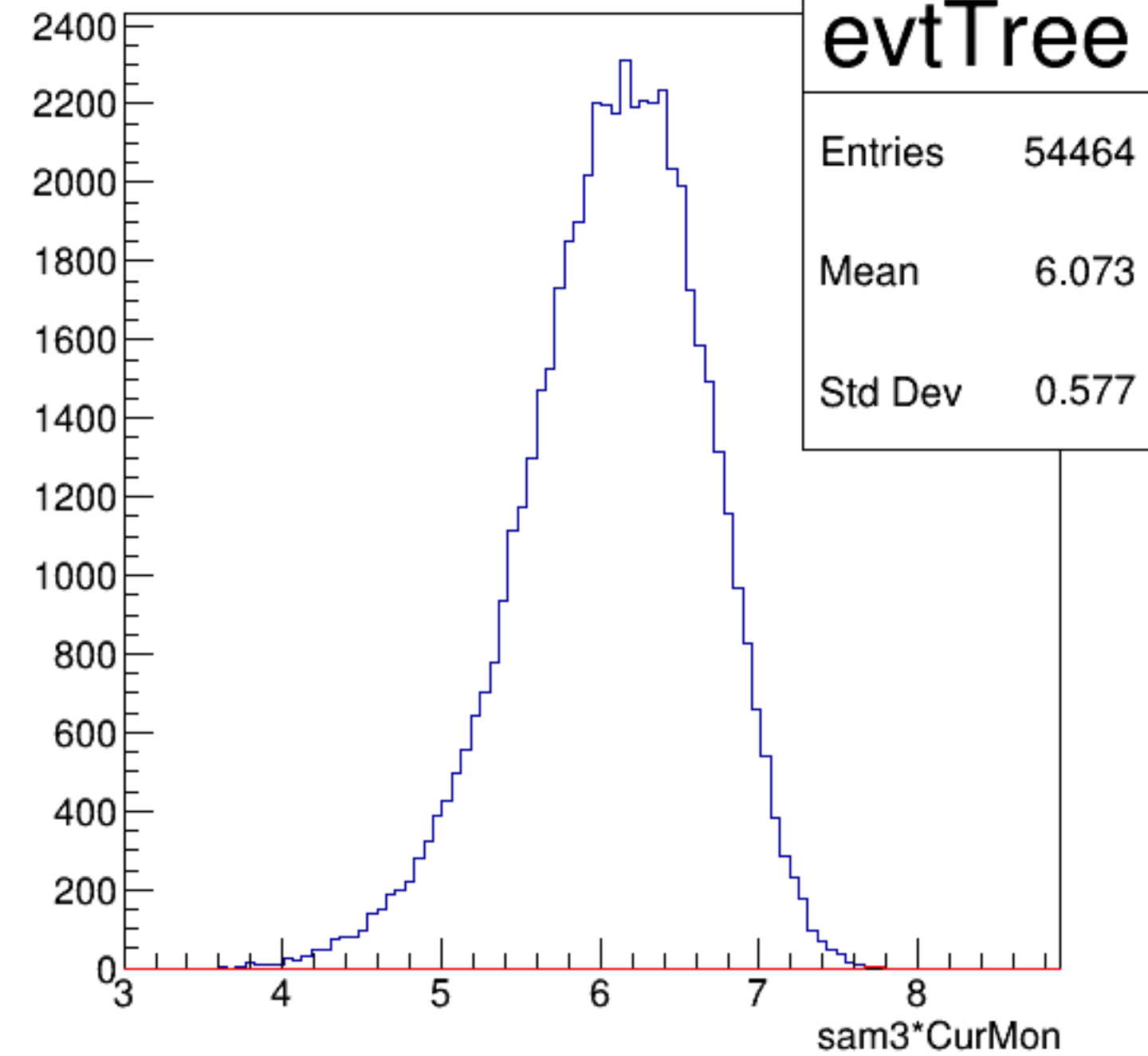
Entries	13609
Mean	1.456
Std Dev	1604
Underflow	1965
Overflow	1777

sam3*CurMon:Entry\$



run3140_000_sam3.png

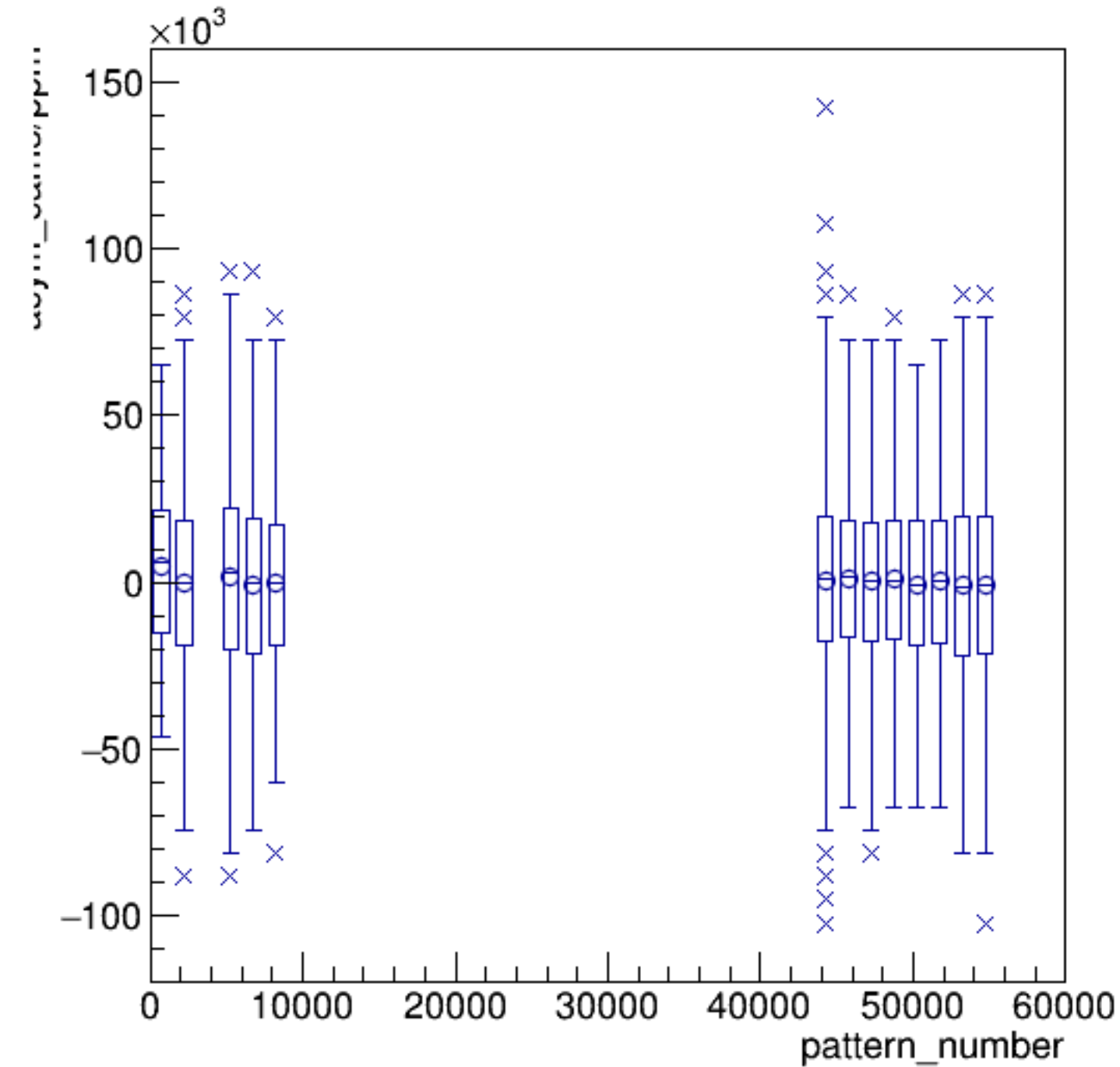
sam3*CurMon {ErrorFlag==0}



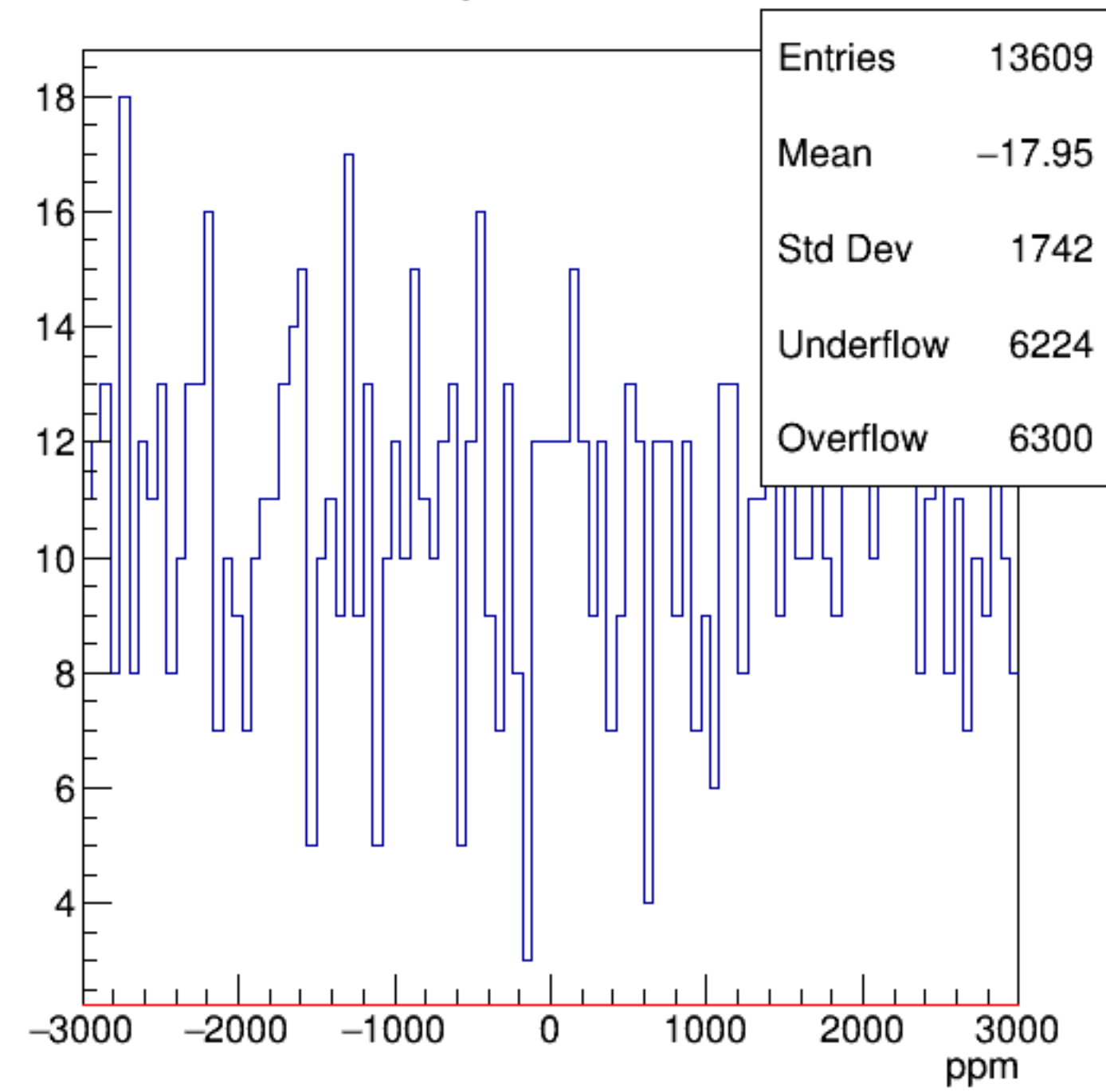
evtTree

Entries	54464
Mean	6.073
Std Dev	0.577

asym_sam3/ppm:pattern_number {ErrorFlag==0}

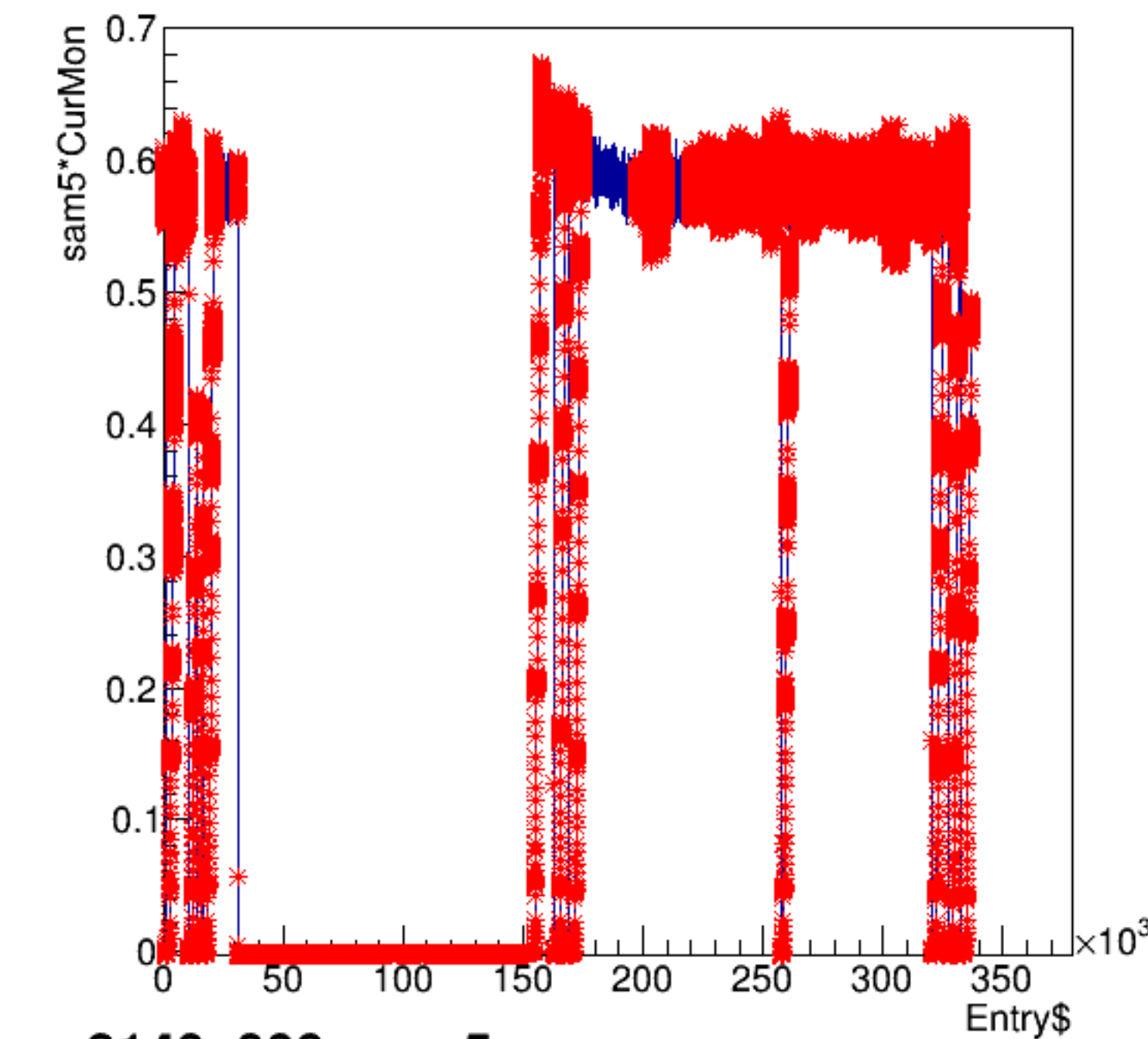


asym_sam3



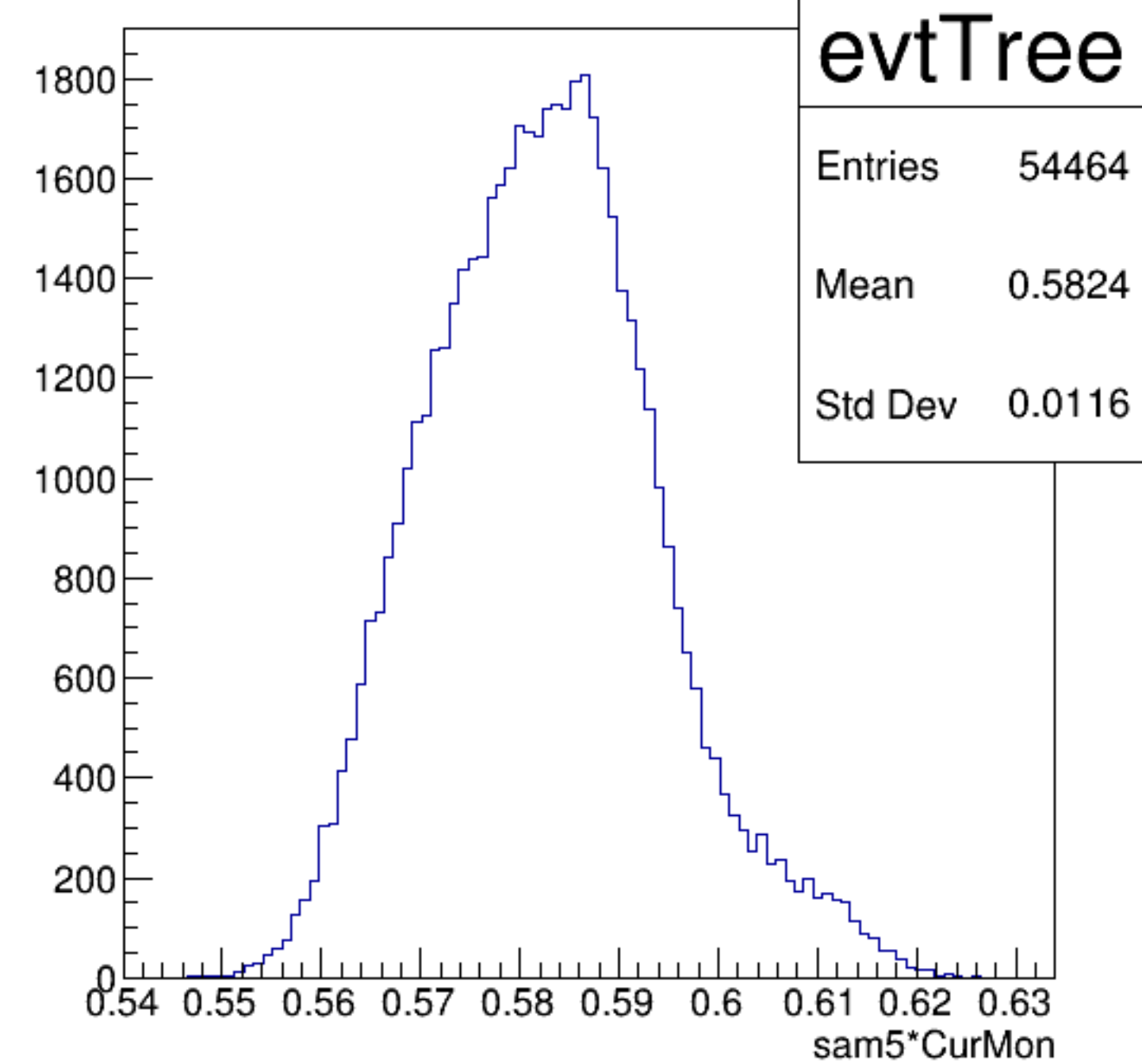
Entries	13609
Mean	-17.95
Std Dev	1742
Underflow	6224
Overflow	6300

sam5*CurMon:Entry\$



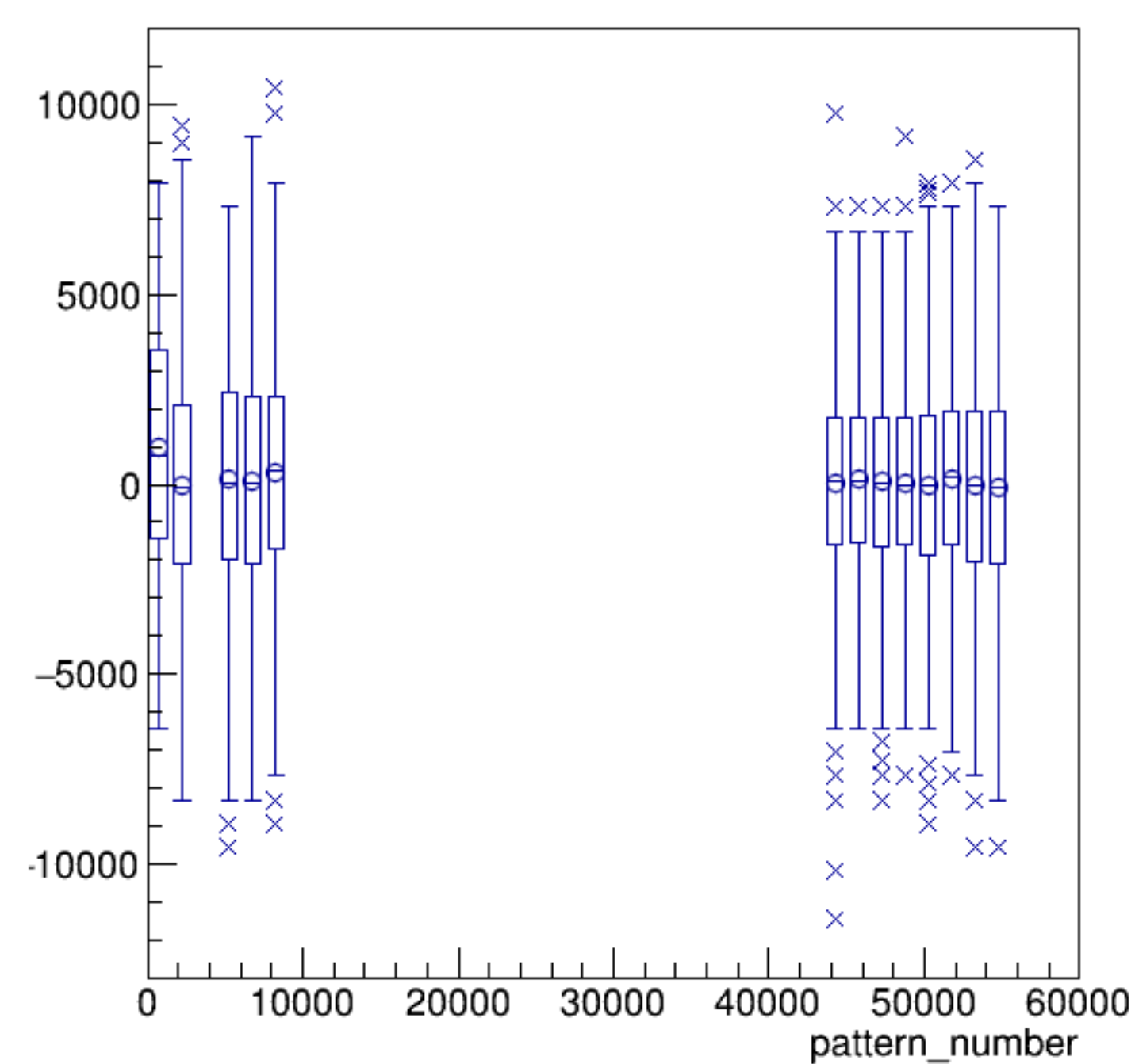
run3140_000_sam5.png

sam5*CurMon {ErrorFlag==0}

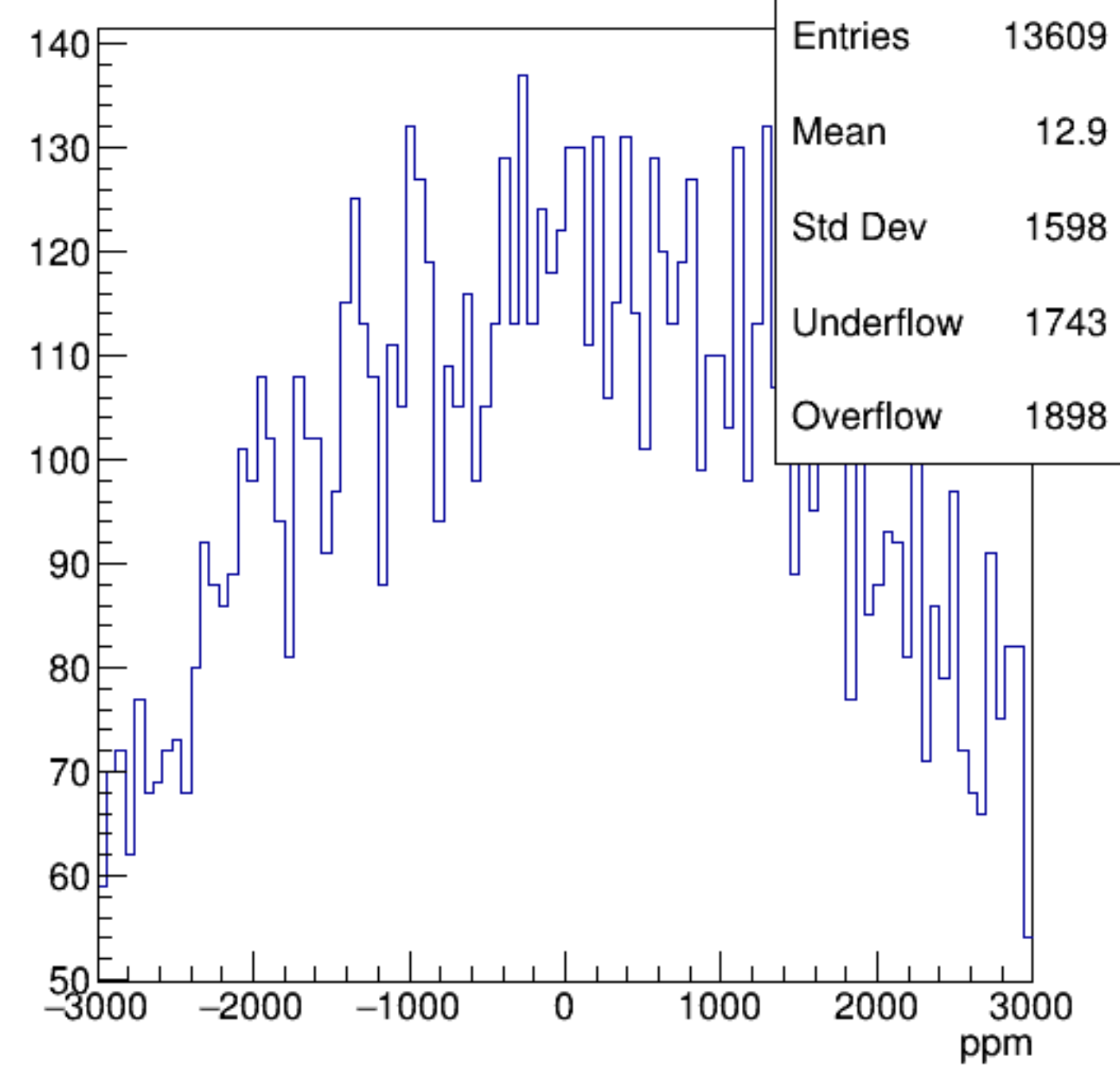


evtTree	
Entries	54464
Mean	0.5824
Std Dev	0.0116

asym_sam5/ppm:pattern_number {ErrorFlag==0}

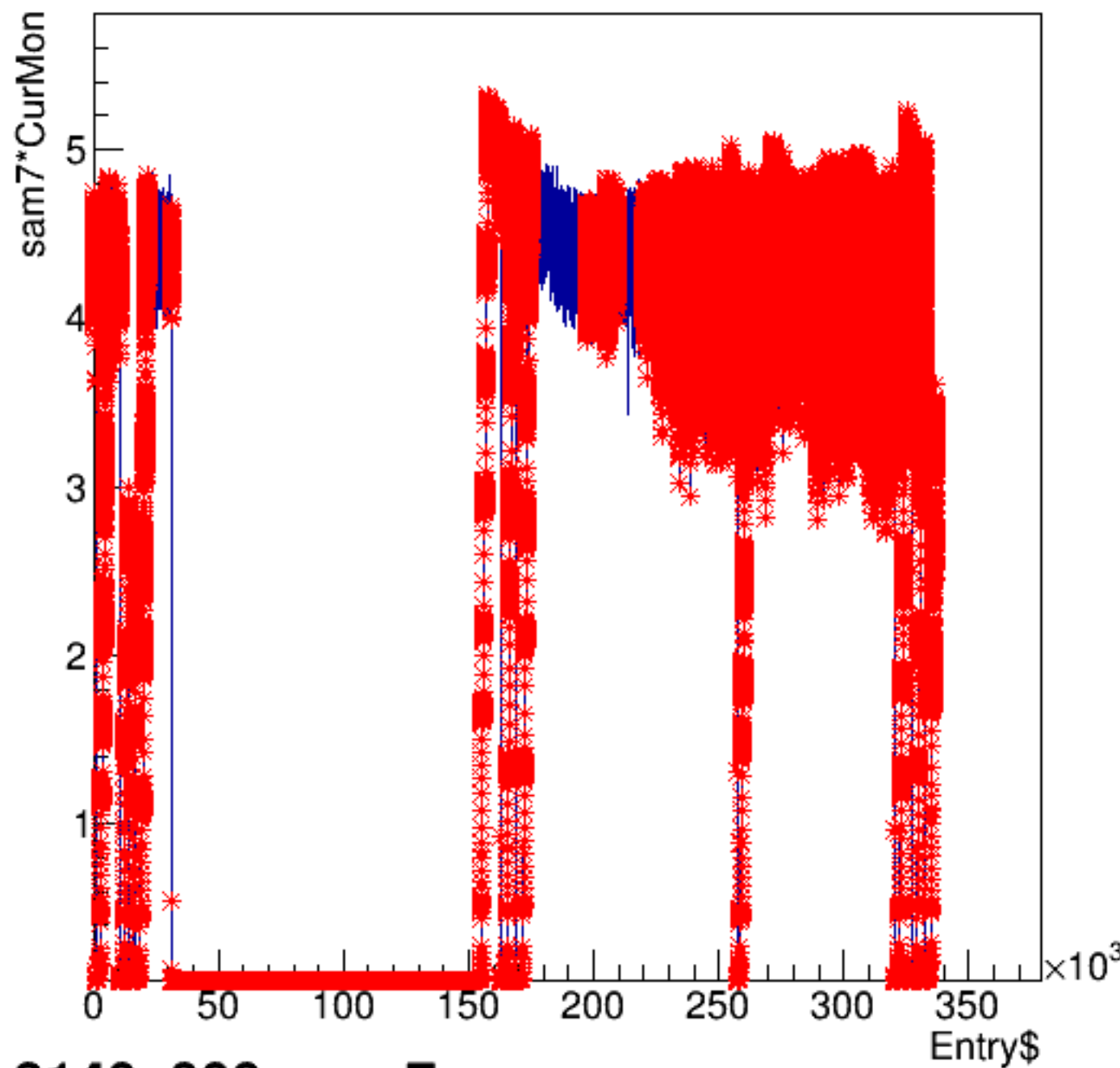


asym_sam5



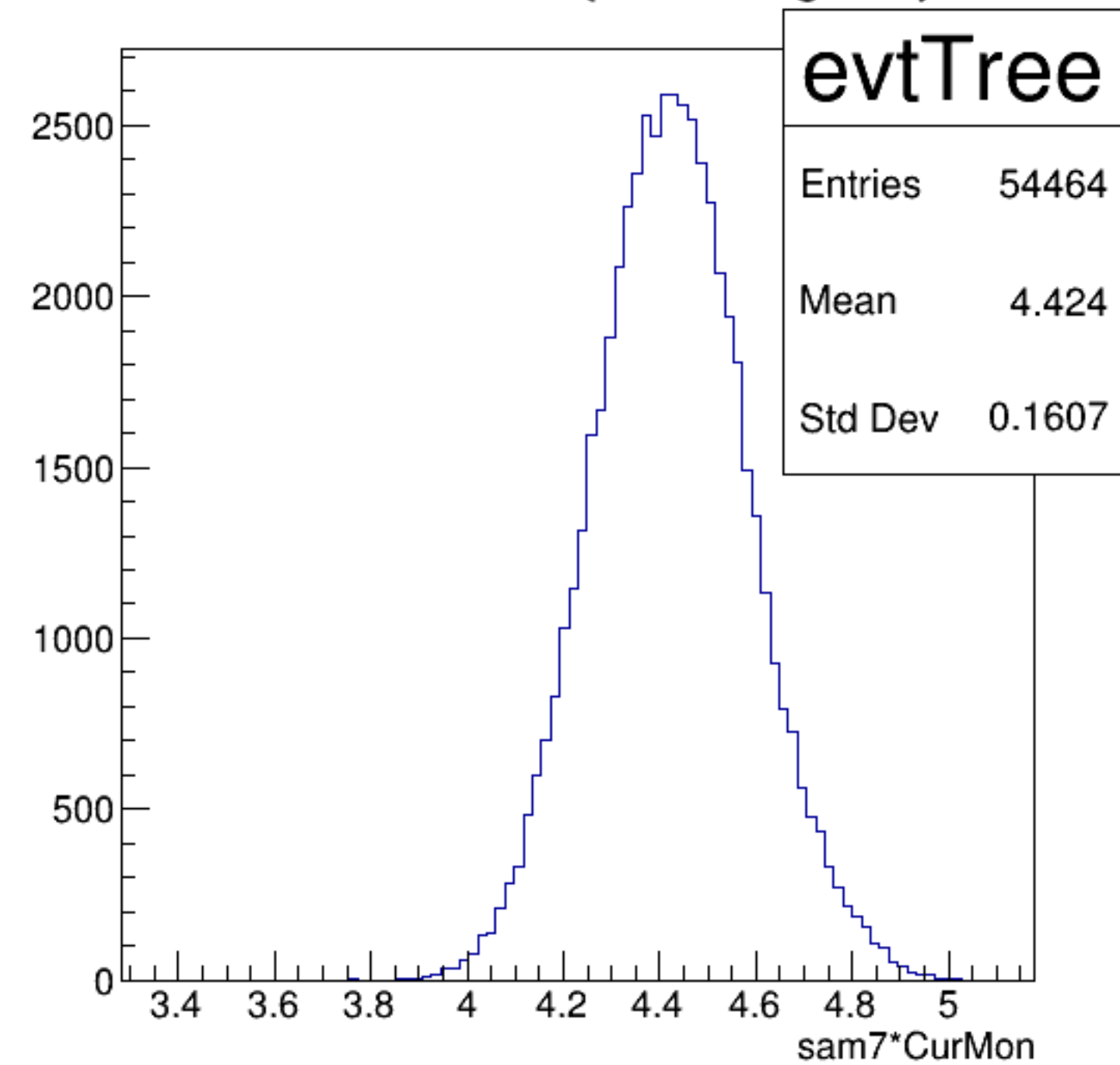
Entries	13609
Mean	12.9
Std Dev	1598
Underflow	1743
Overflow	1898

sam7*CurMon:Entry\$

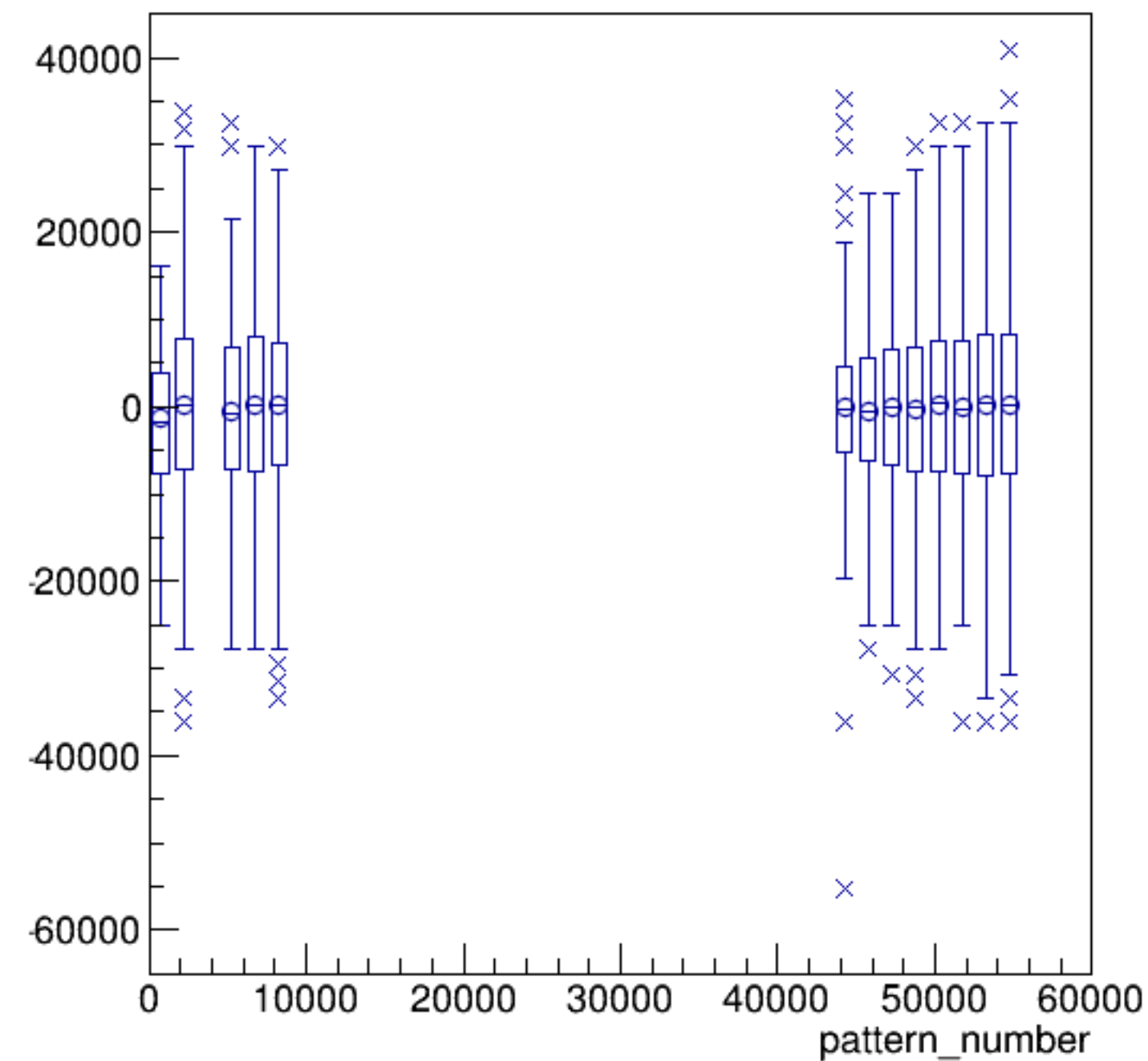


run3140_000_sam7.png

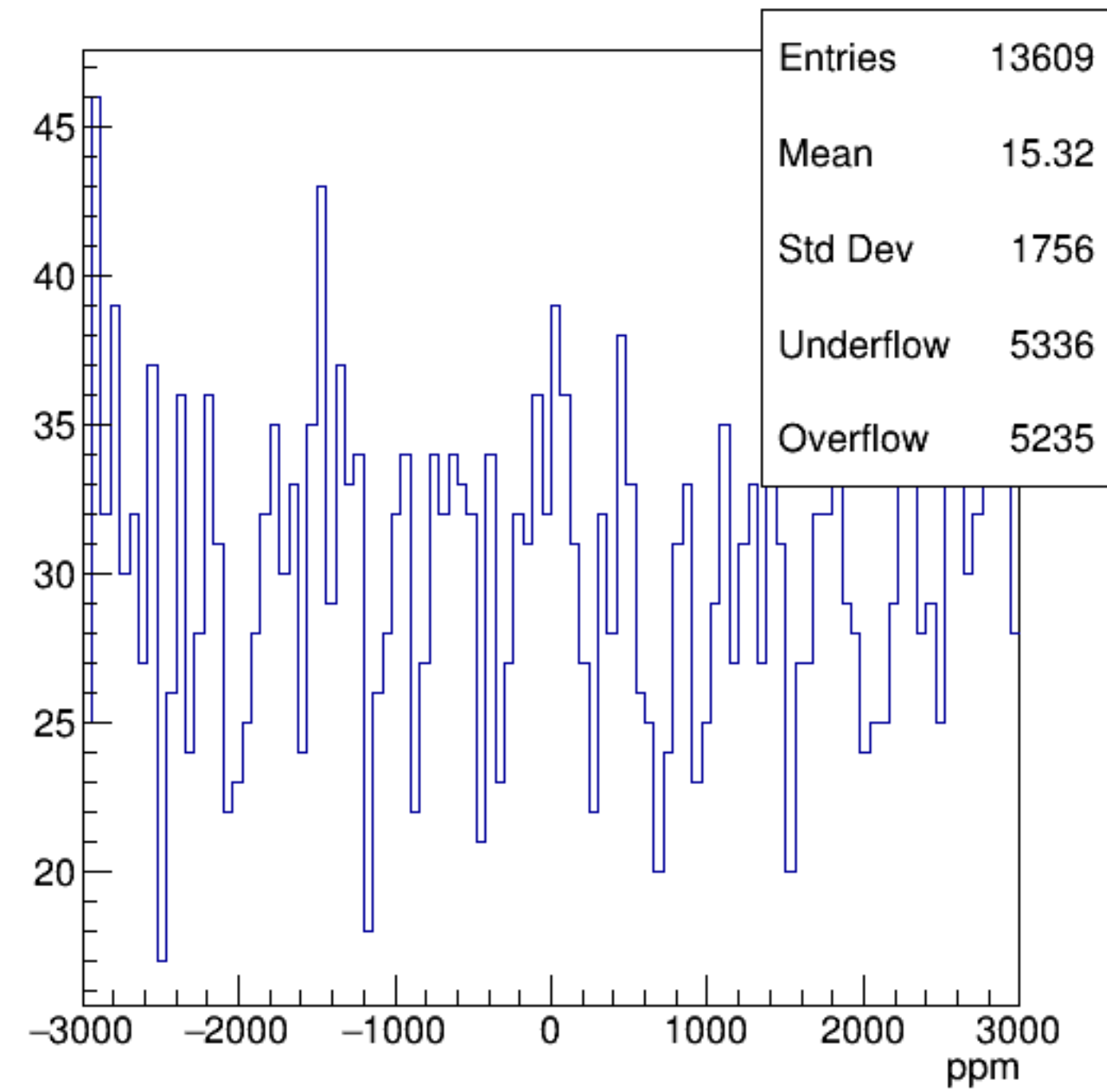
sam7*CurMon {ErrorFlag==0}

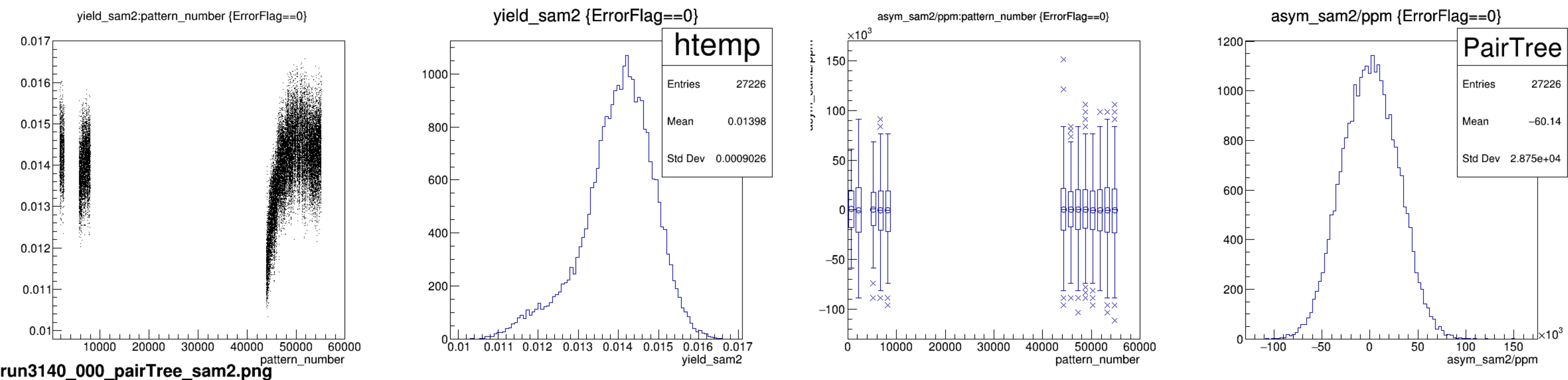


asym_sam7/ppm:pattern_number {ErrorFlag==0}

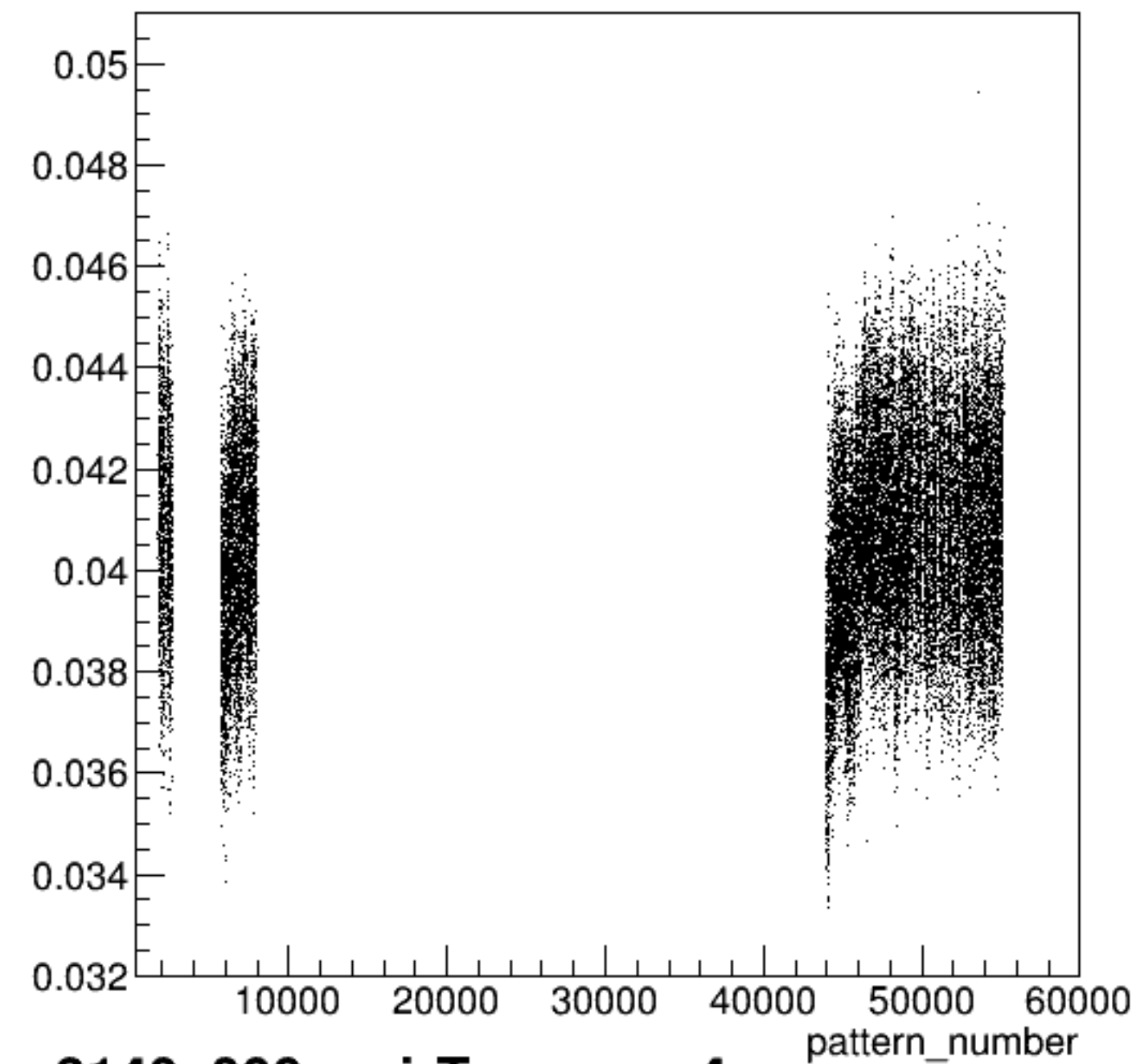


asym_sam7



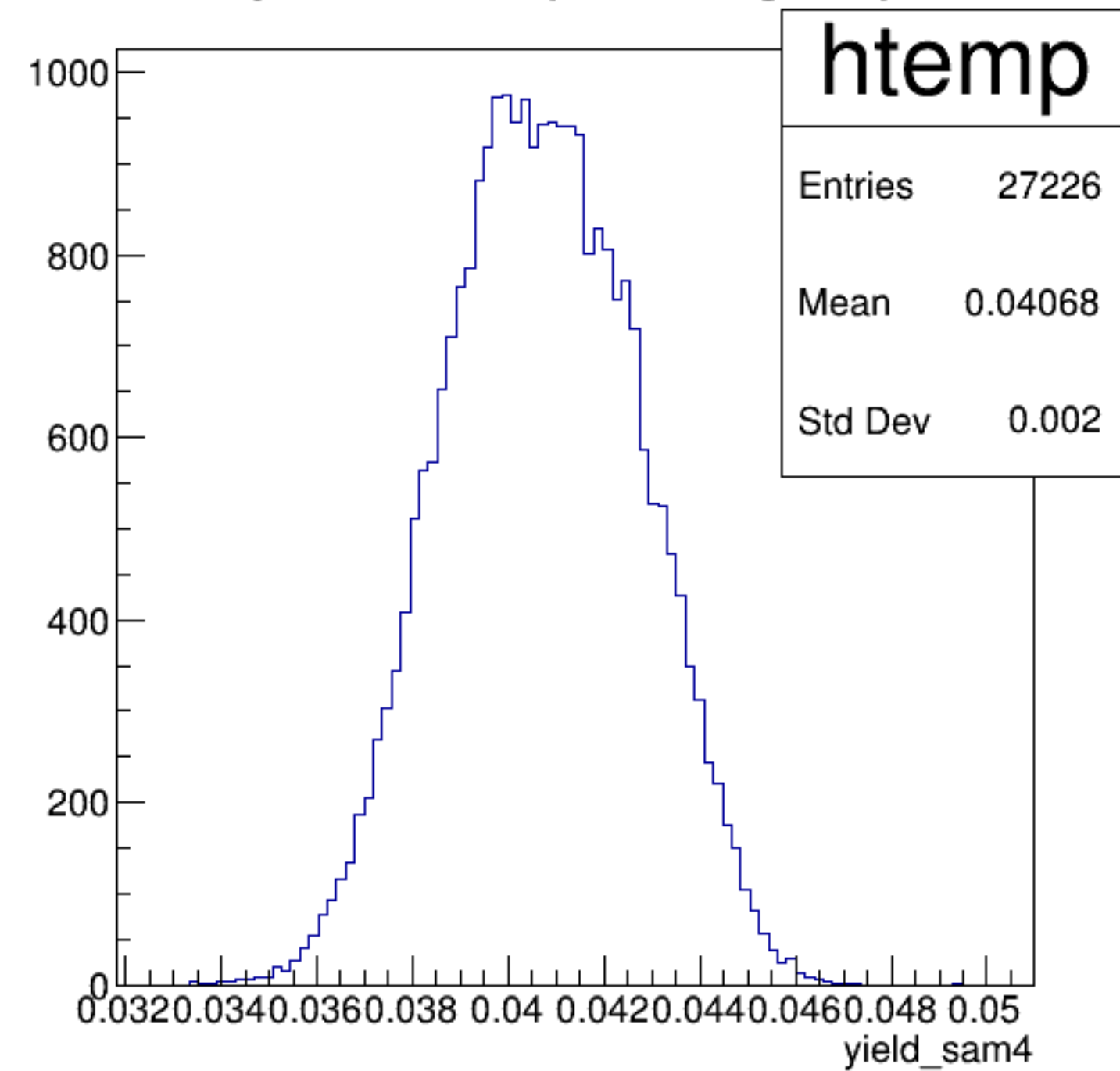


yield_sam4:pattern_number {ErrorFlag==0}

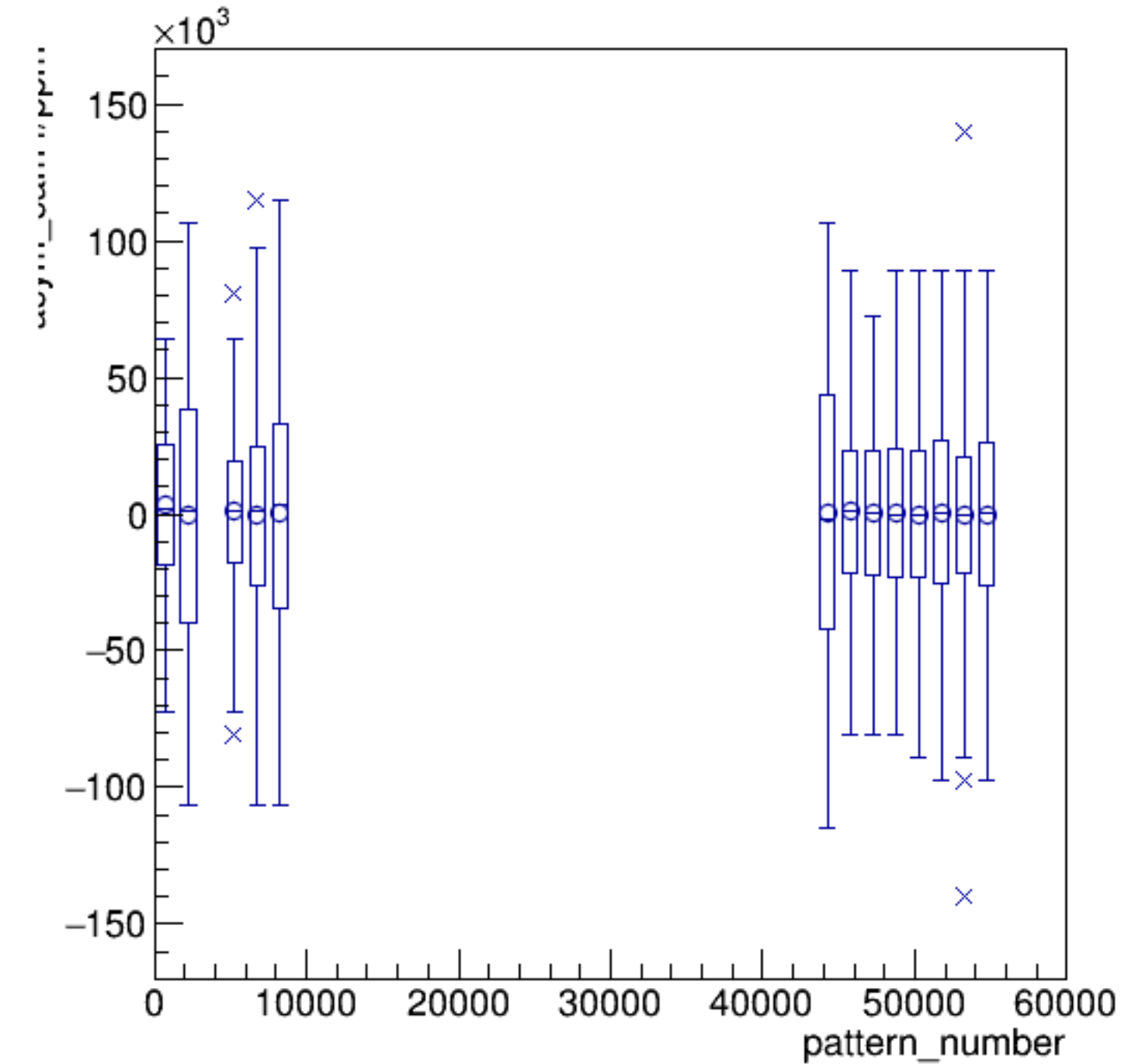


run3140_000_pairTree_sam4.png

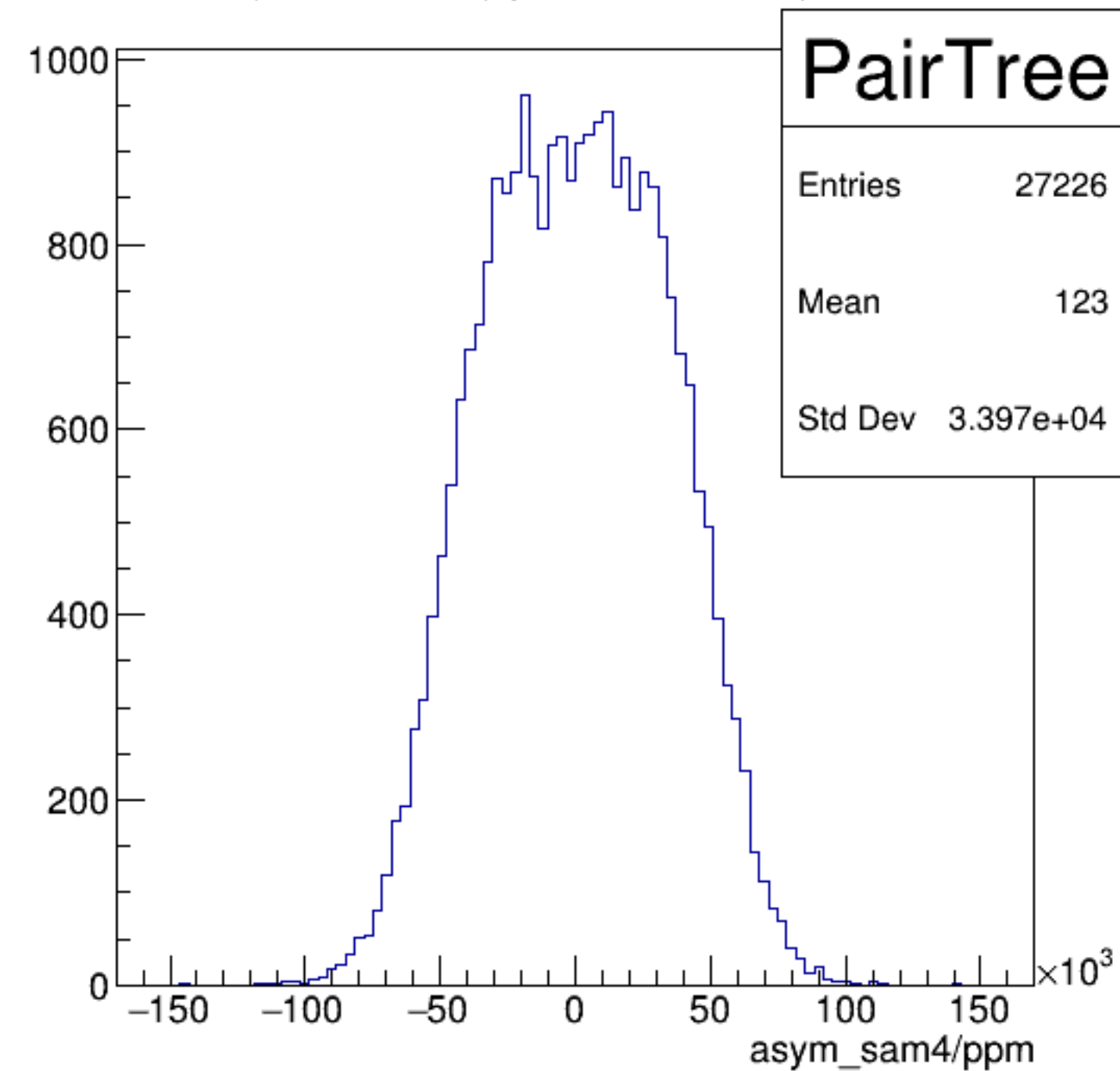
yield_sam4 {ErrorFlag==0}



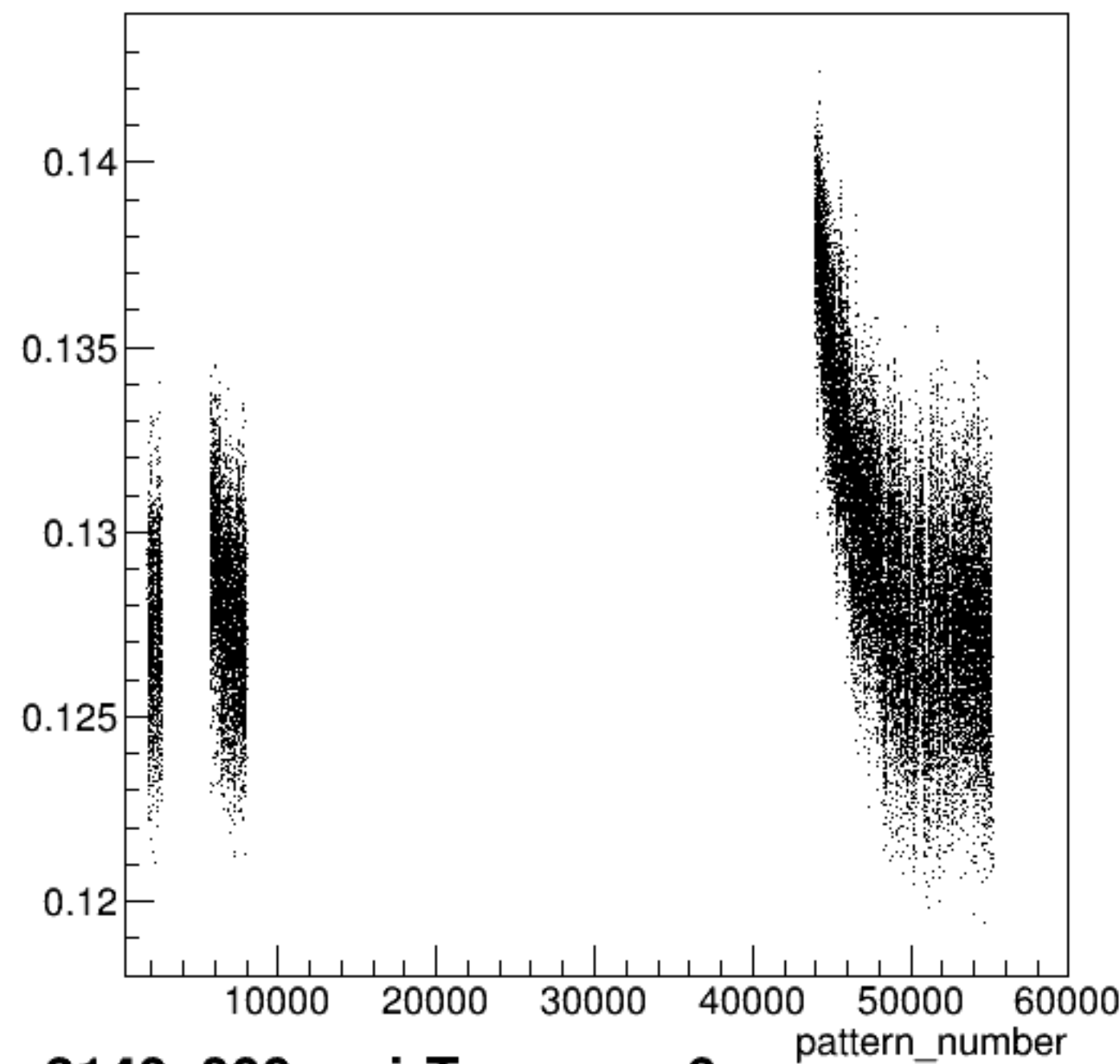
asym_sam4/ppm:pattern_number {ErrorFlag==0}



asym_sam4/ppm {ErrorFlag==0}

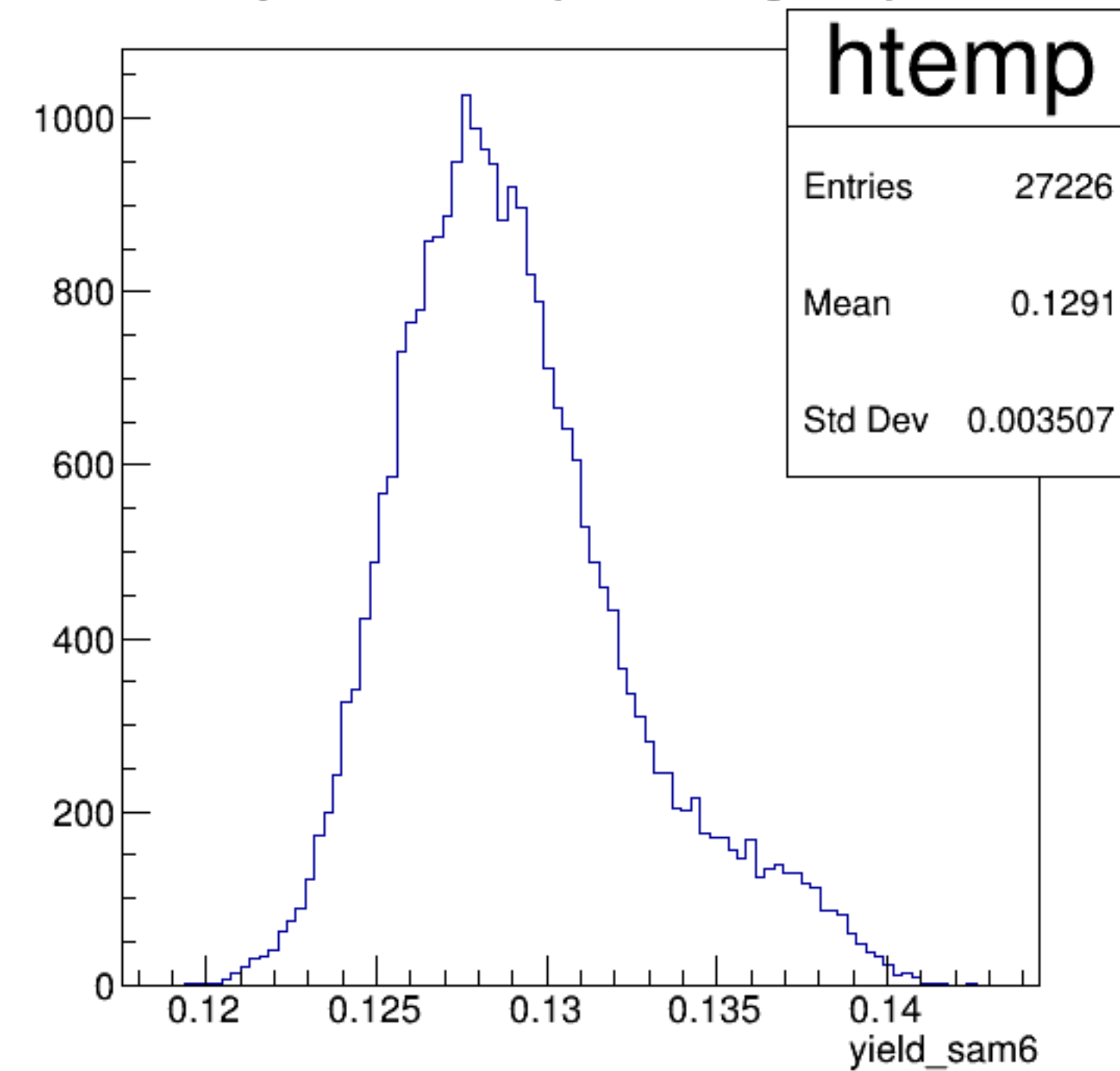


yield_sam6:pattern_number {ErrorFlag==0}

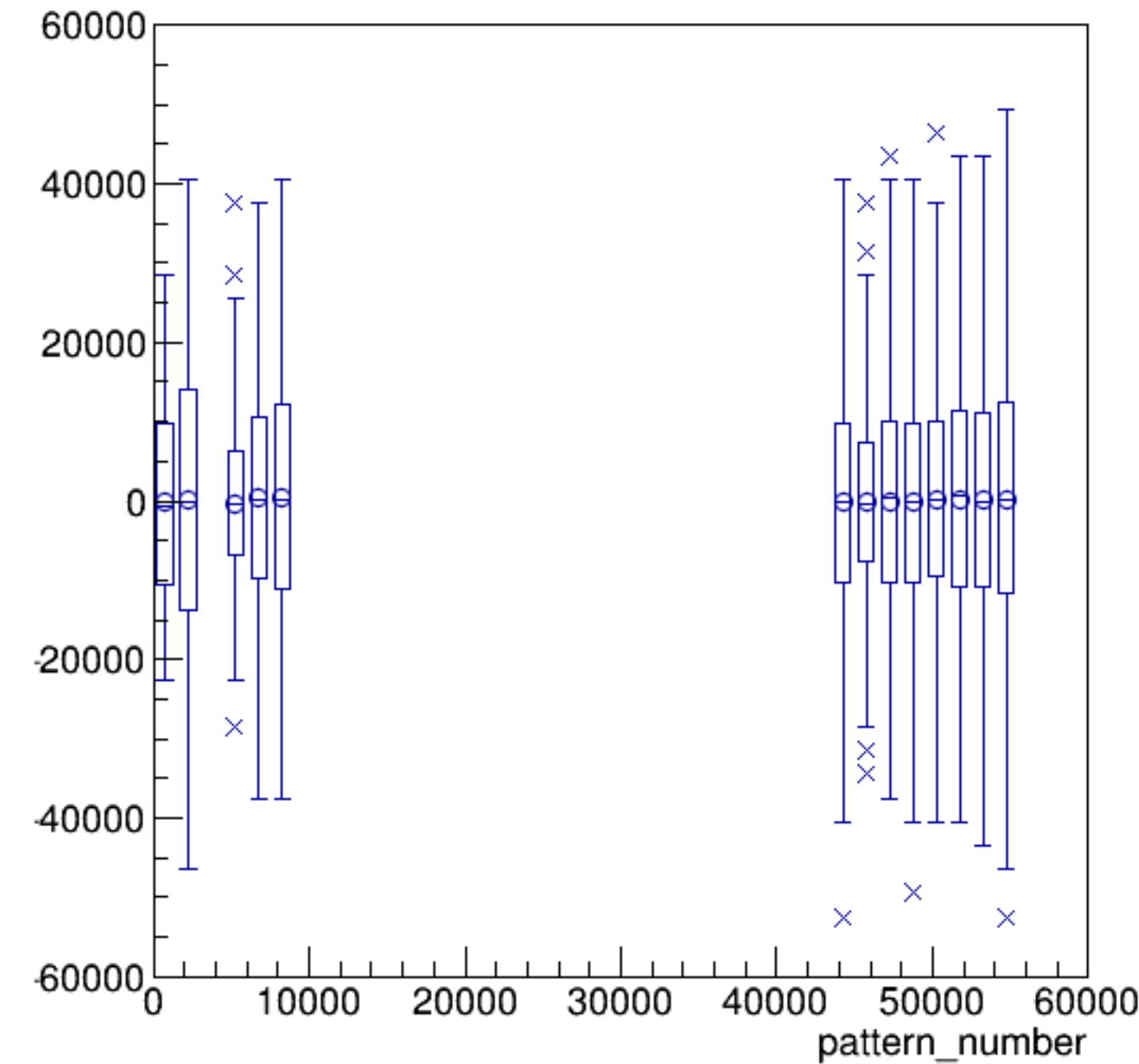


run3140_000_pairTree_sam6.png

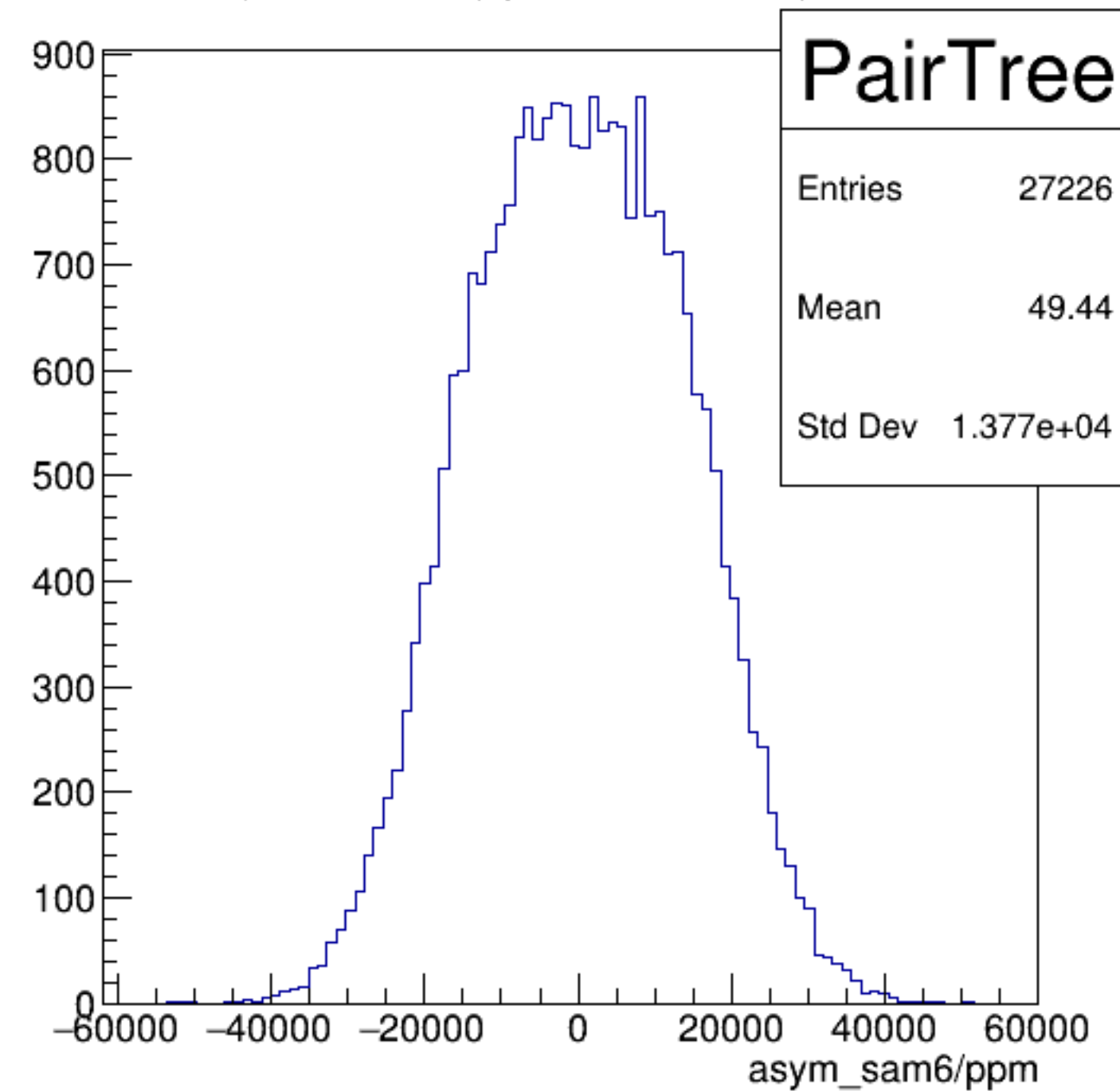
yield_sam6 {ErrorFlag==0}



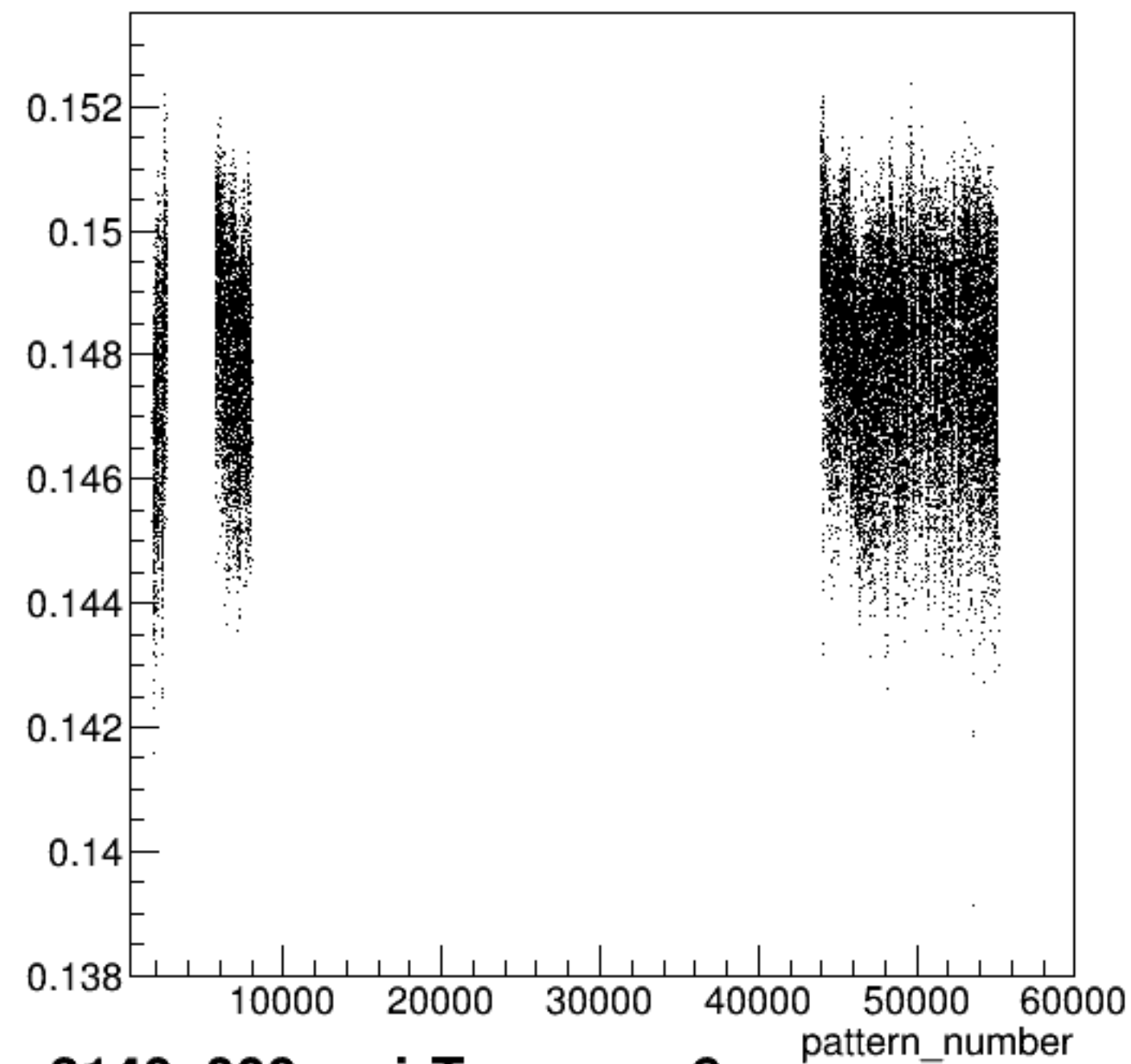
asym_sam6/ppm:pattern_number {ErrorFlag==0}



asym_sam6/ppm {ErrorFlag==0}

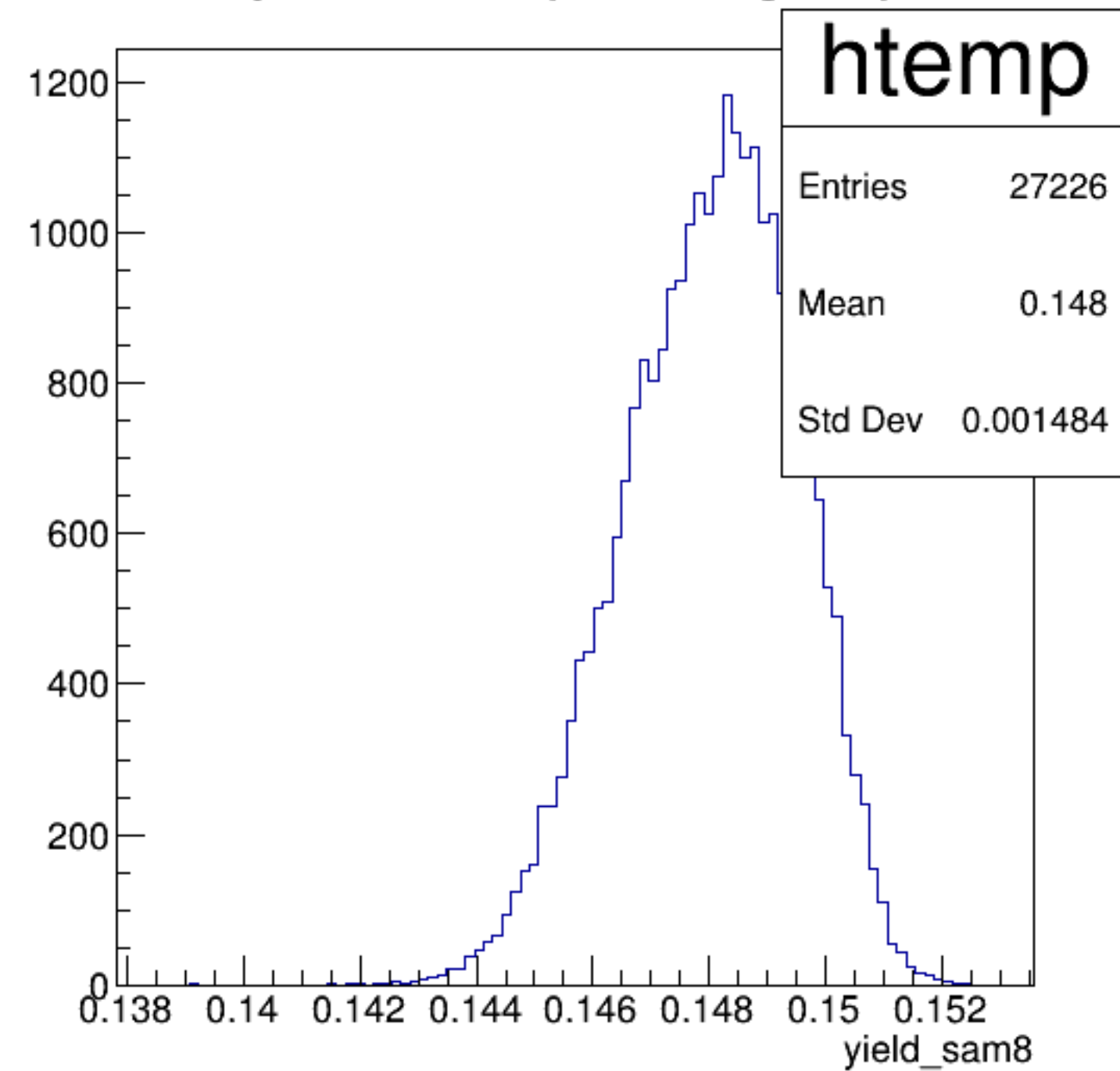


yield_sam8:pattern_number {ErrorFlag==0}

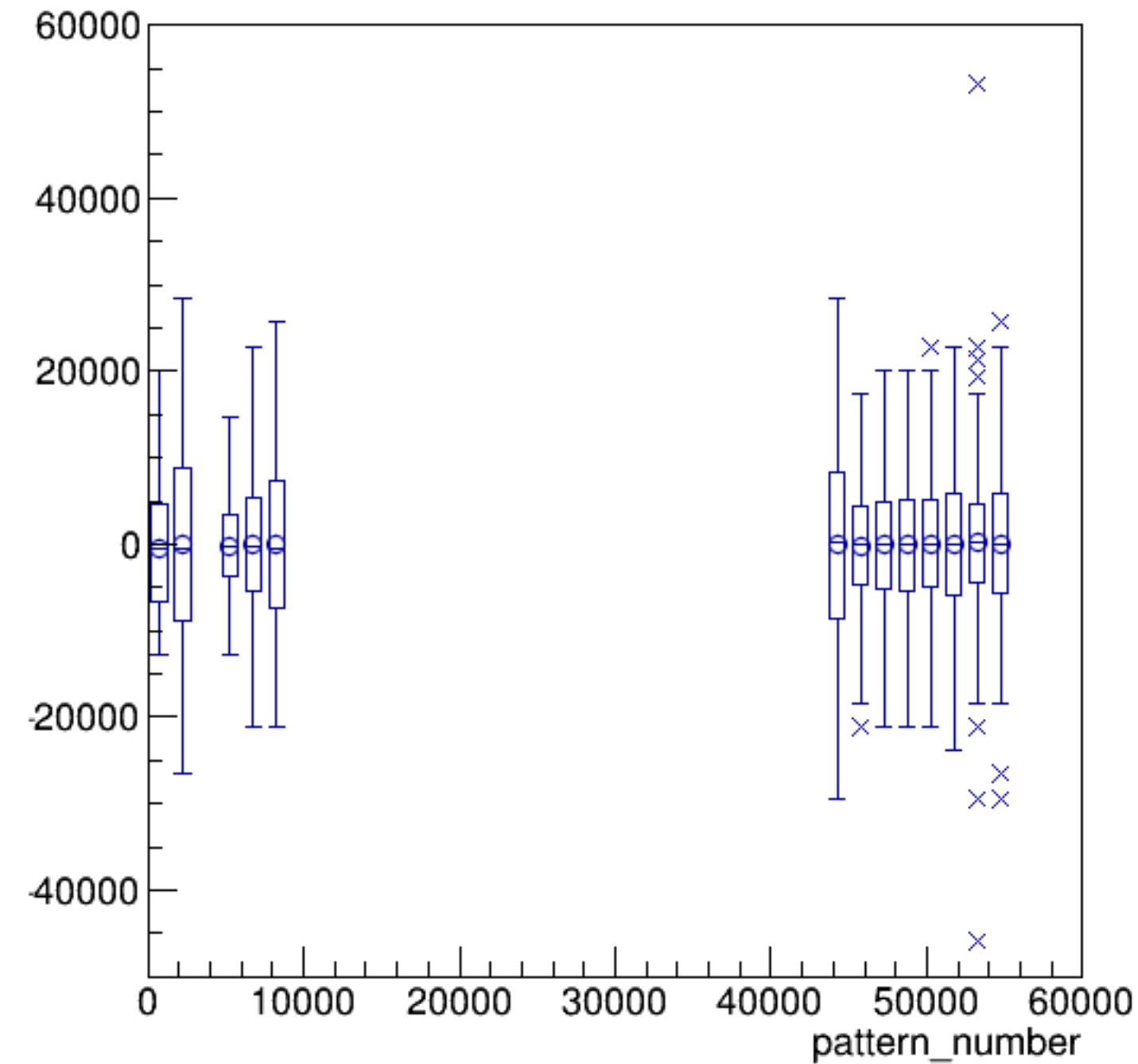


run3140_000_pairTree_sam8.png

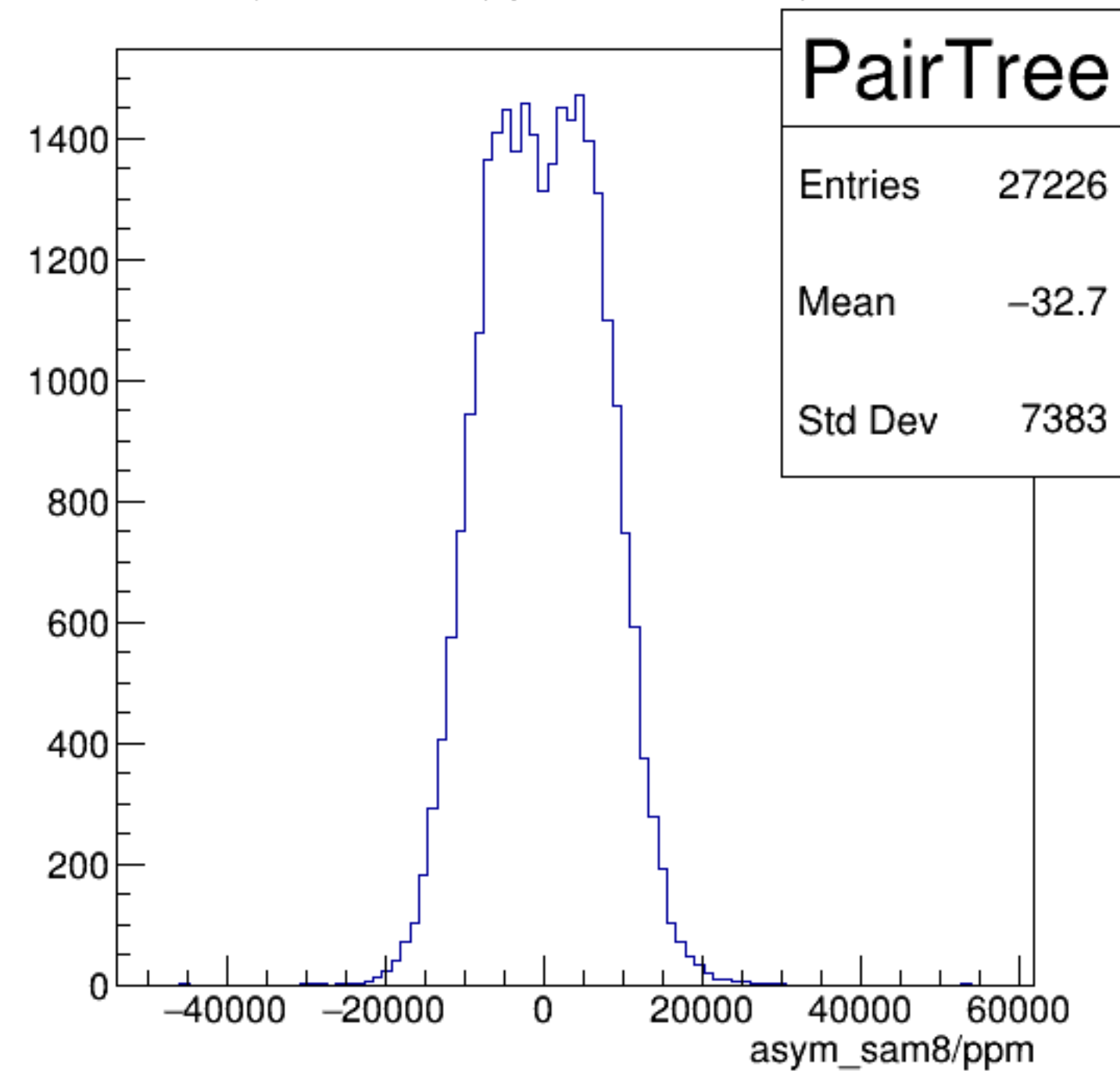
yield_sam8 {ErrorFlag==0}



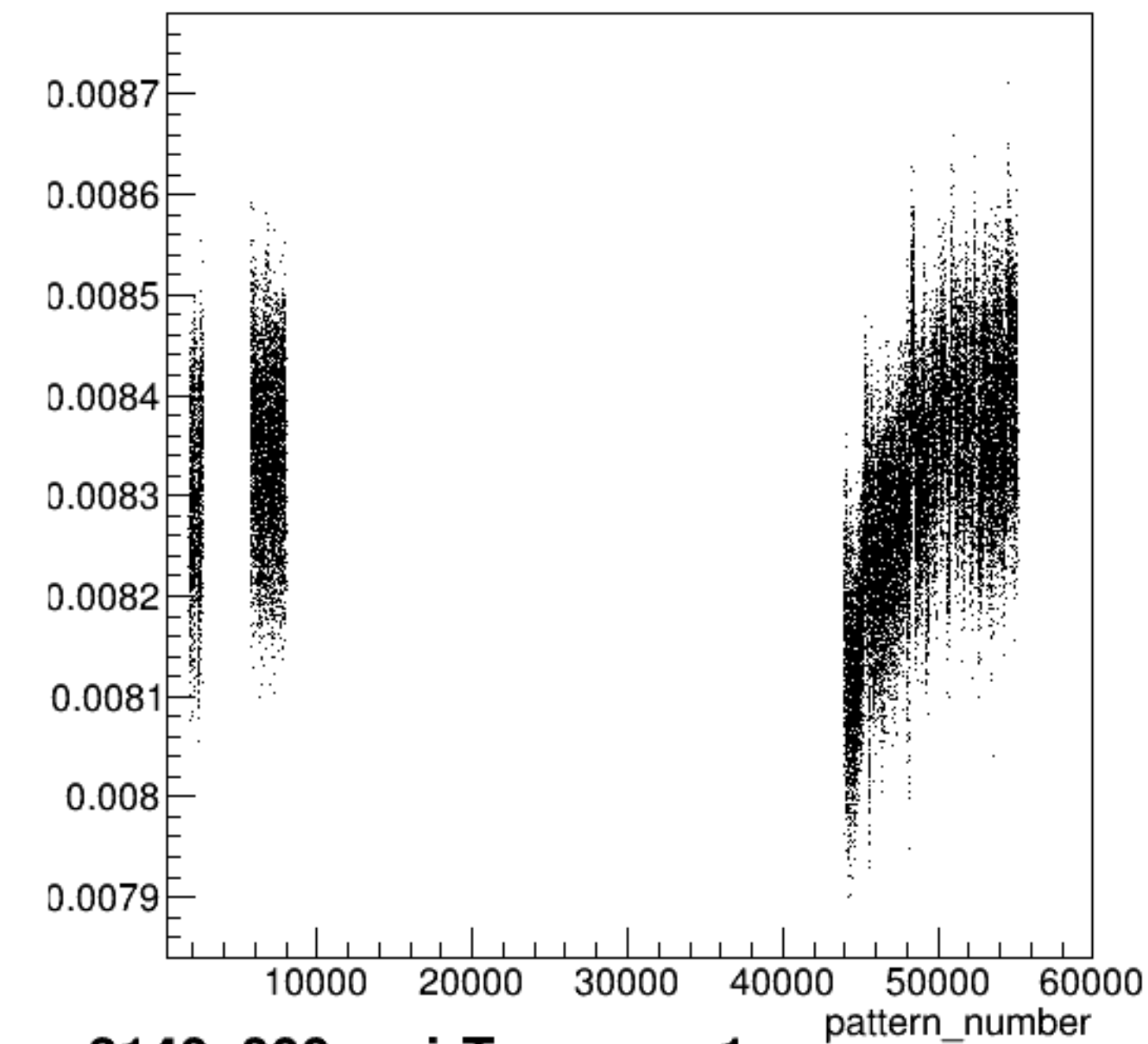
asym_sam8/ppm:pattern_number {ErrorFlag==0}



asym_sam8/ppm {ErrorFlag==0}

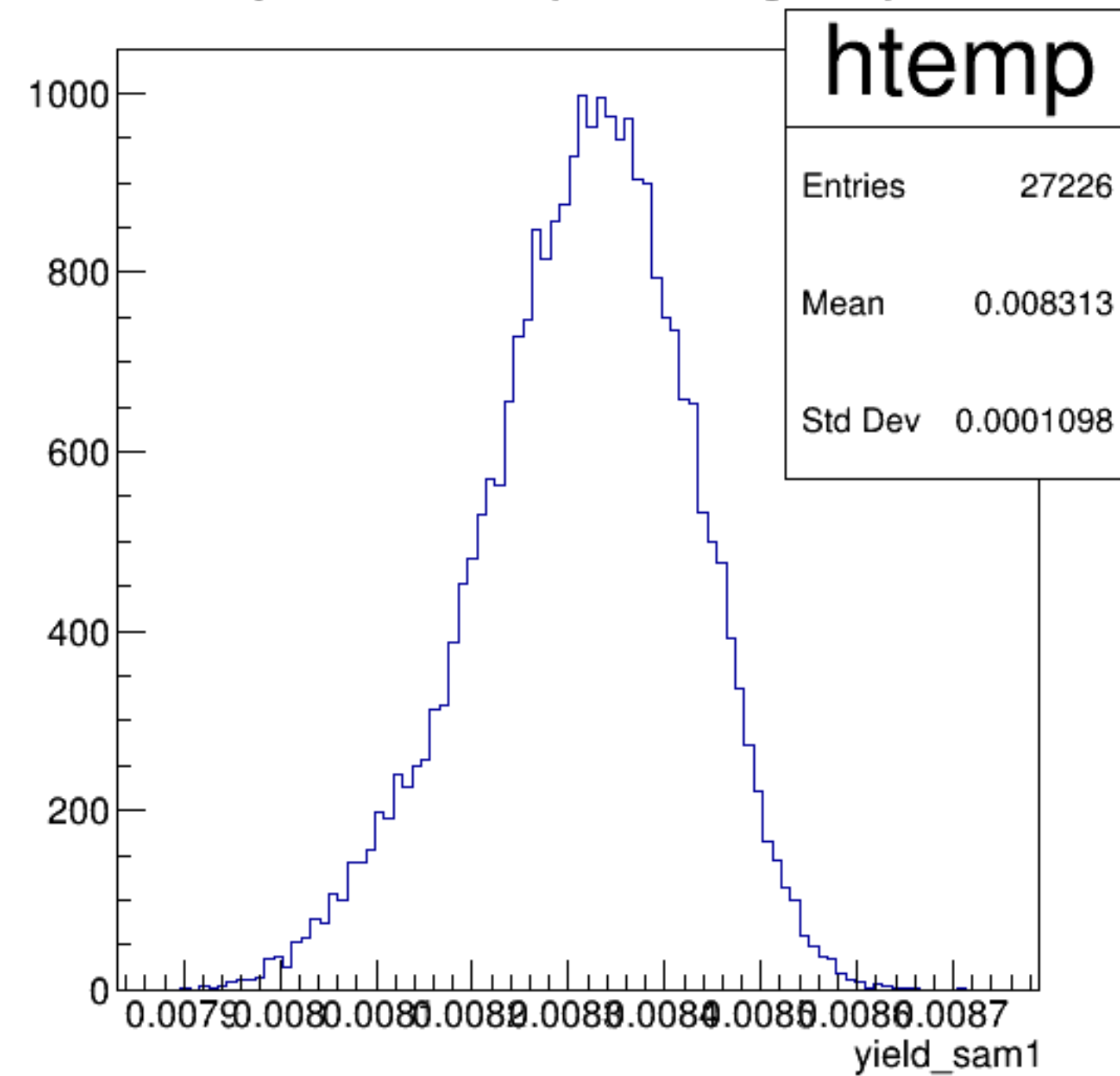


yield_sam1:pattern_number {ErrorFlag==0}

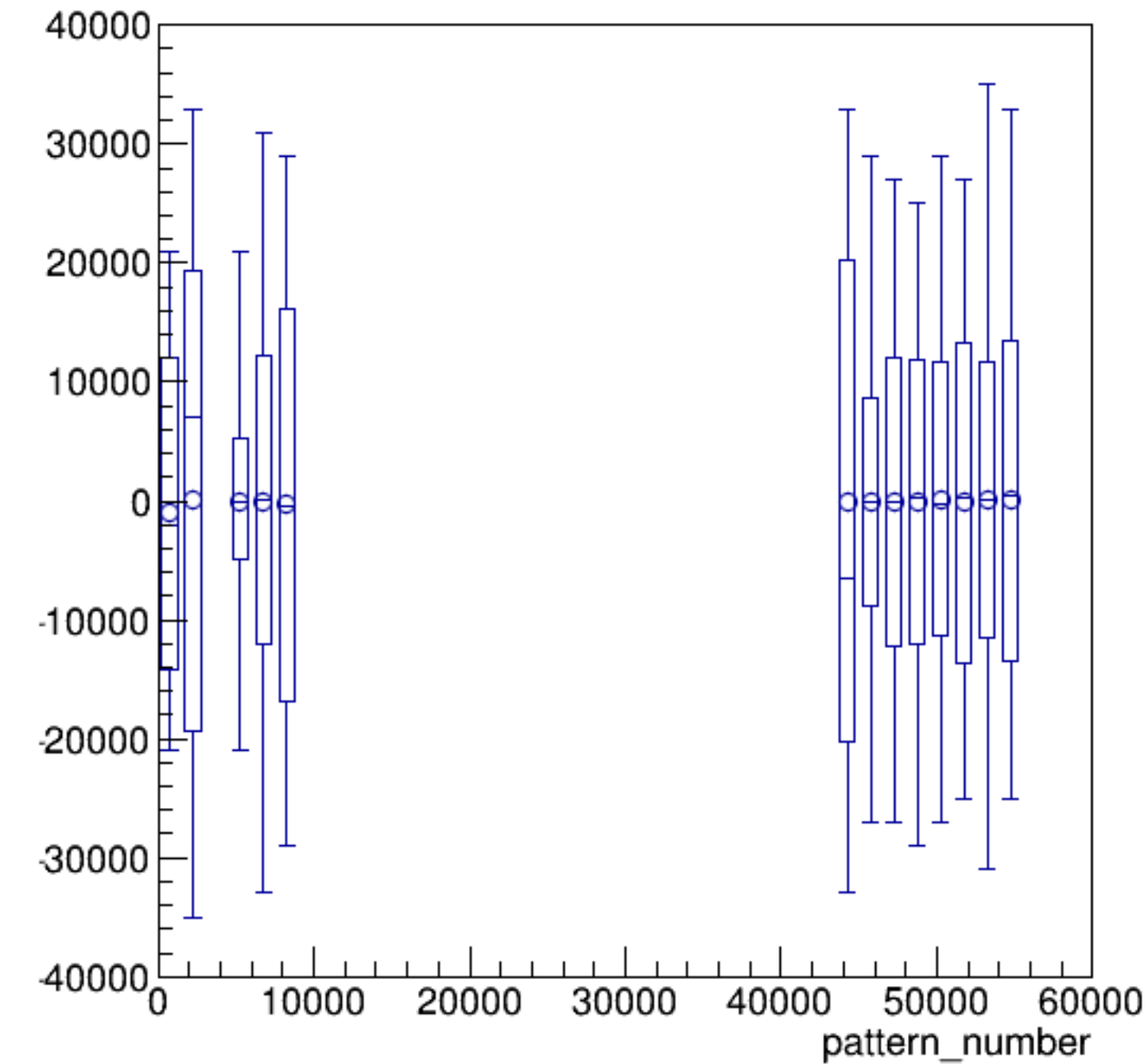


run3140_000_pairTree_sam1.png

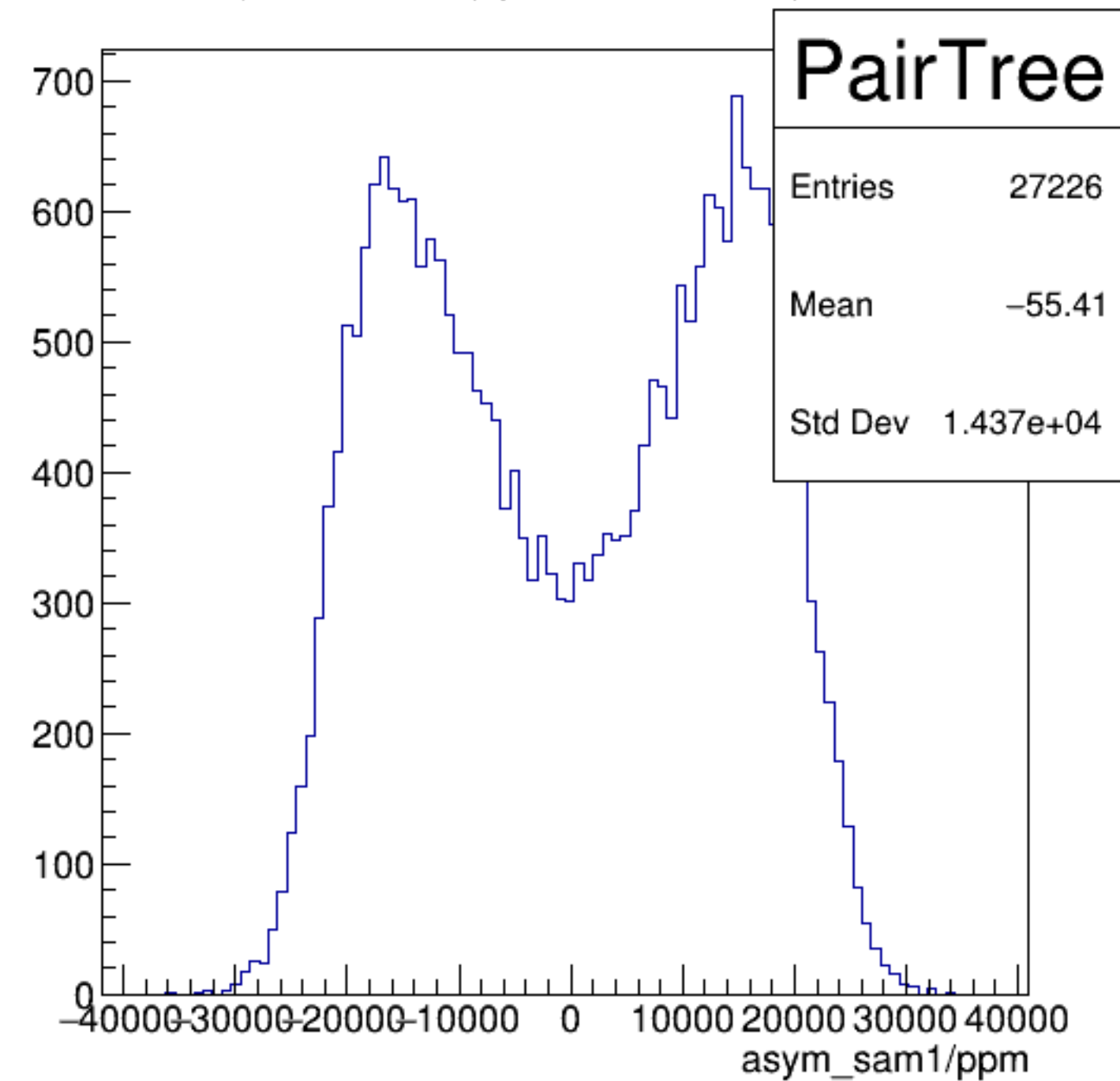
yield_sam1 {ErrorFlag==0}



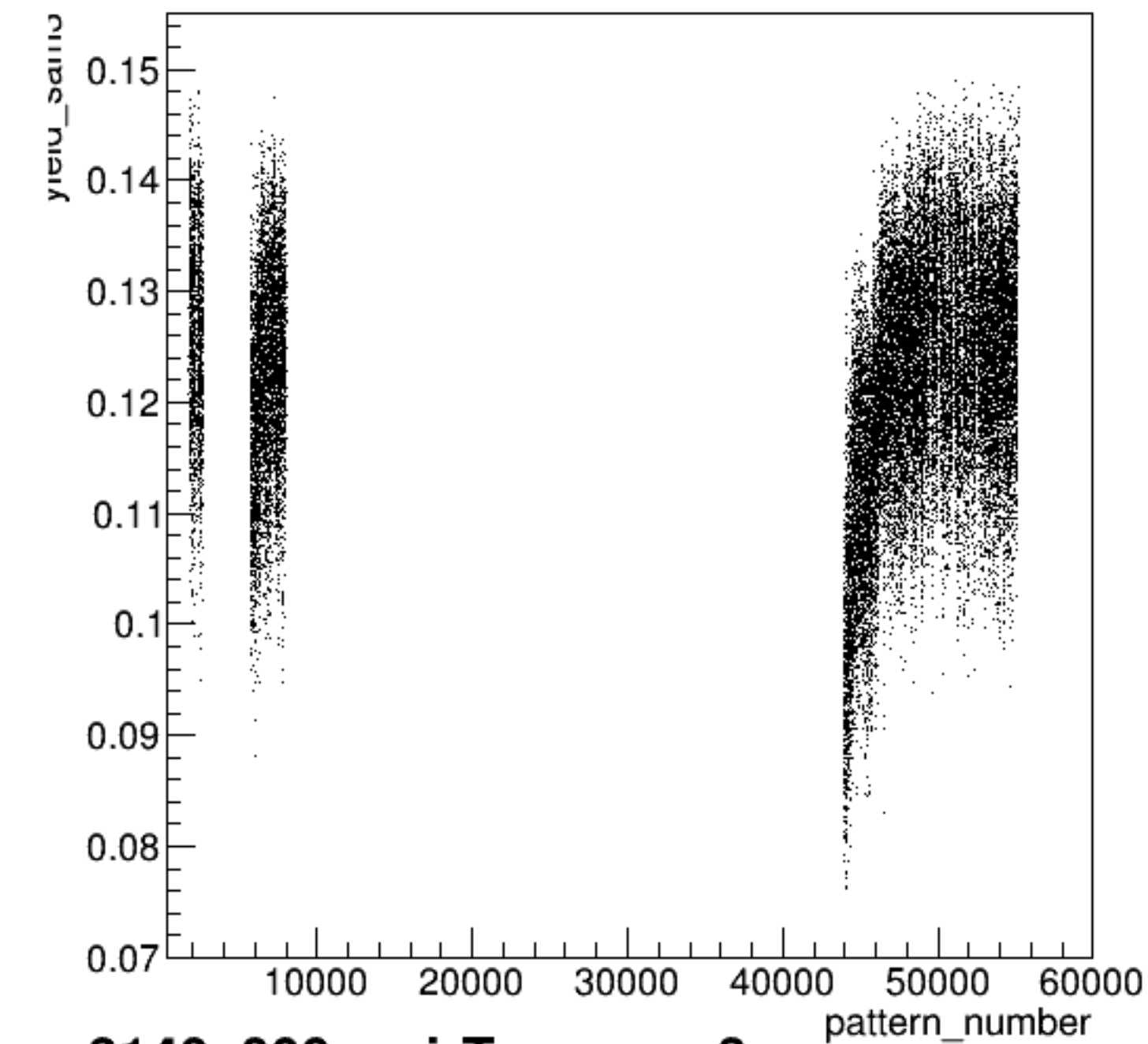
asym_sam1/ppm:pattern_number {ErrorFlag==0}



asym_sam1/ppm {ErrorFlag==0}

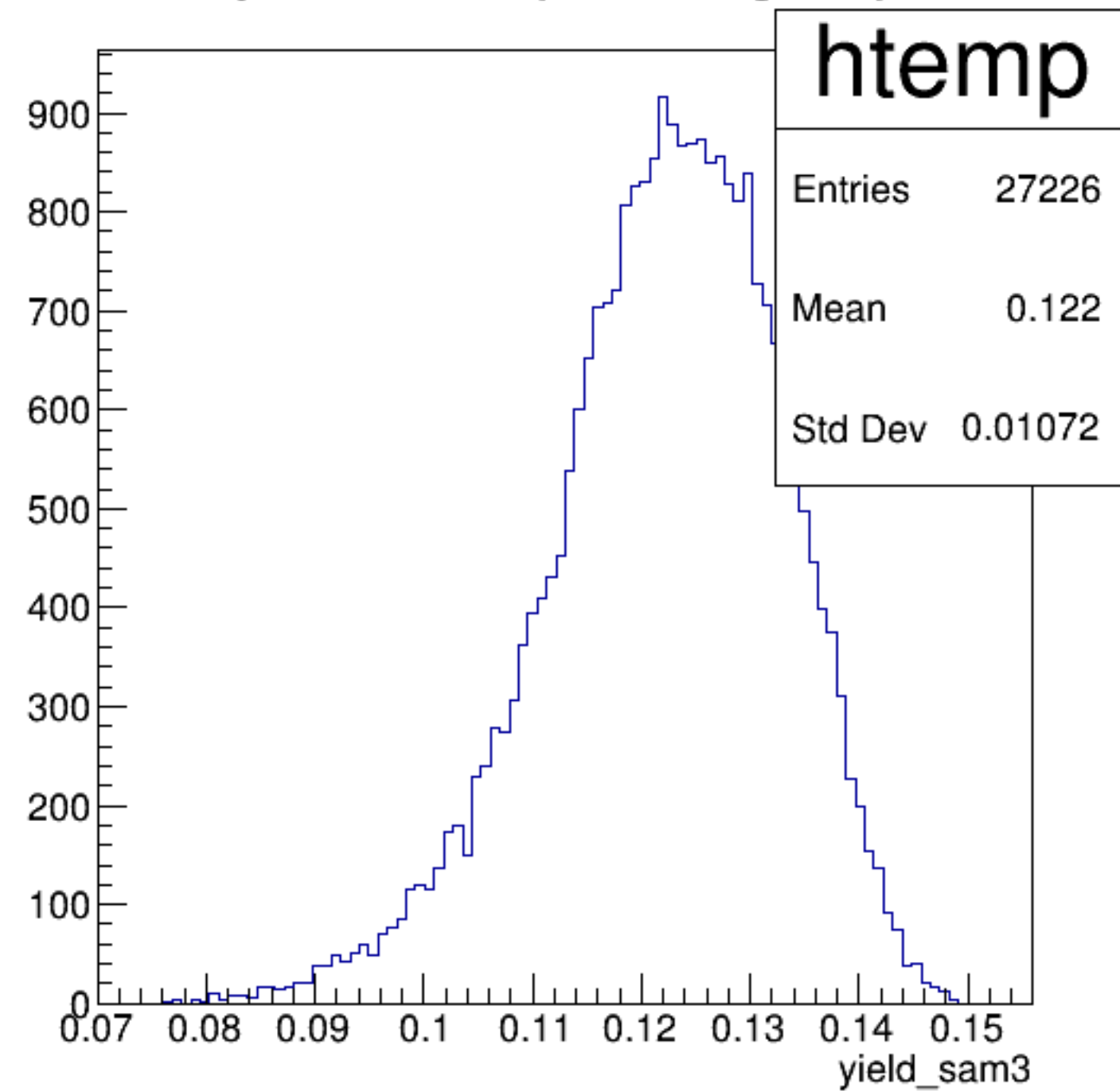


yield_sam3:pattern_number {ErrorFlag==0}

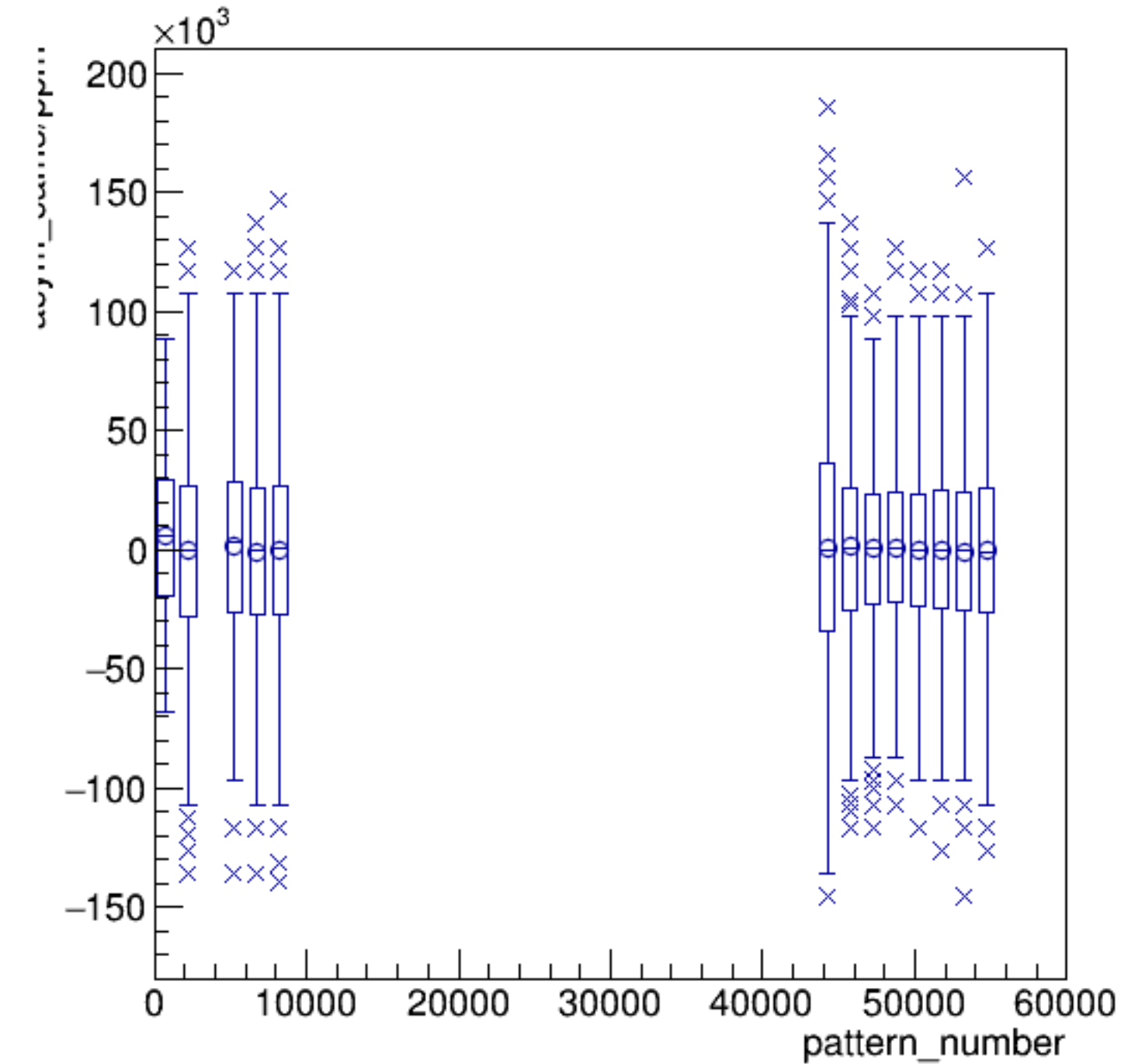


run3140_000_pairTree_sam3.png

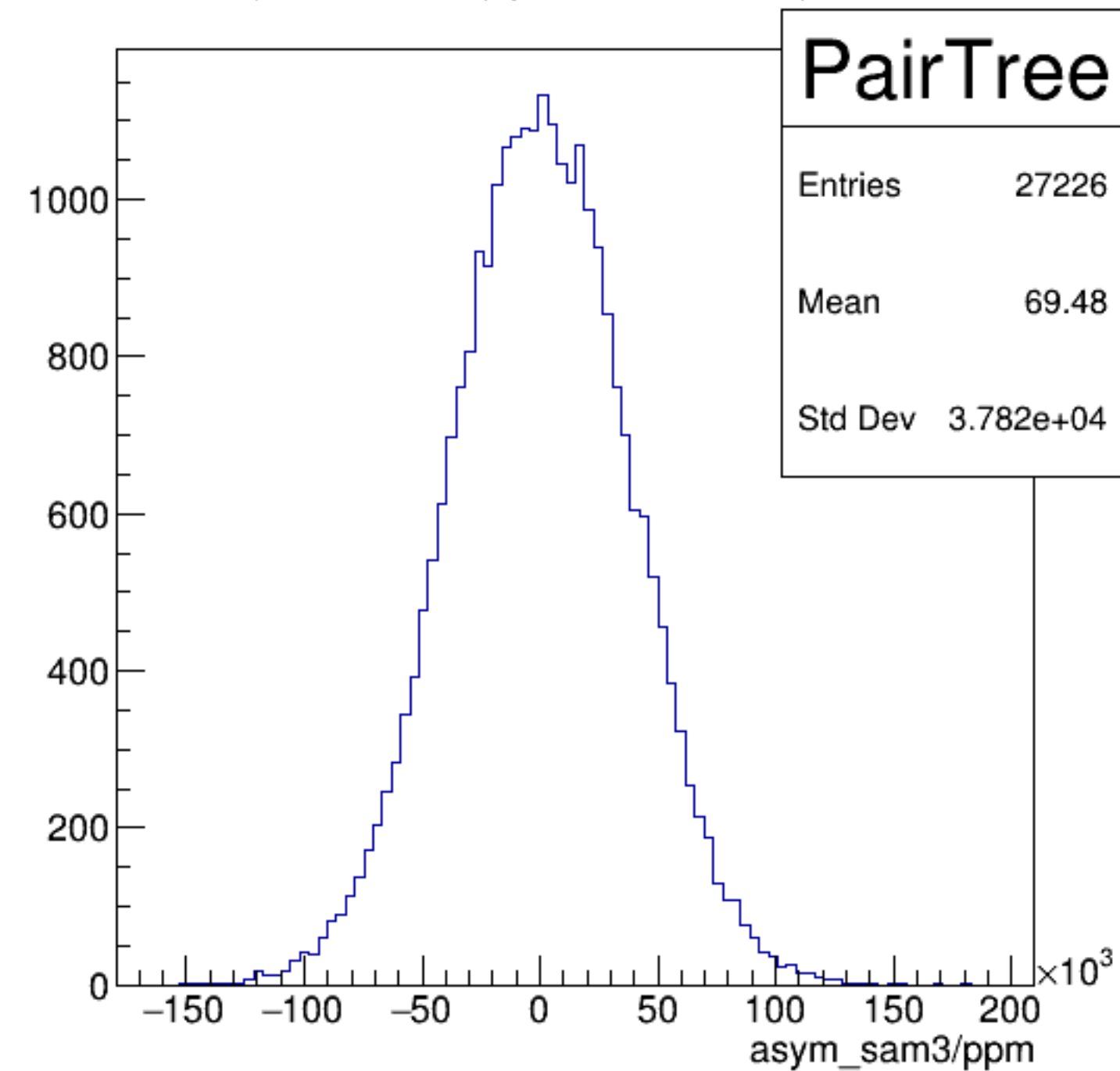
yield_sam3 {ErrorFlag==0}



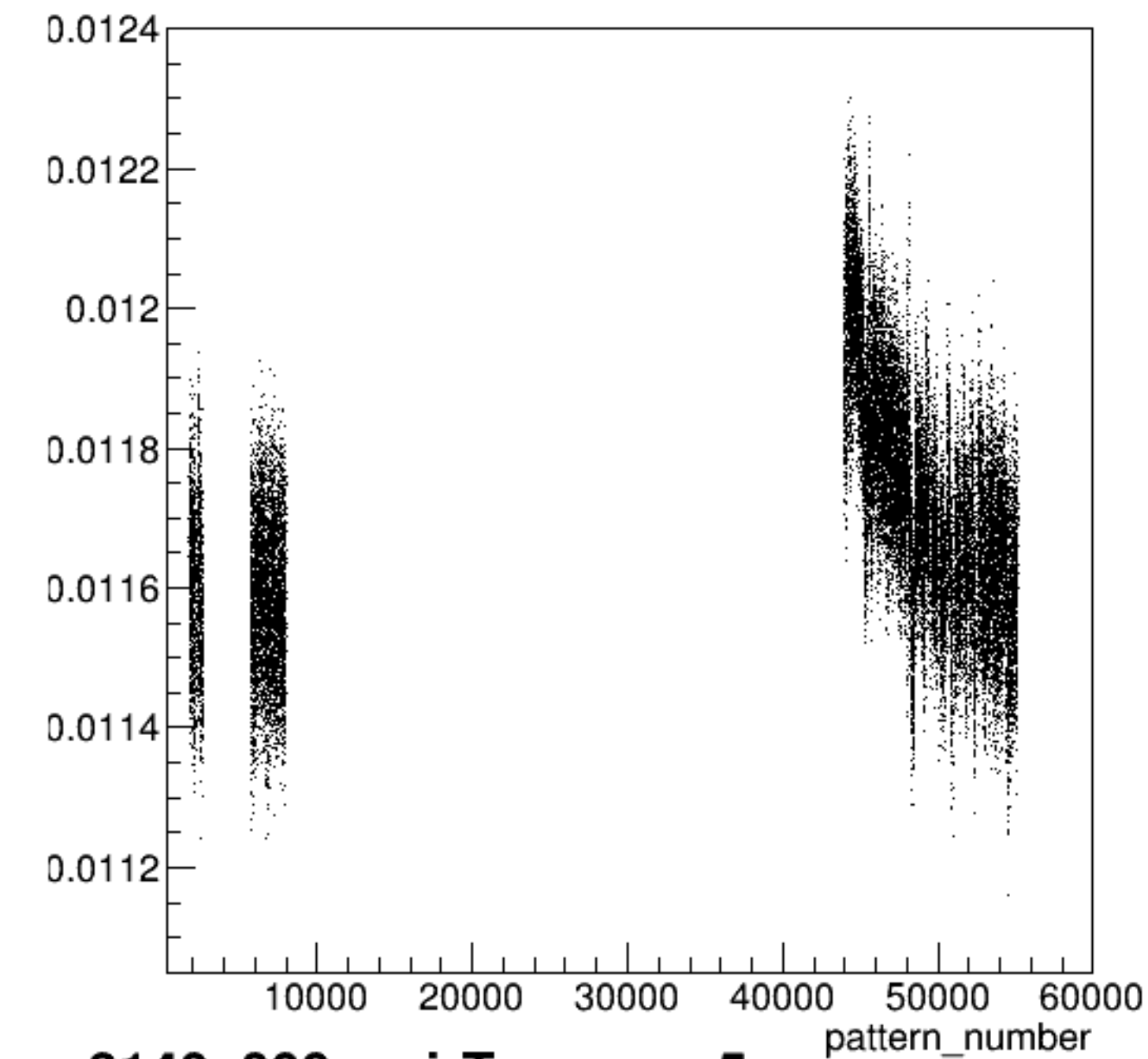
asym_sam3/ppm:pattern_number {ErrorFlag==0}



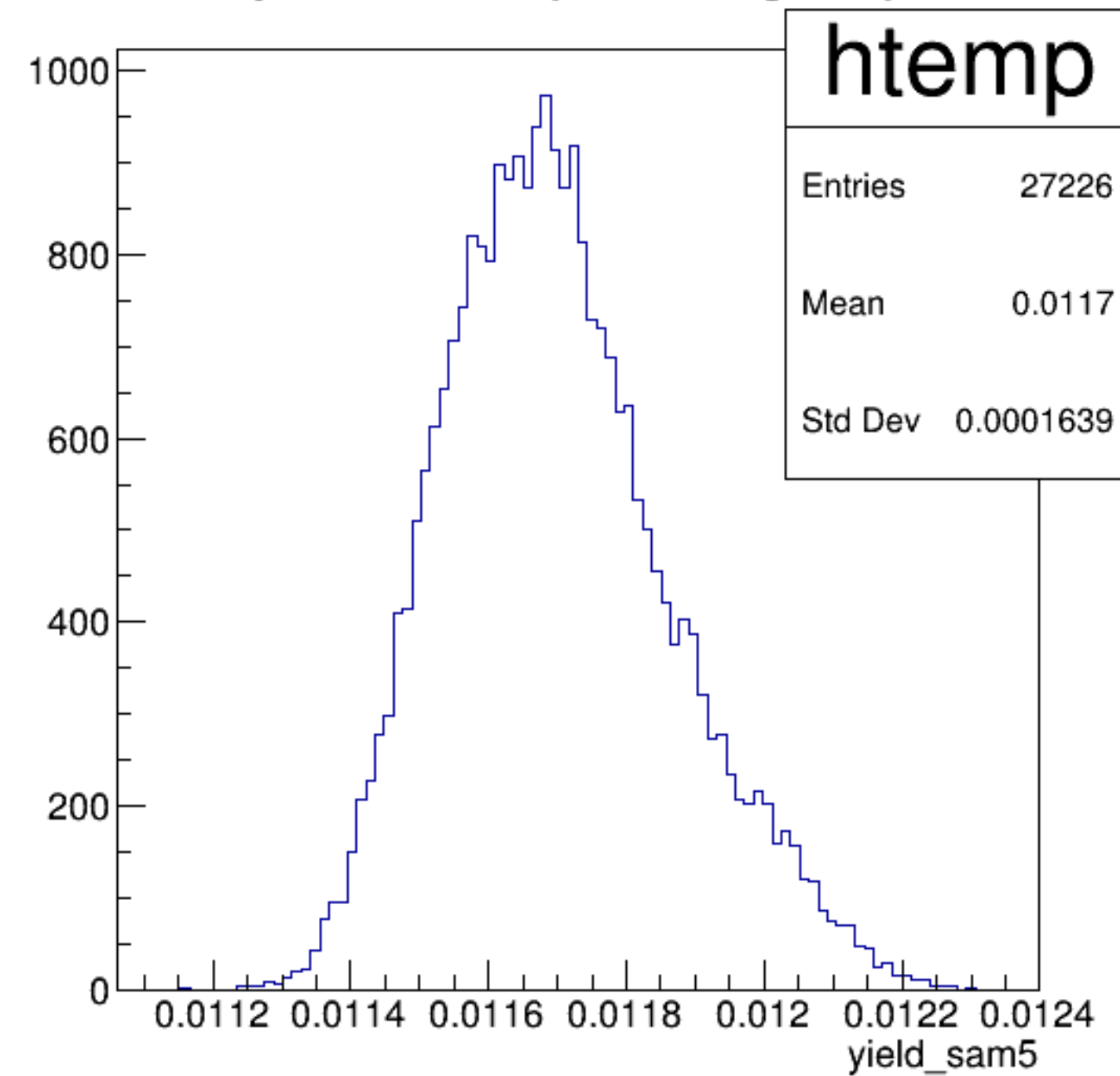
asym_sam3/ppm {ErrorFlag==0}



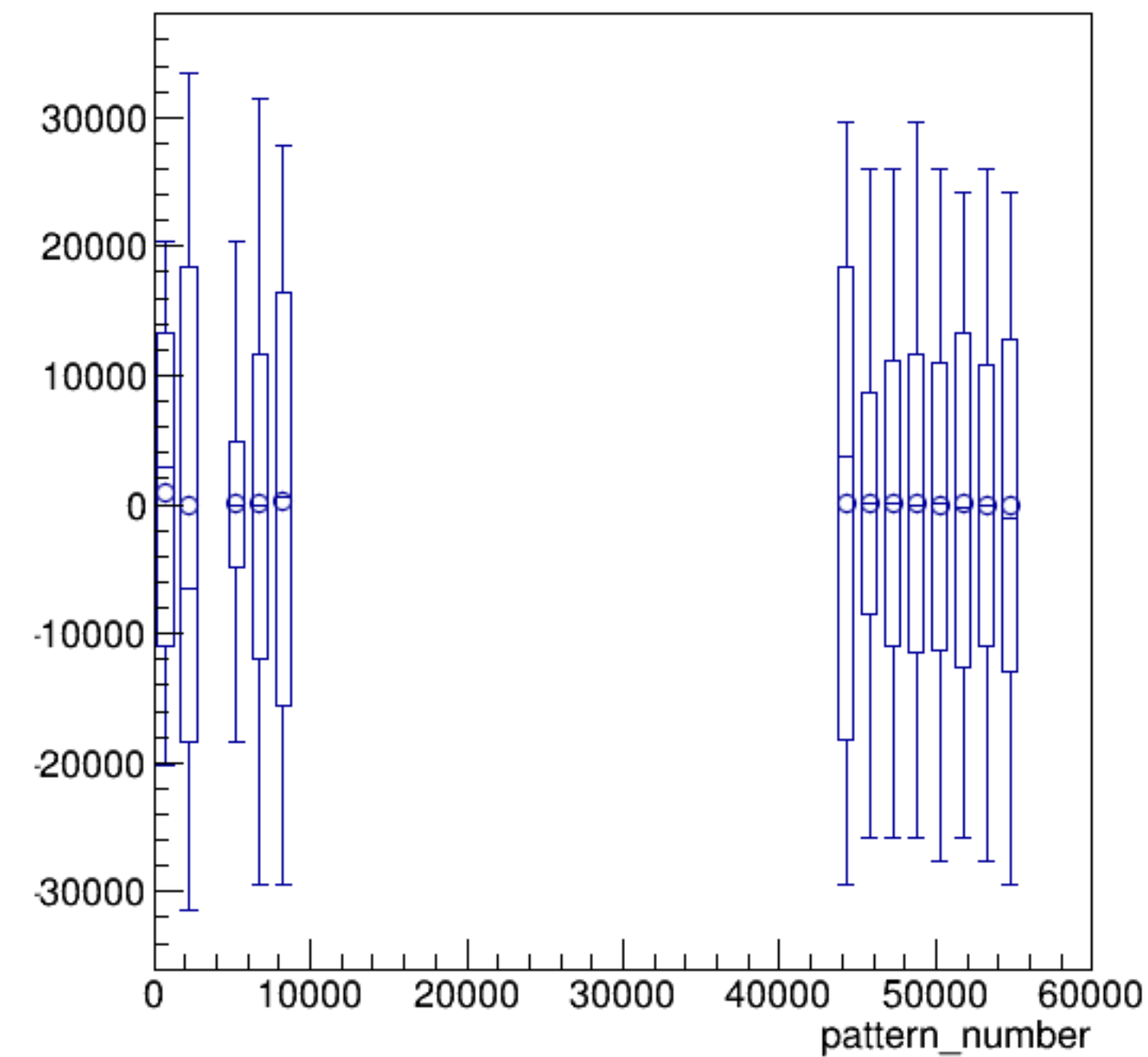
yield_sam5:pattern_number {ErrorFlag==0}



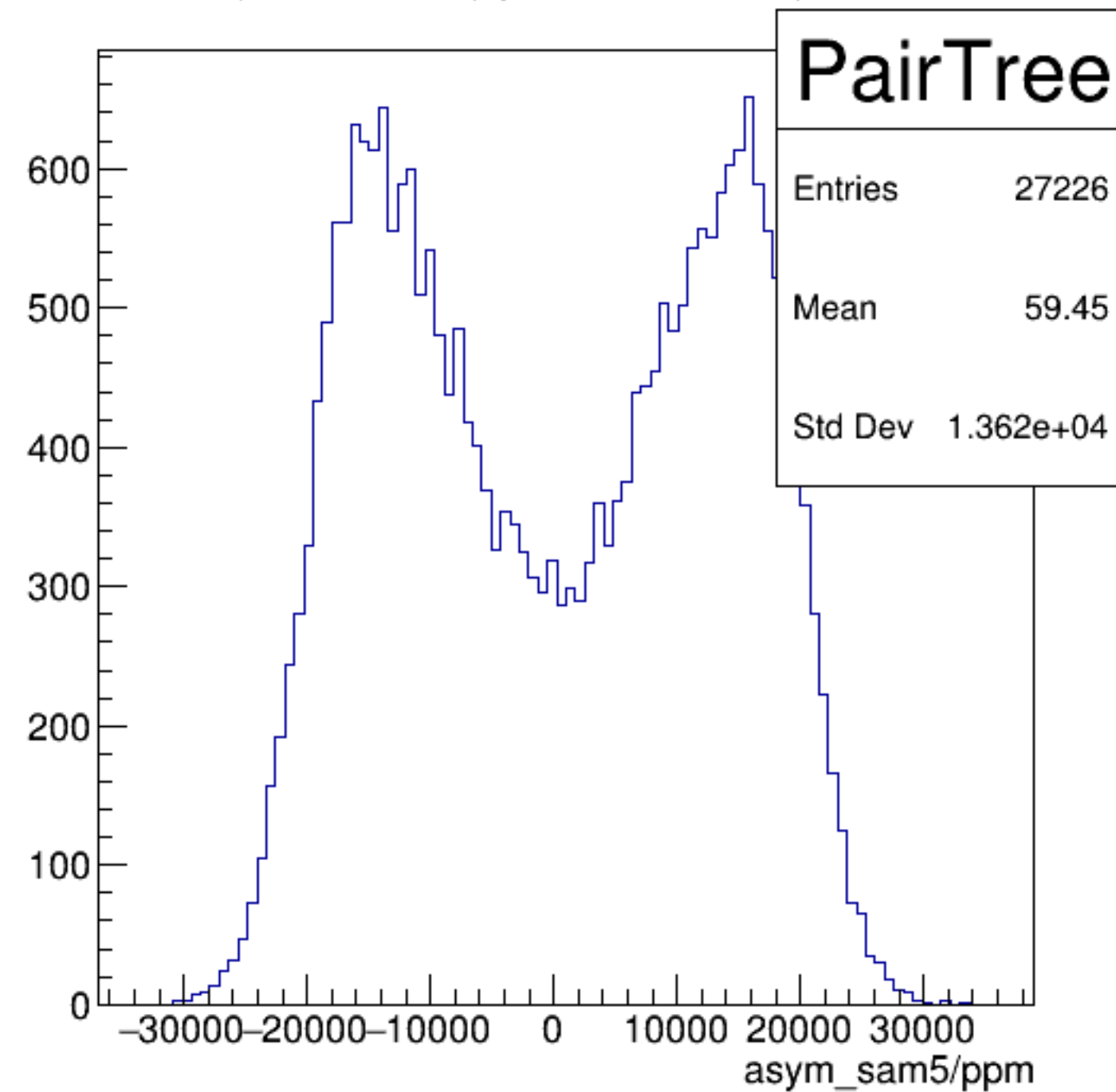
yield_sam5 {ErrorFlag==0}



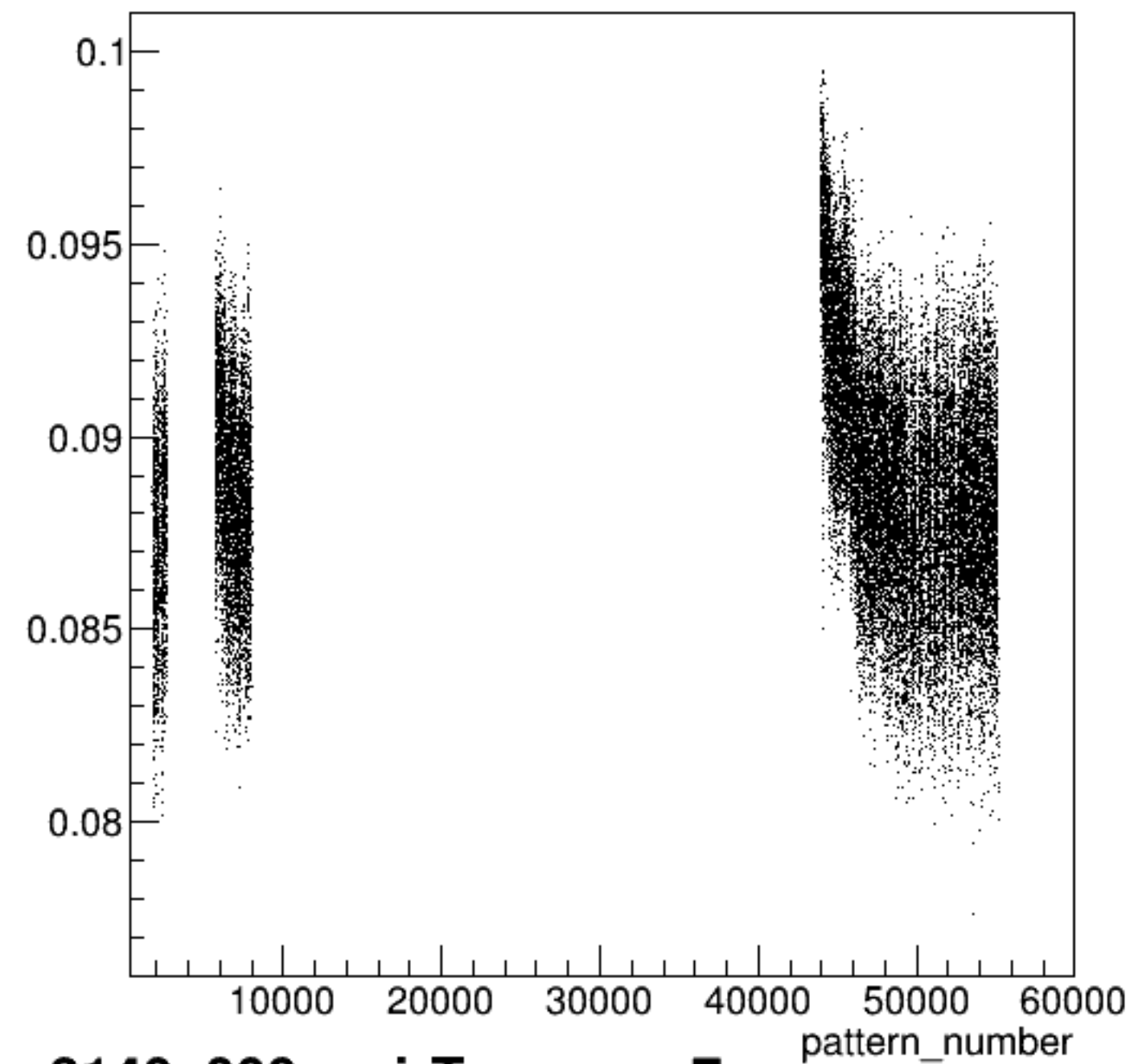
asym_sam5/ppm:pattern_number {ErrorFlag==0}



asym_sam5/ppm {ErrorFlag==0}

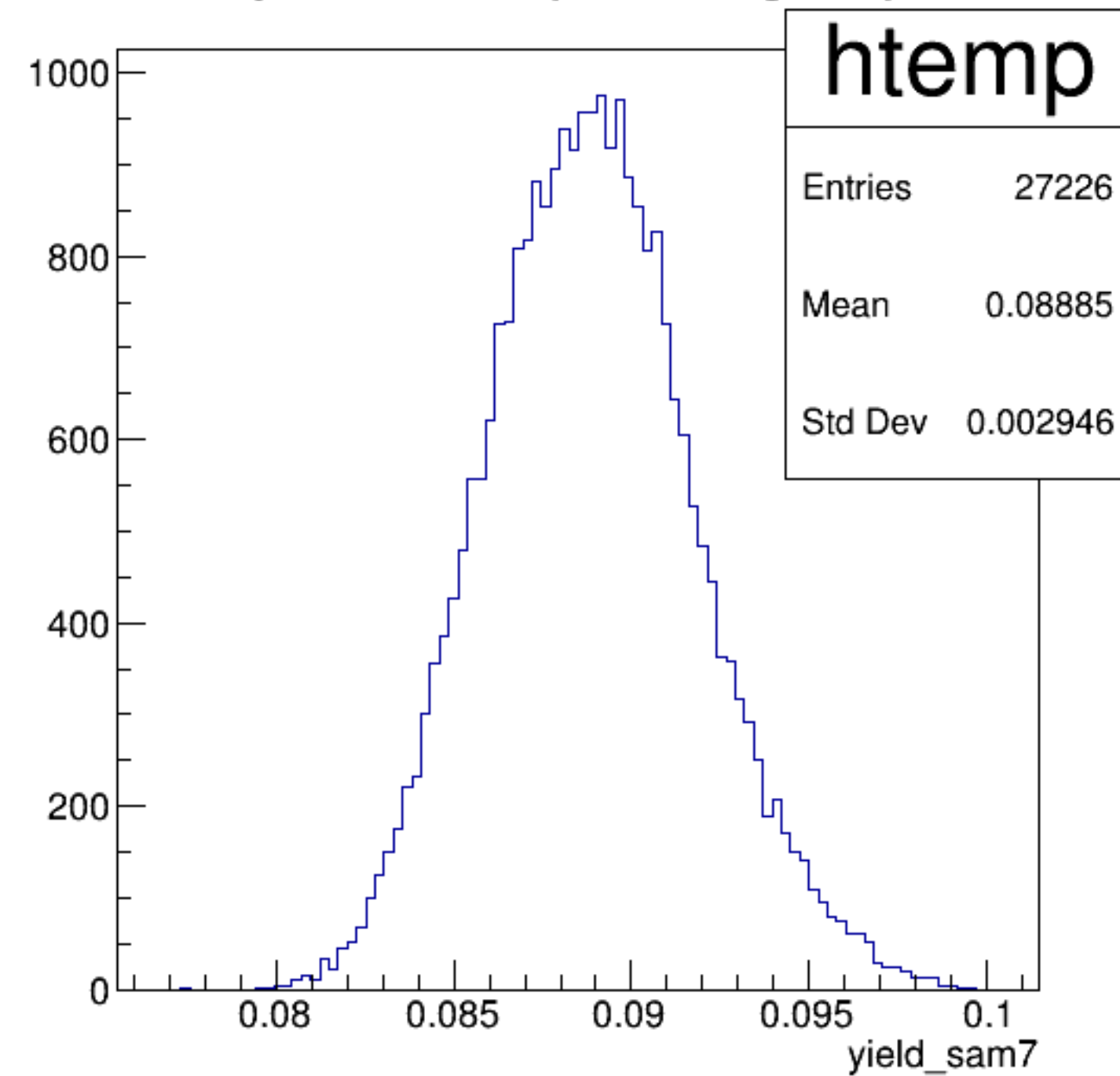


yield_sam7:pattern_number {ErrorFlag==0}

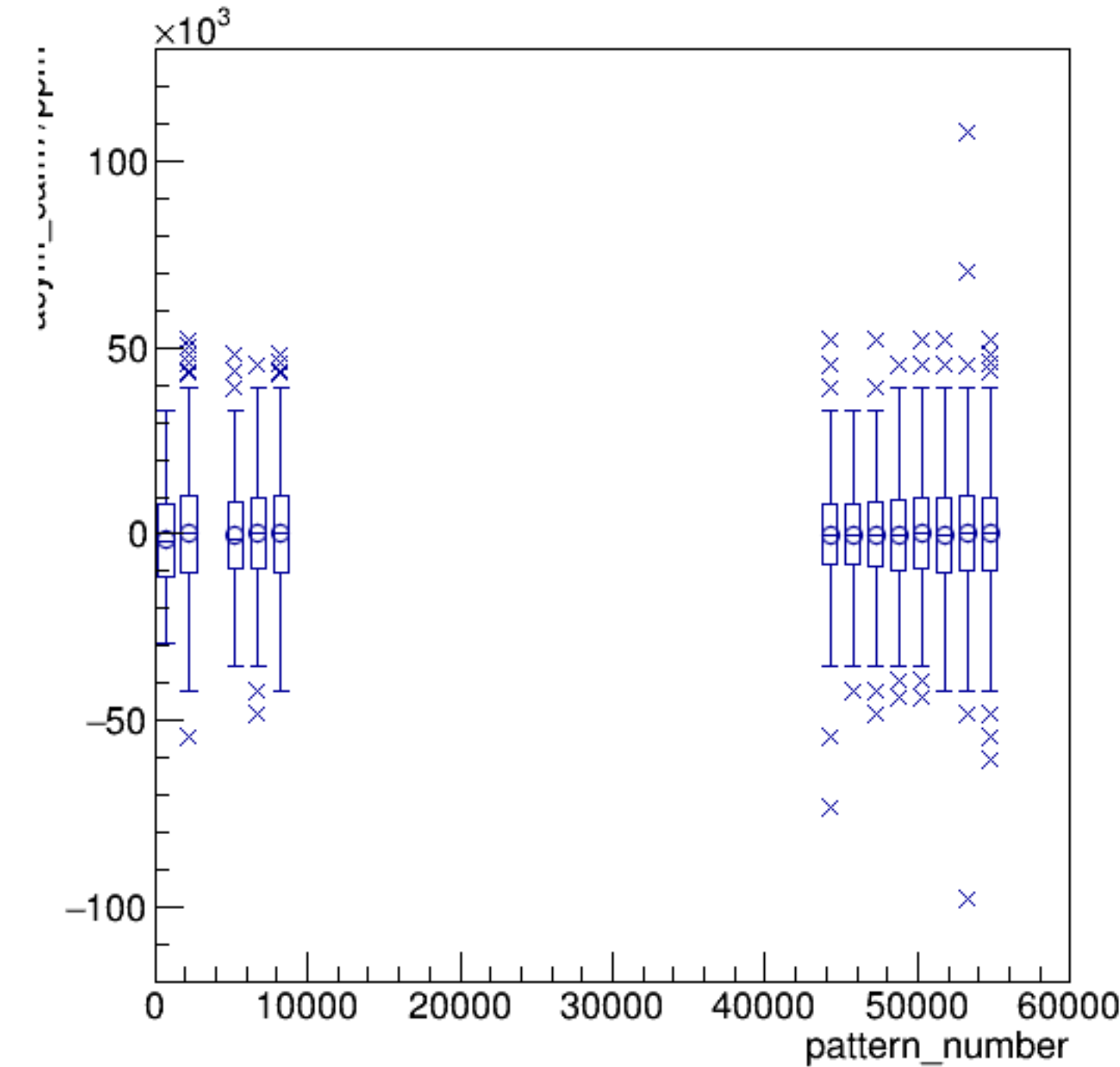


run3140_000_pairTree_sam7.png

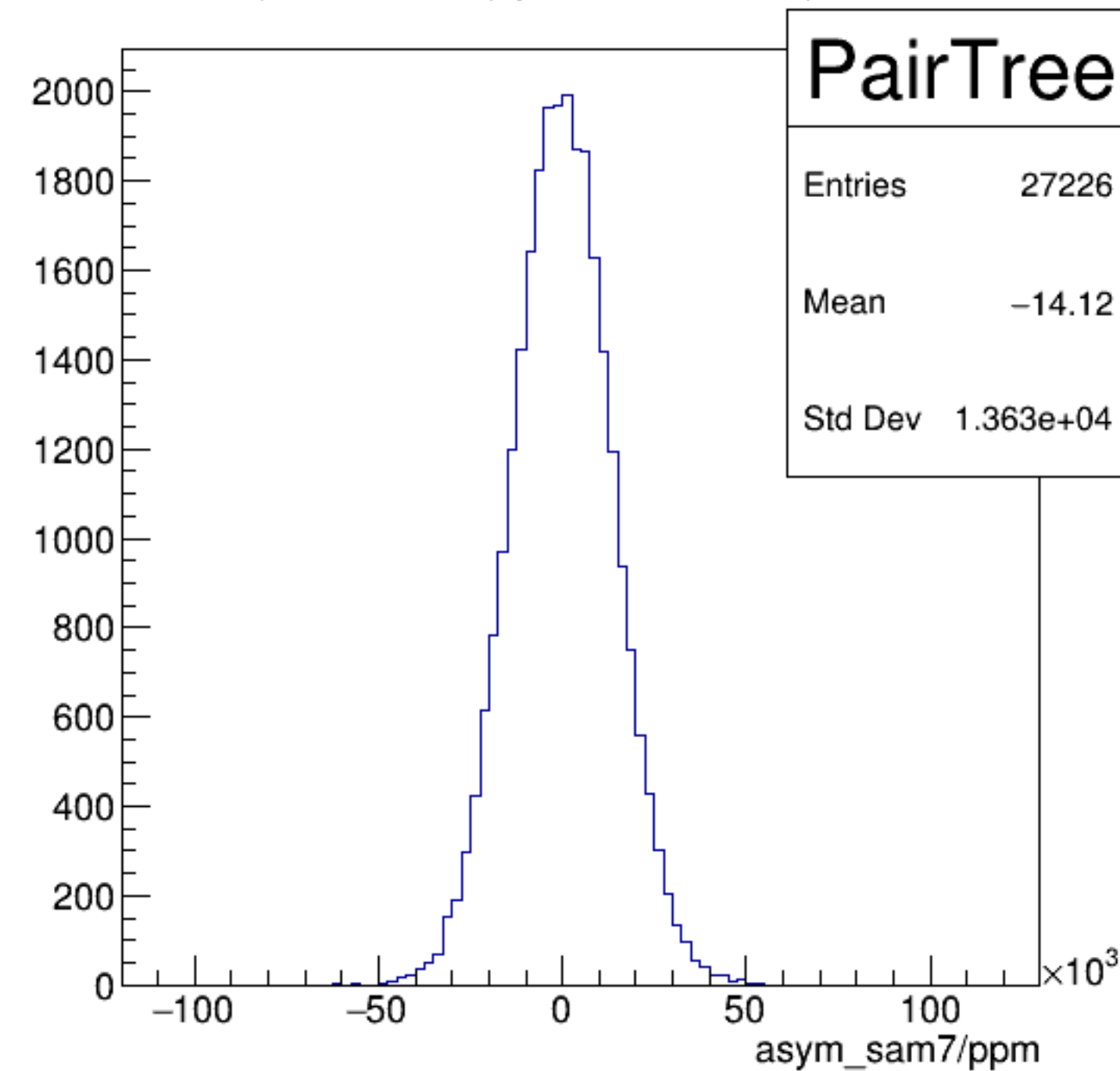
yield_sam7 {ErrorFlag==0}



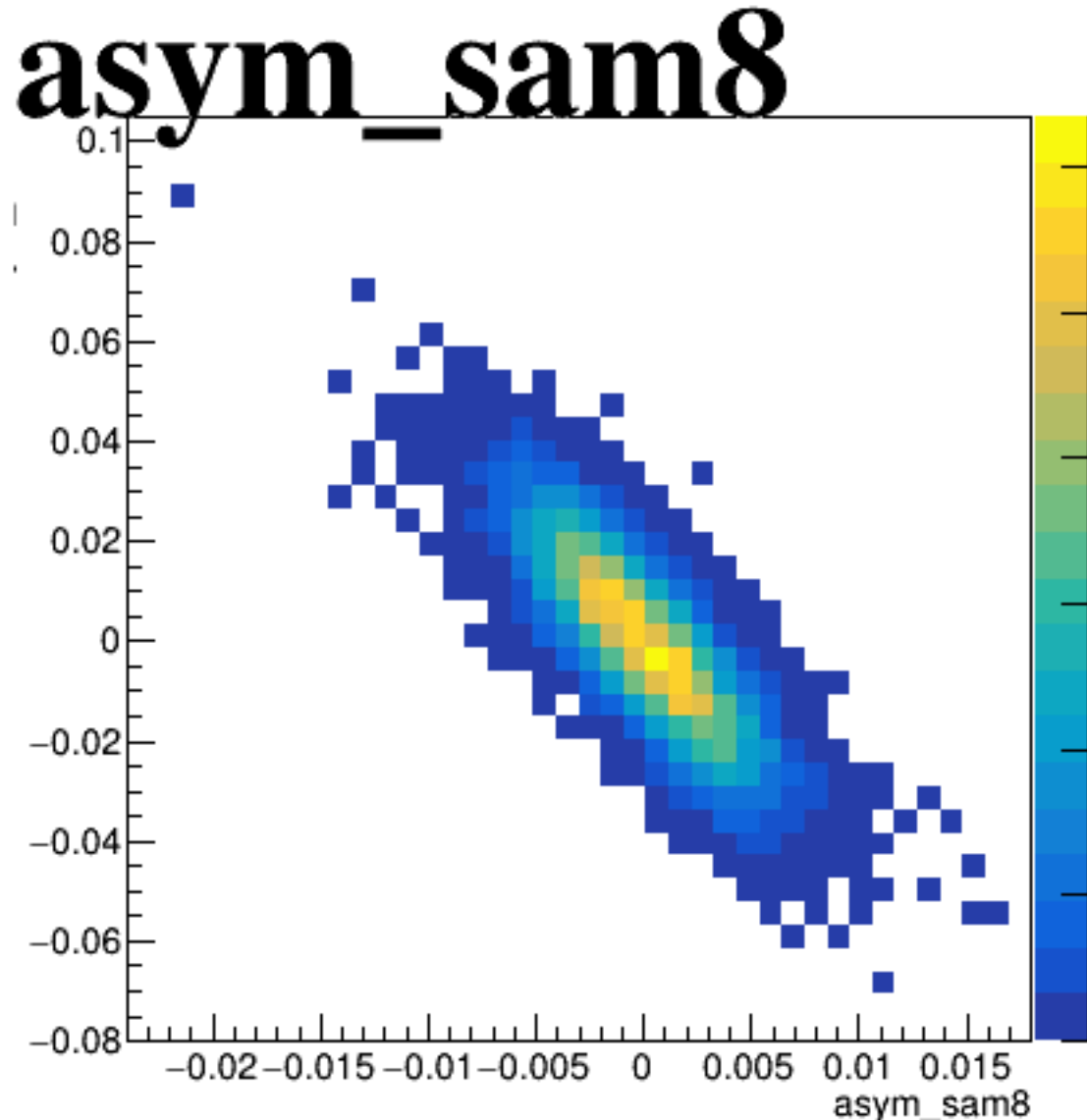
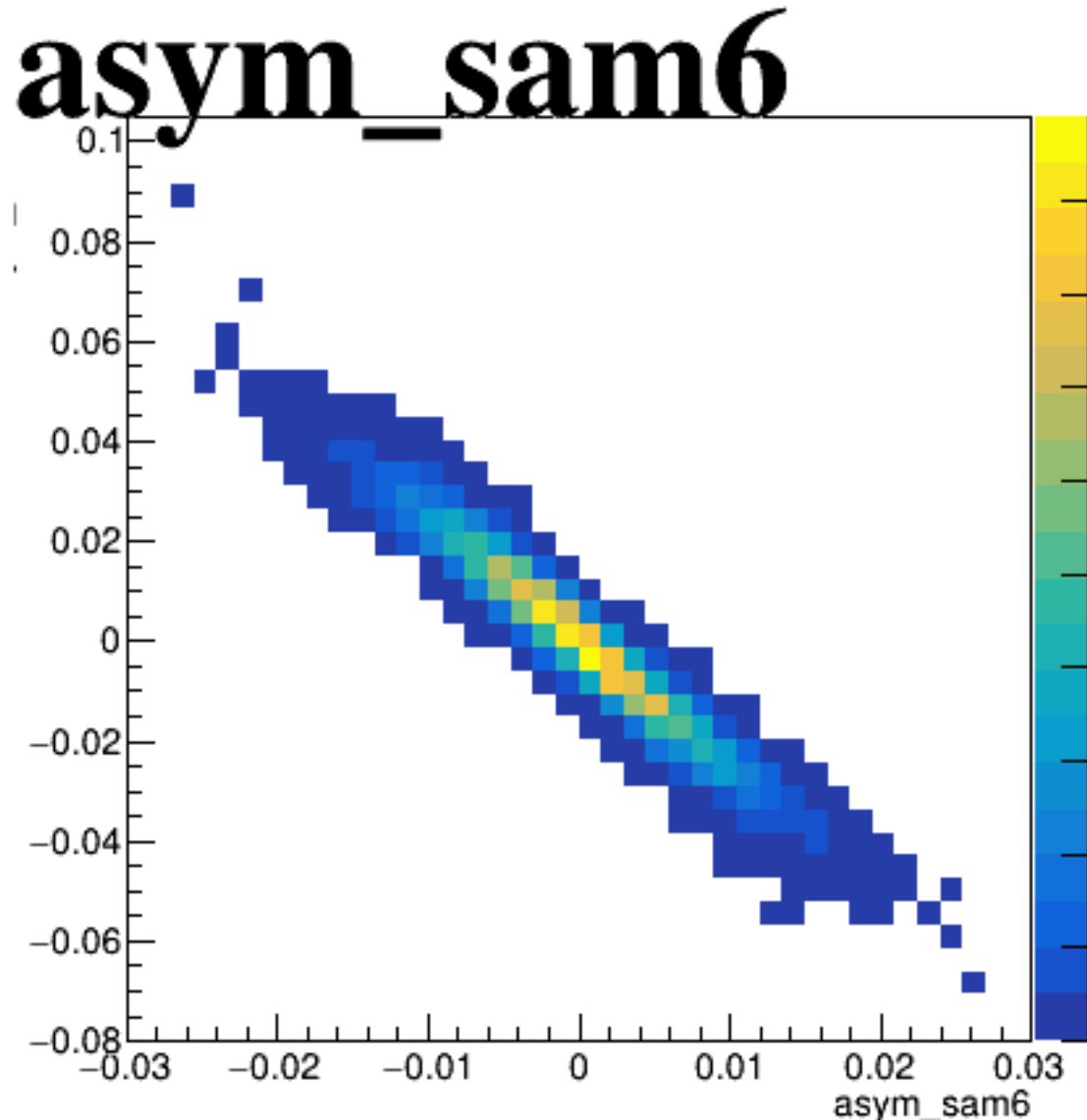
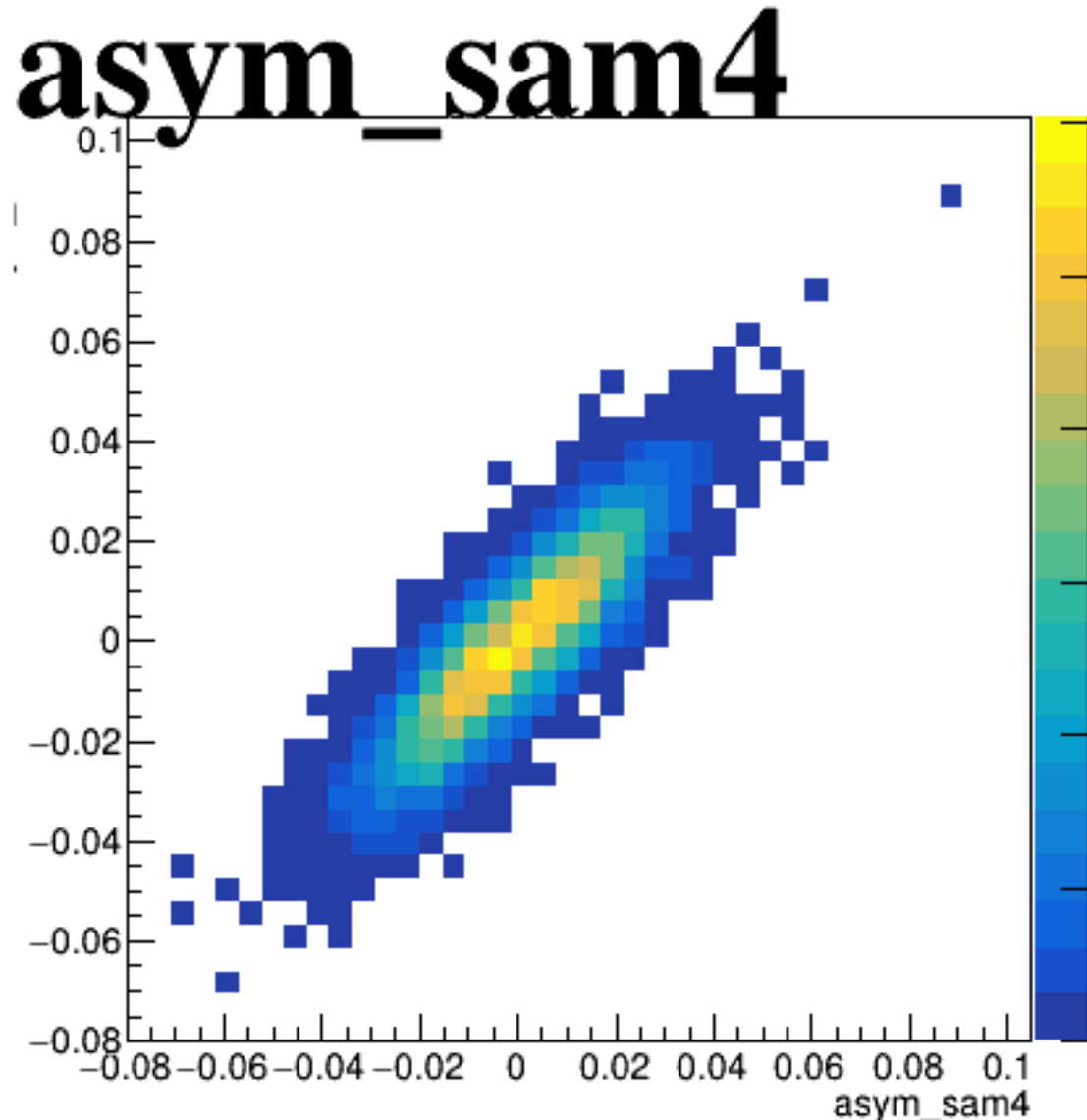
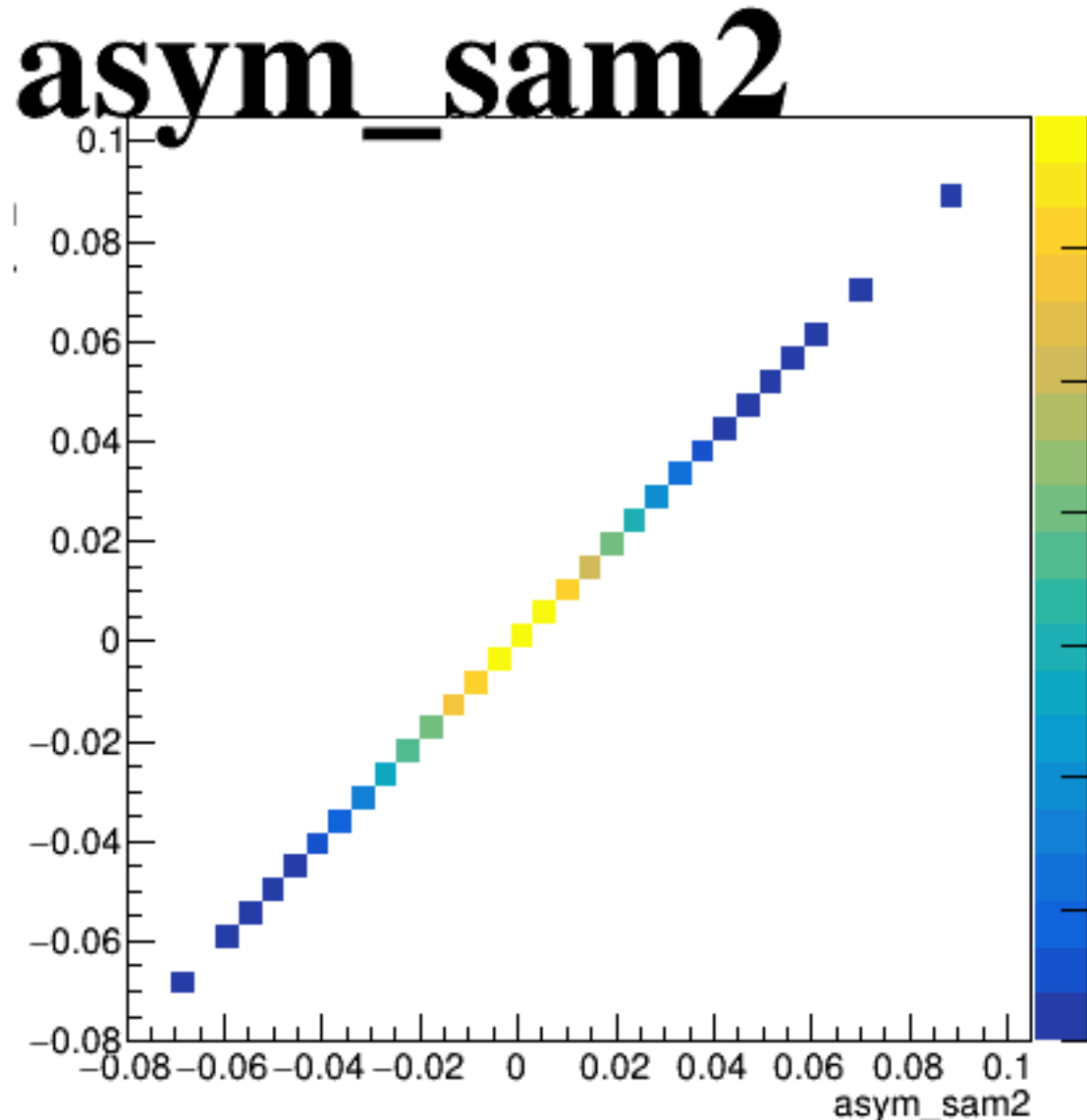
asym_sam7/ppm:pattern_number {ErrorFlag==0}



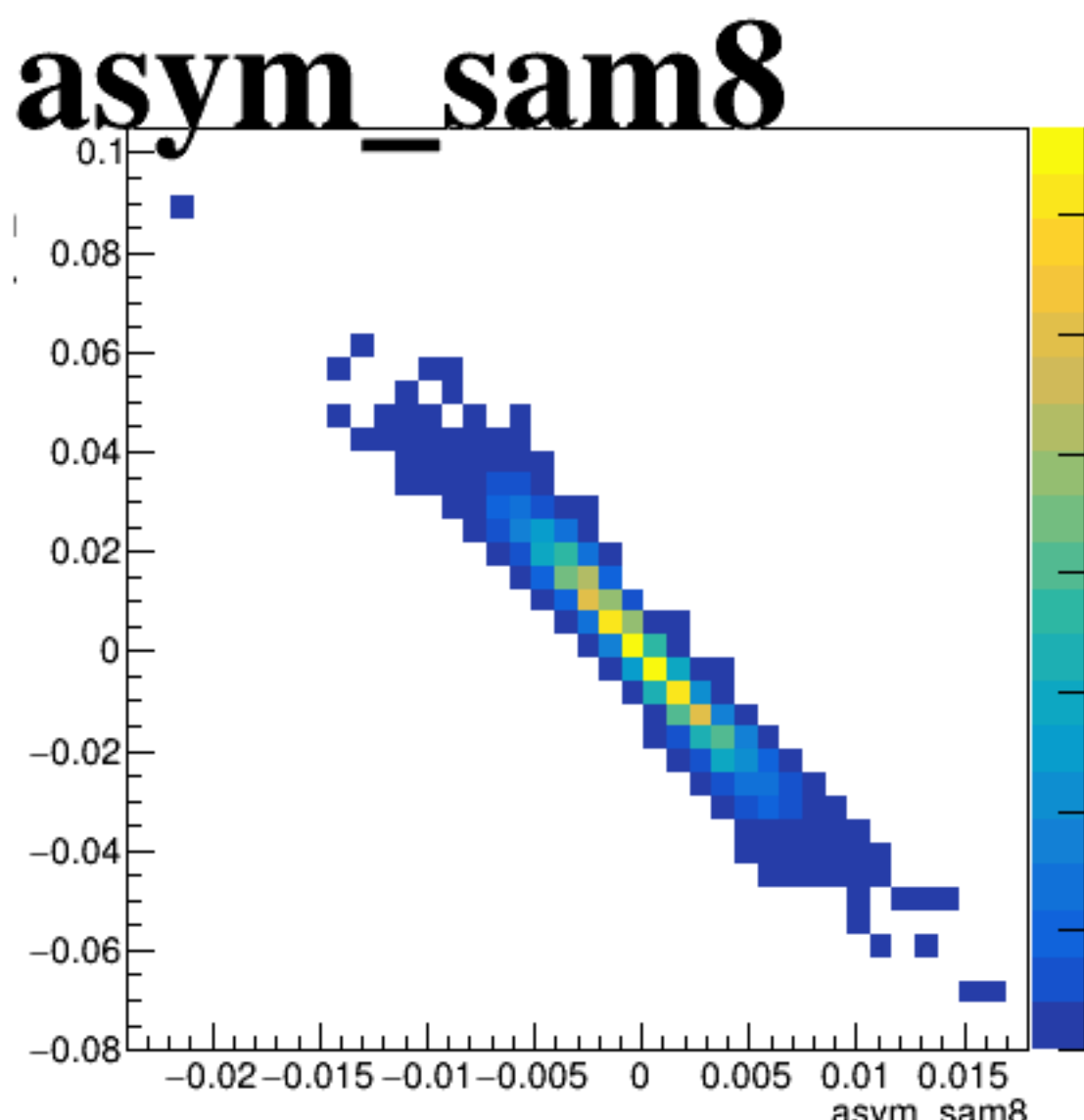
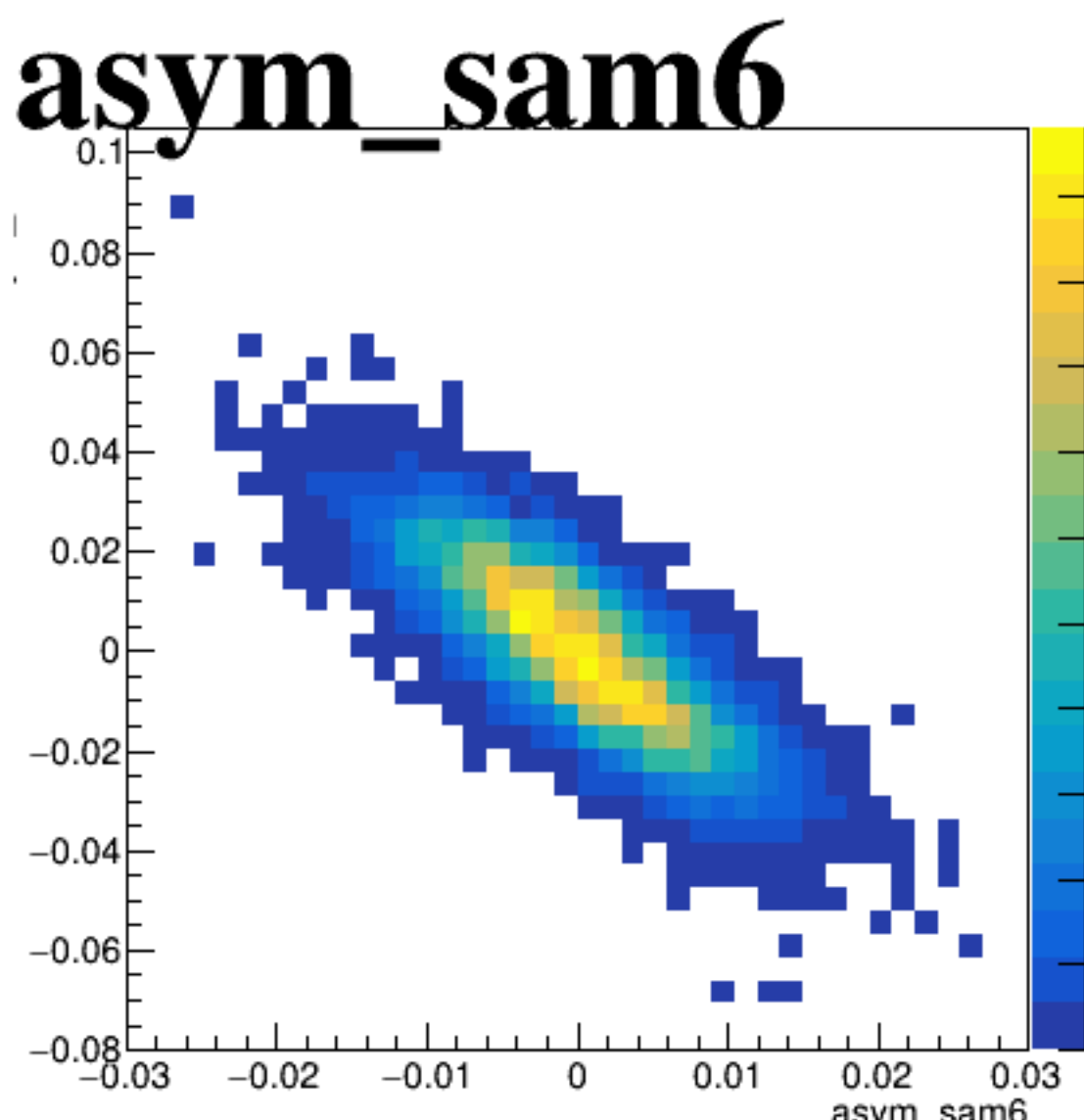
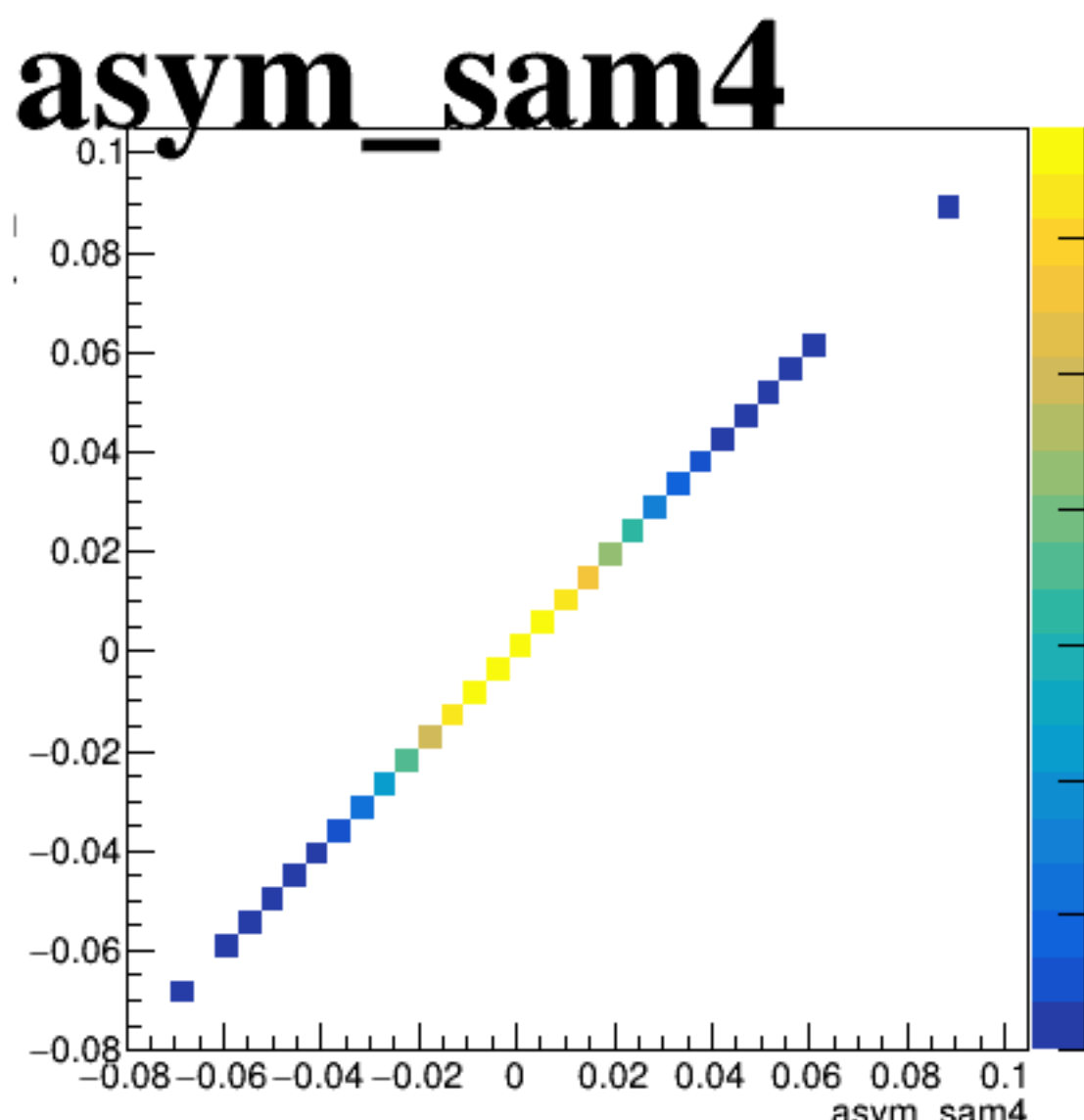
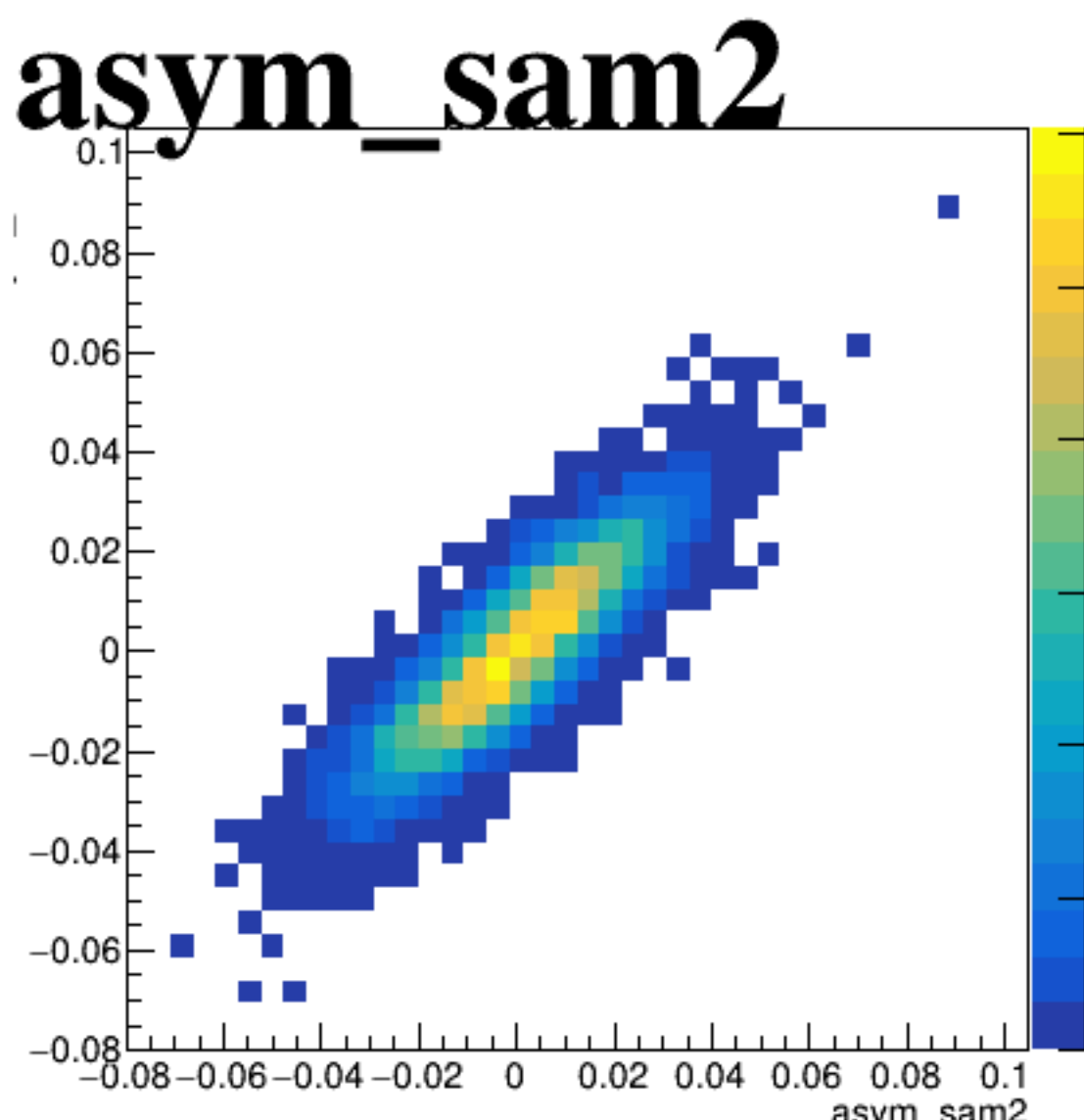
asym_sam7/ppm {ErrorFlag==0}



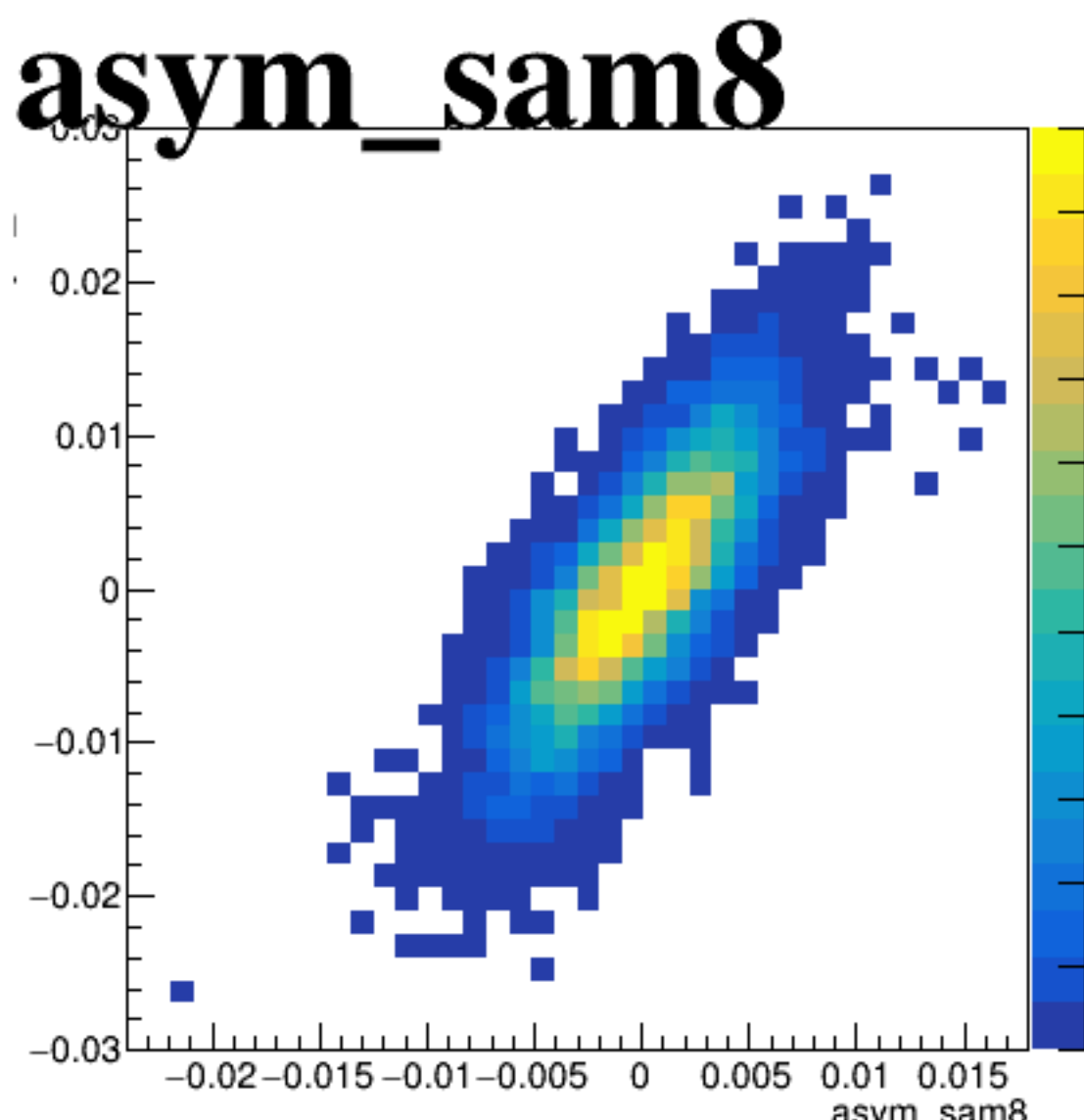
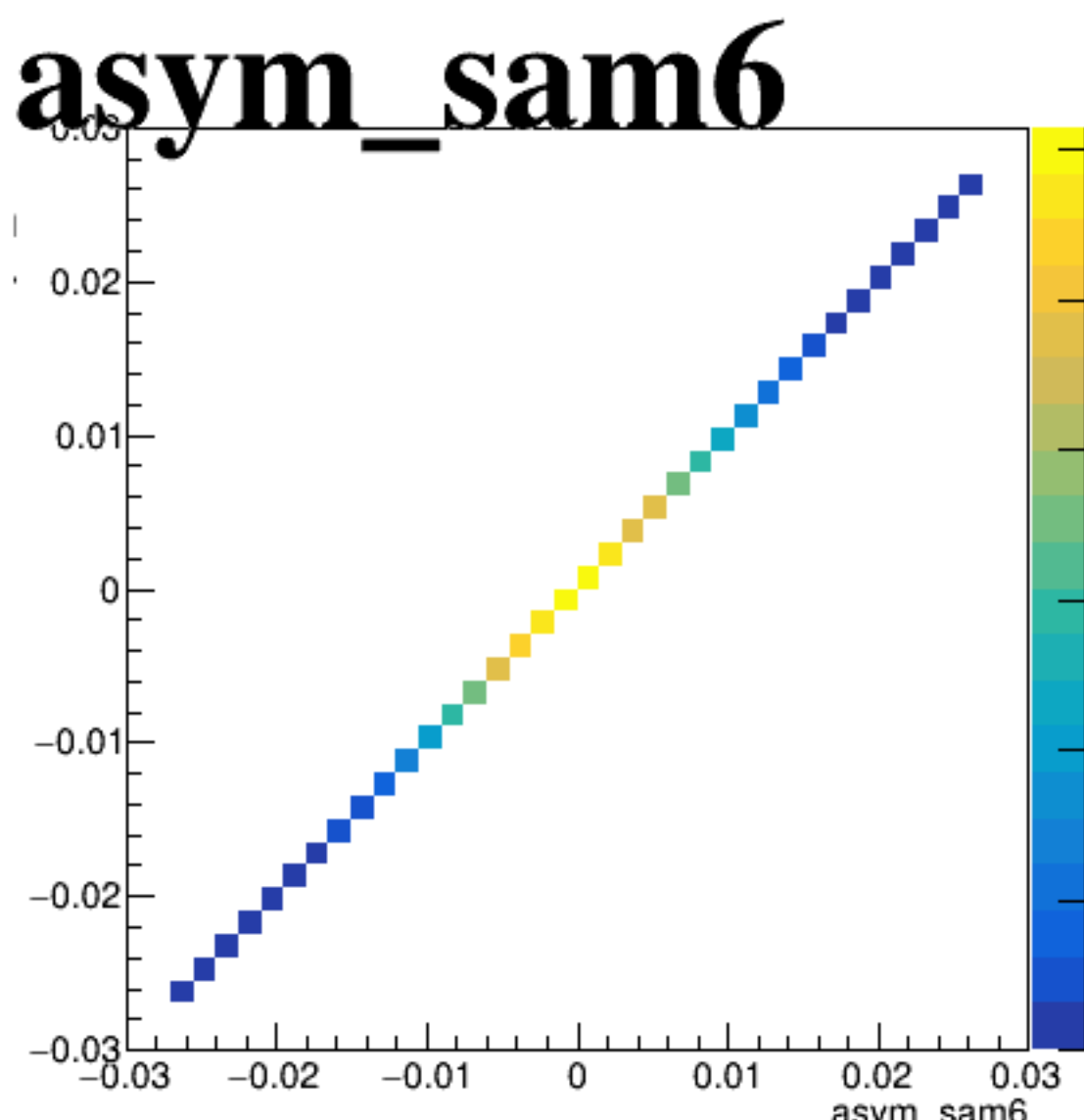
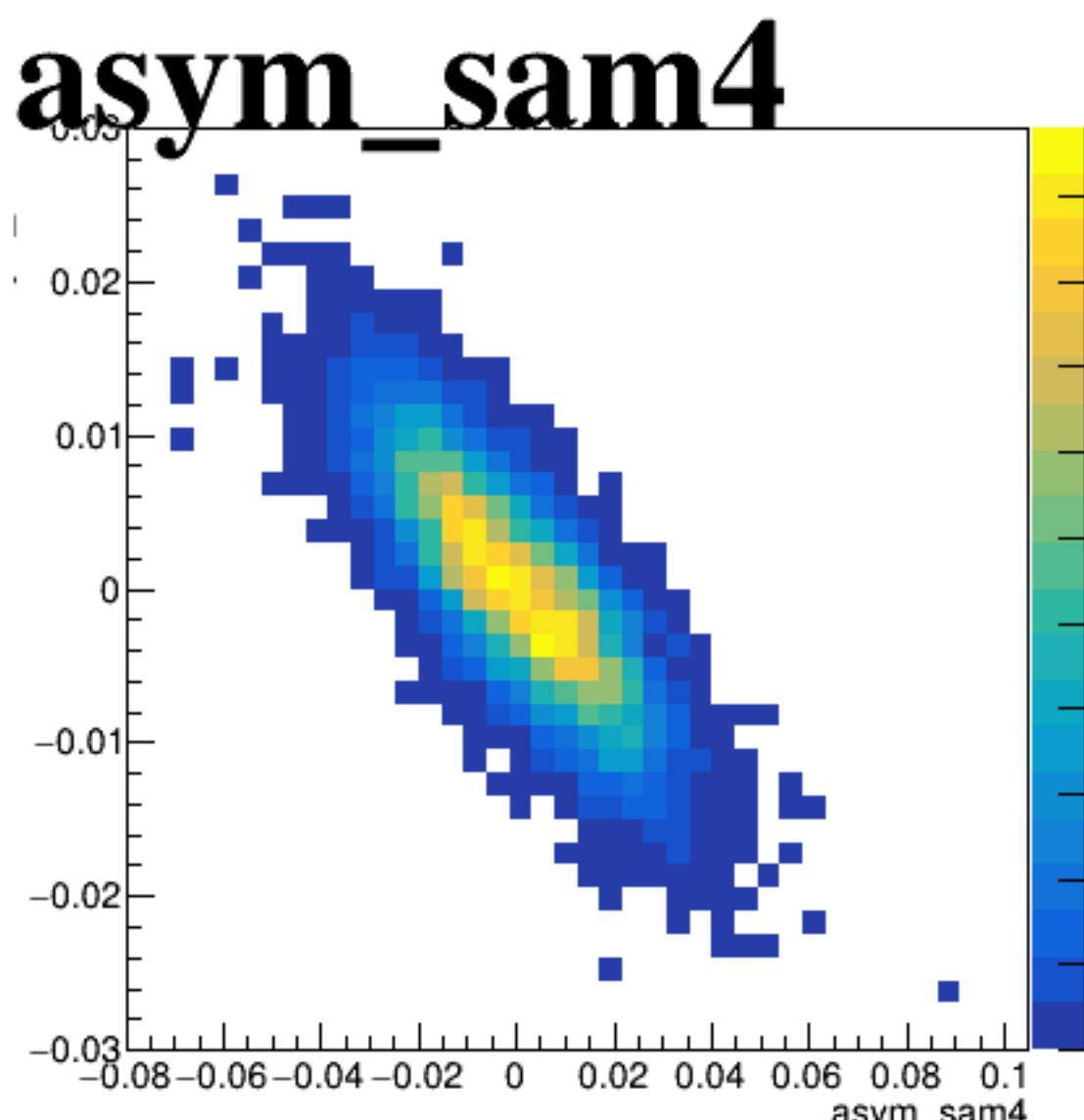
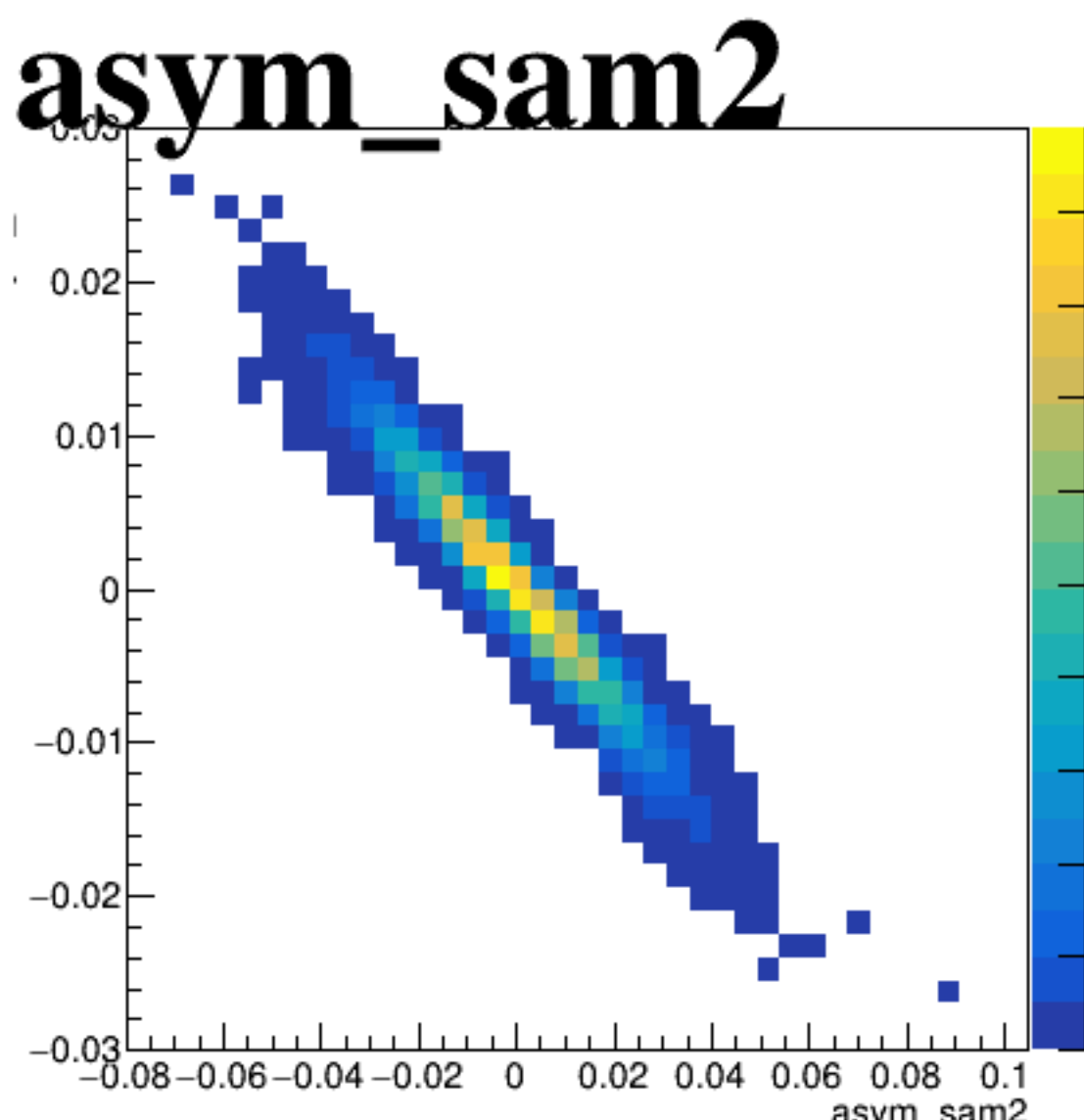
asym_sam2



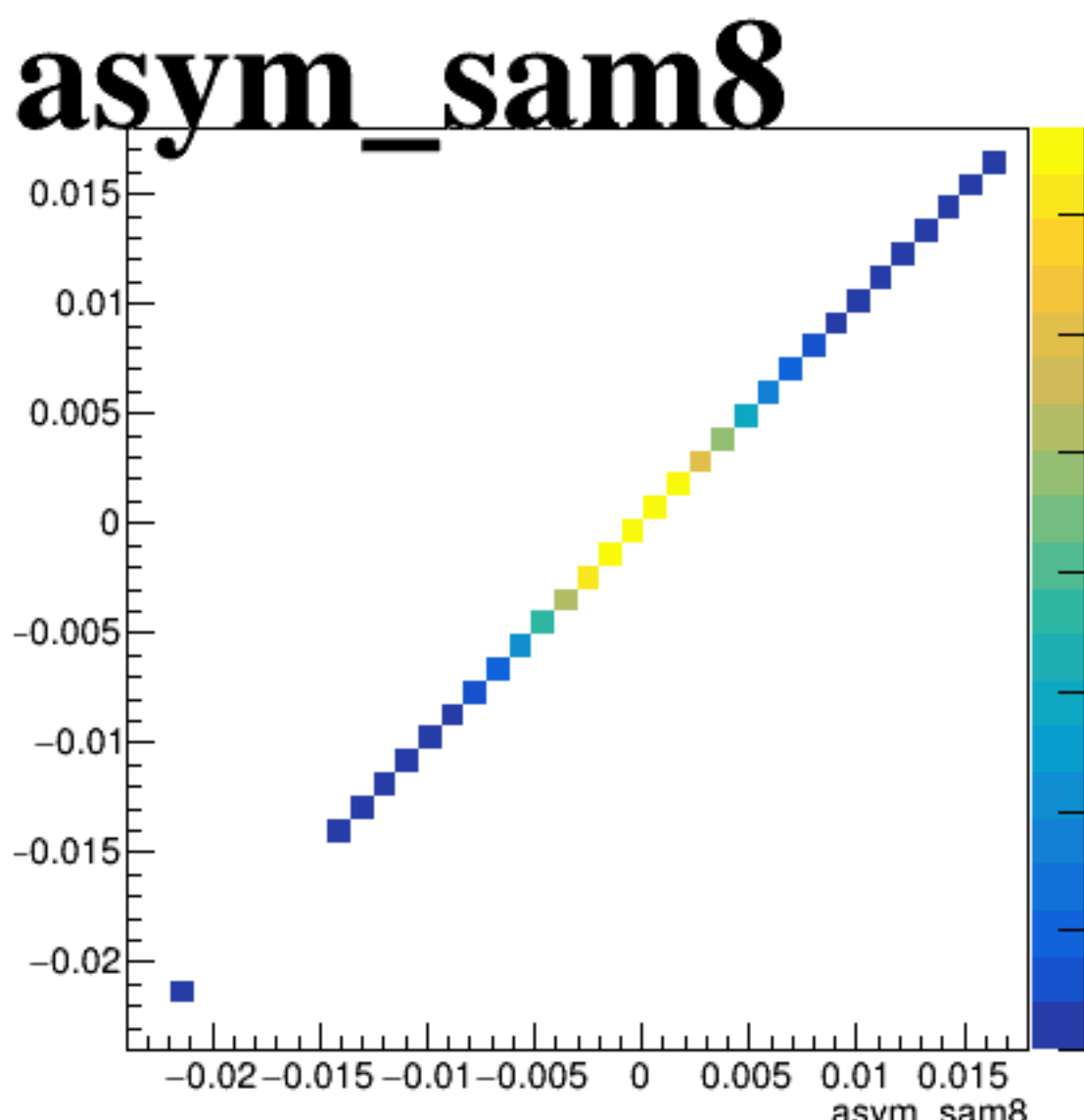
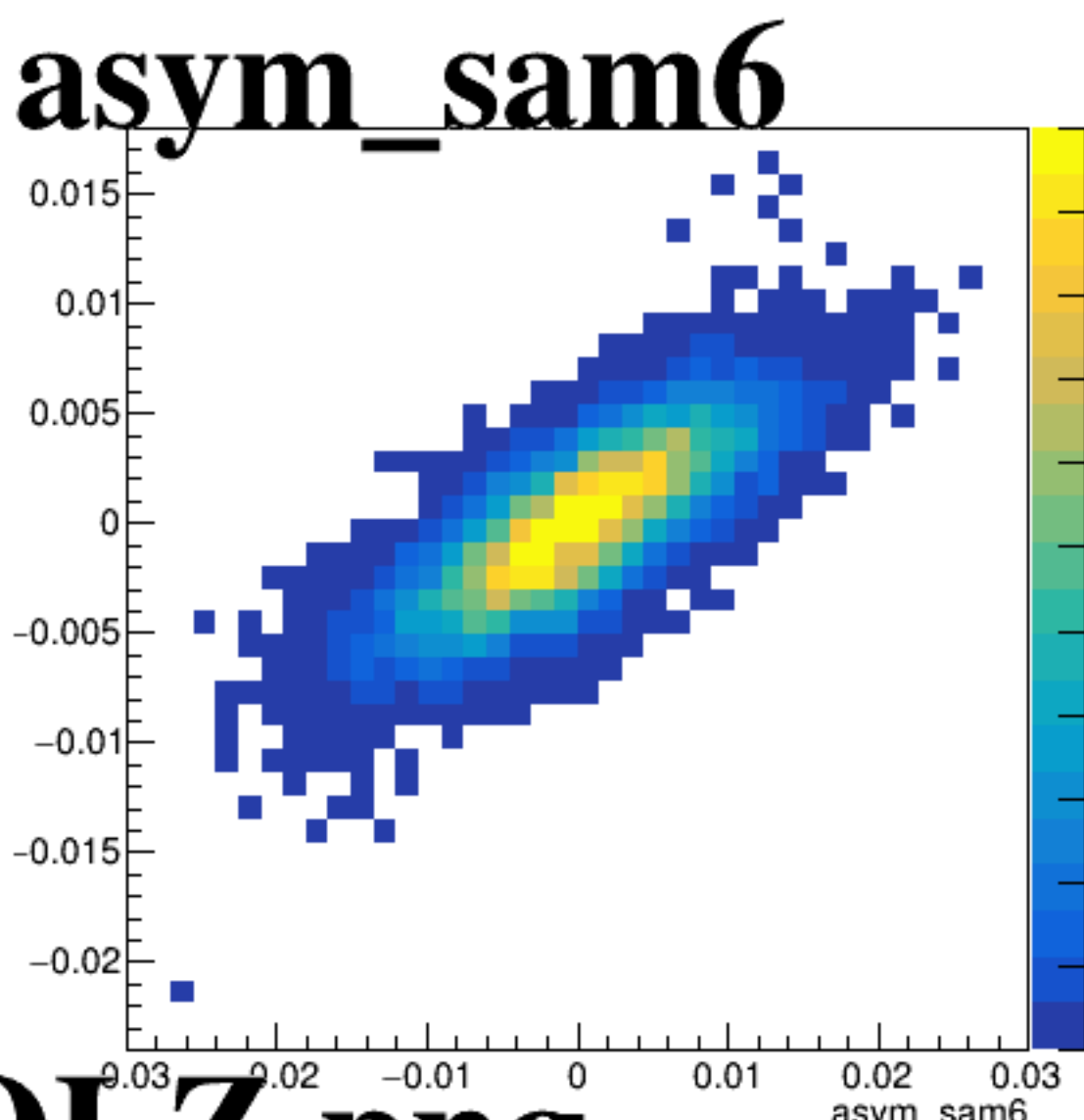
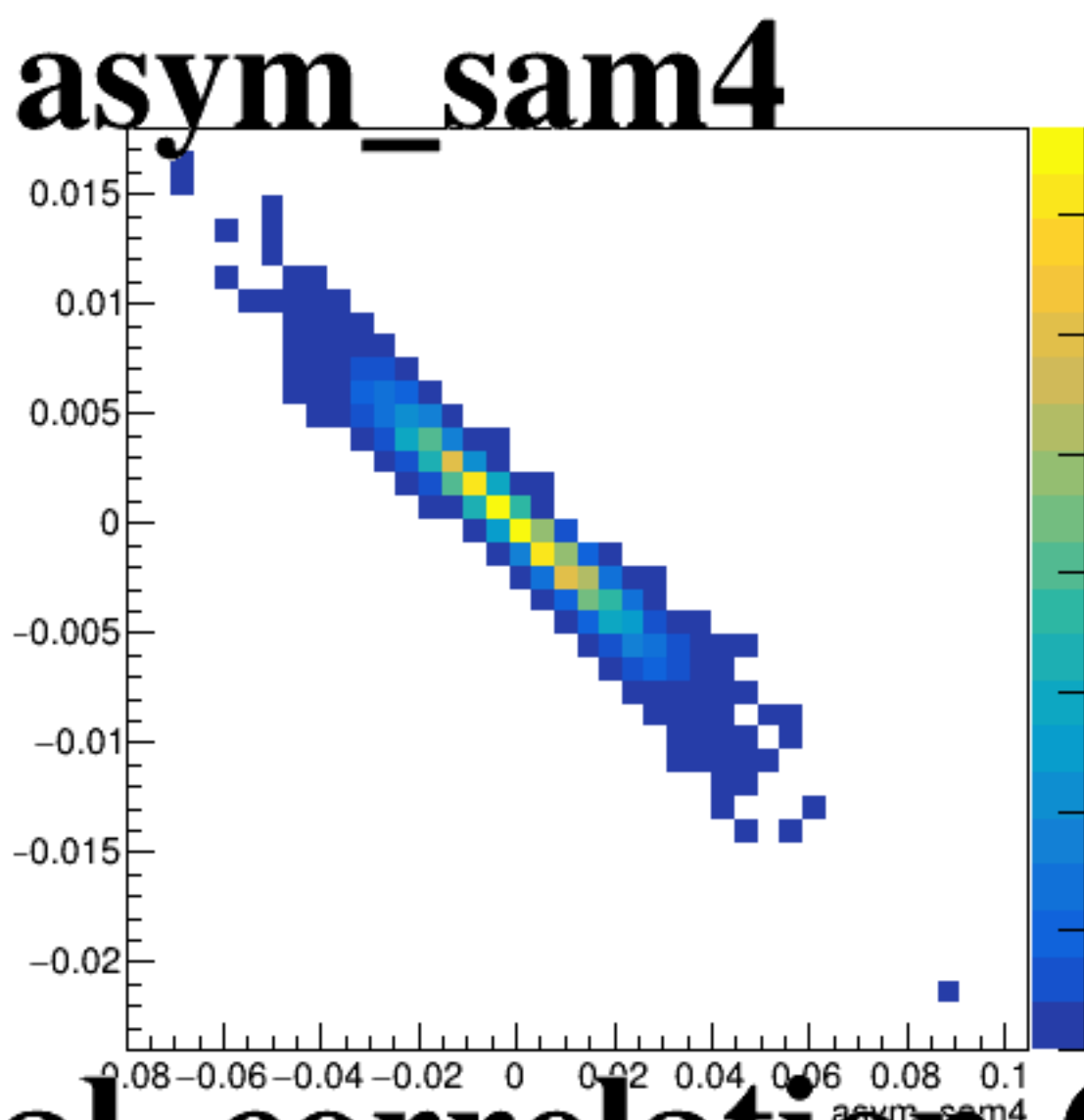
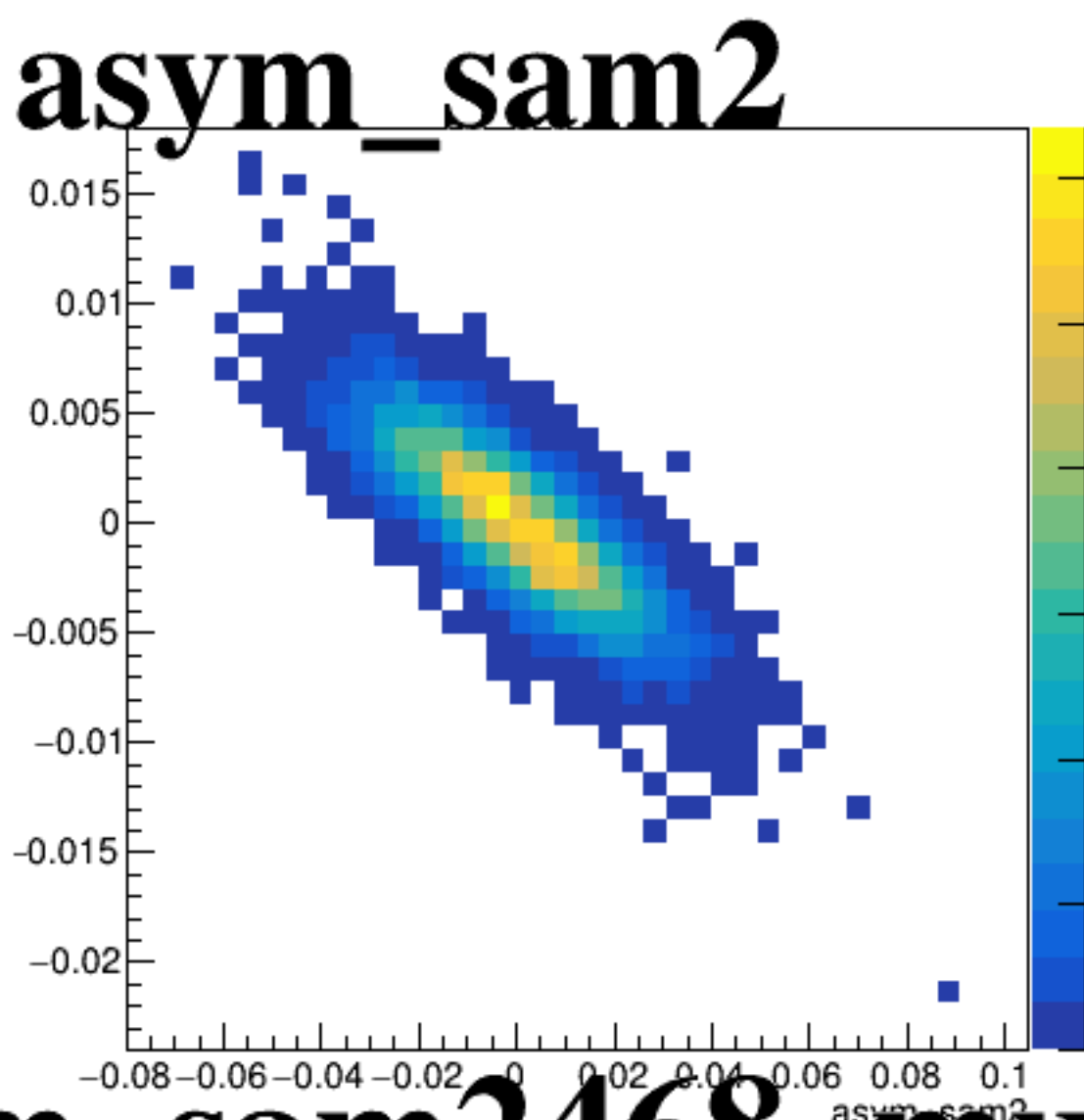
asym_sam4



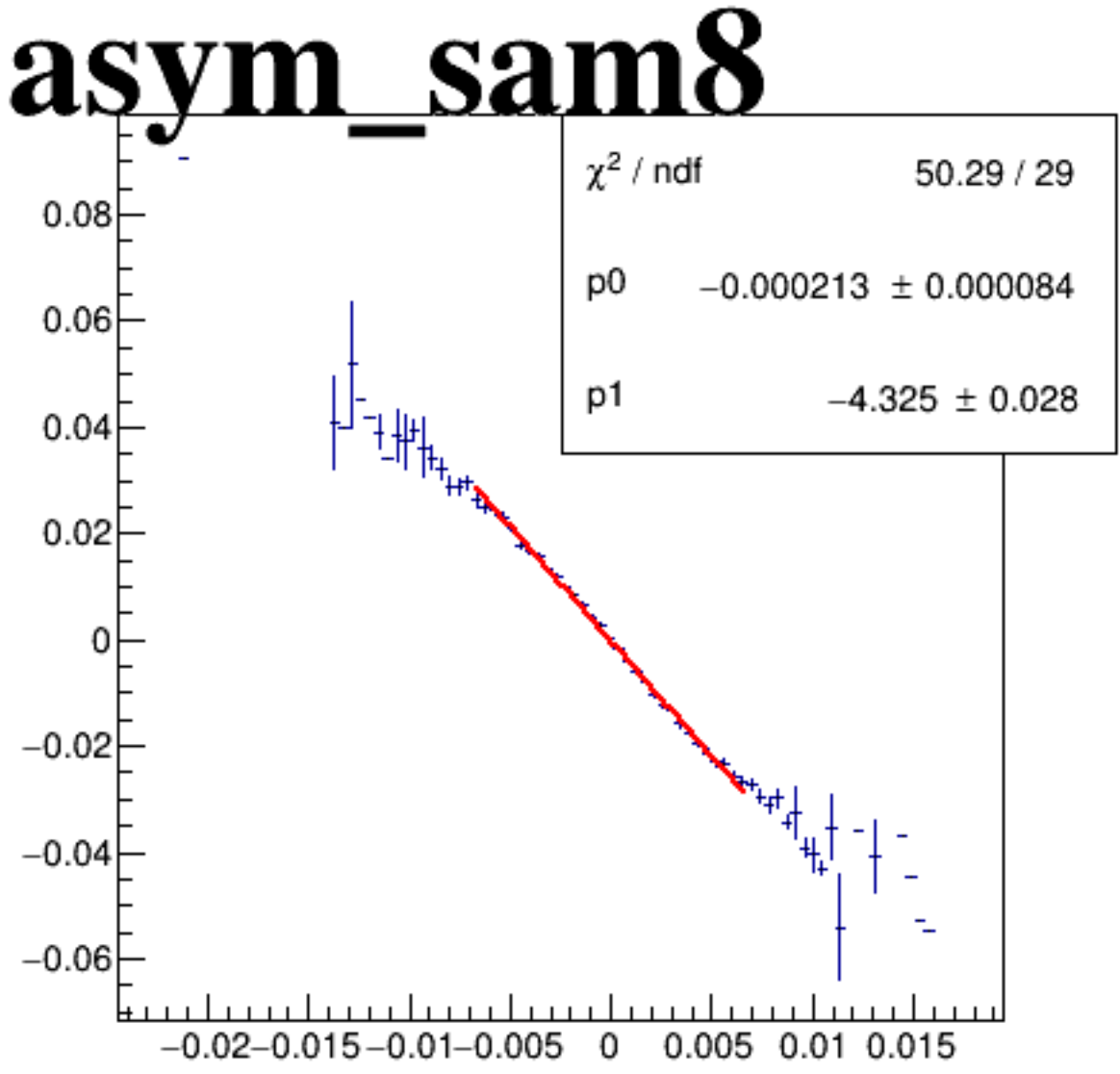
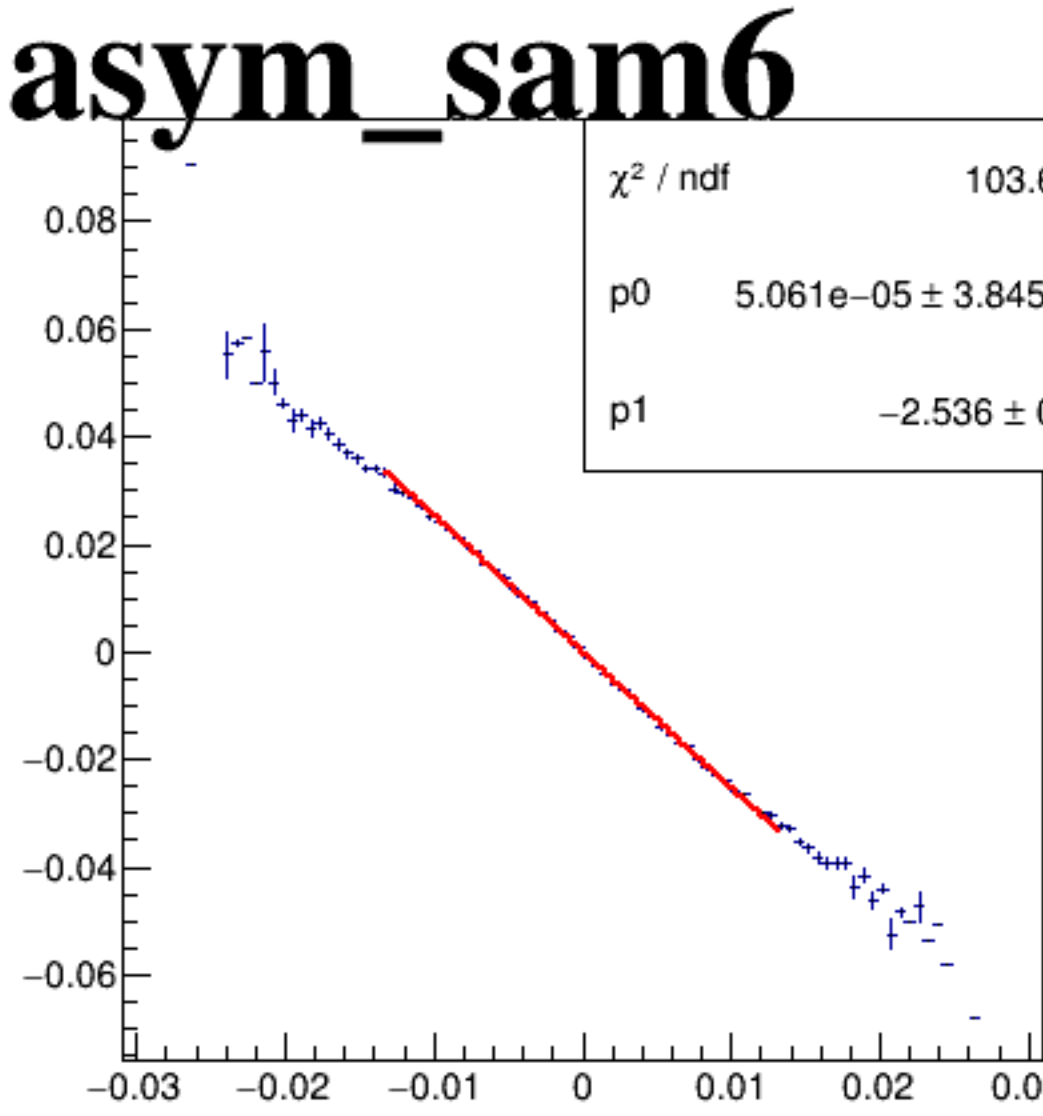
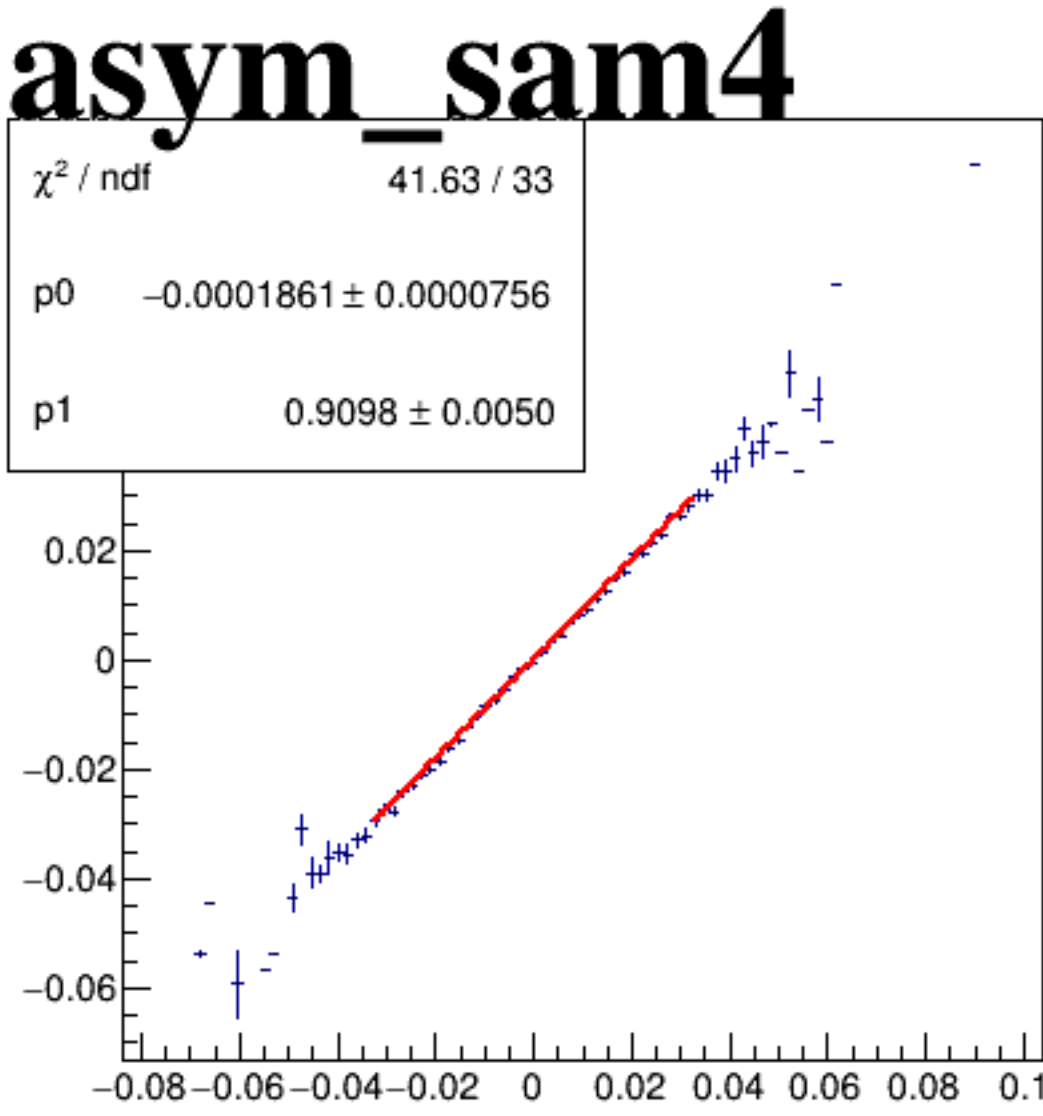
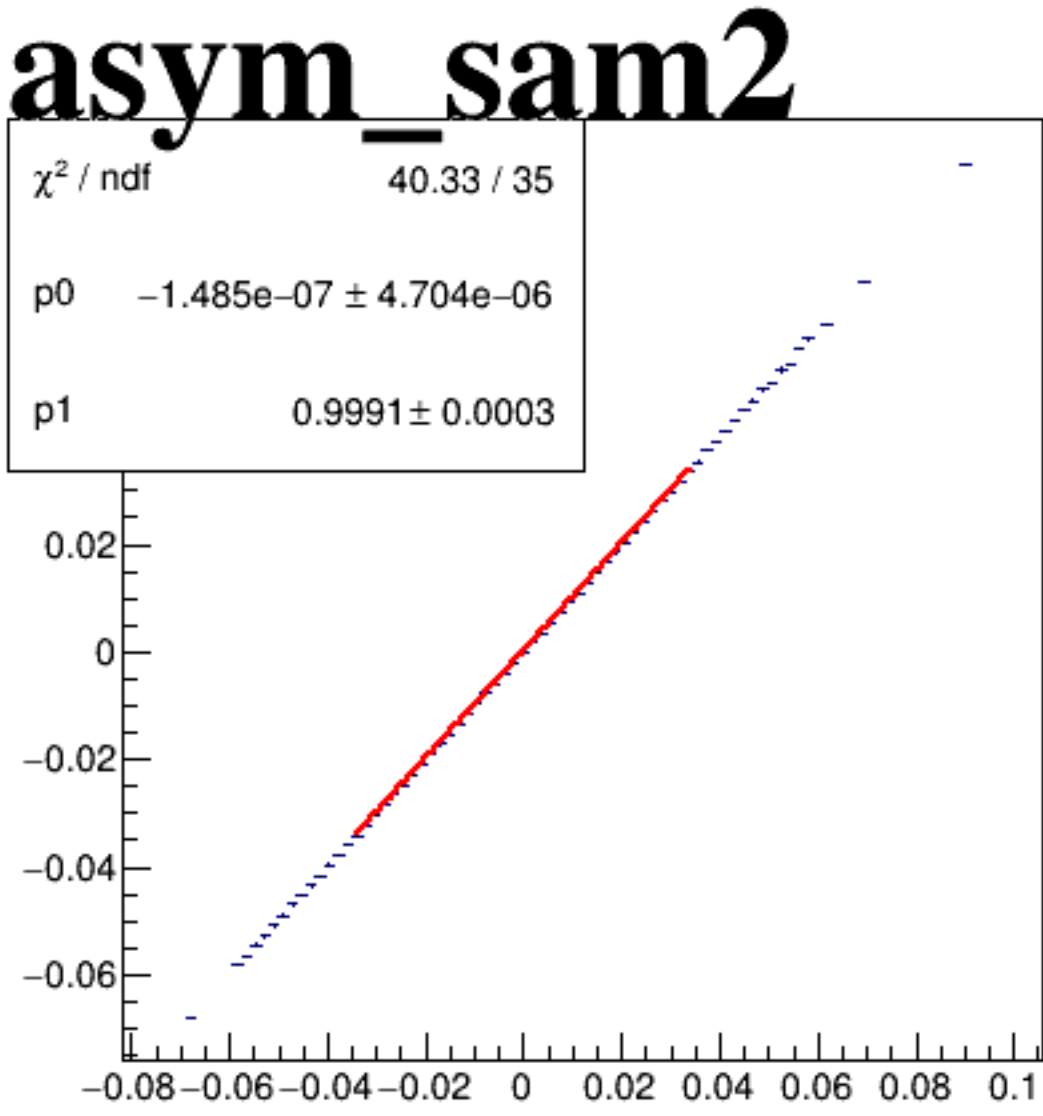
asym_sam6



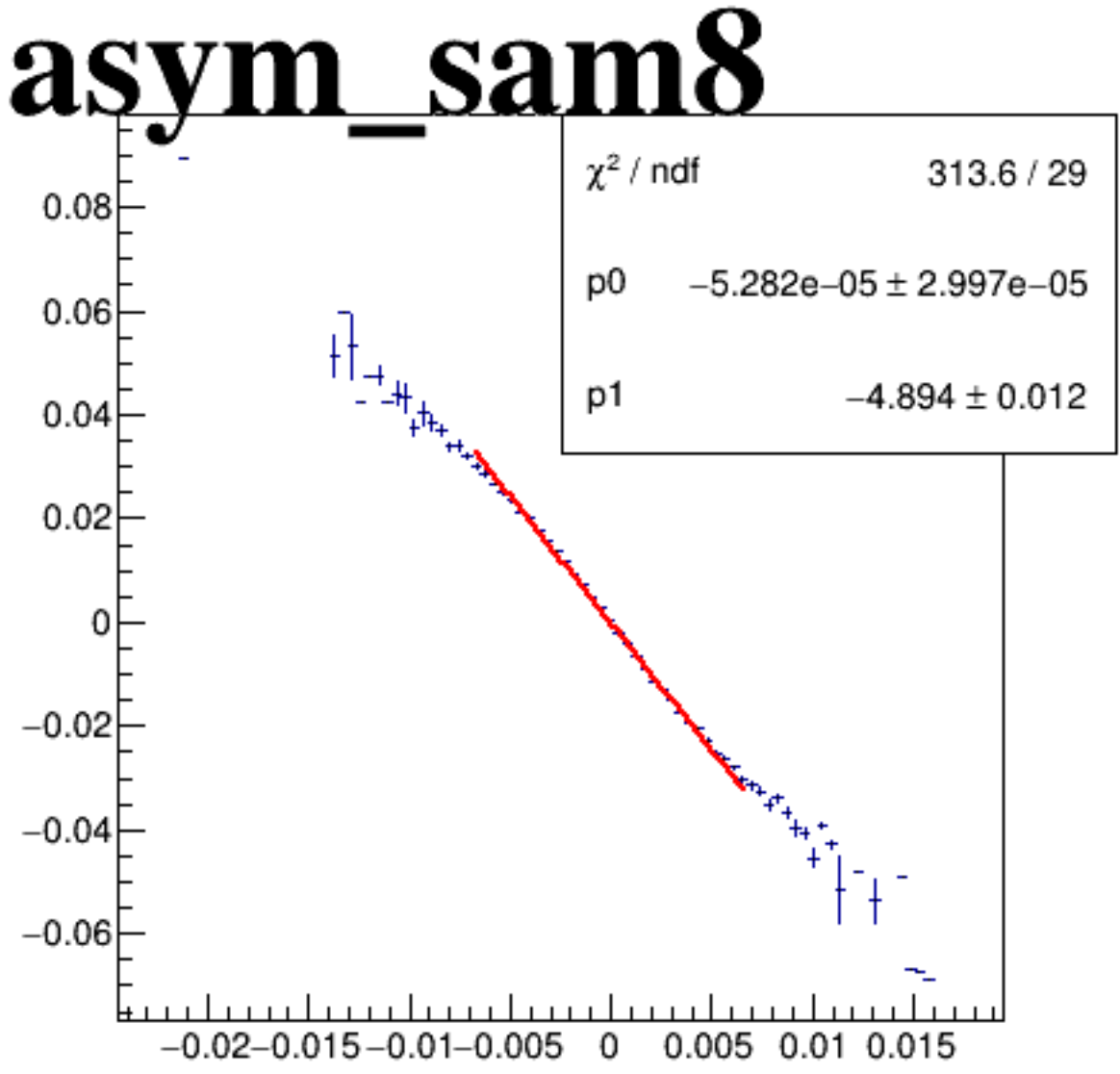
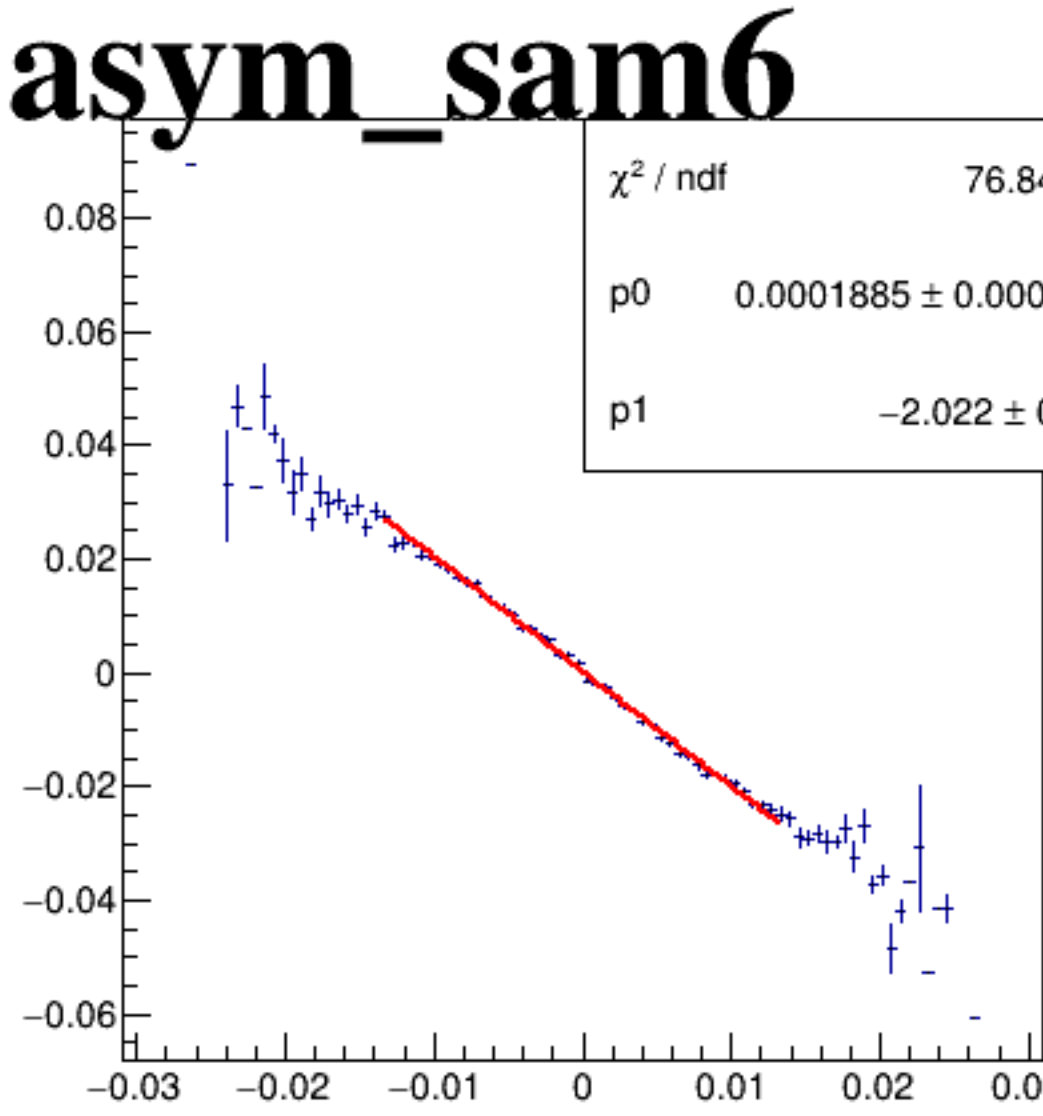
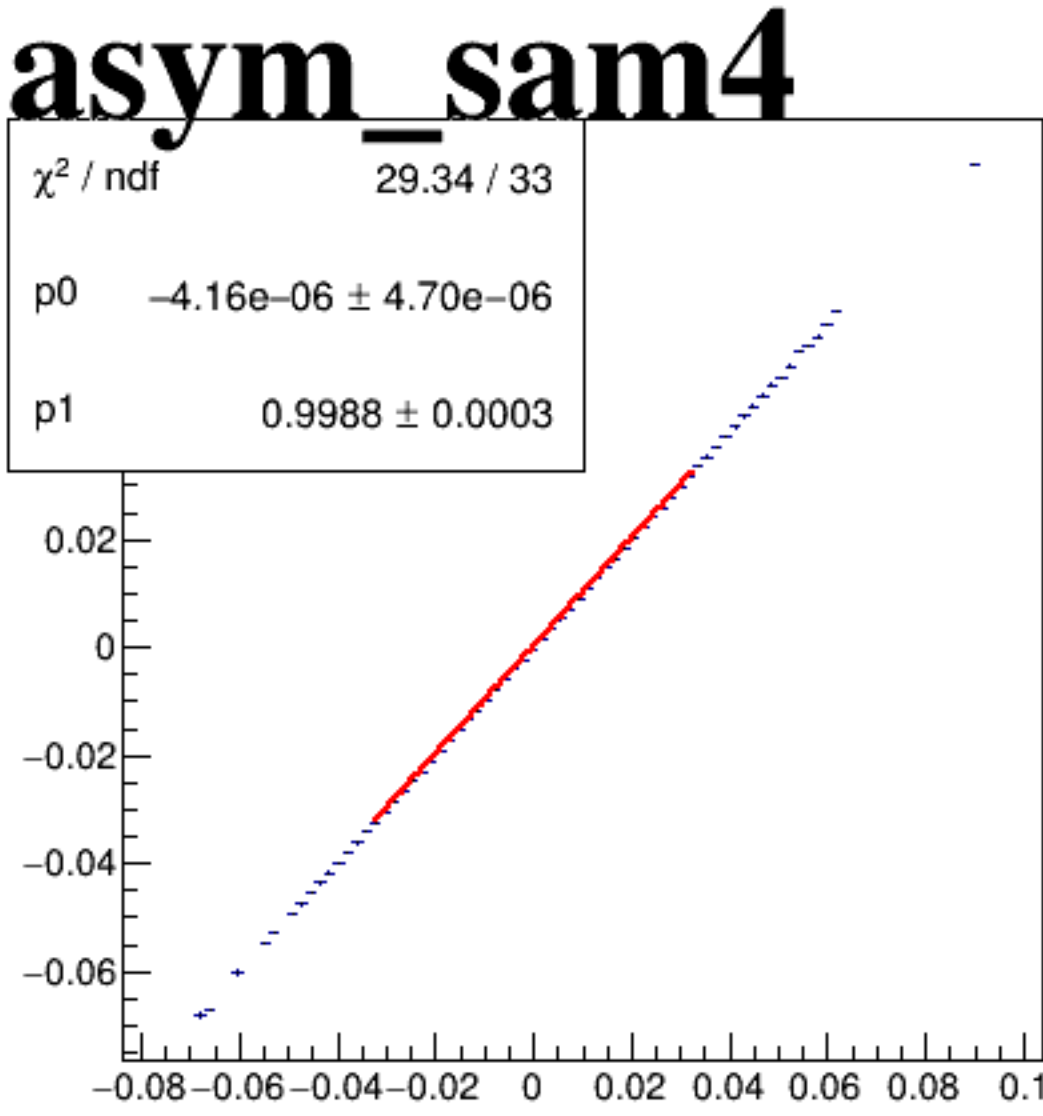
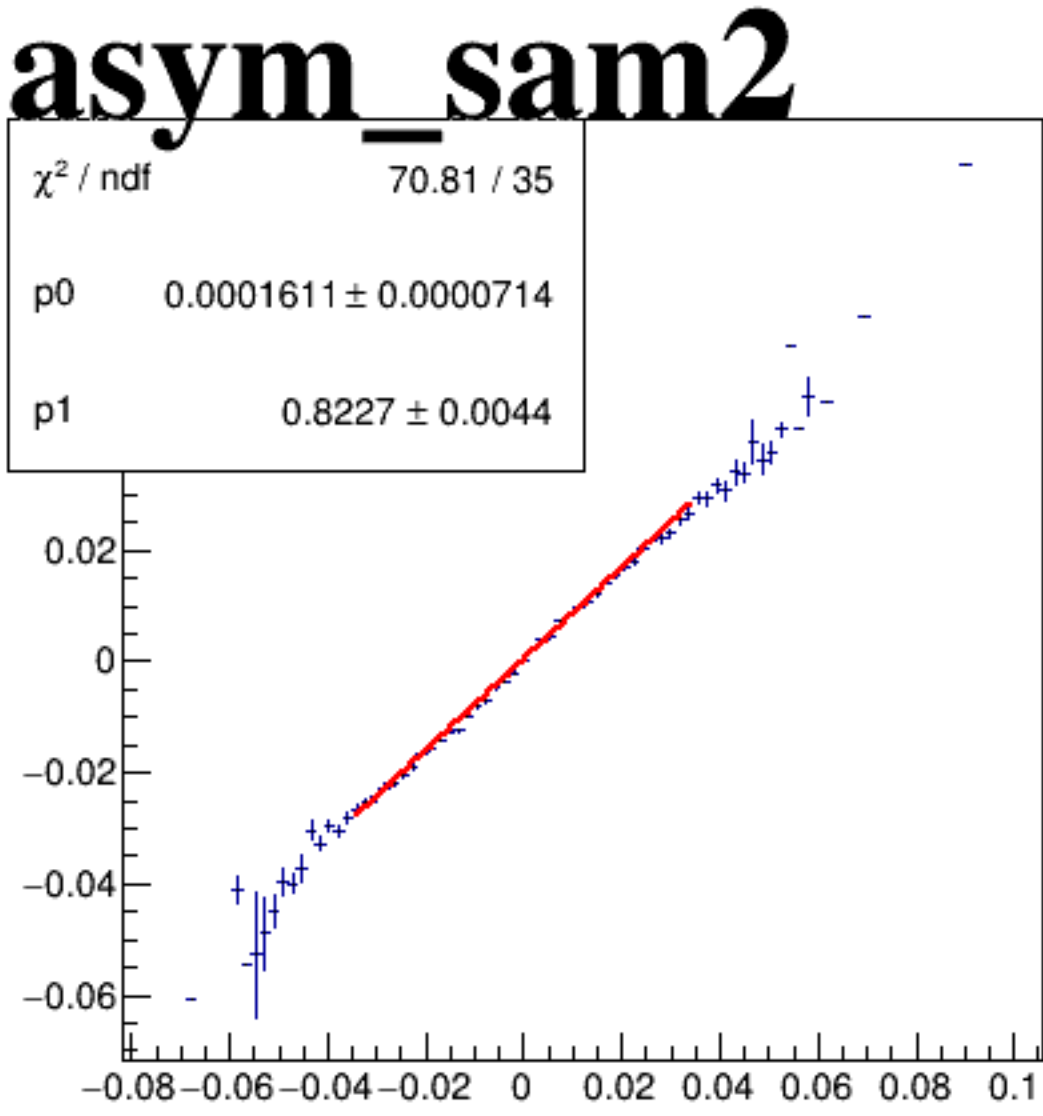
asym_sam8



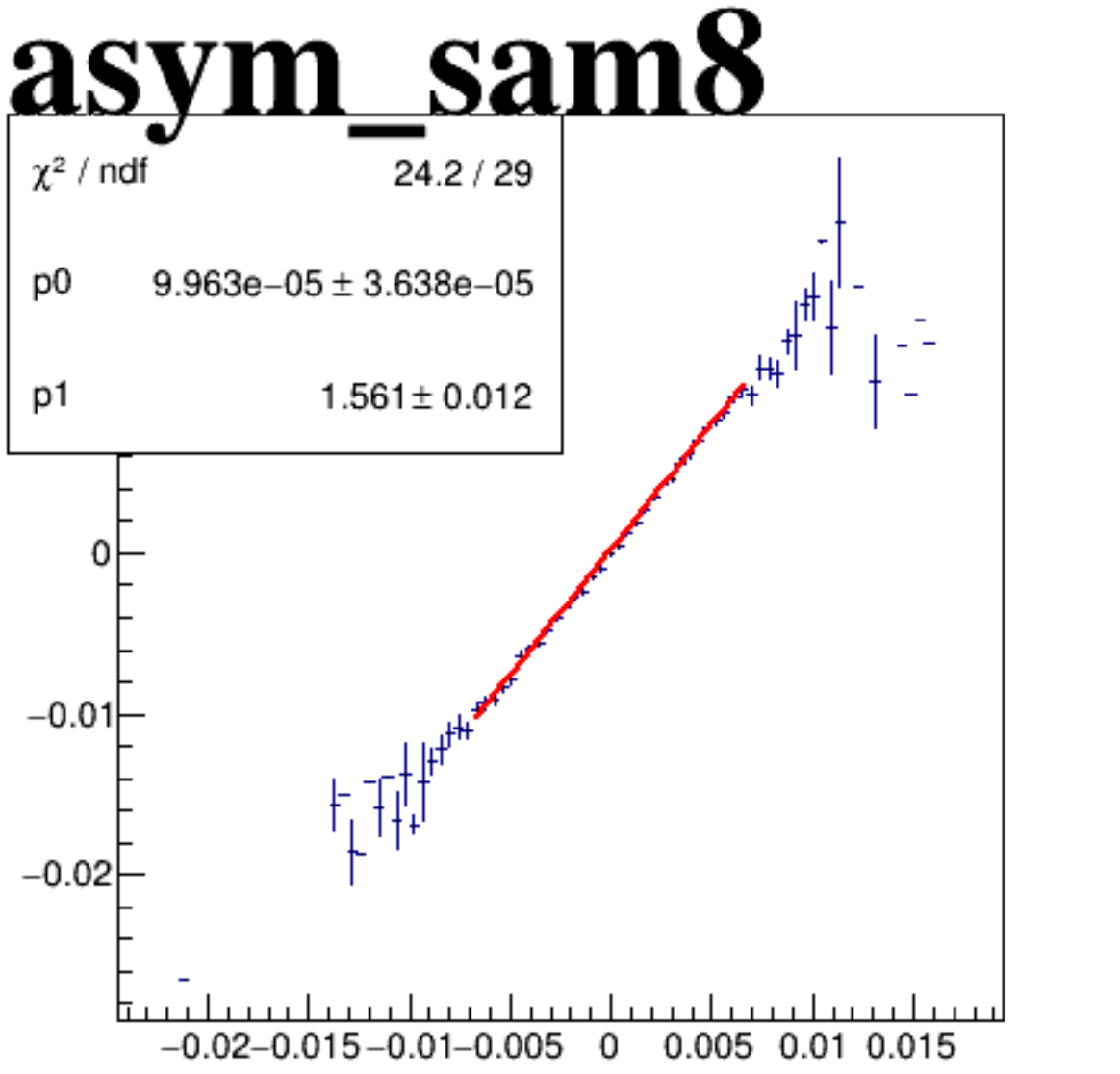
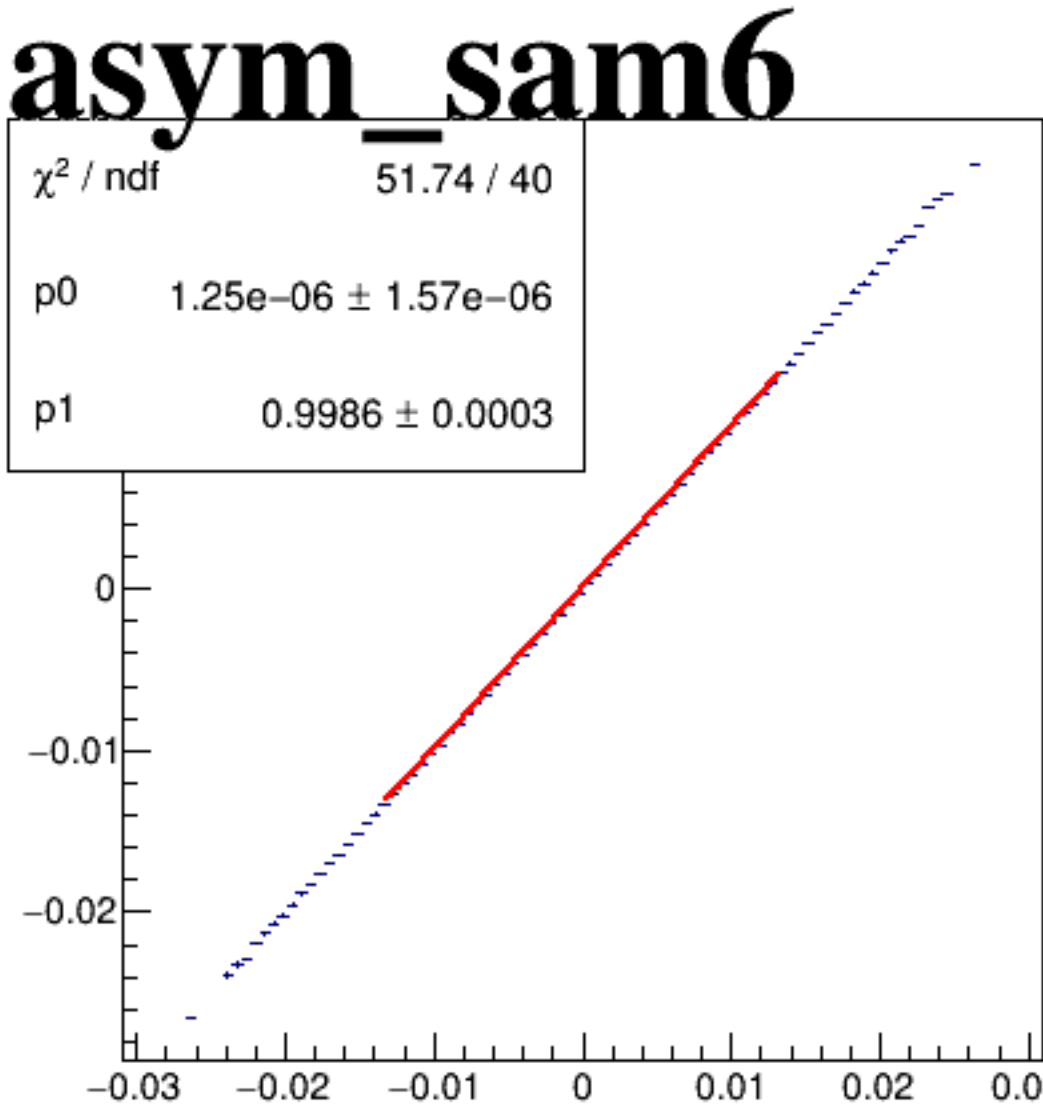
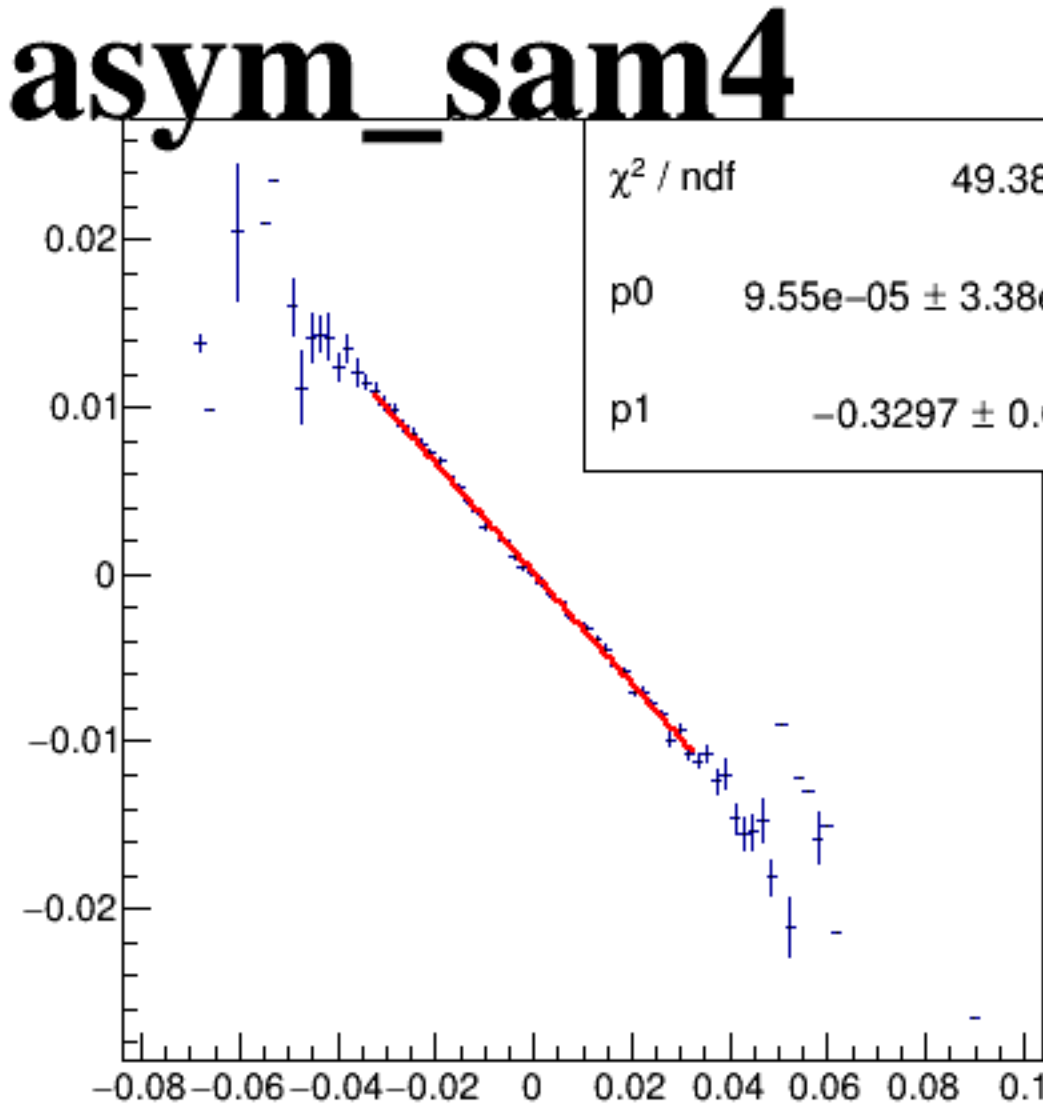
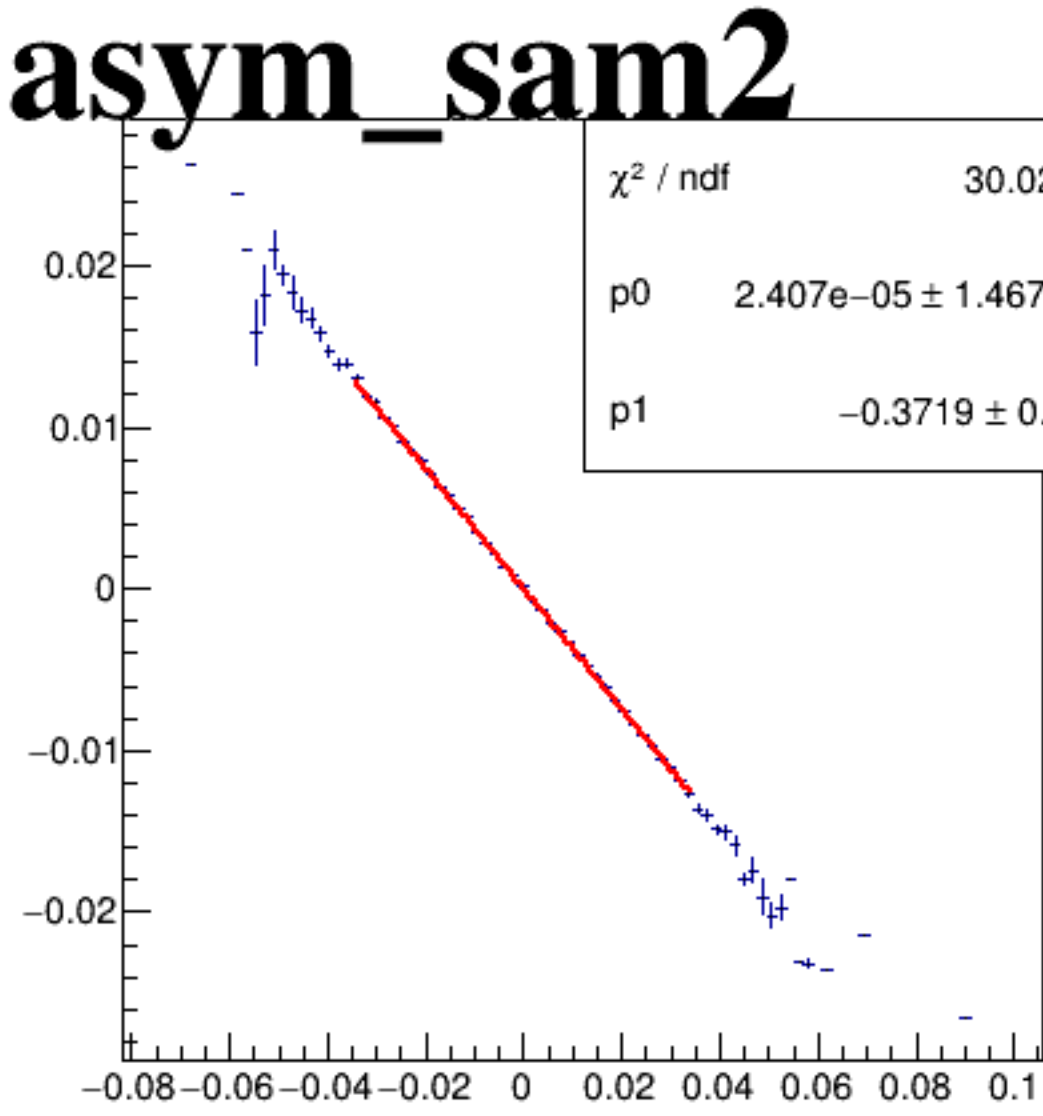
asym_sam2



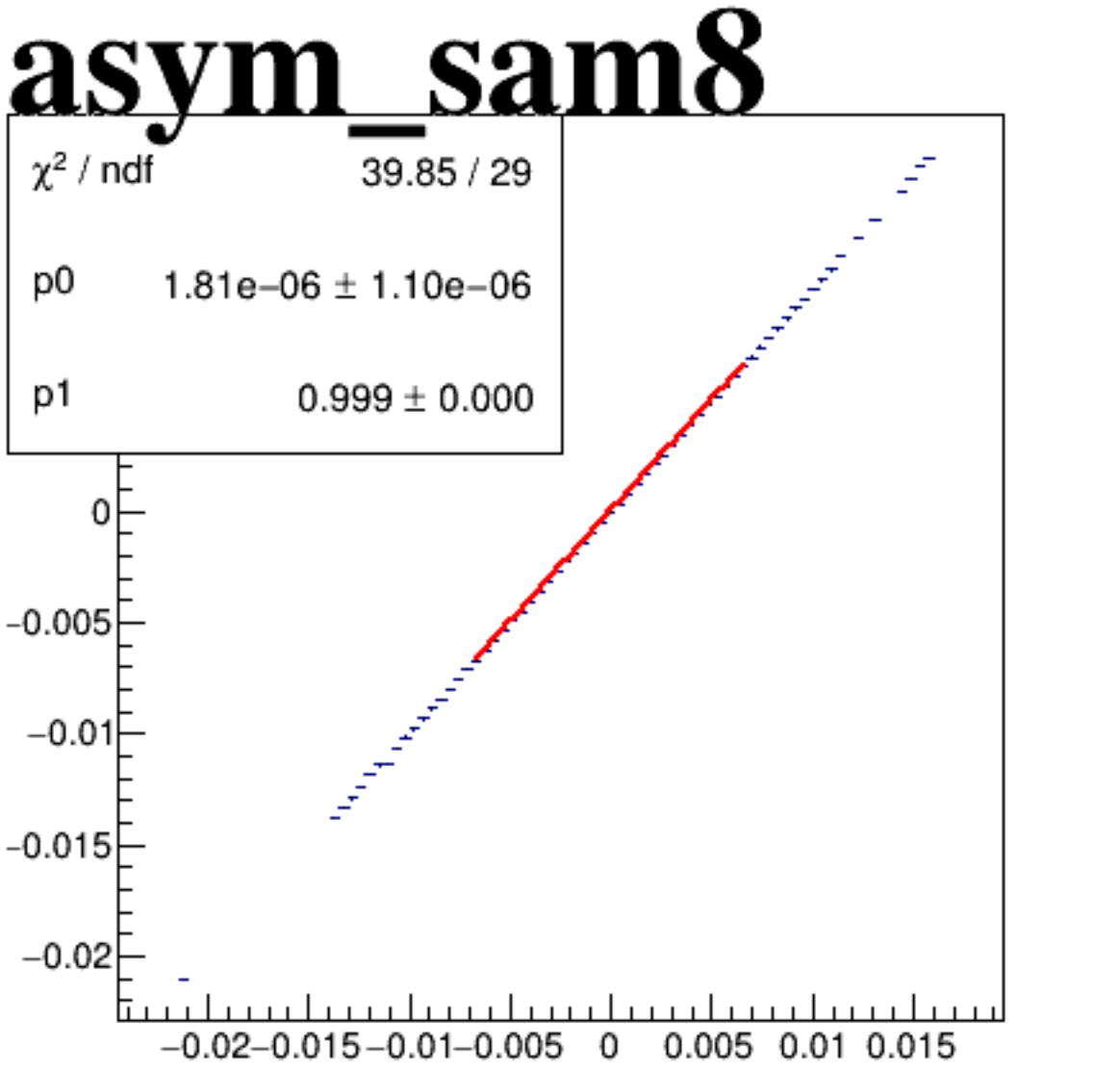
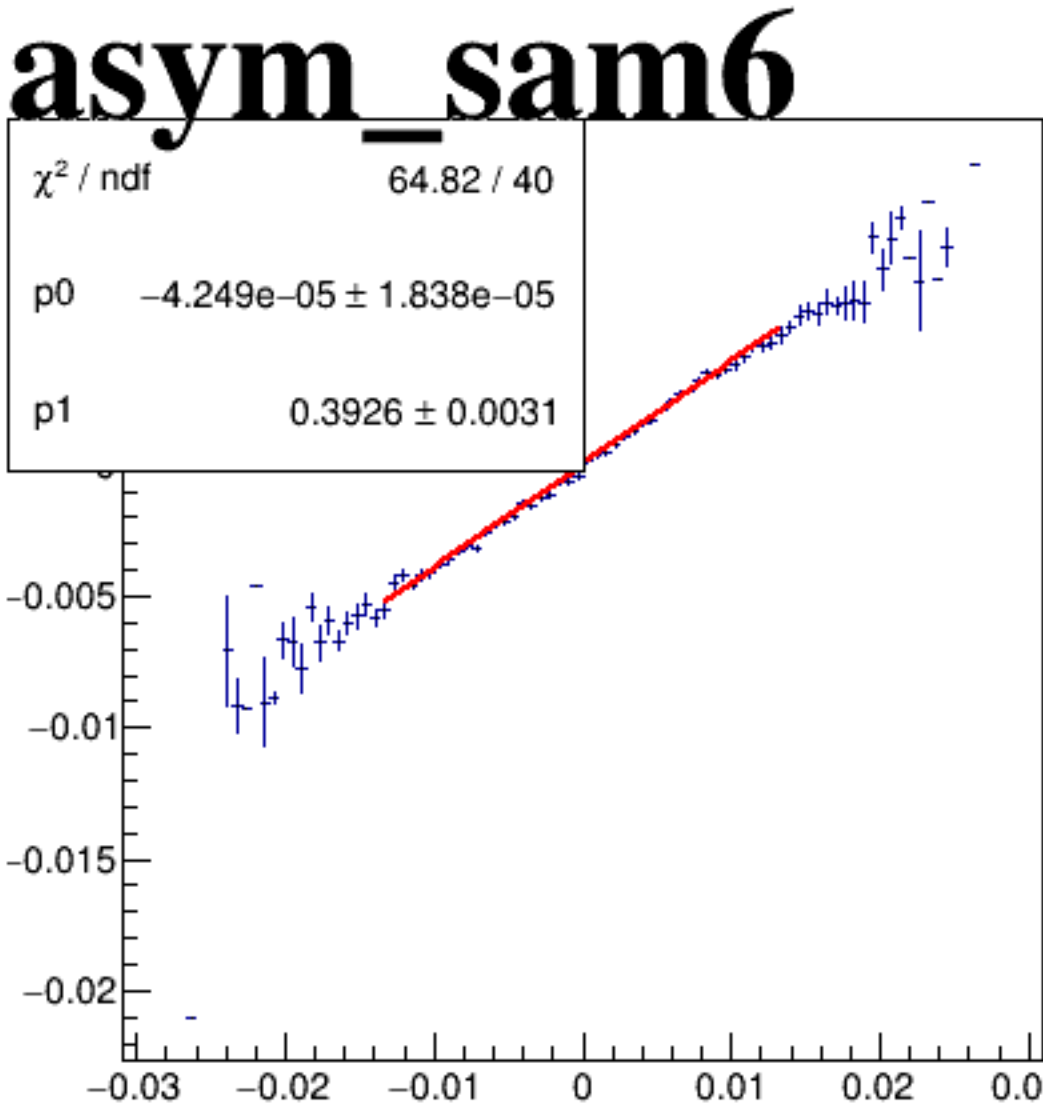
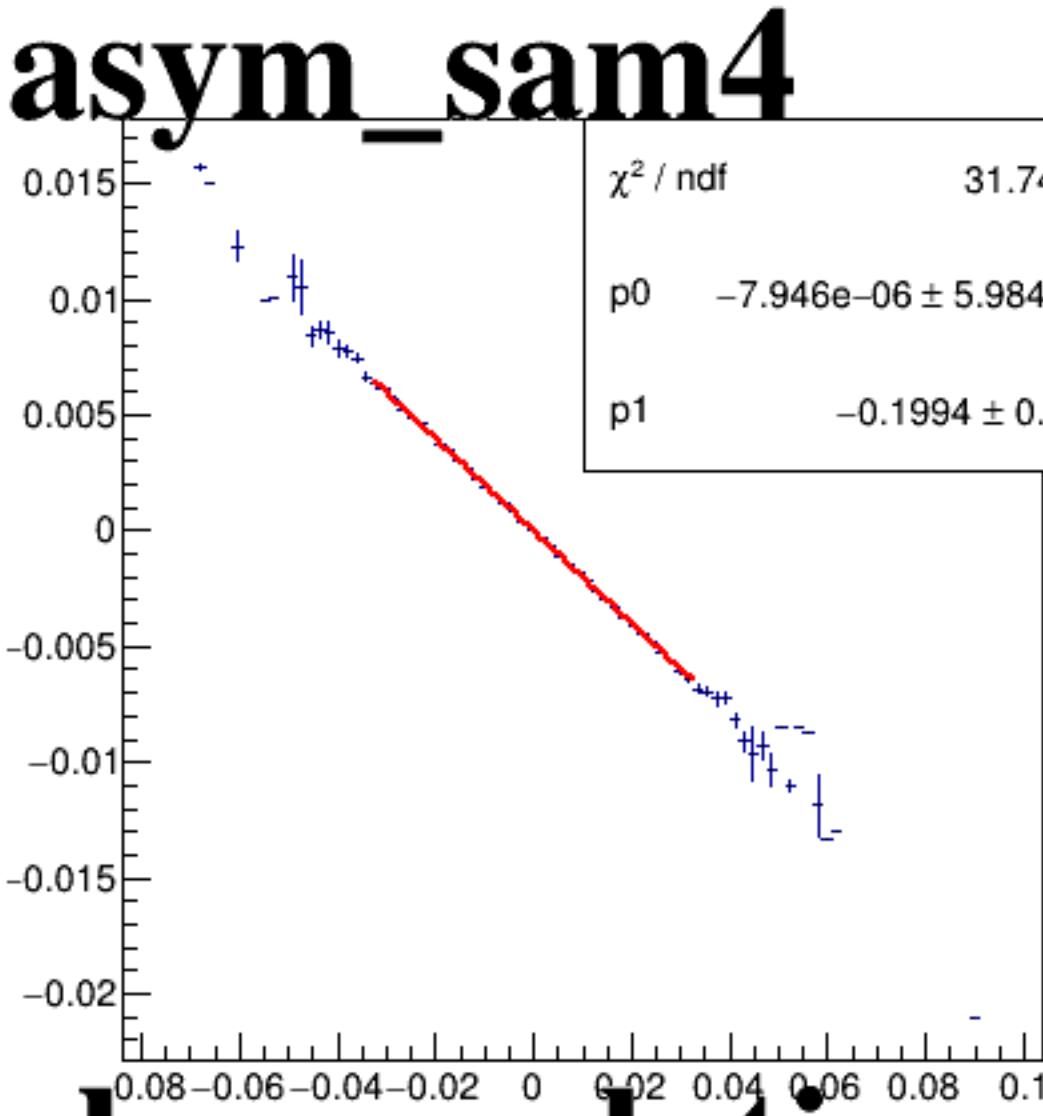
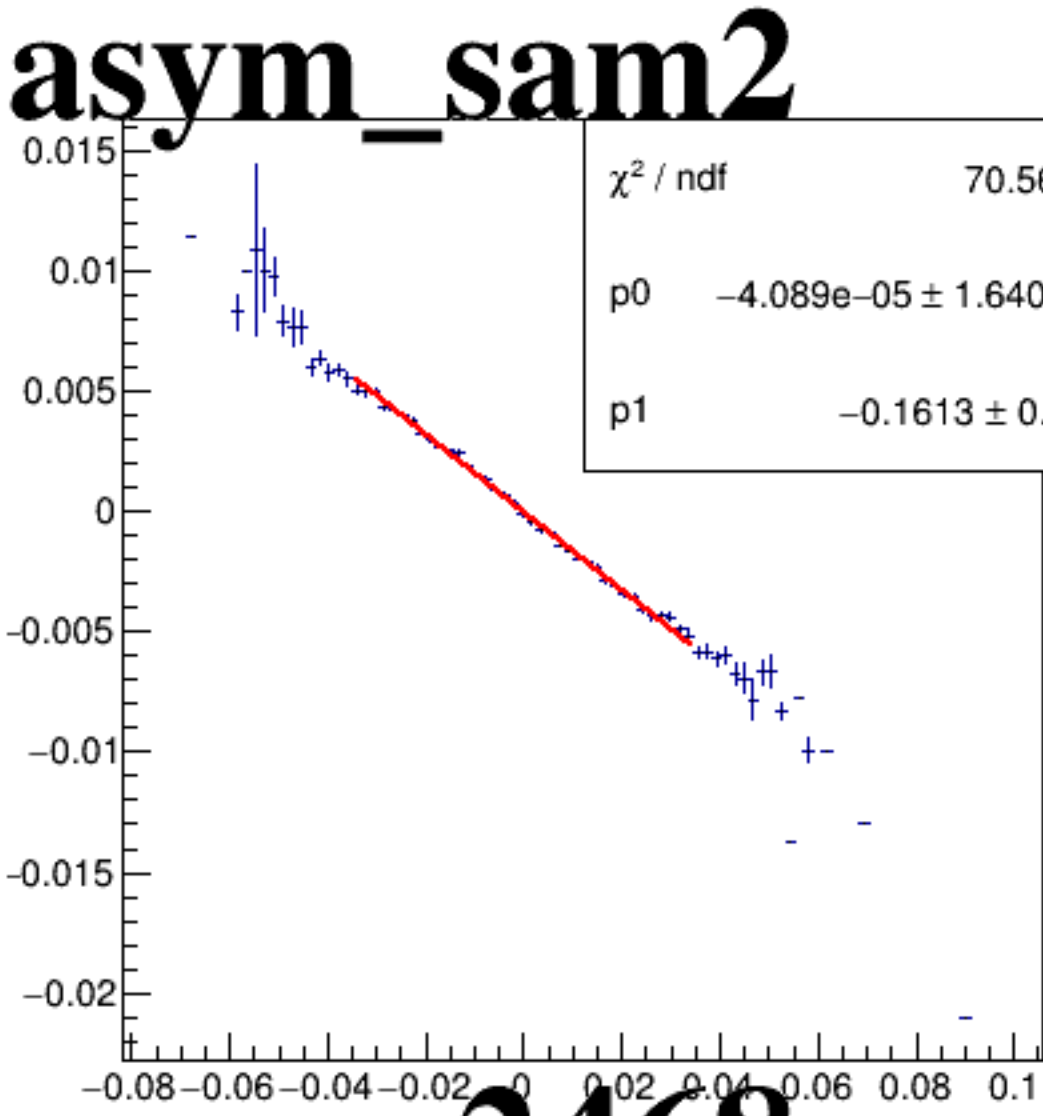
asym_sam4



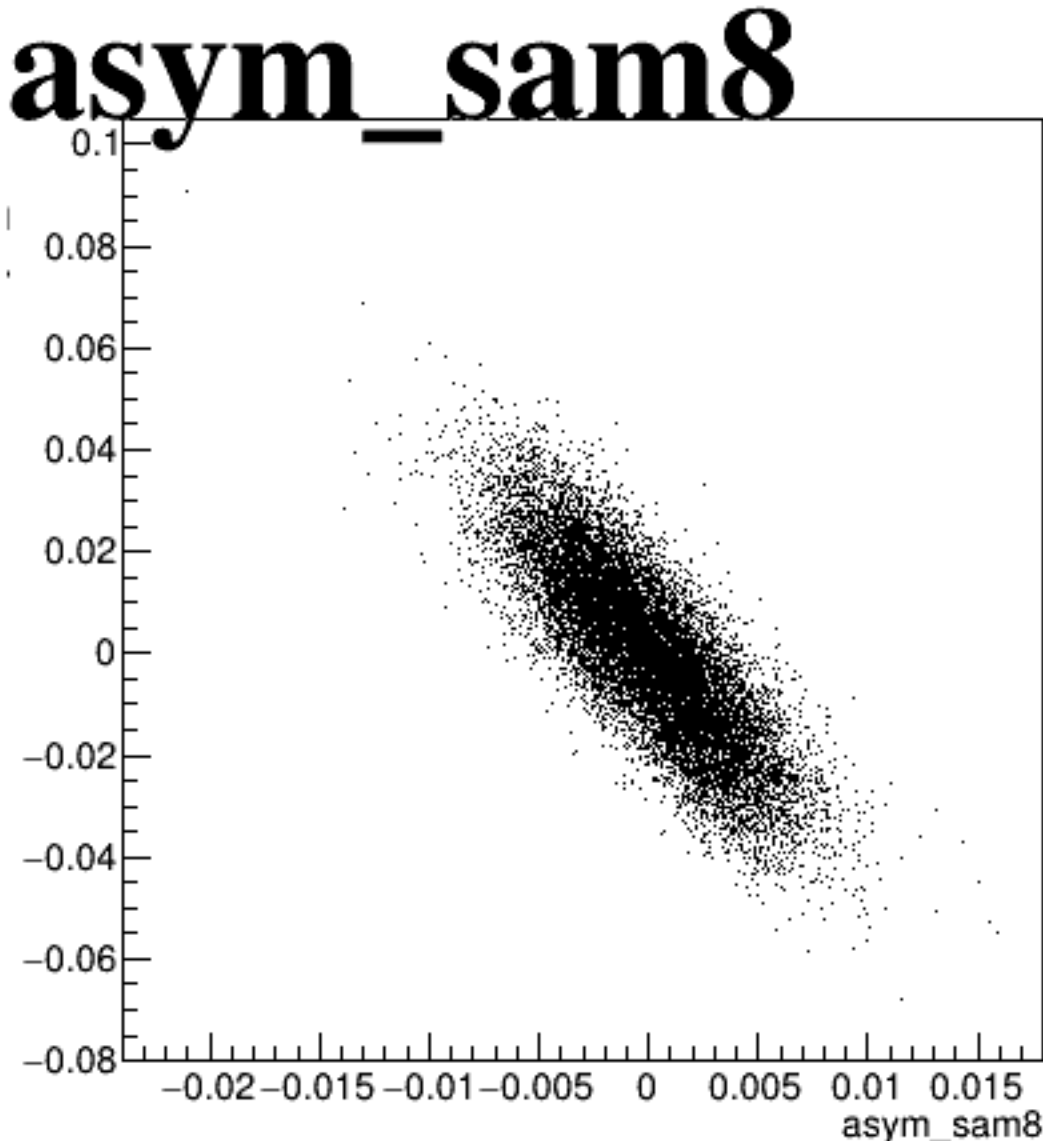
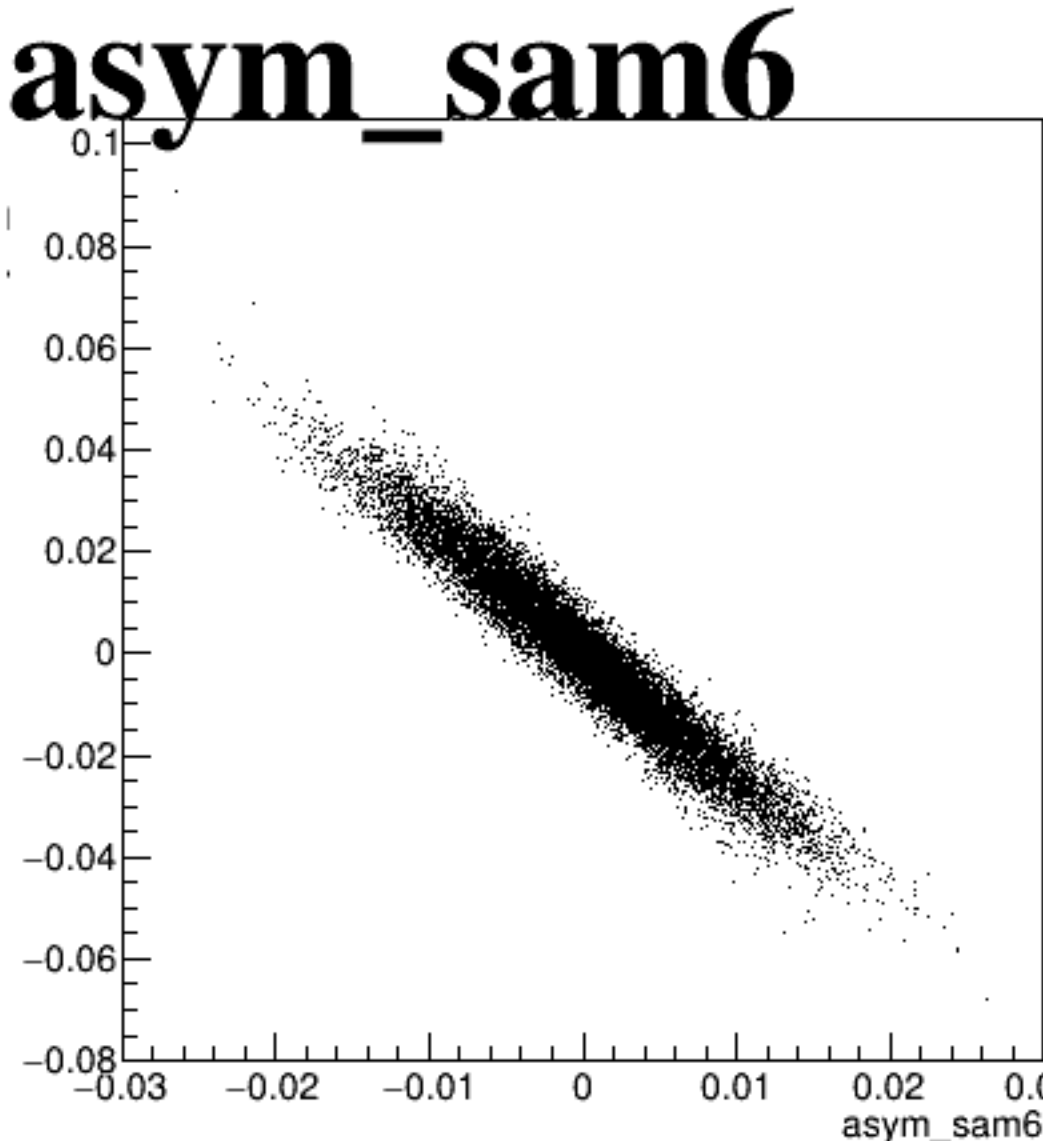
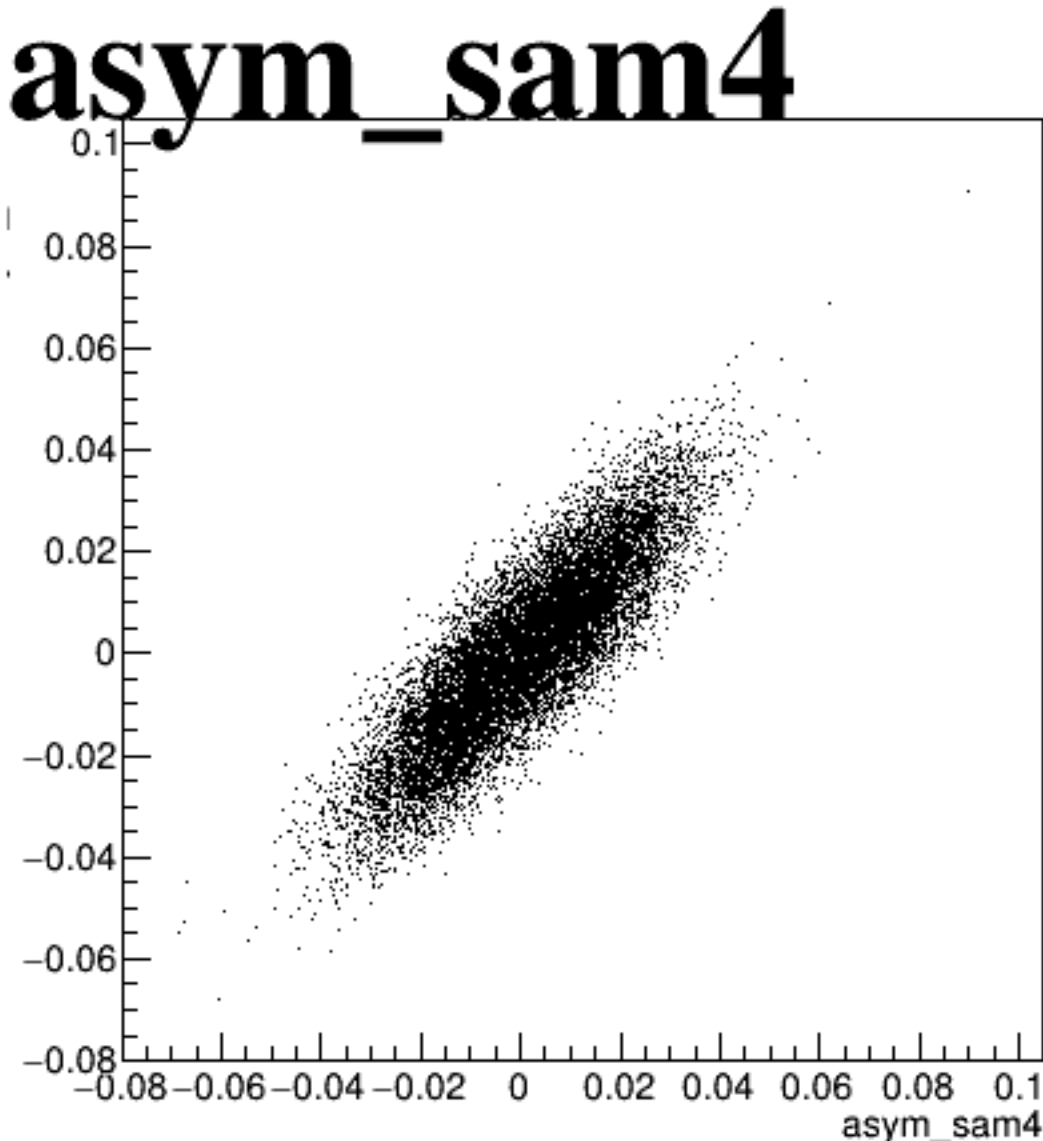
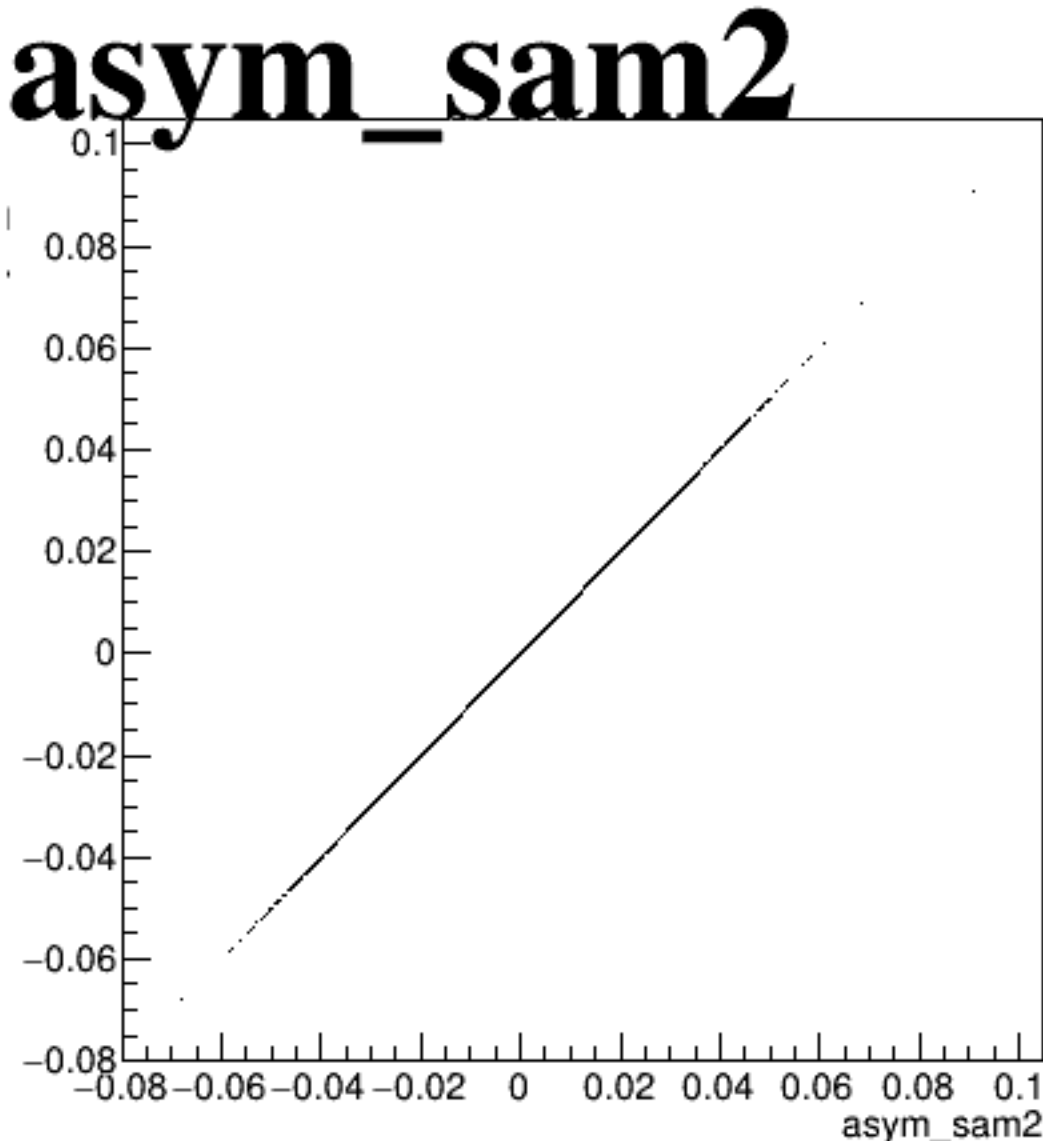
asym_sam6



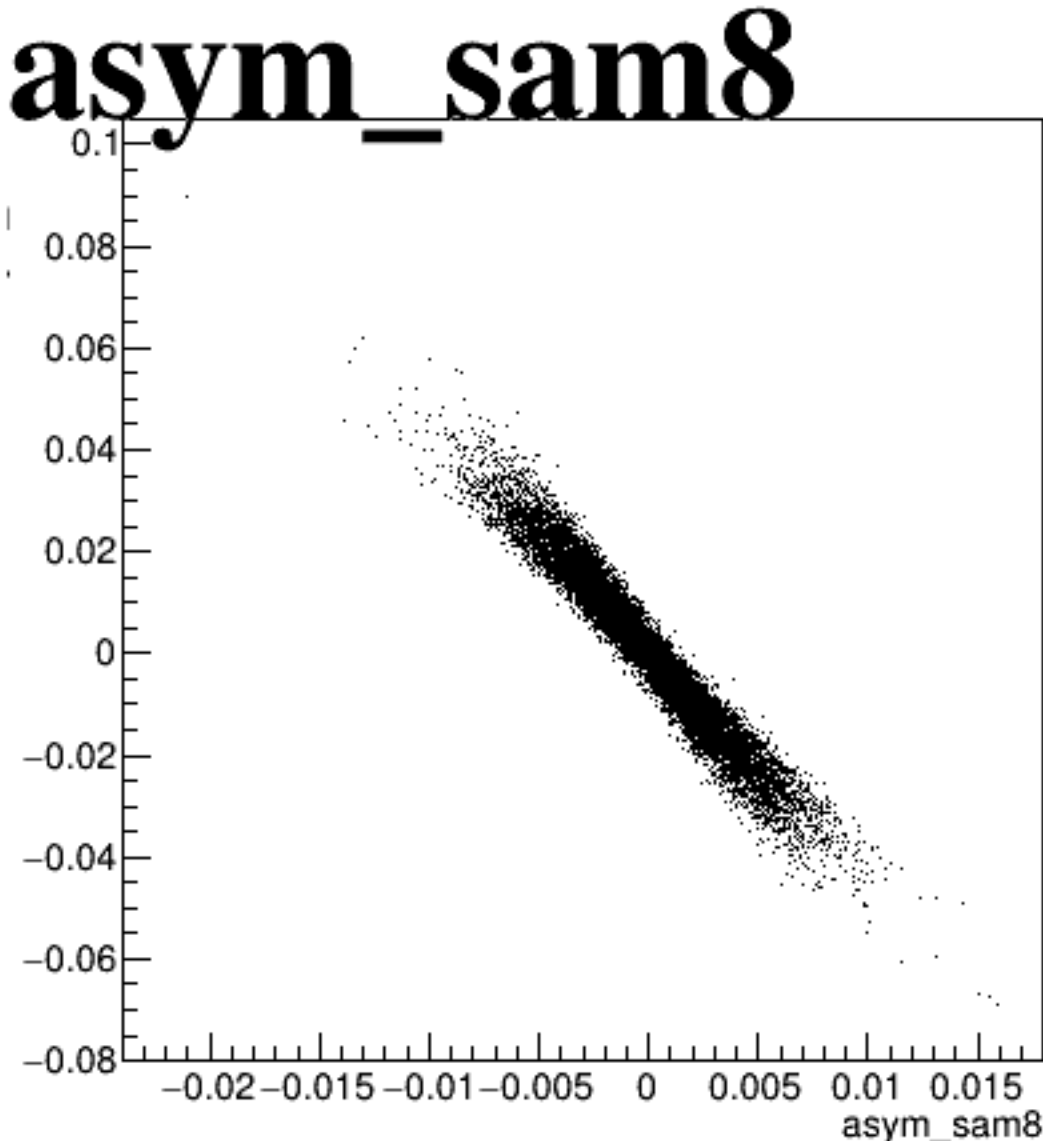
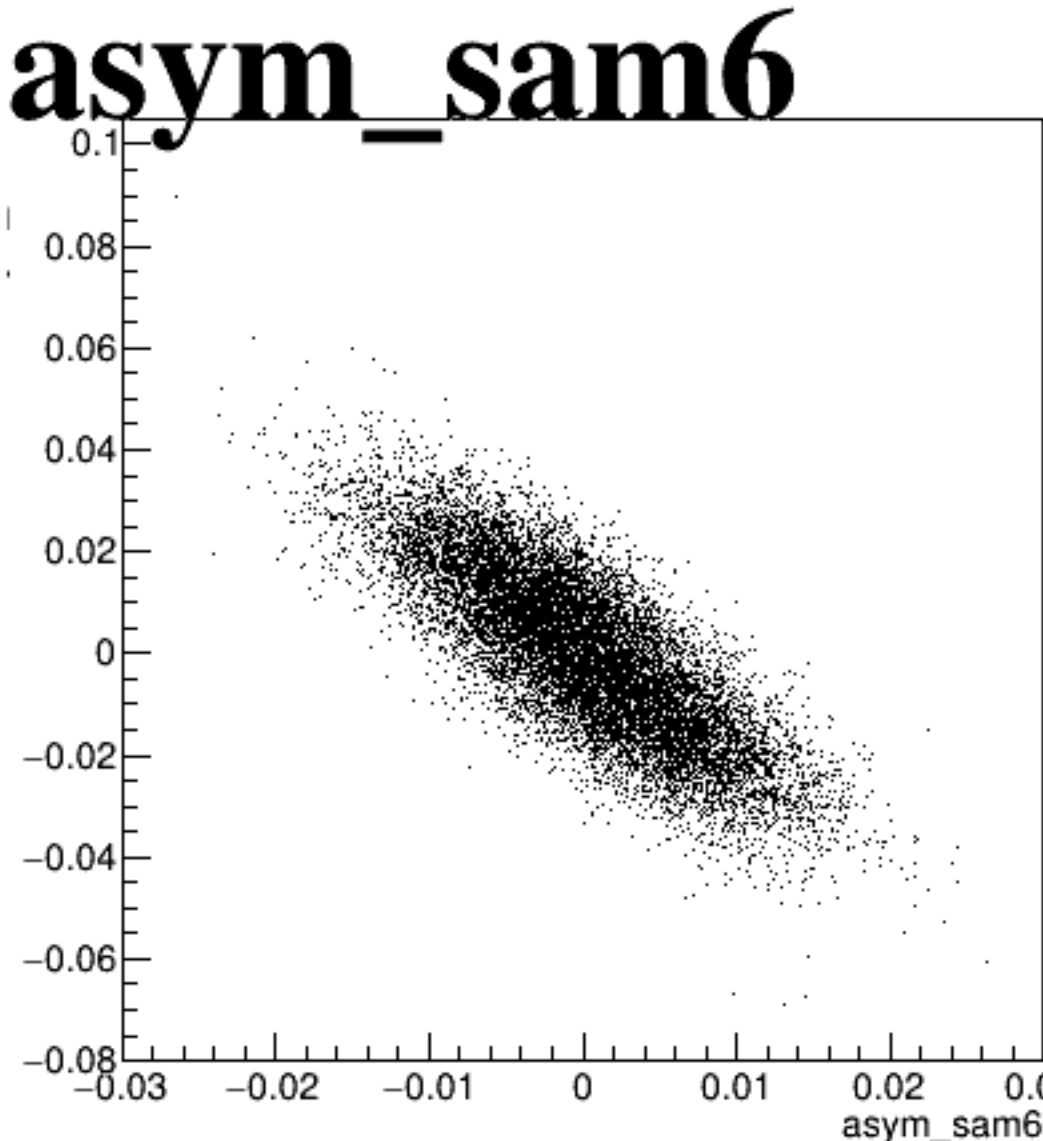
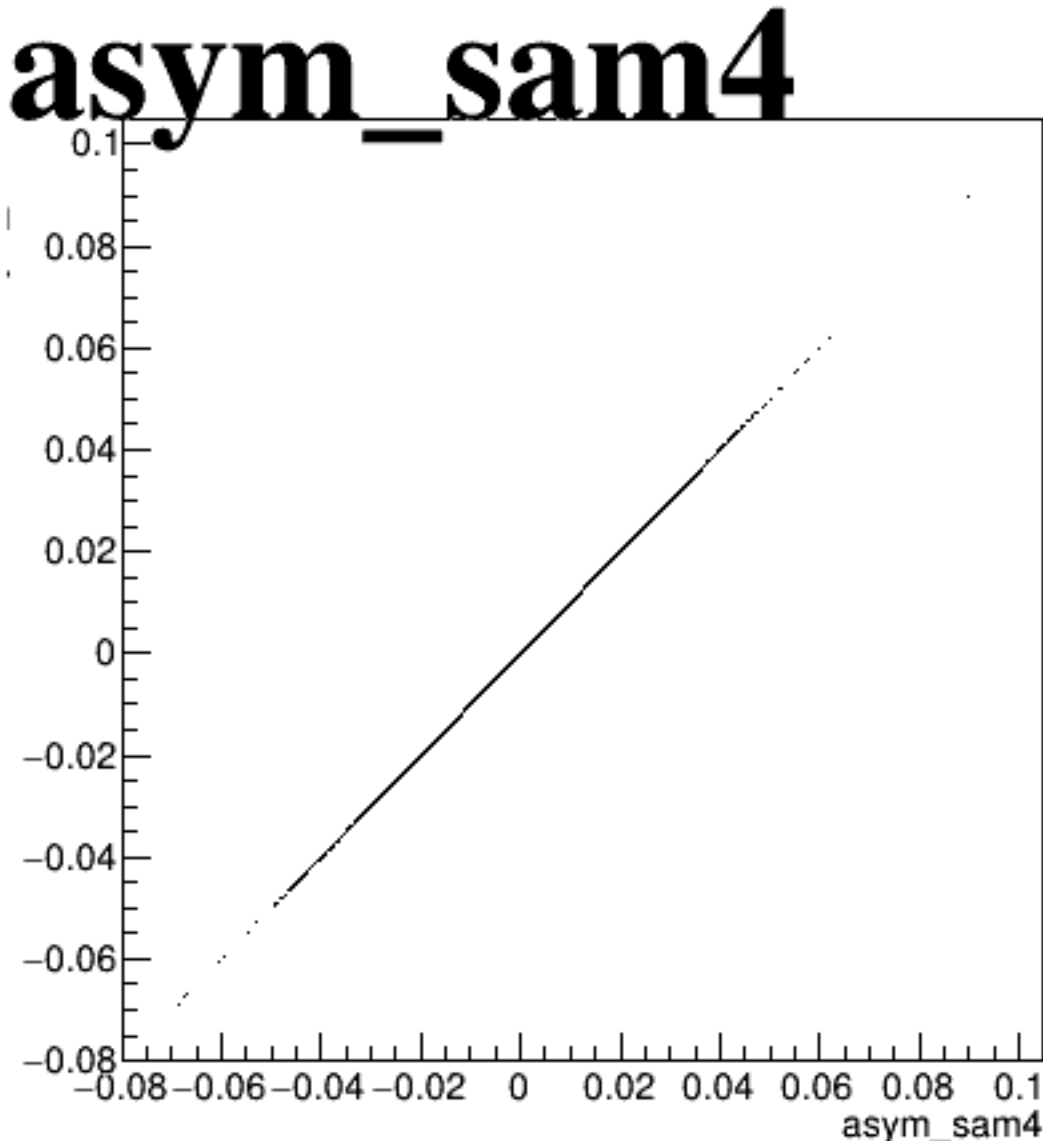
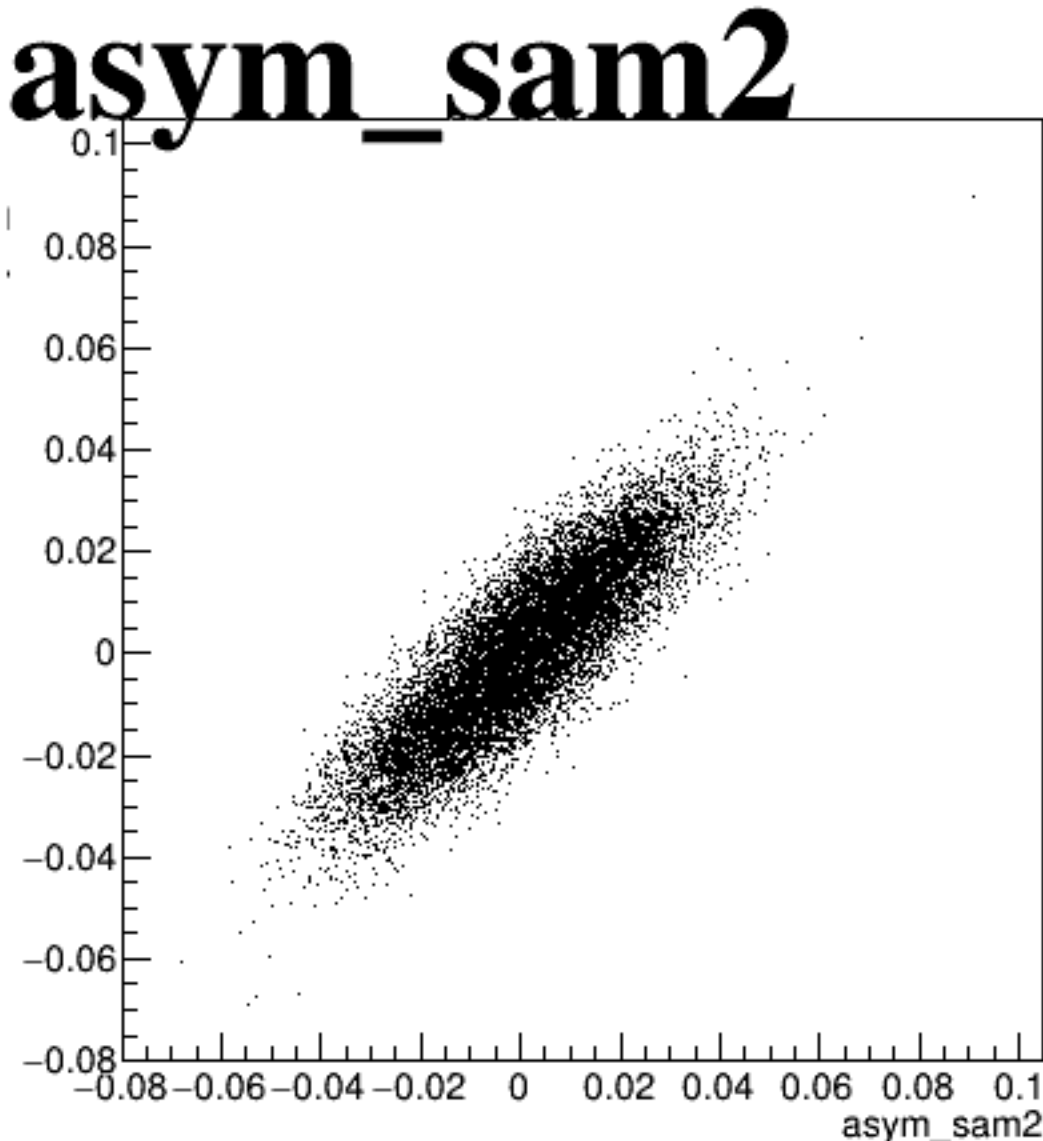
asym_sam8



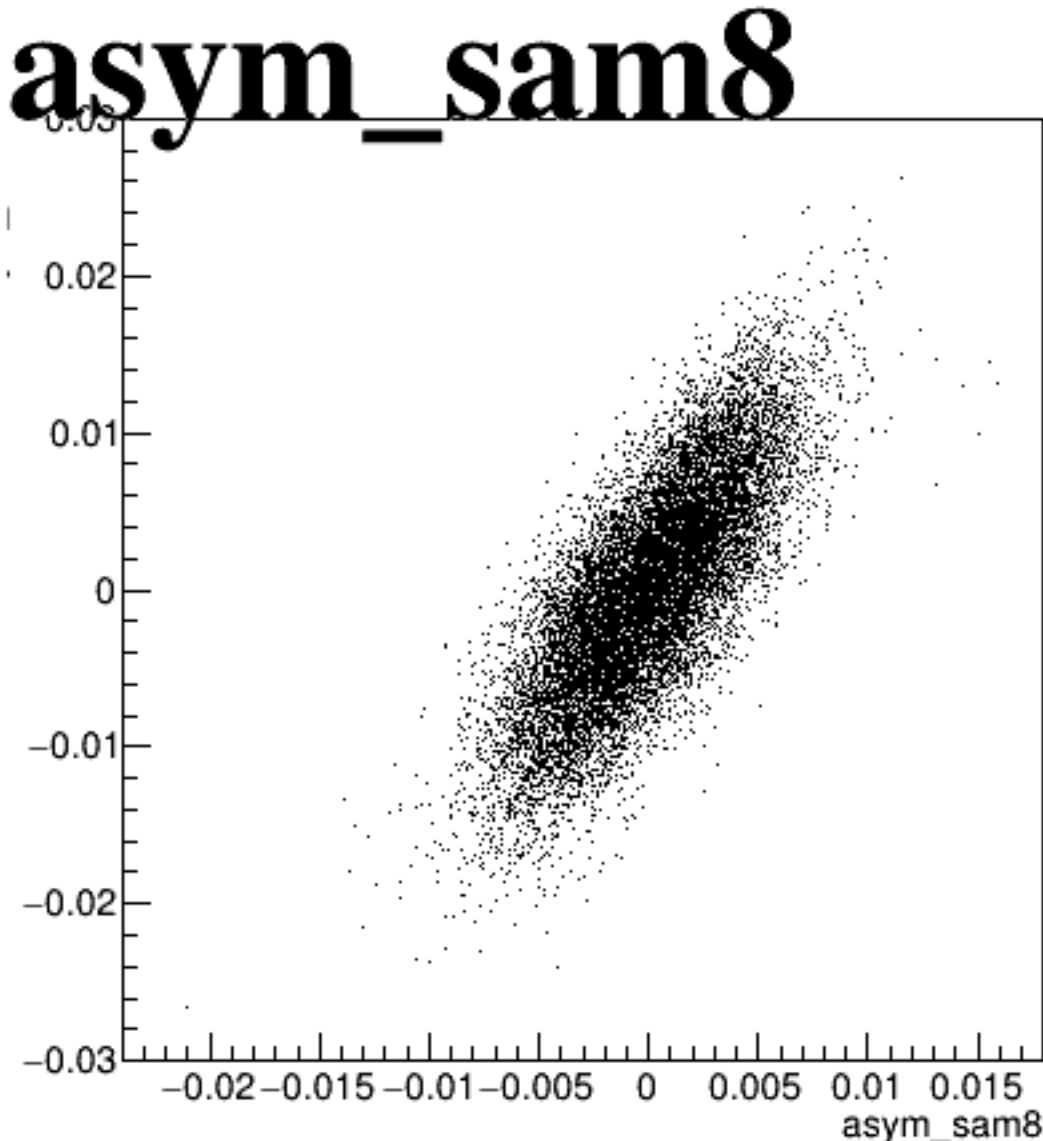
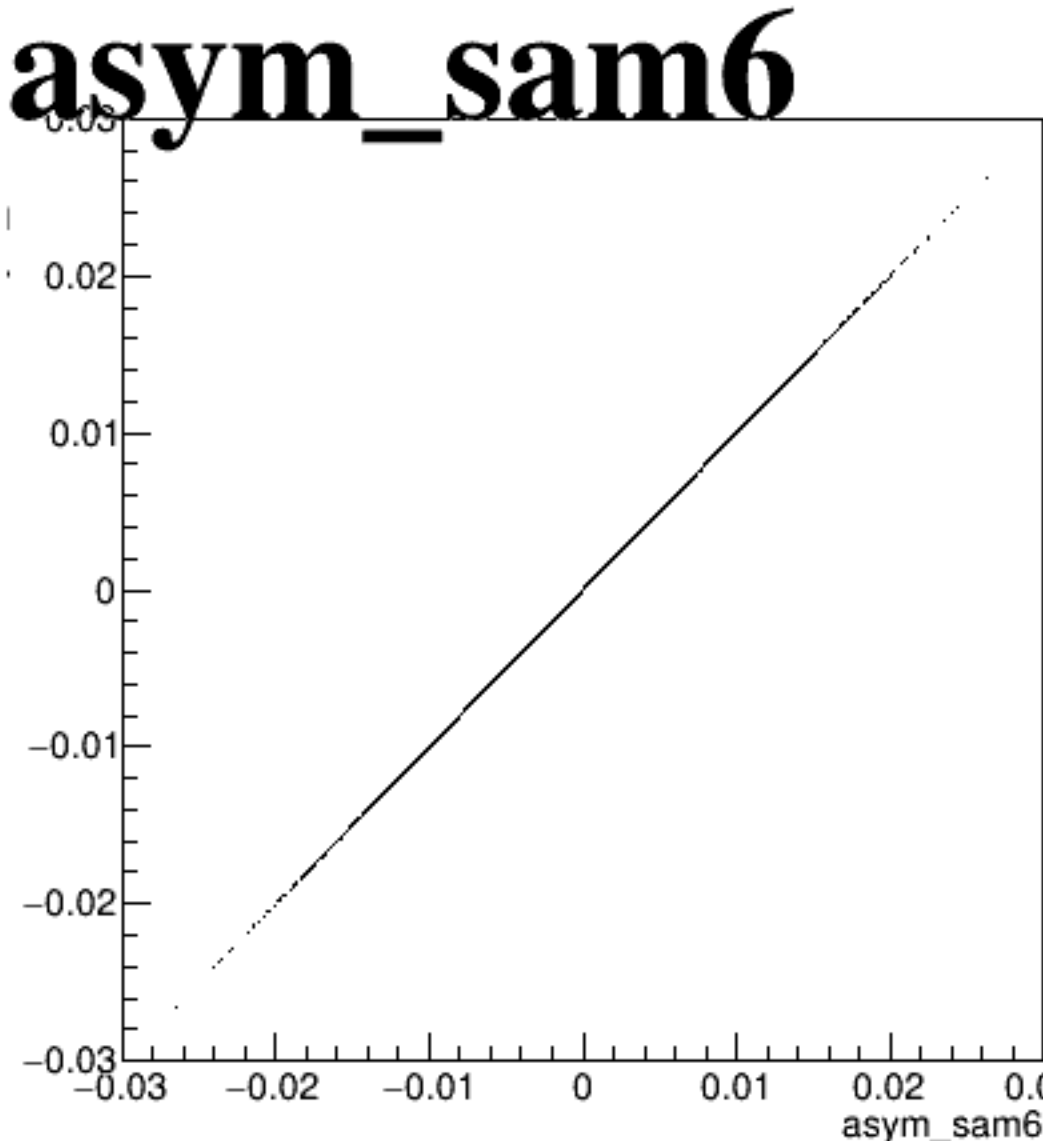
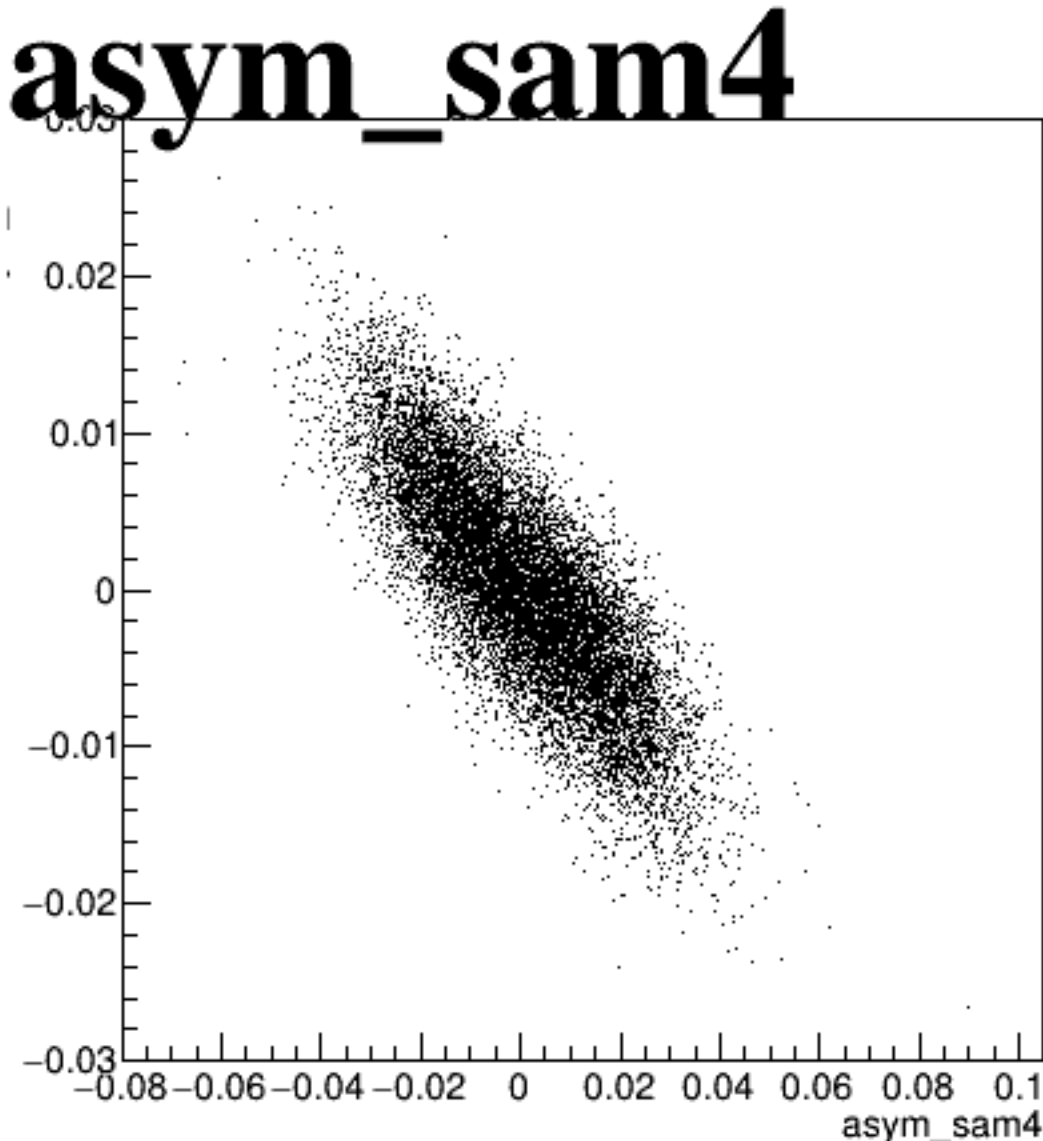
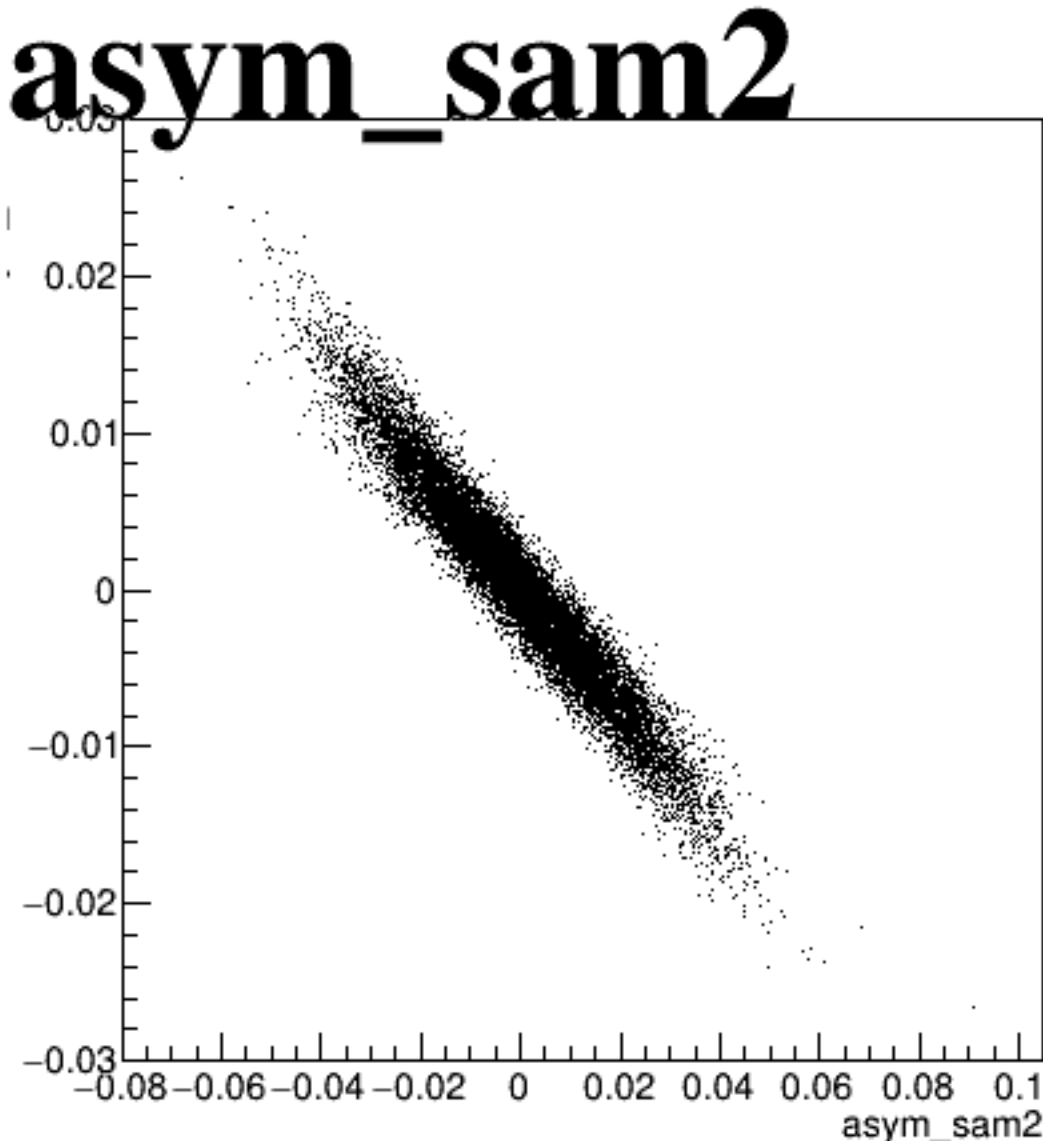
asym_sam2



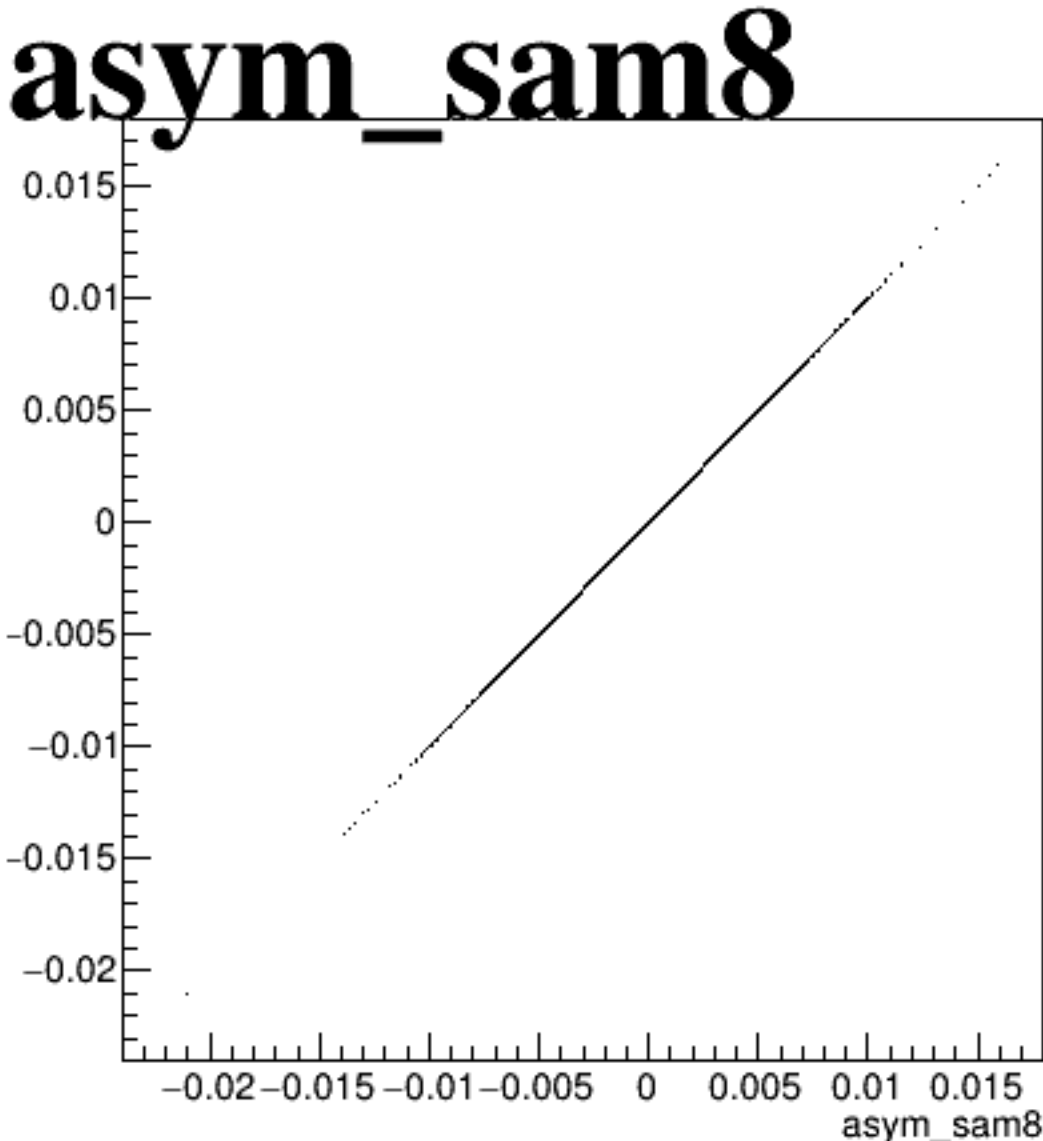
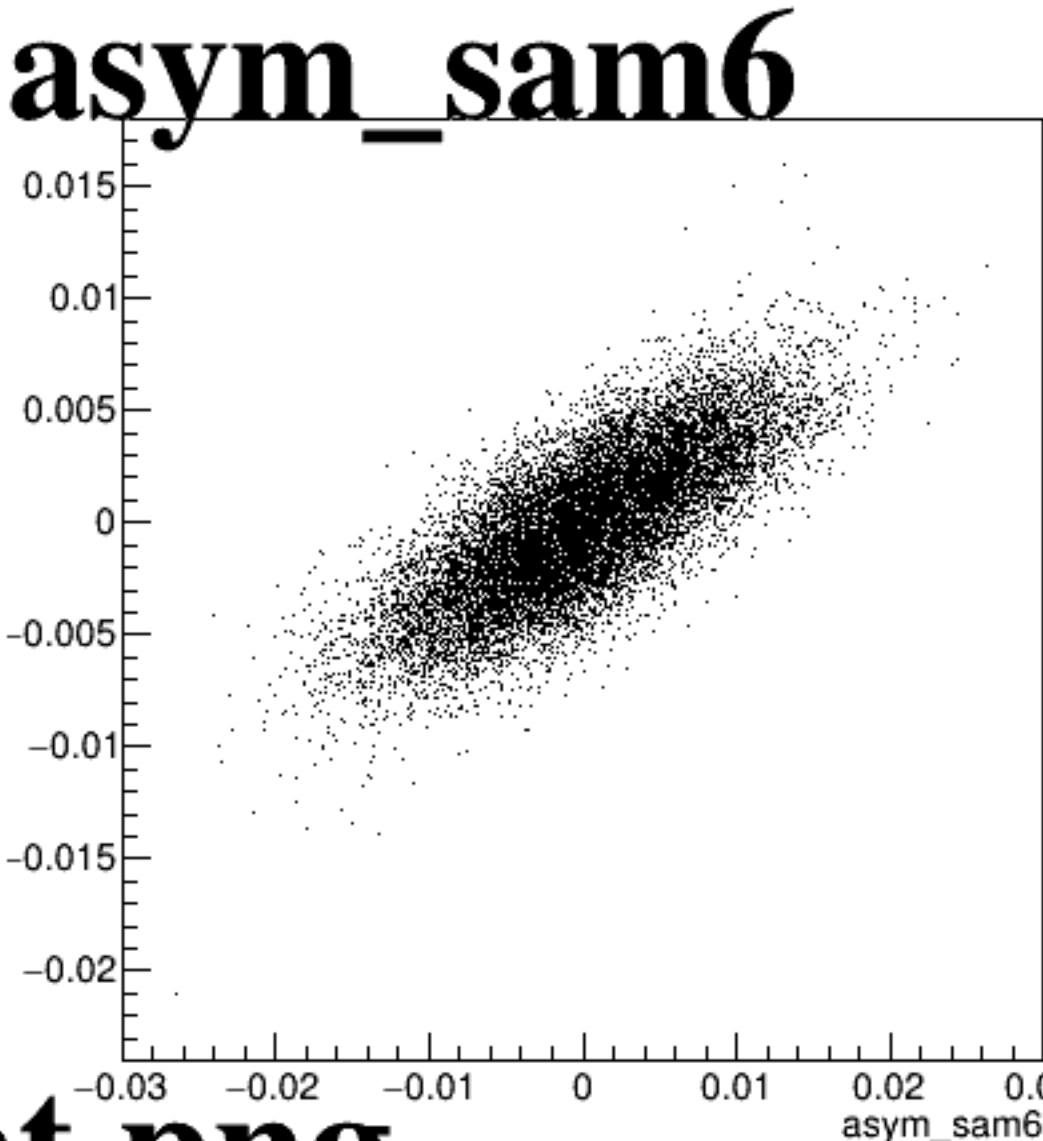
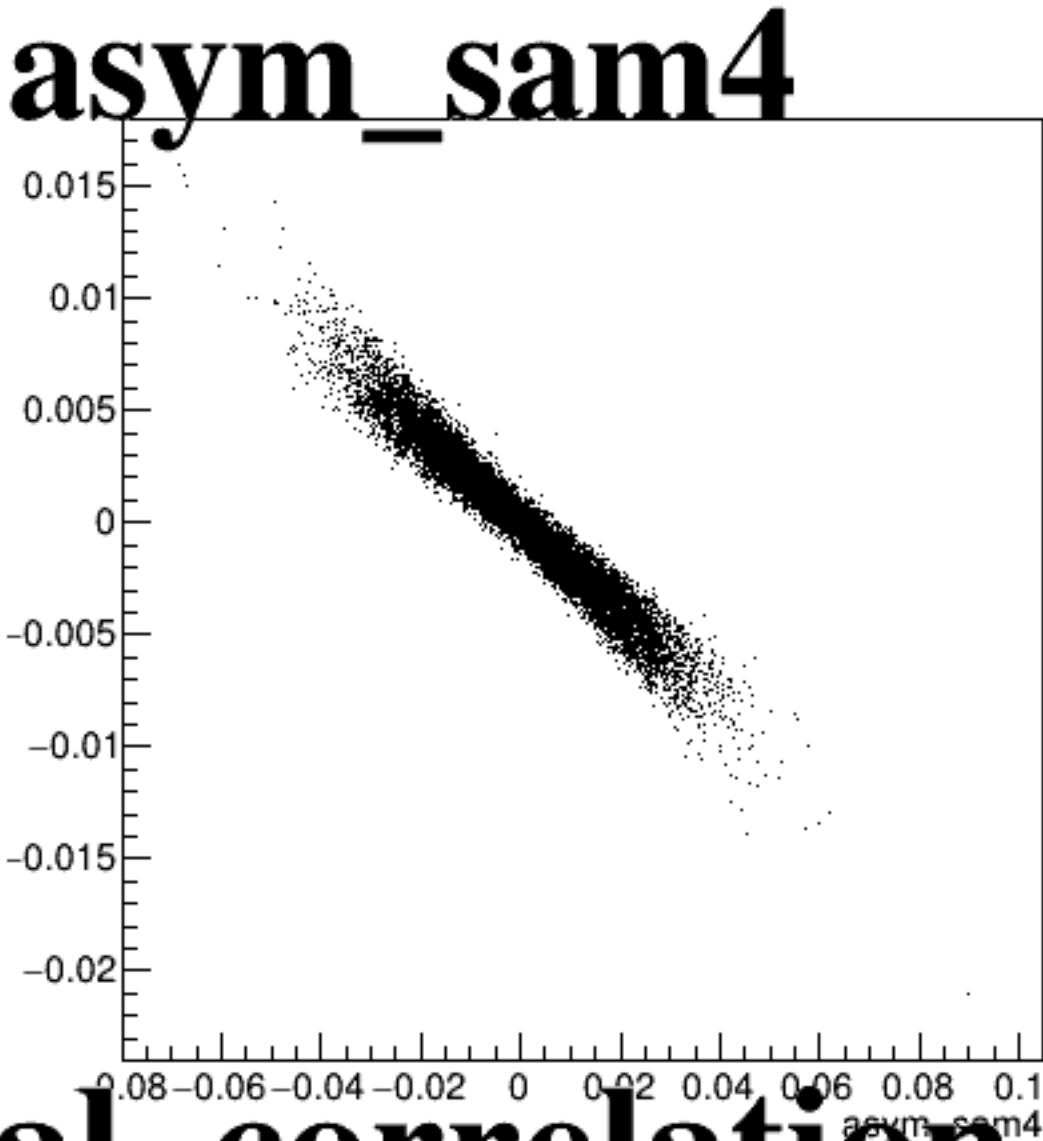
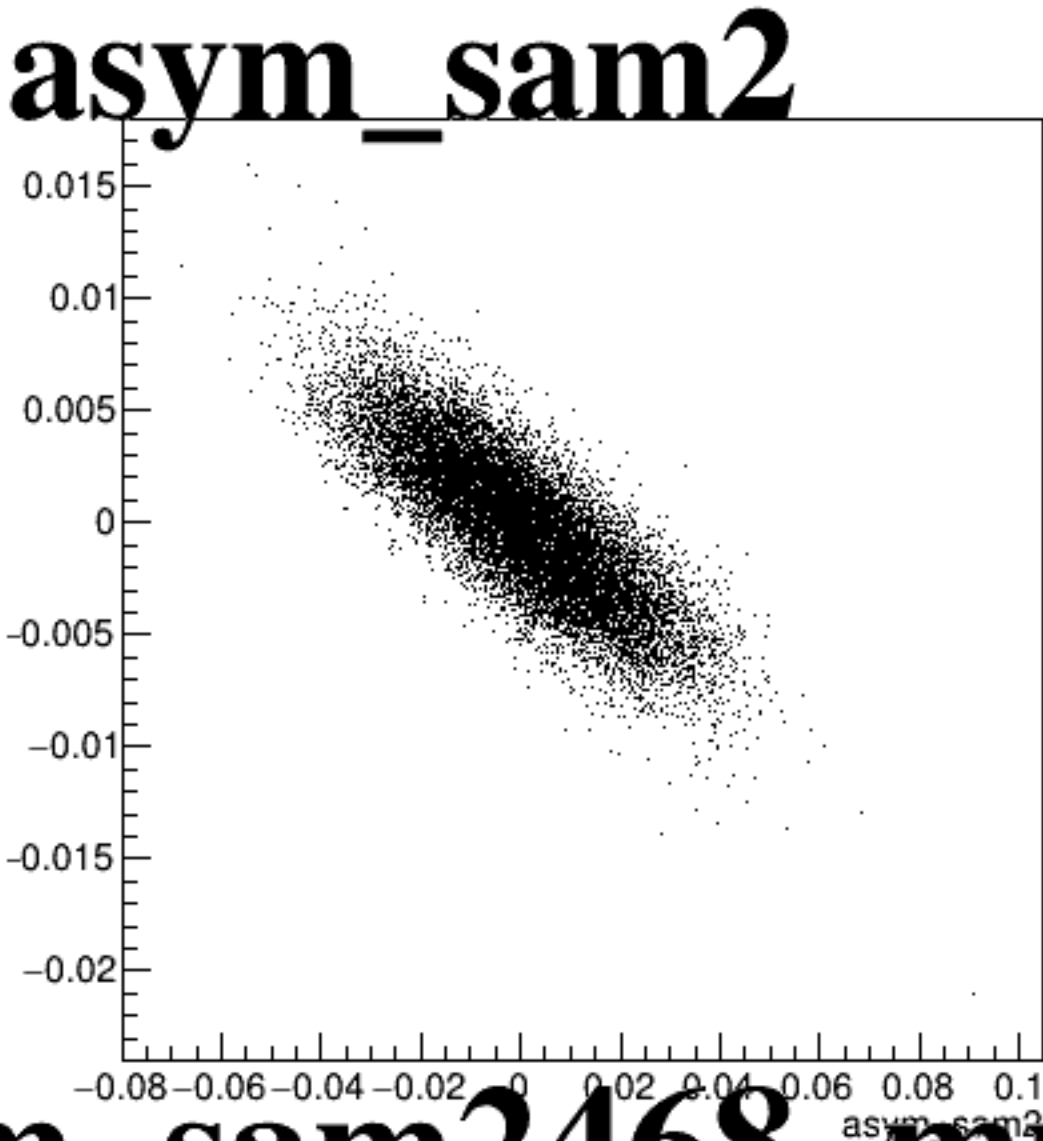
asym_sam4



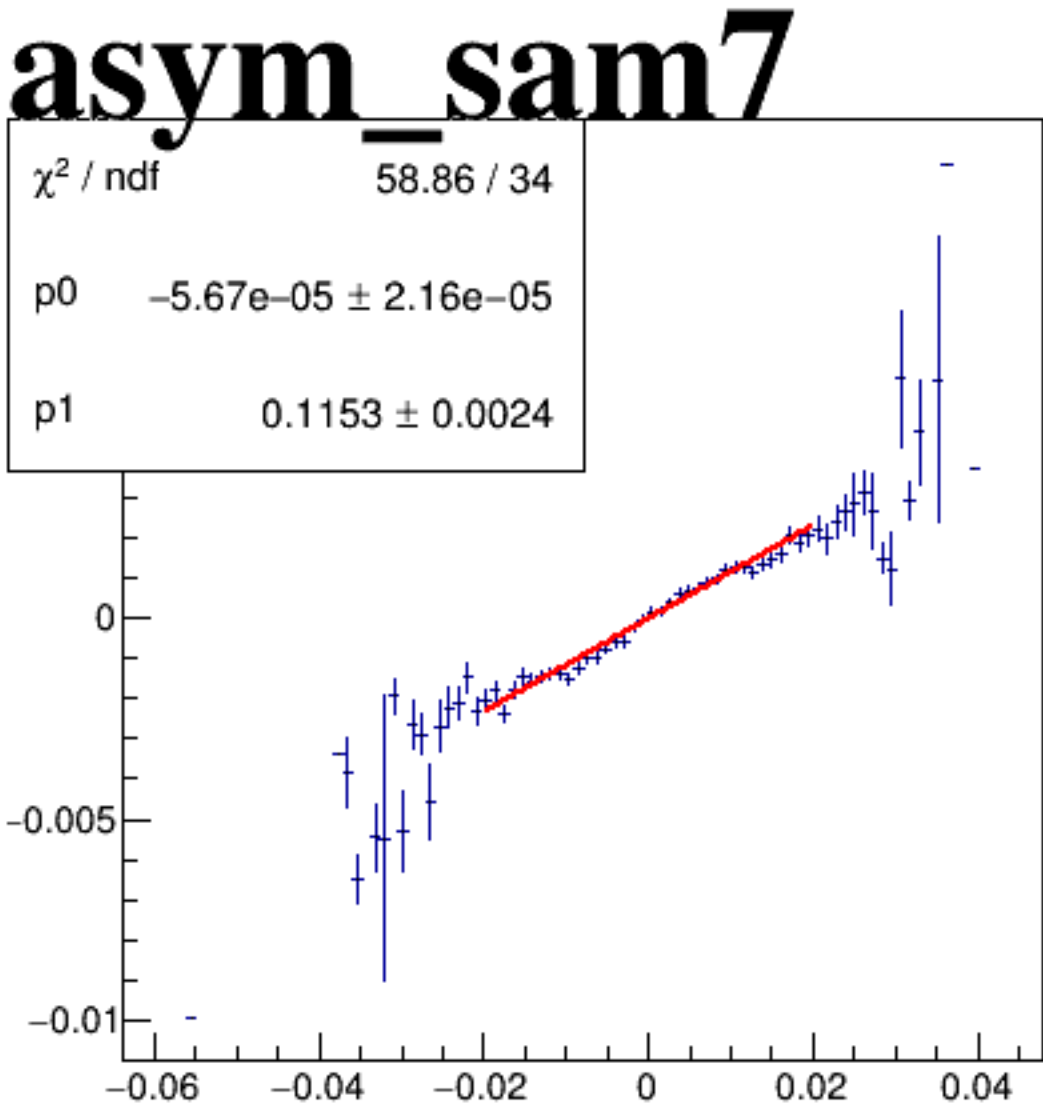
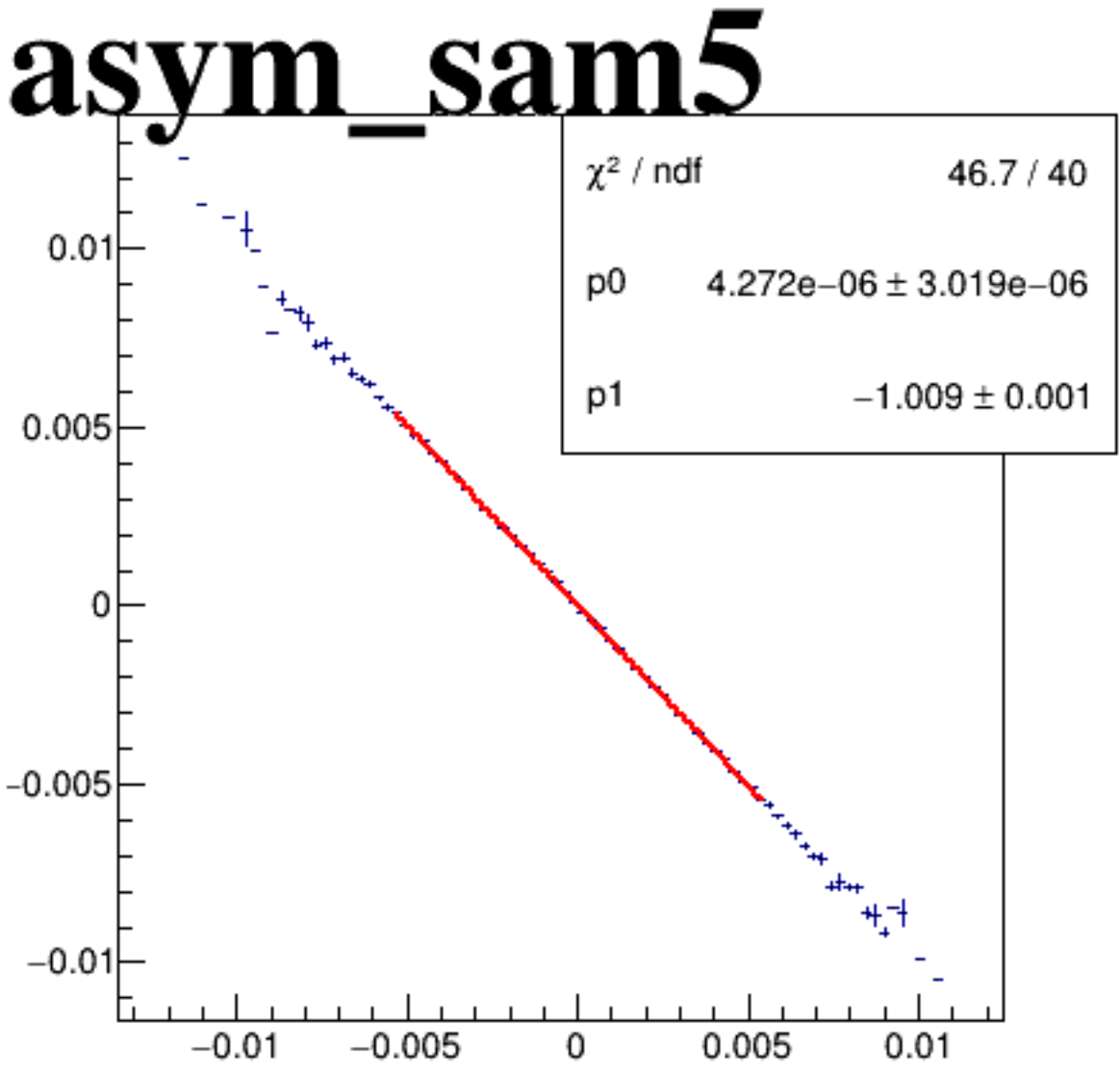
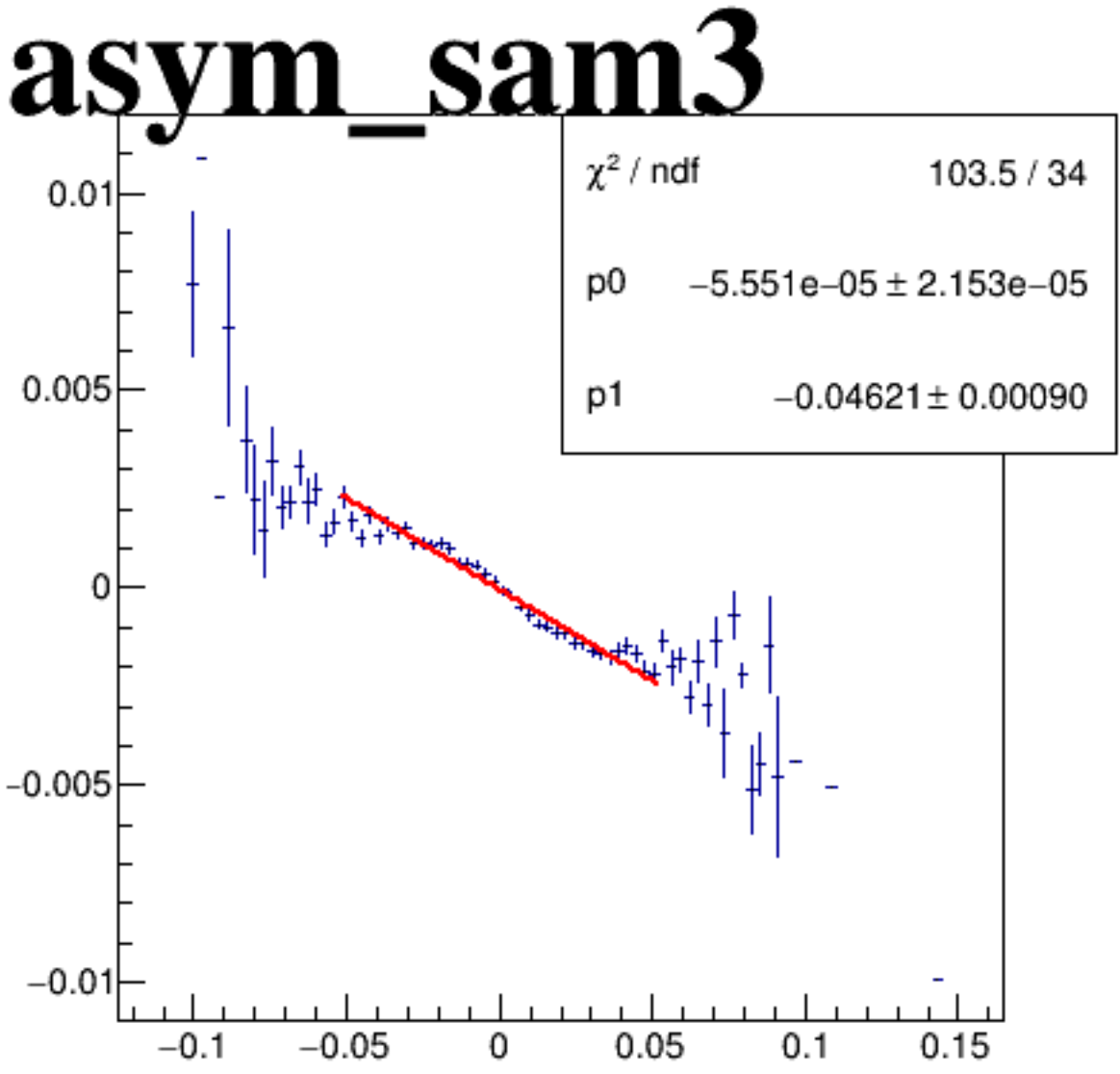
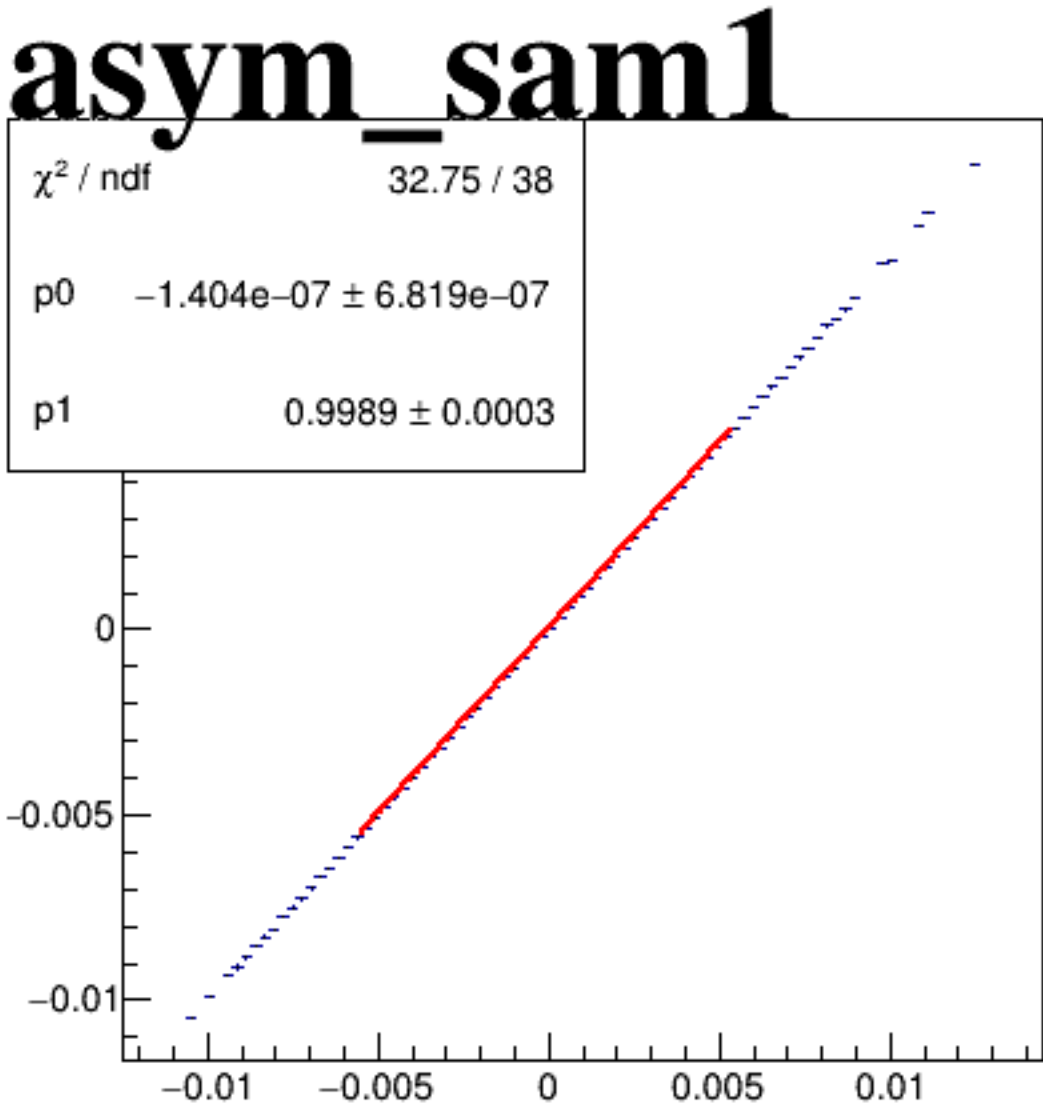
asym_sam6



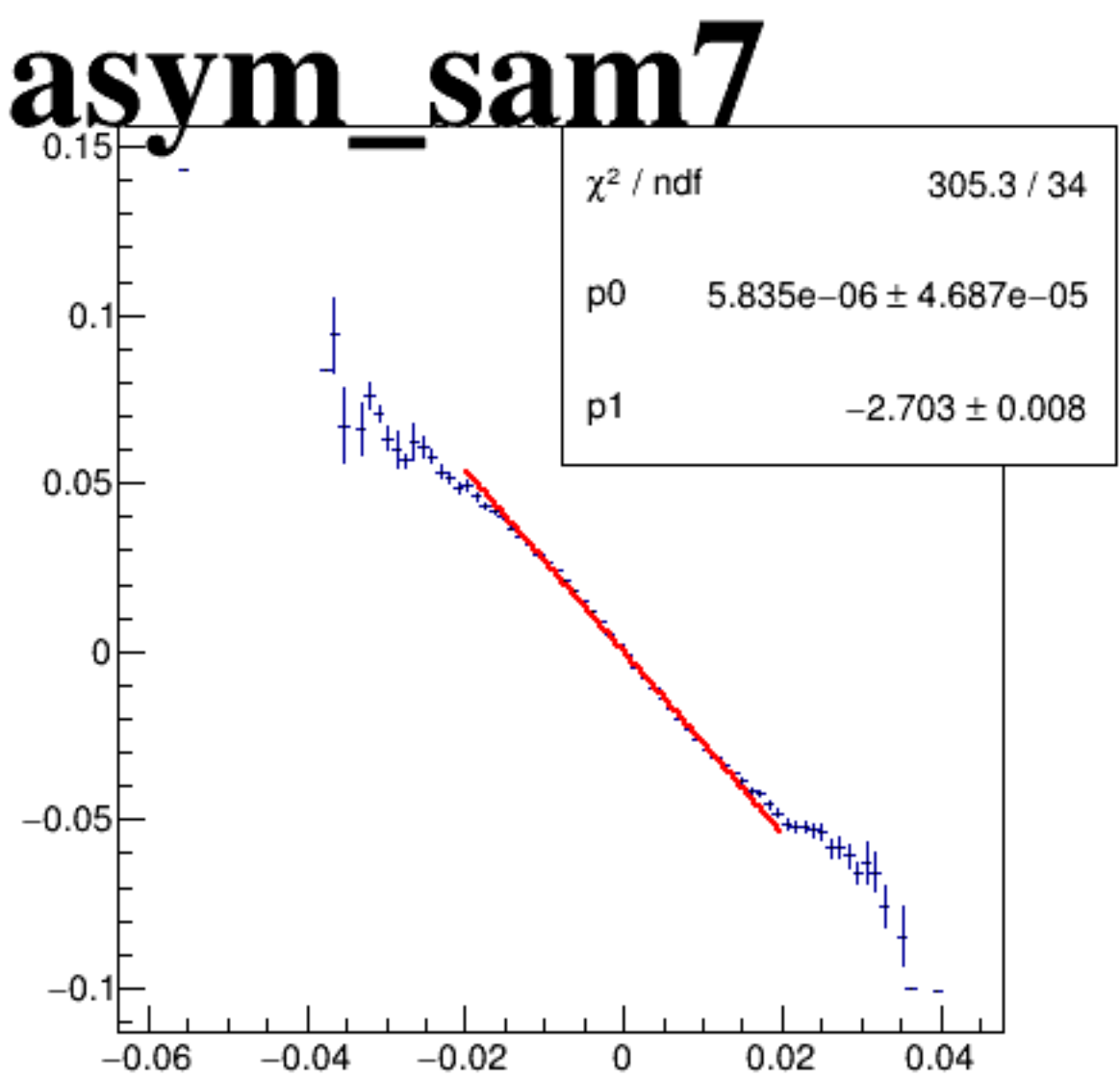
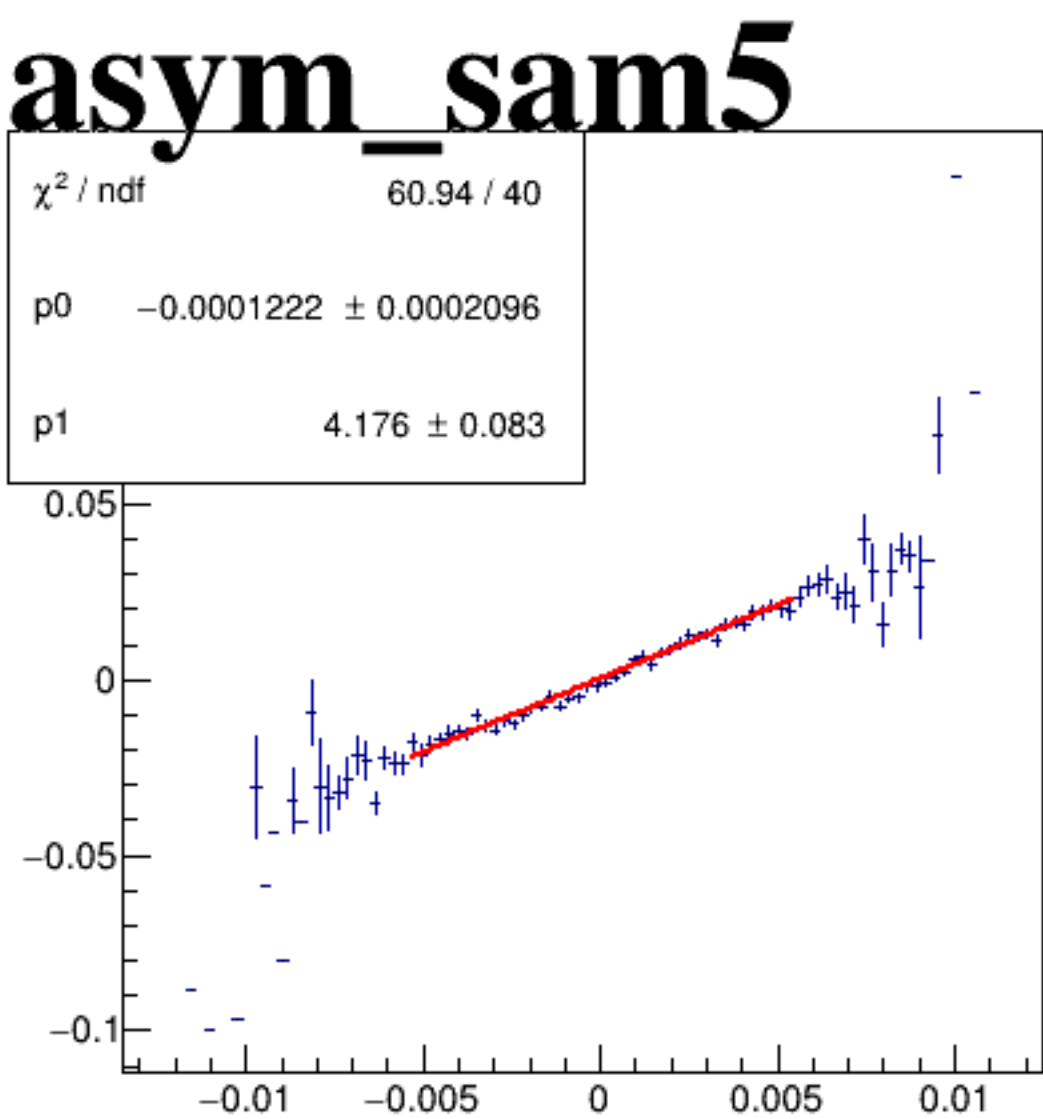
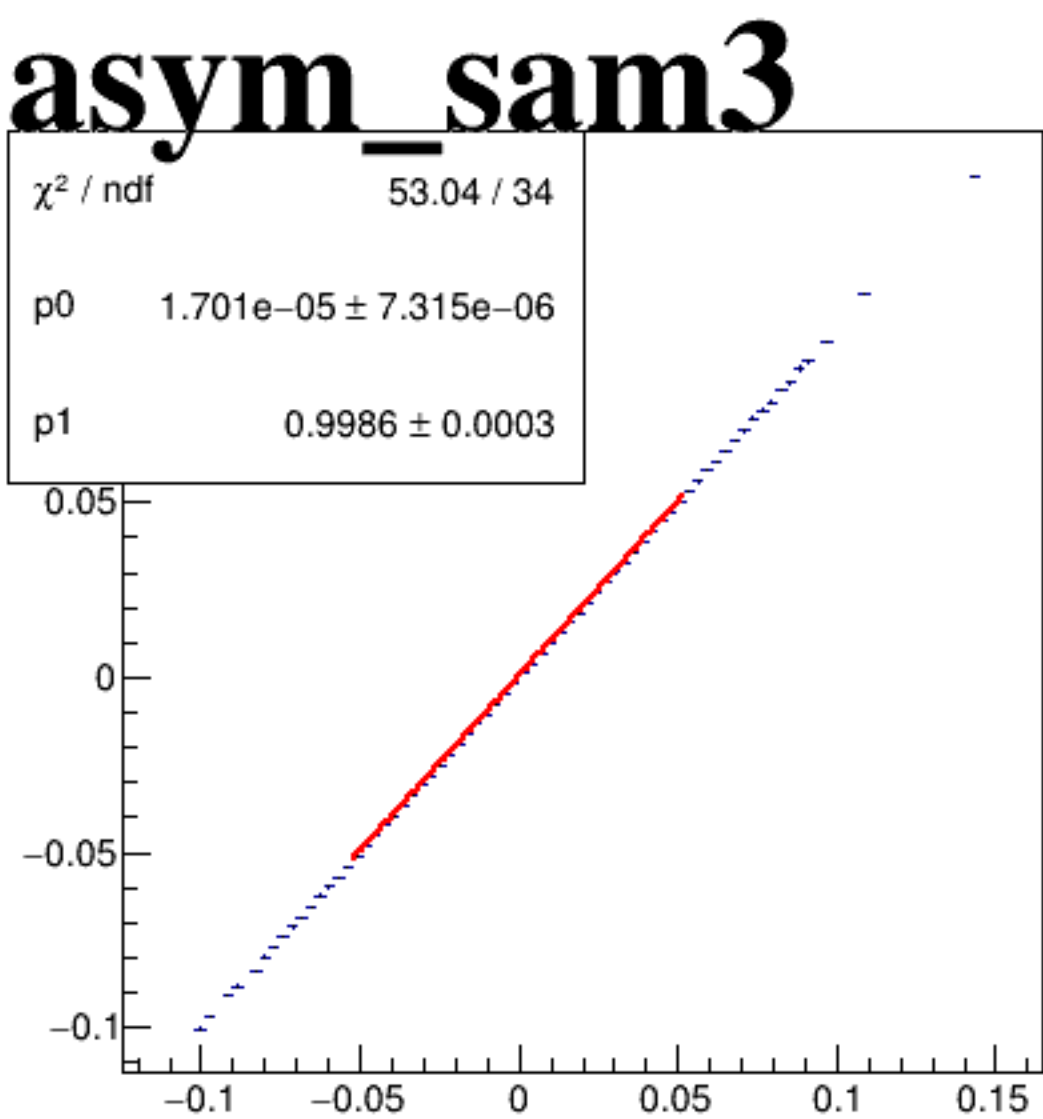
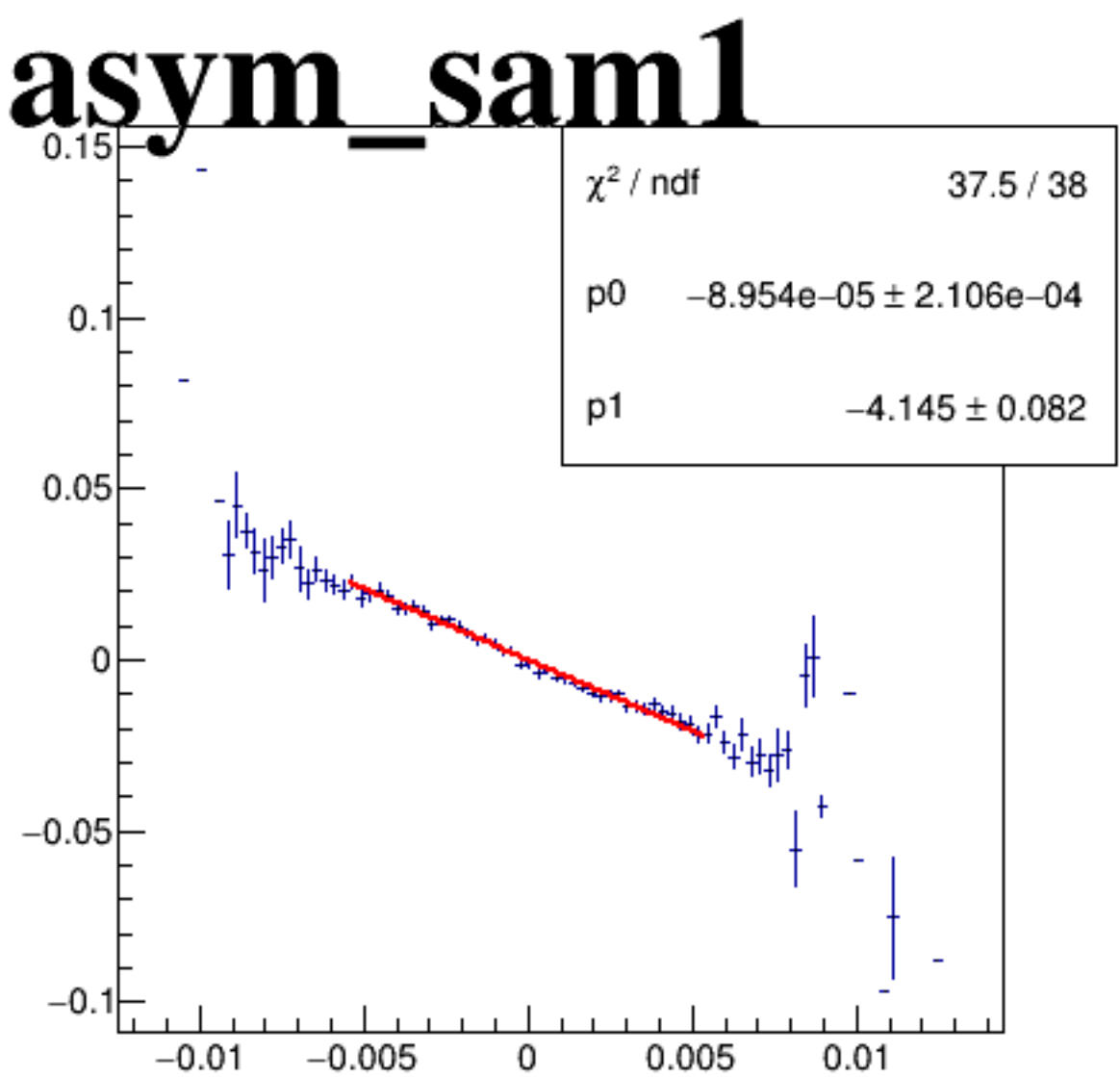
asym_sam8



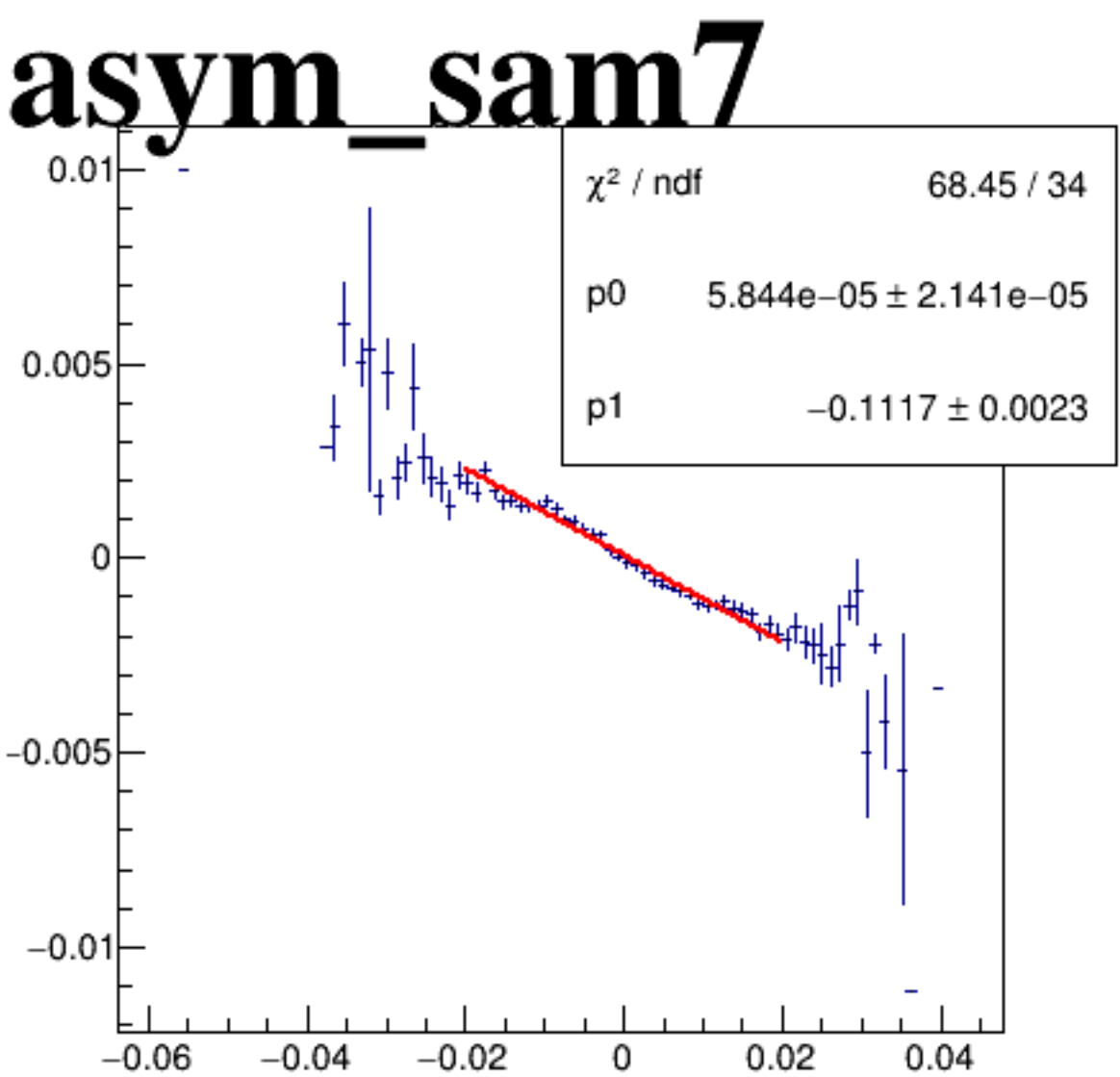
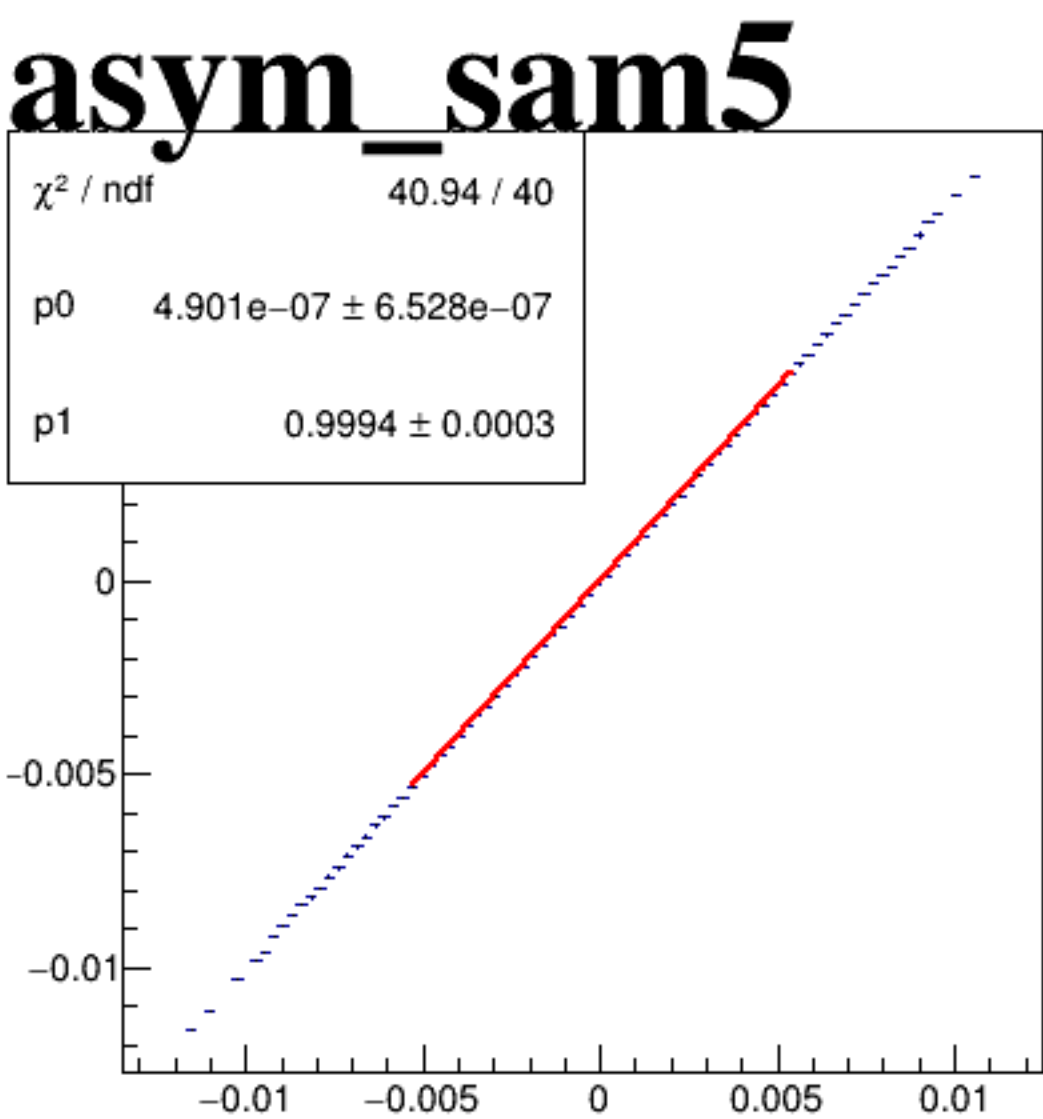
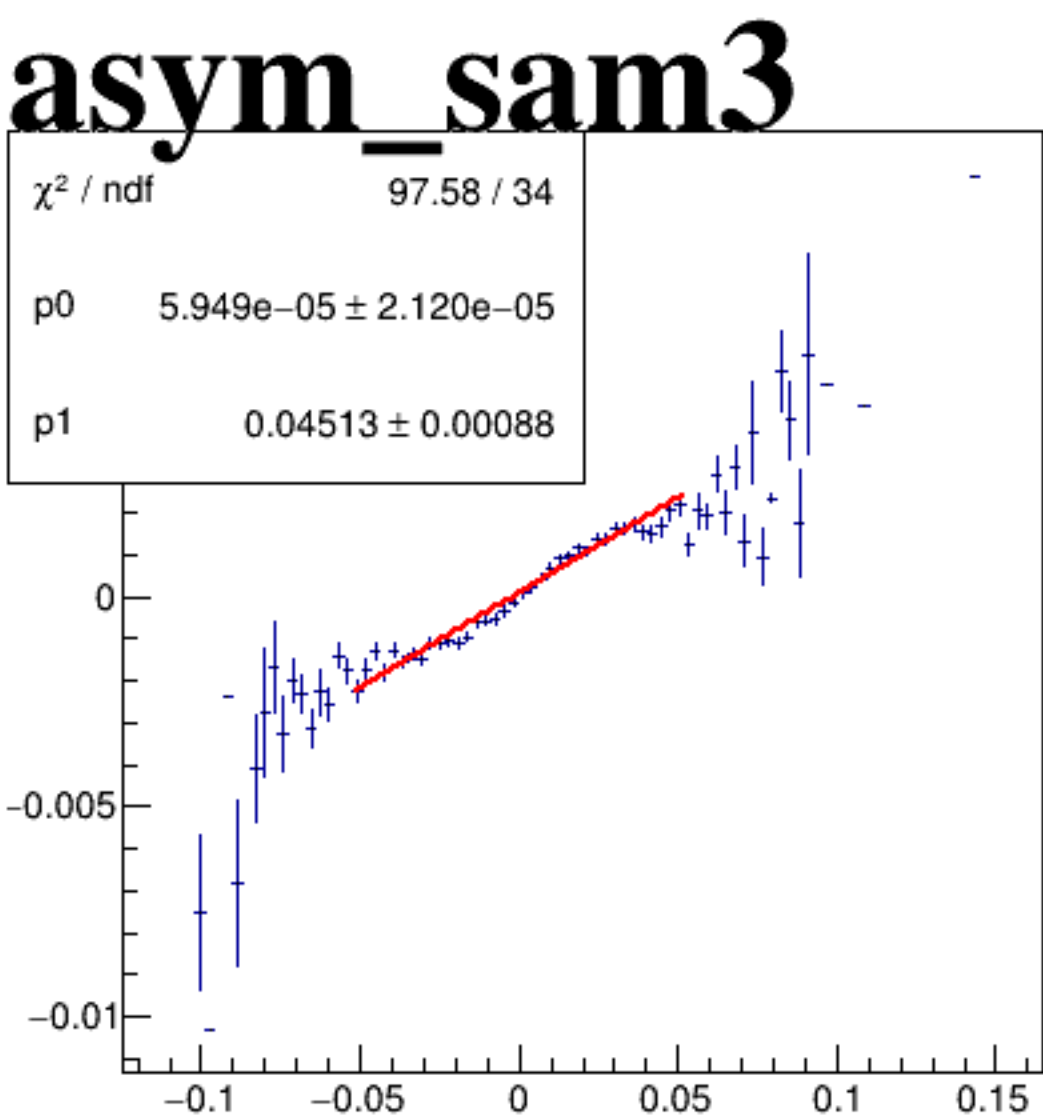
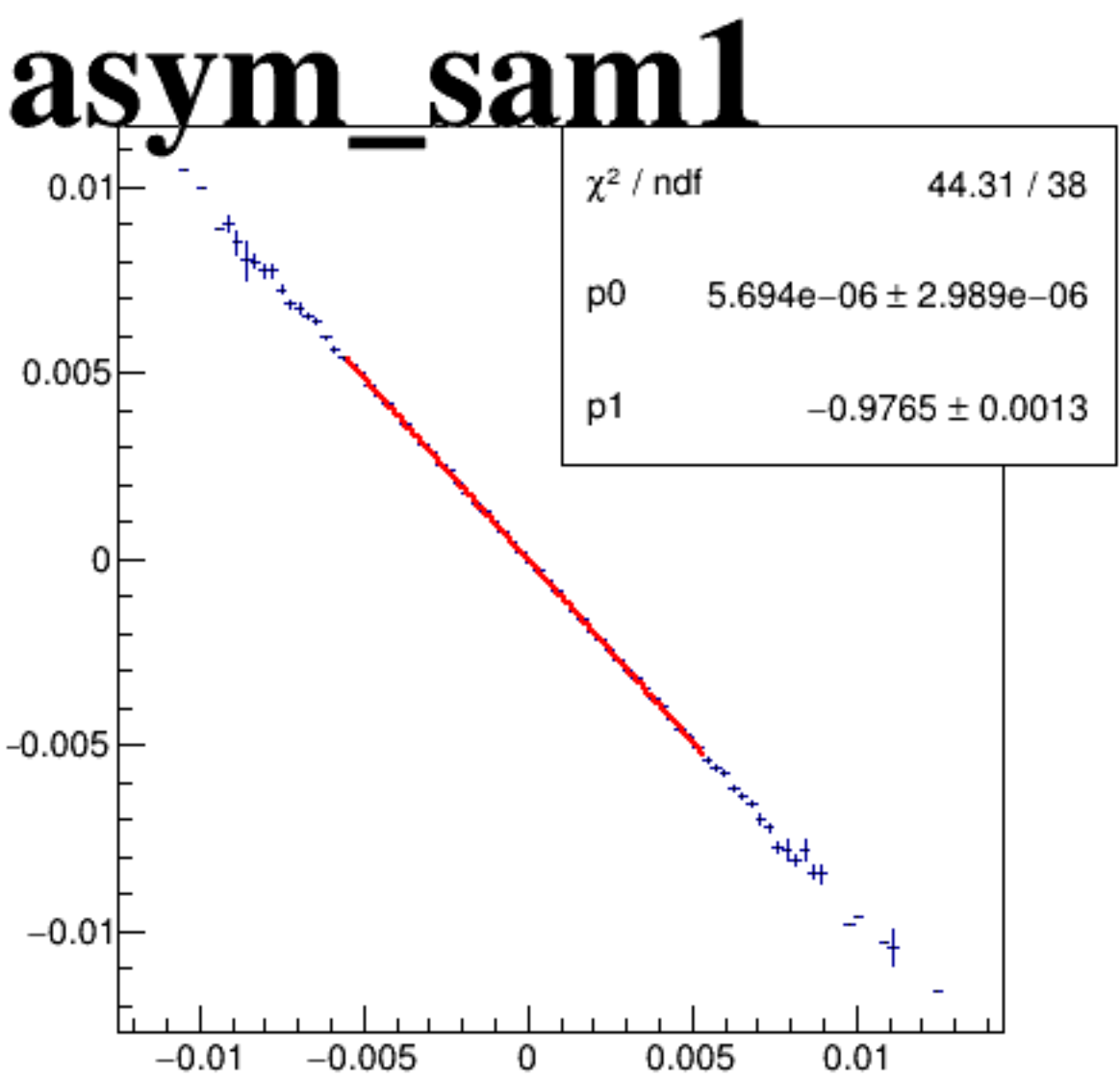
asym_sam1



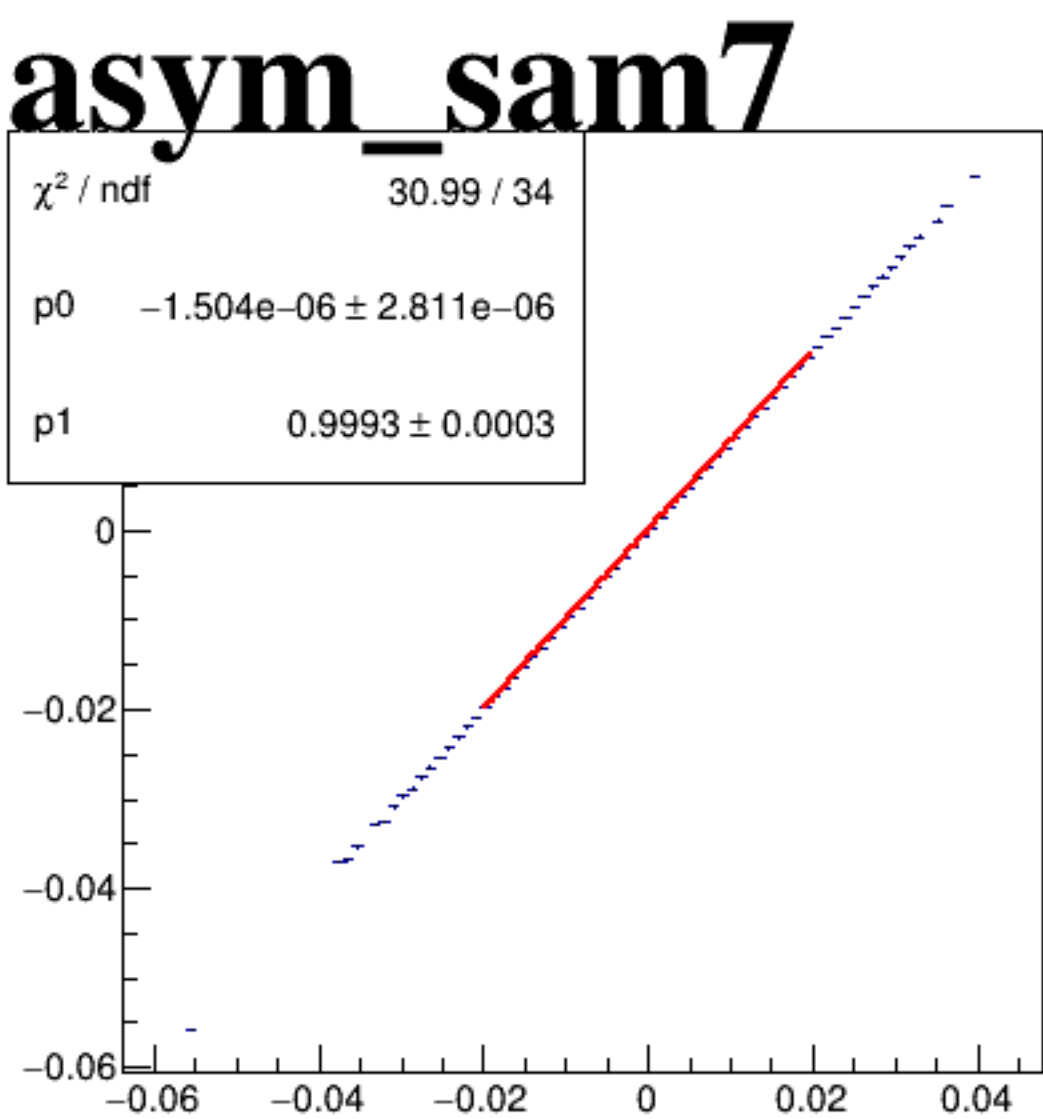
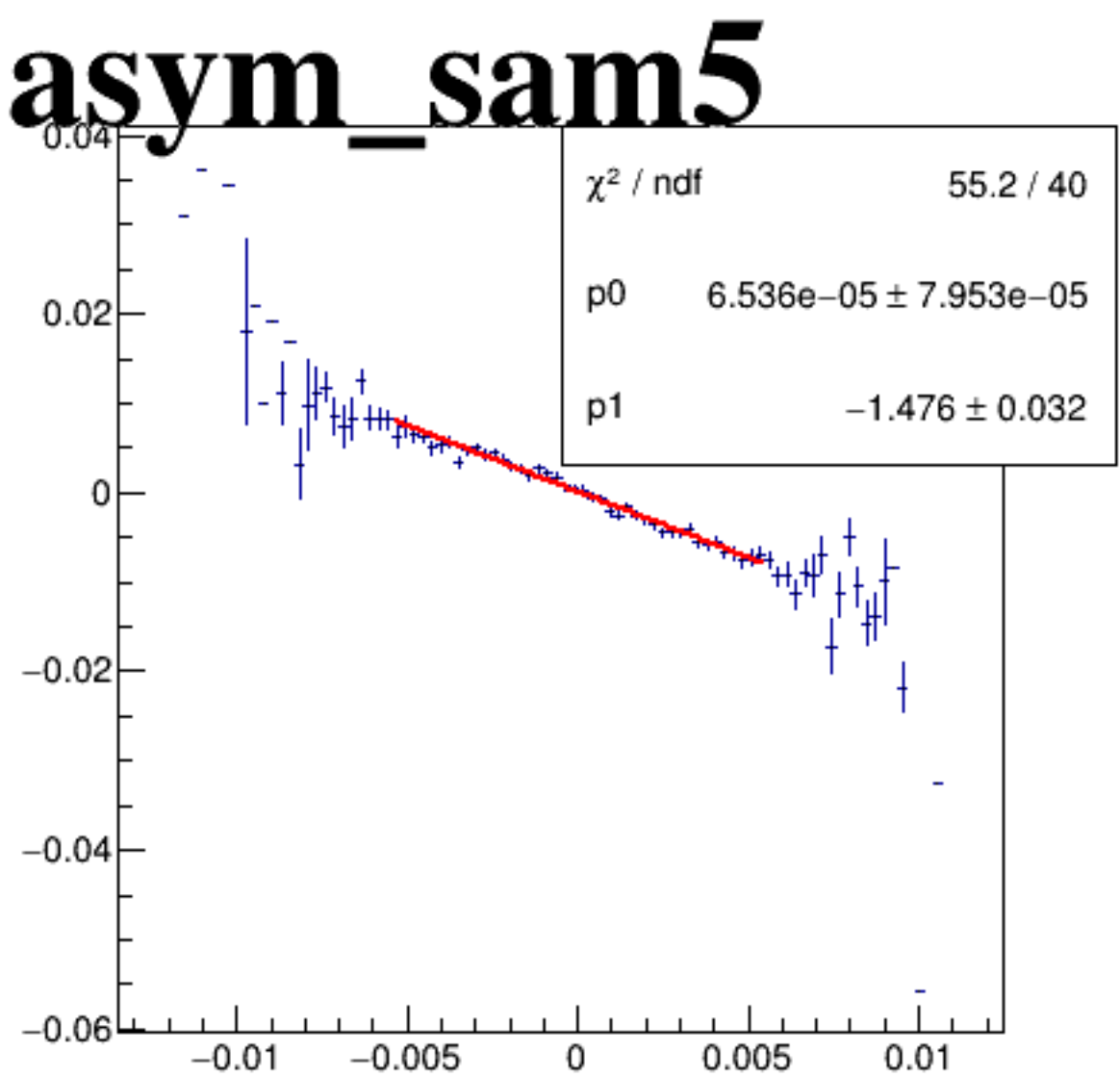
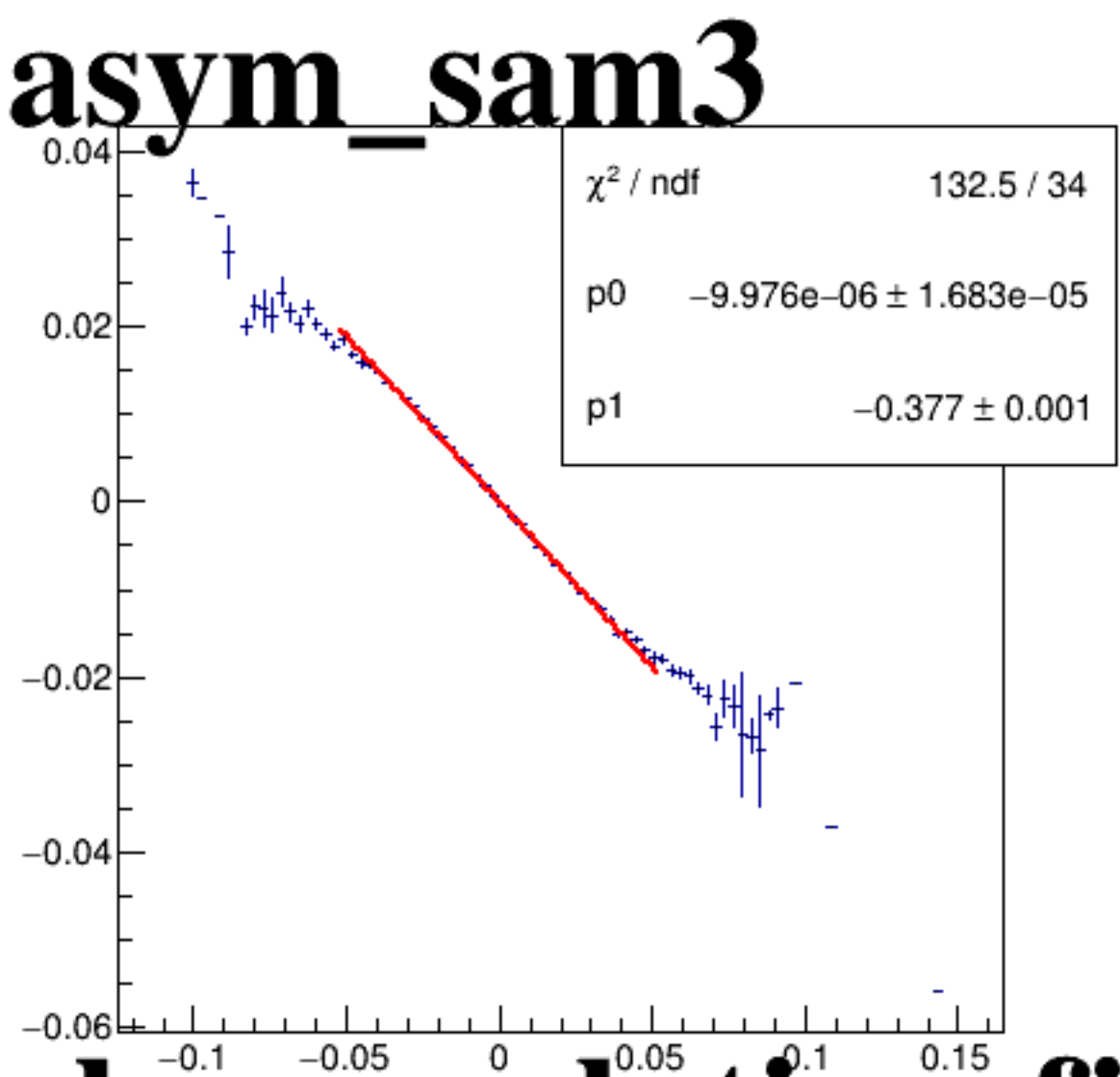
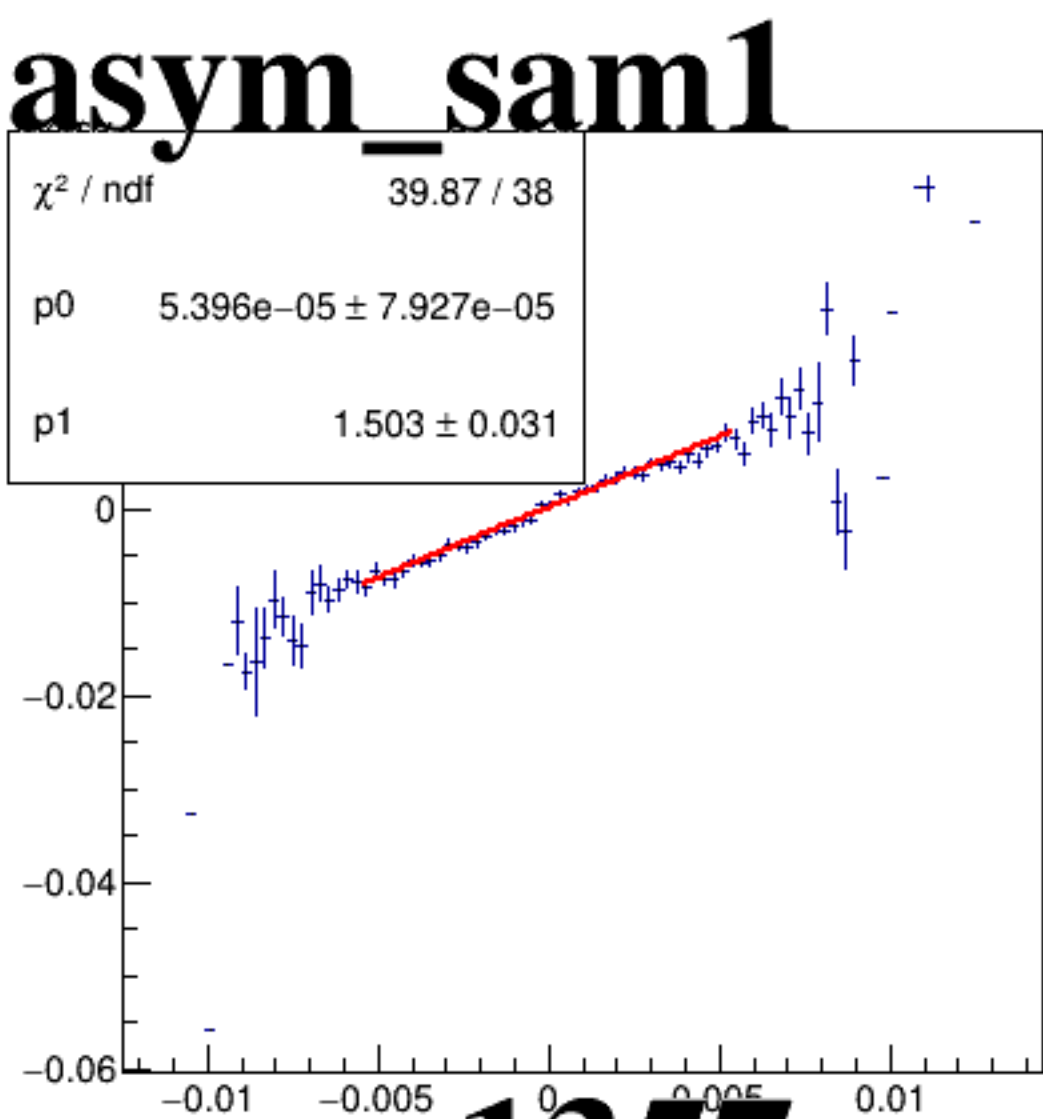
asym_sam3



asym_sam5



asym_sam7



asym_sam7

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