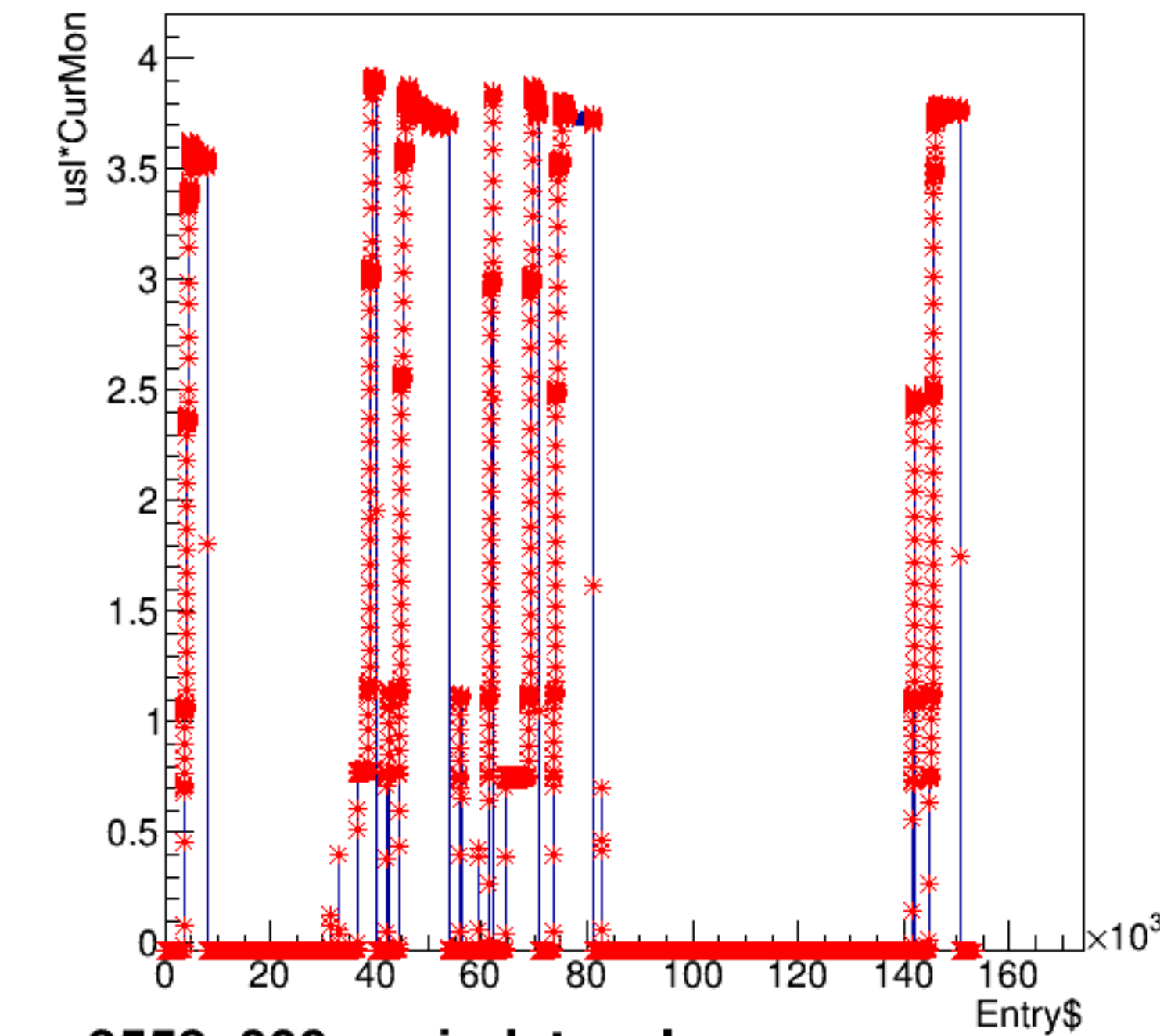
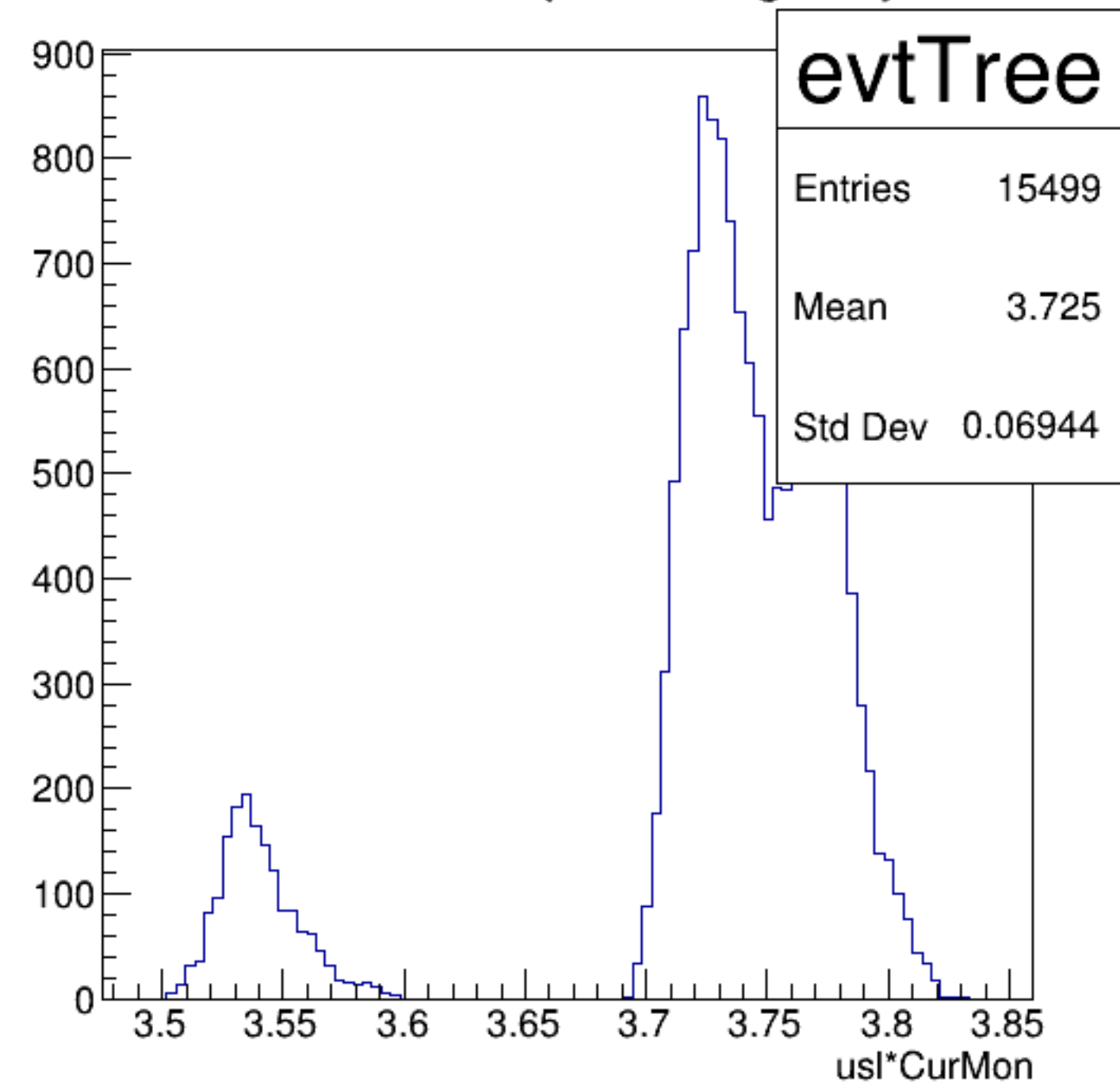


usl*CurMon:Entry\$

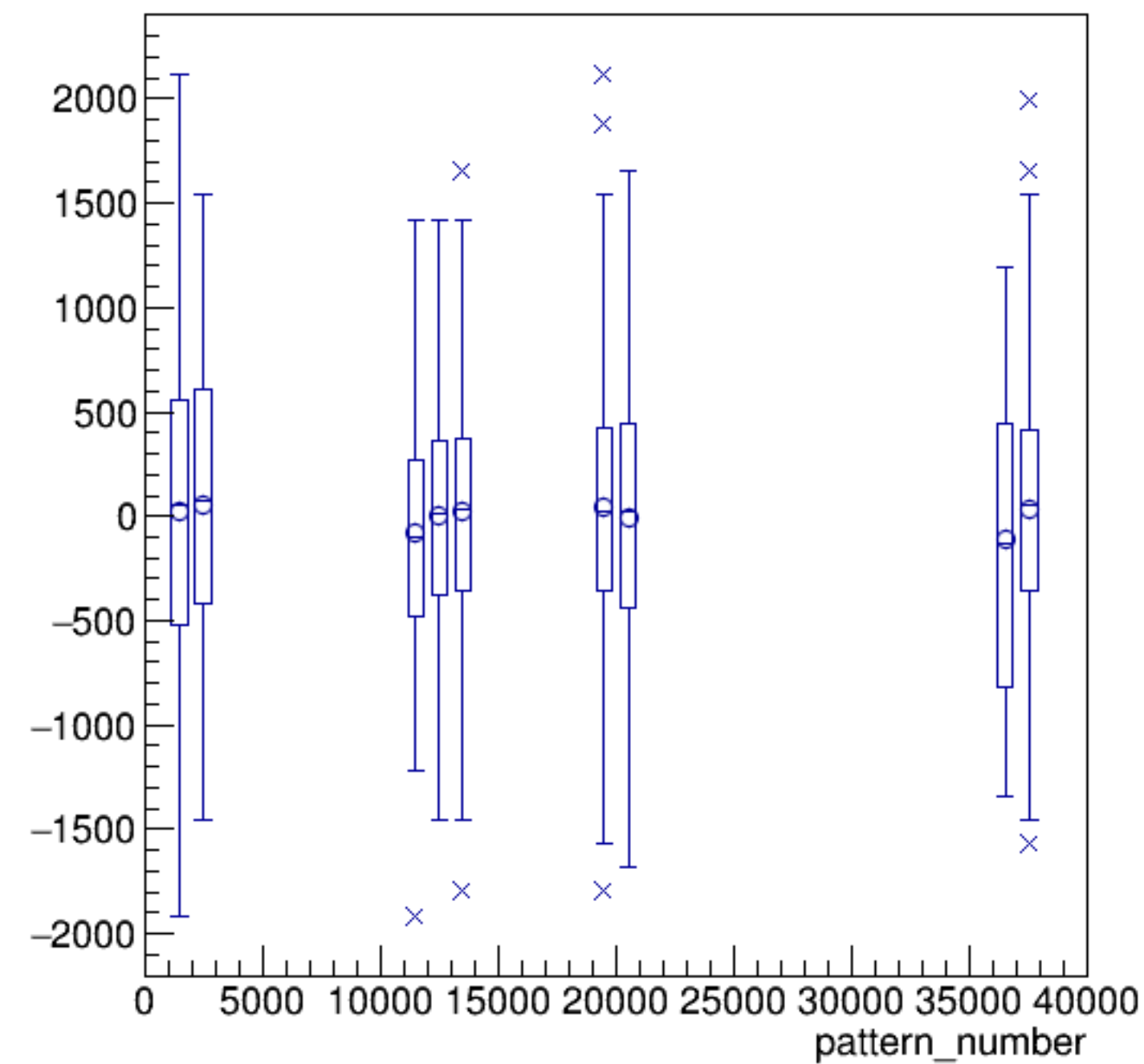


run3553_000_maindet_usl.png

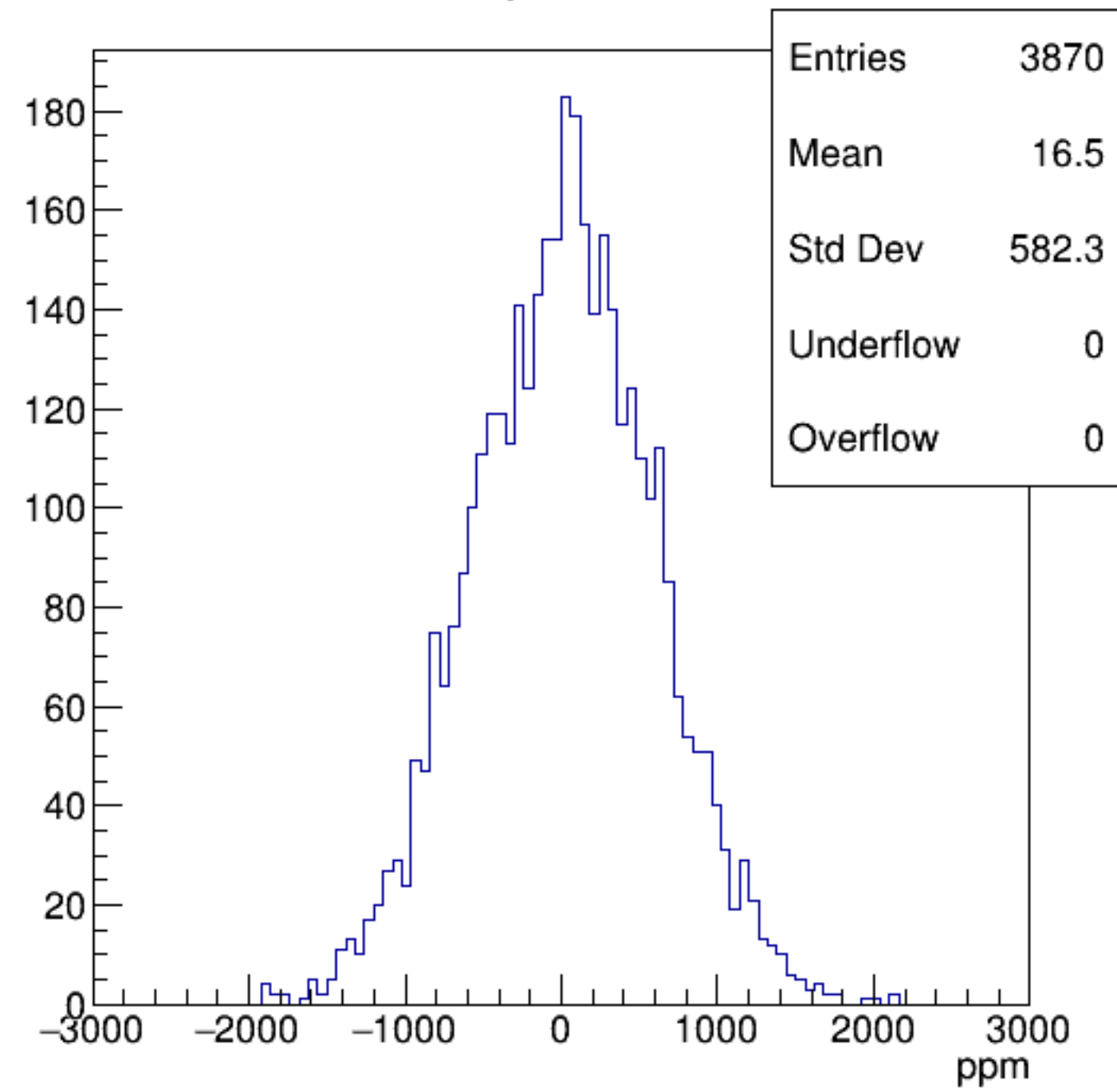
usl*CurMon {ErrorFlag==0}



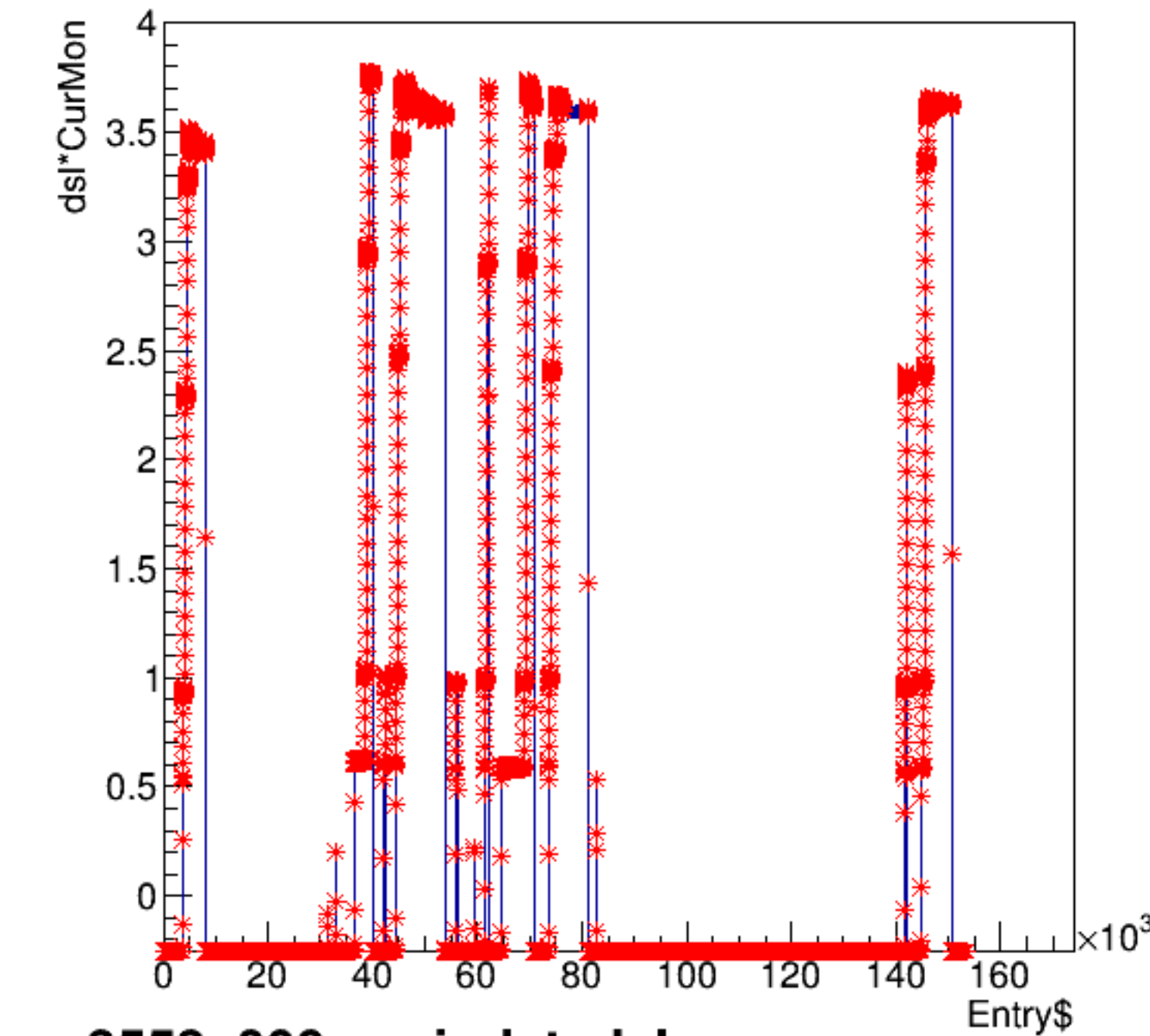
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asym_usl

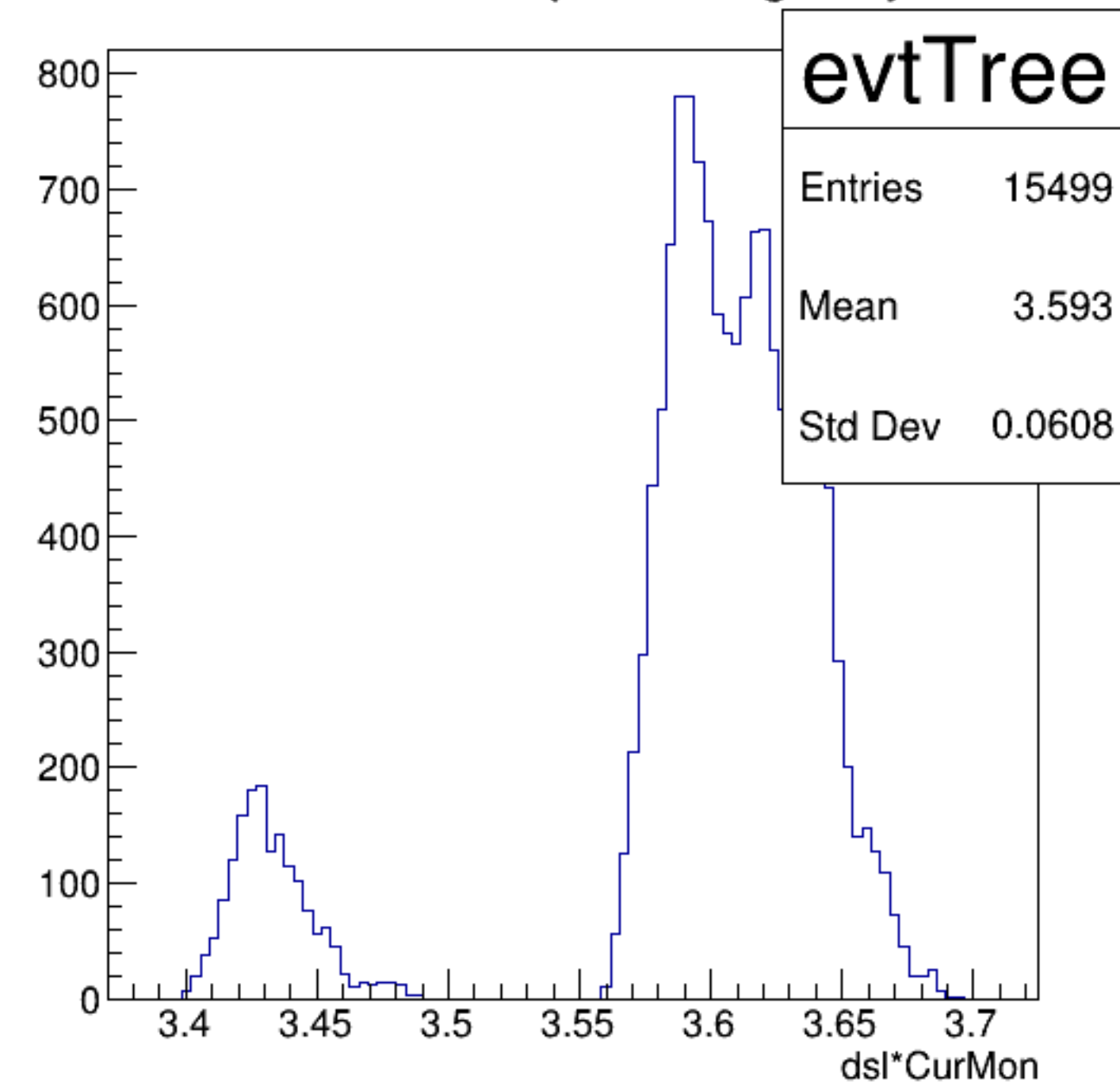


dsl*CurMon:Entry\$

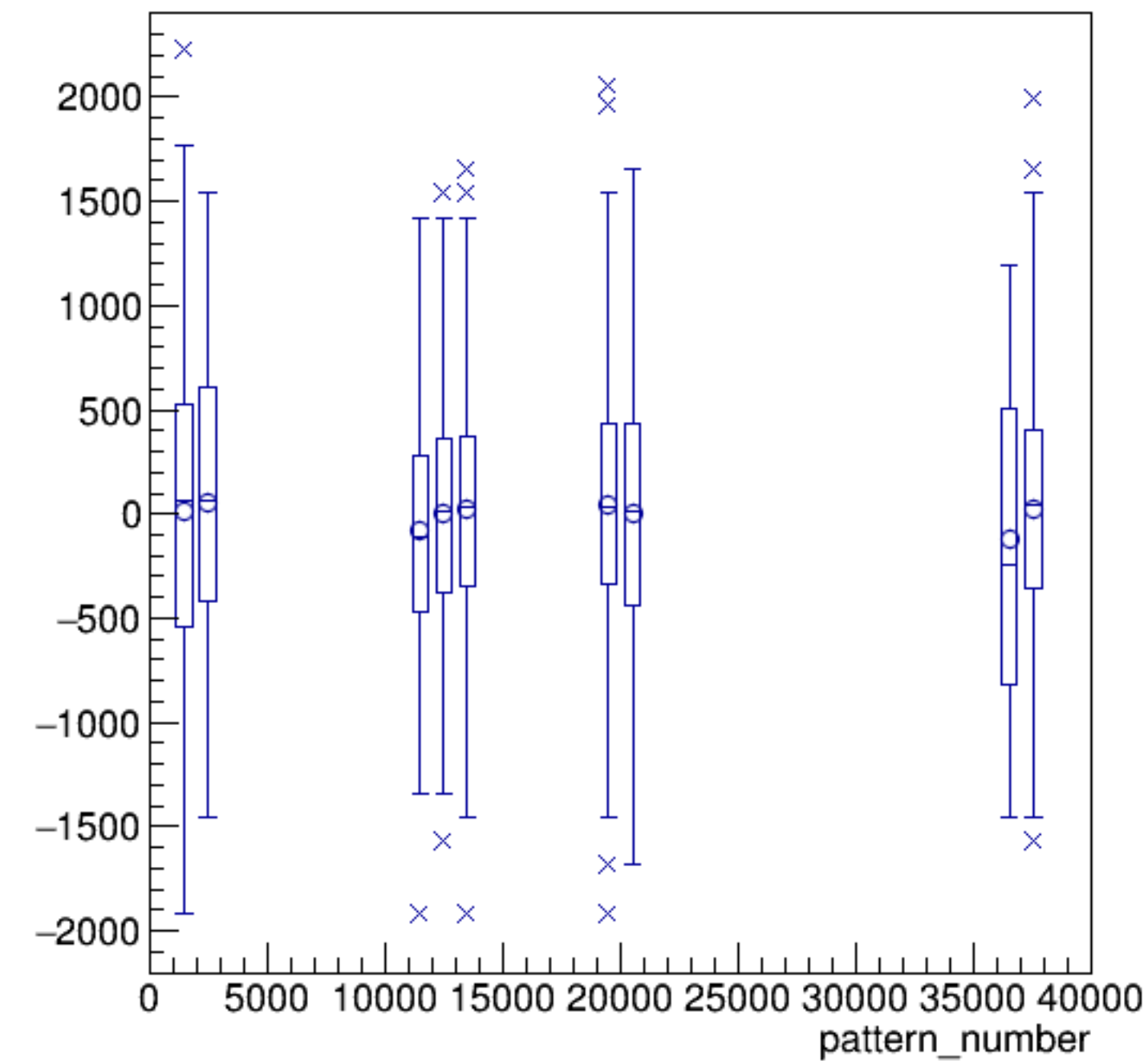


run3553_000_maindet_dsl.png

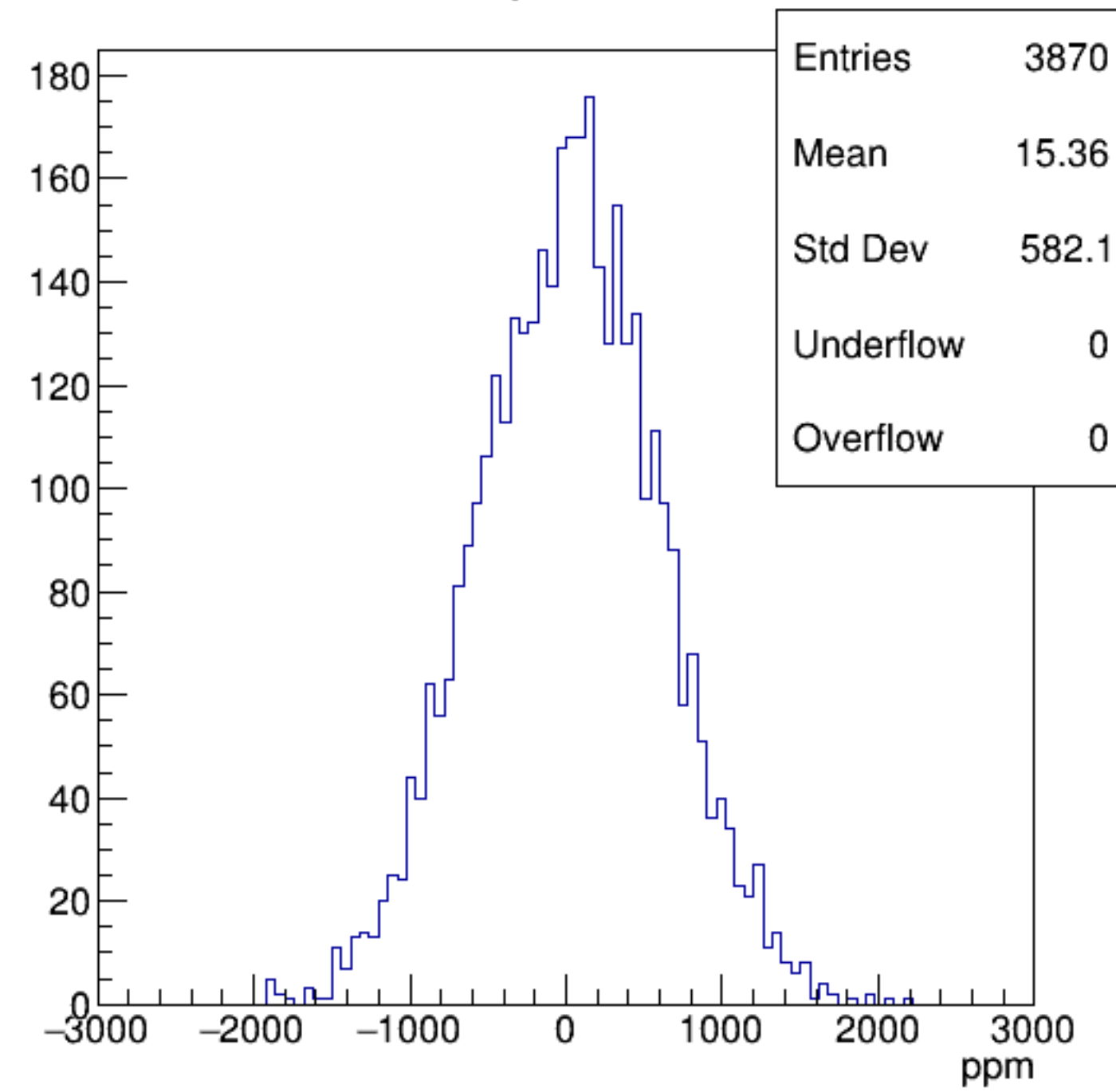
dsl*CurMon {ErrorFlag==0}



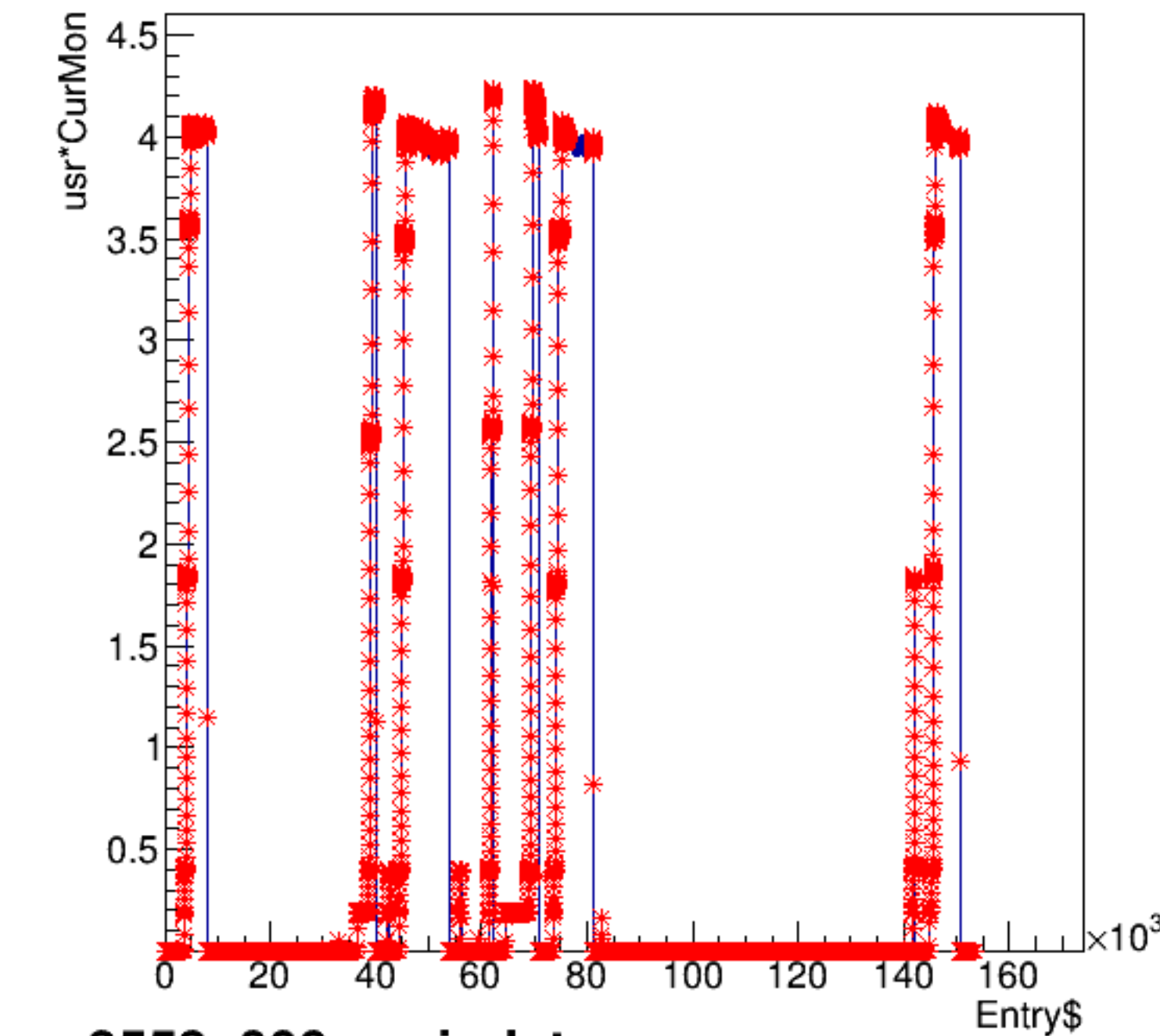
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asym_dsl

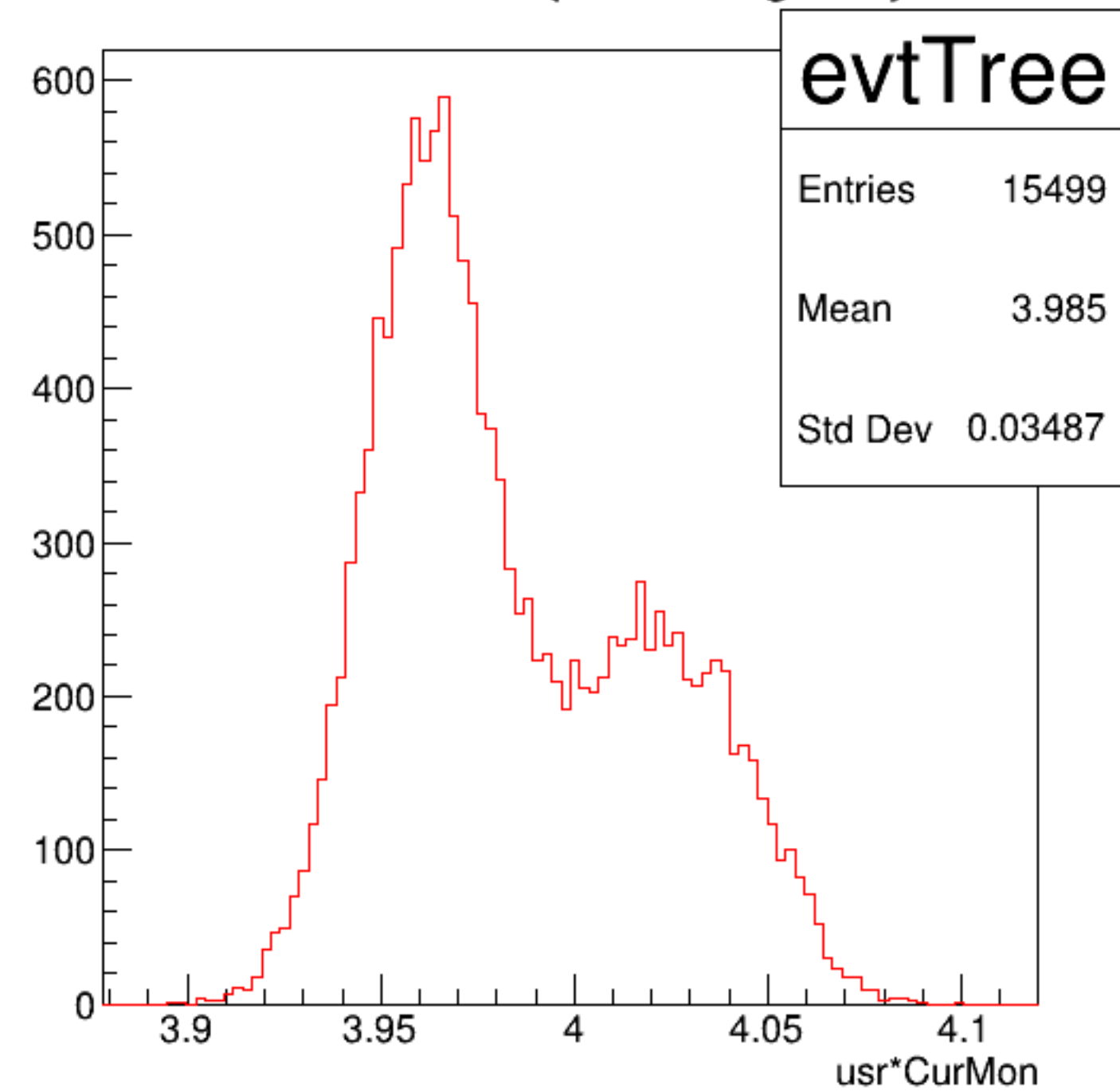


usr*CurMon:Entry\$

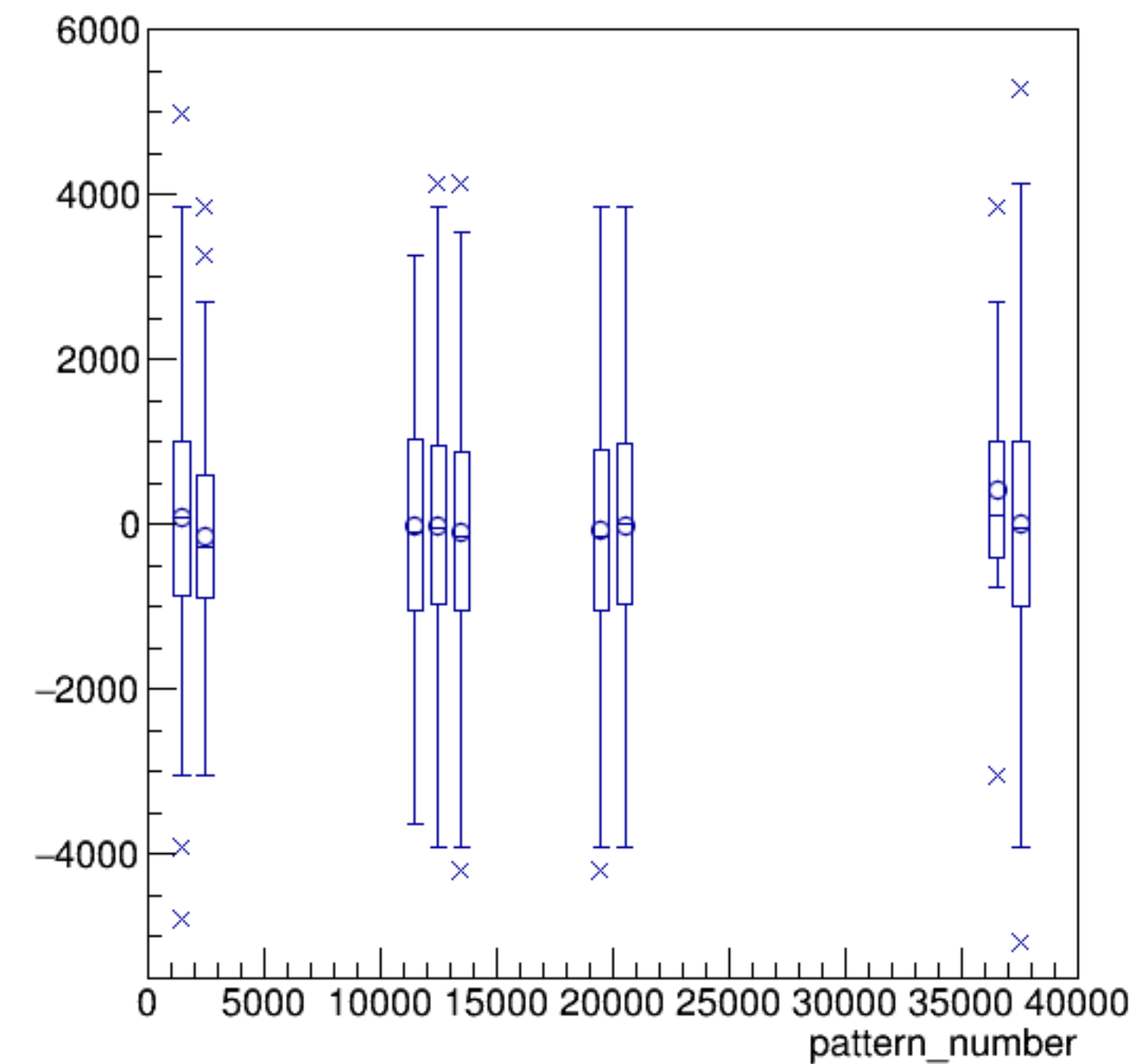


run3553_000_maindet_usr.png

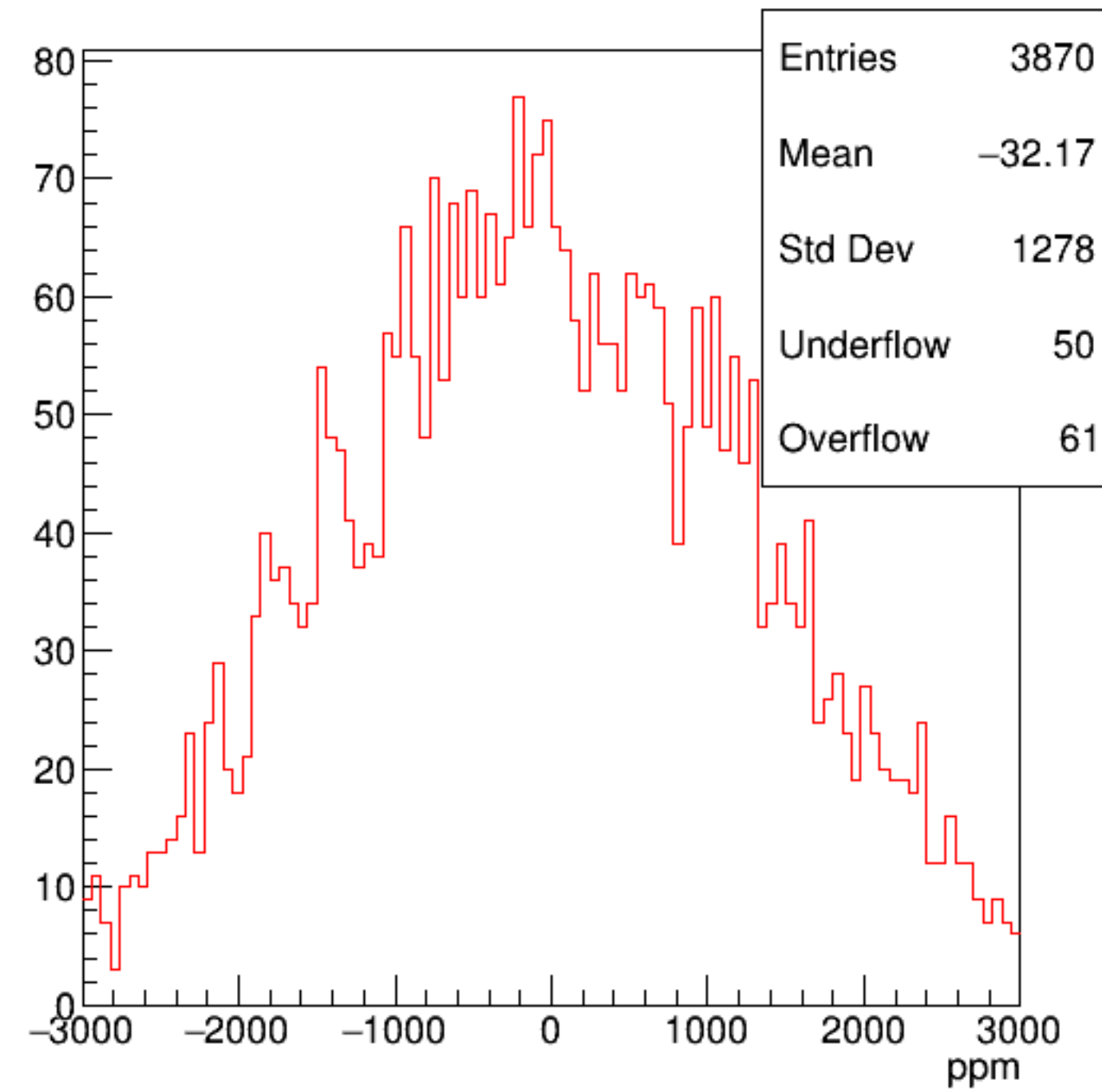
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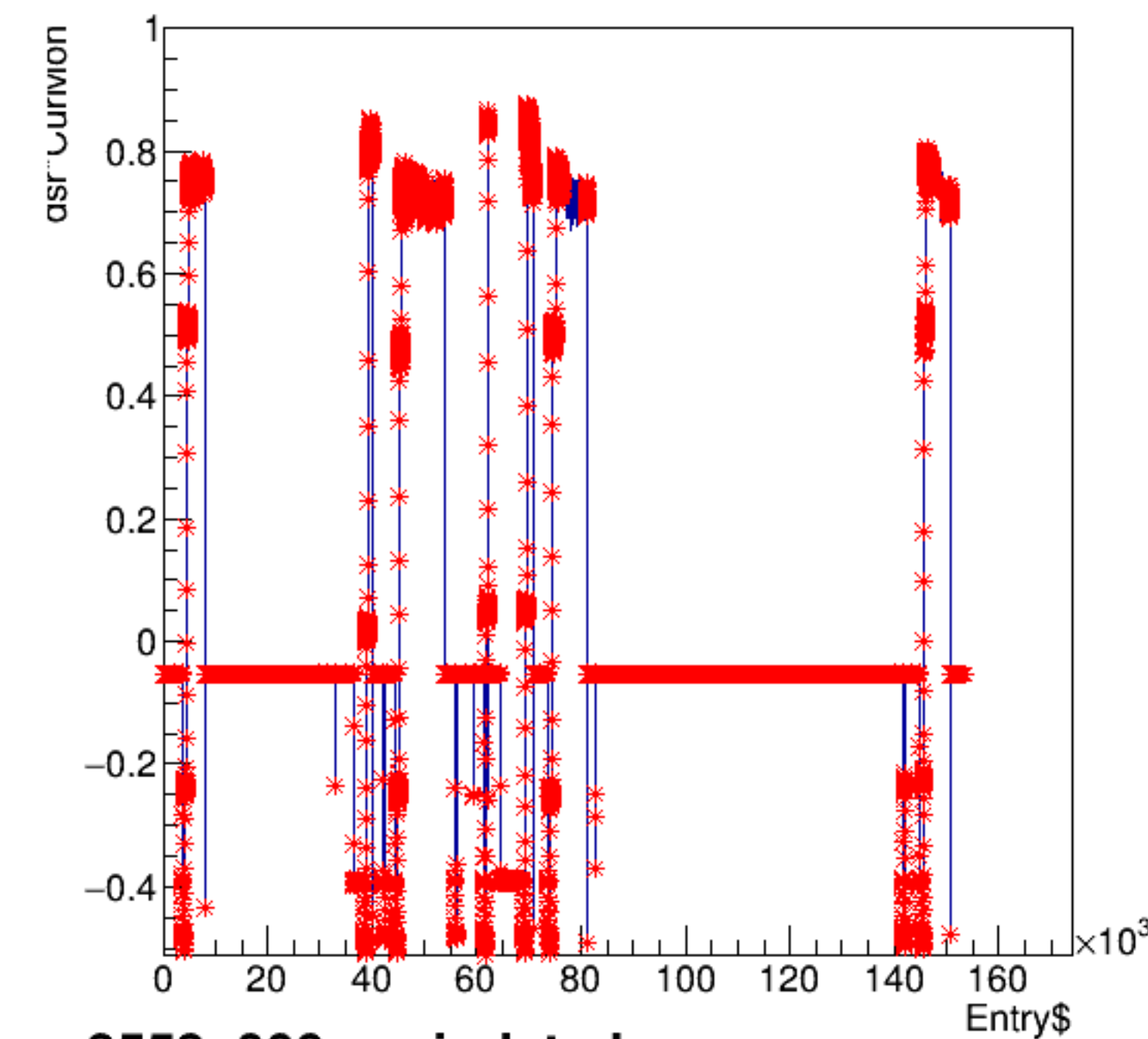
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asym_usr

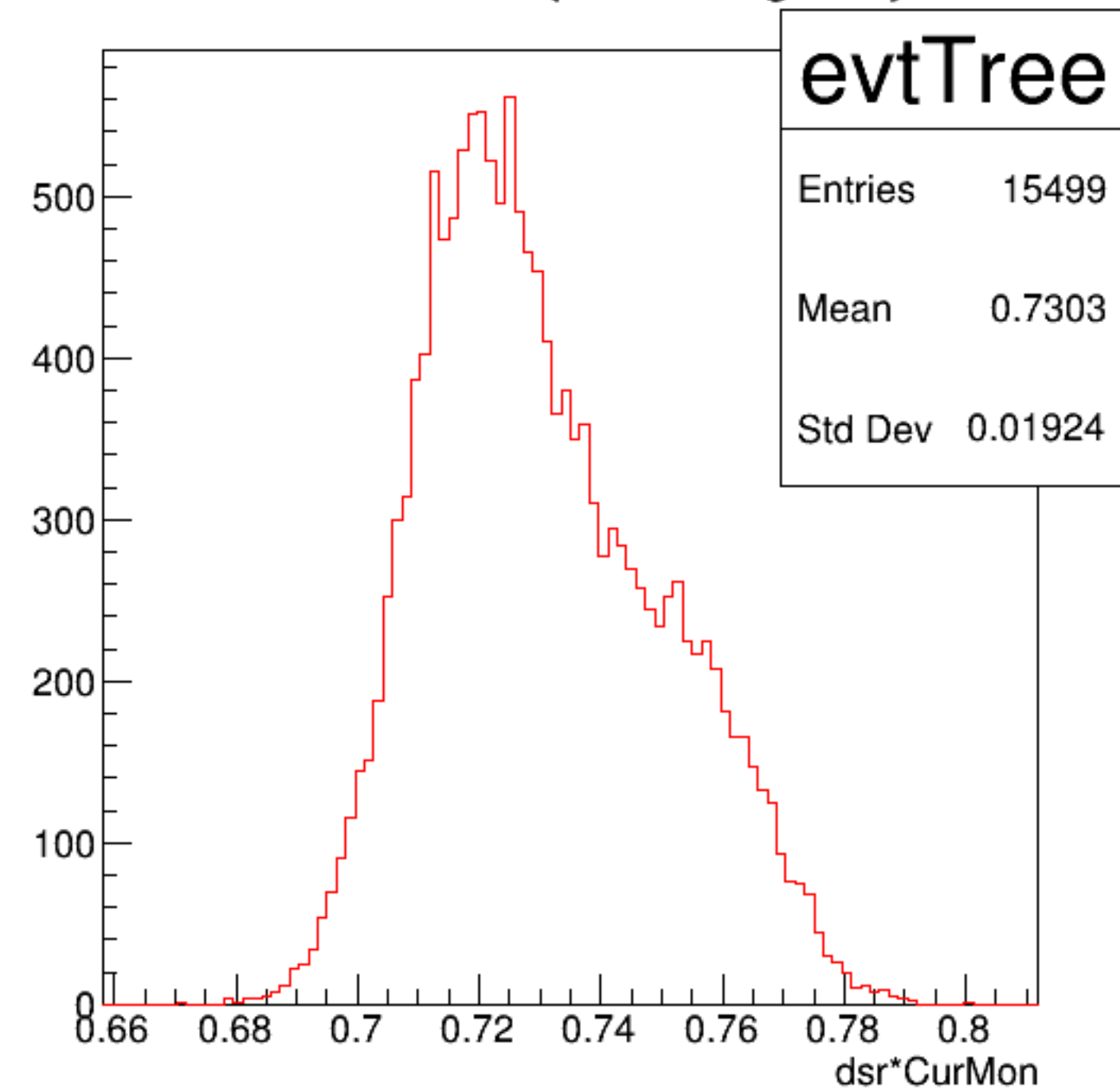


dsr*CurMon:Entry\$

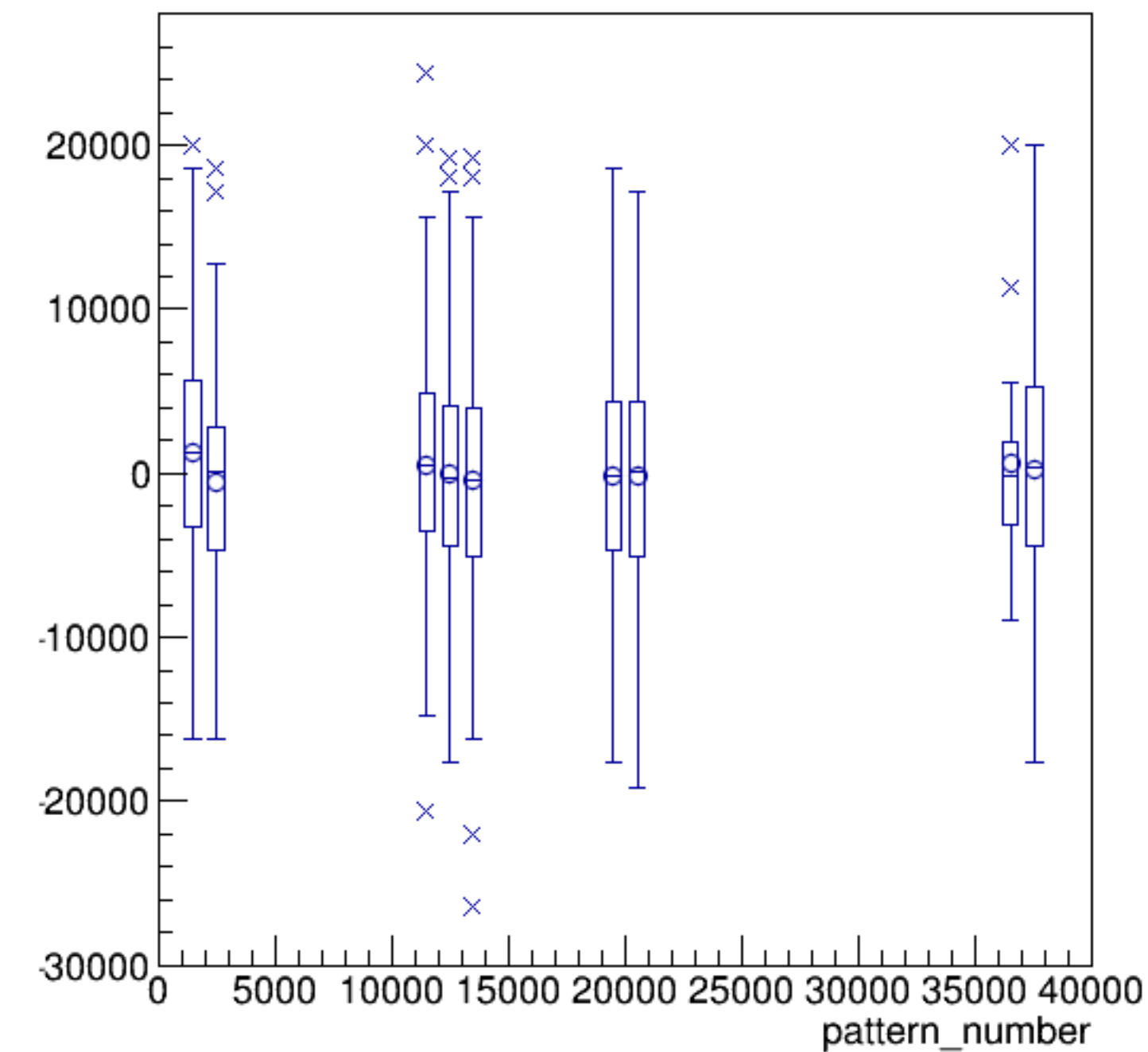


run3553_000_maindet_dsr.png

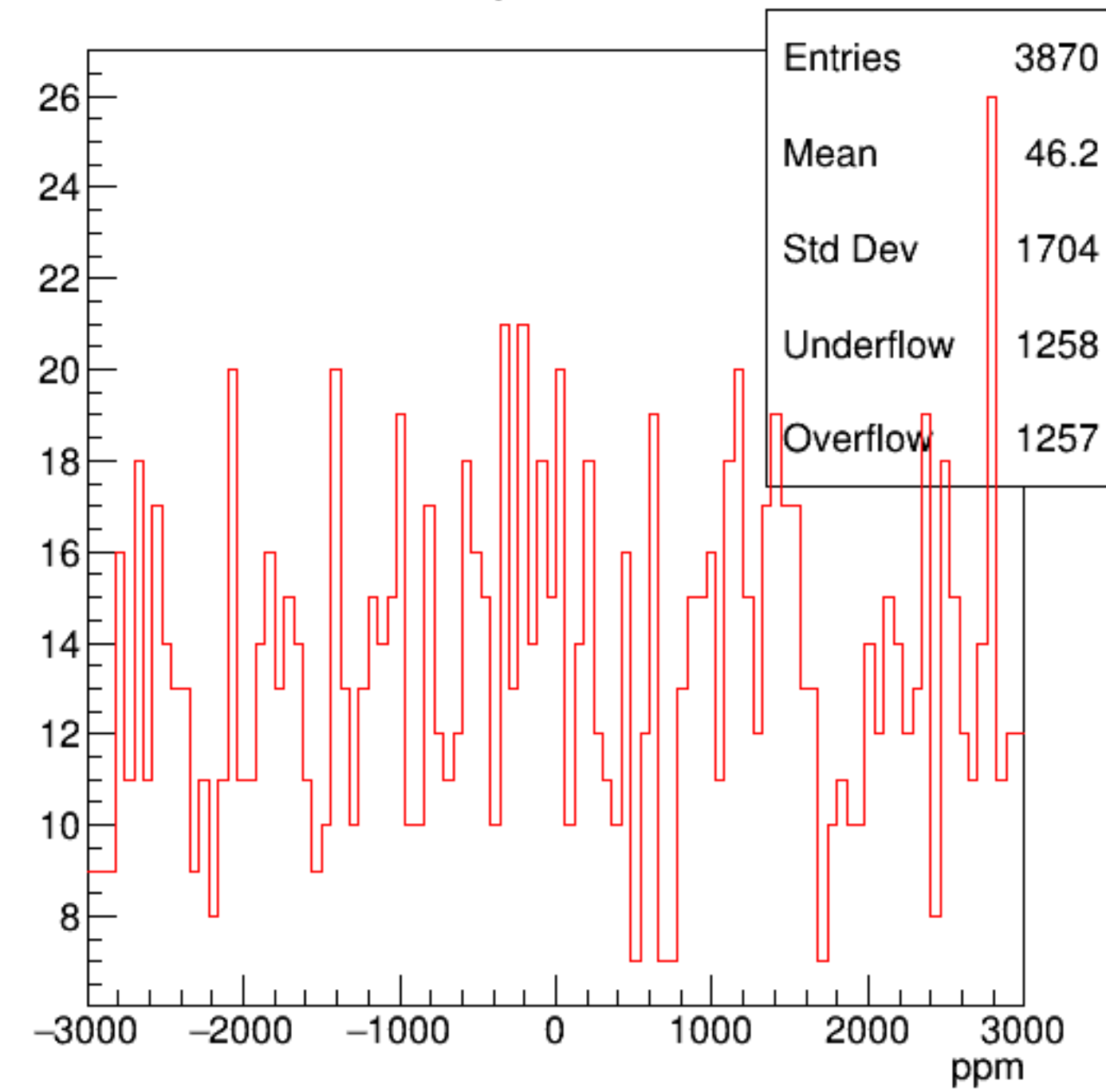
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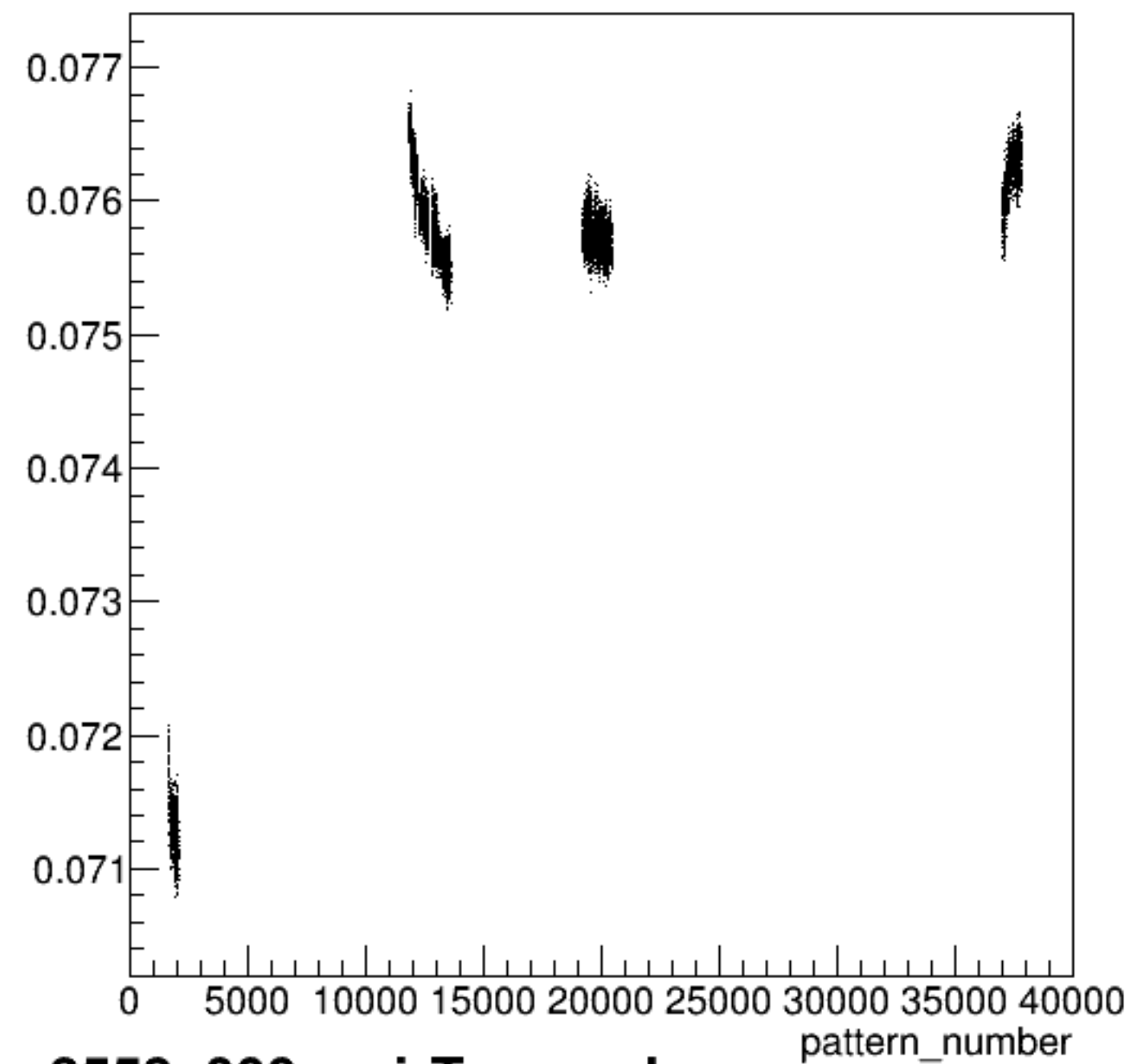
asym_dsr/ppm:pattern_number {ErrorFlag==0}



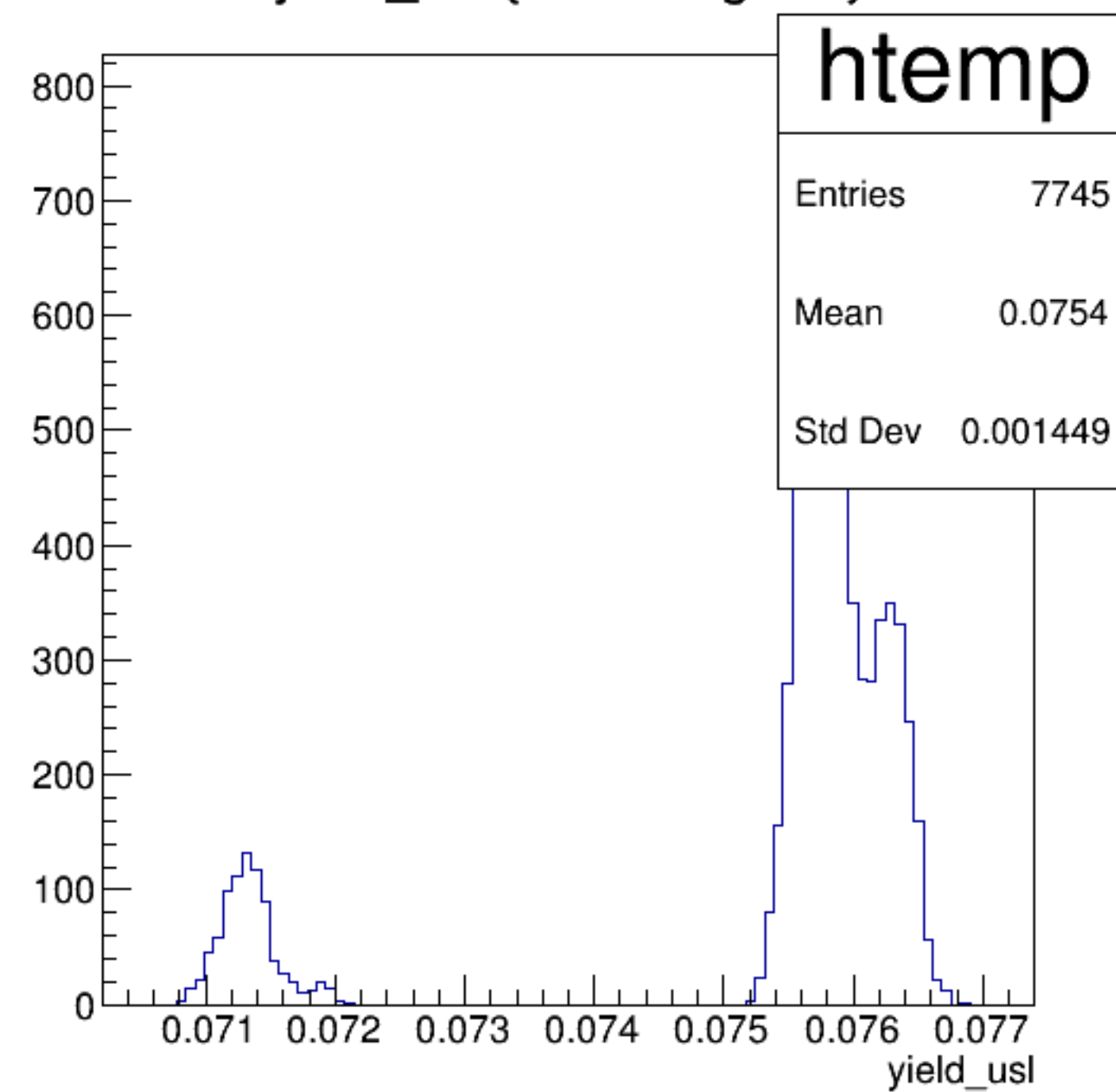
asym_dsr



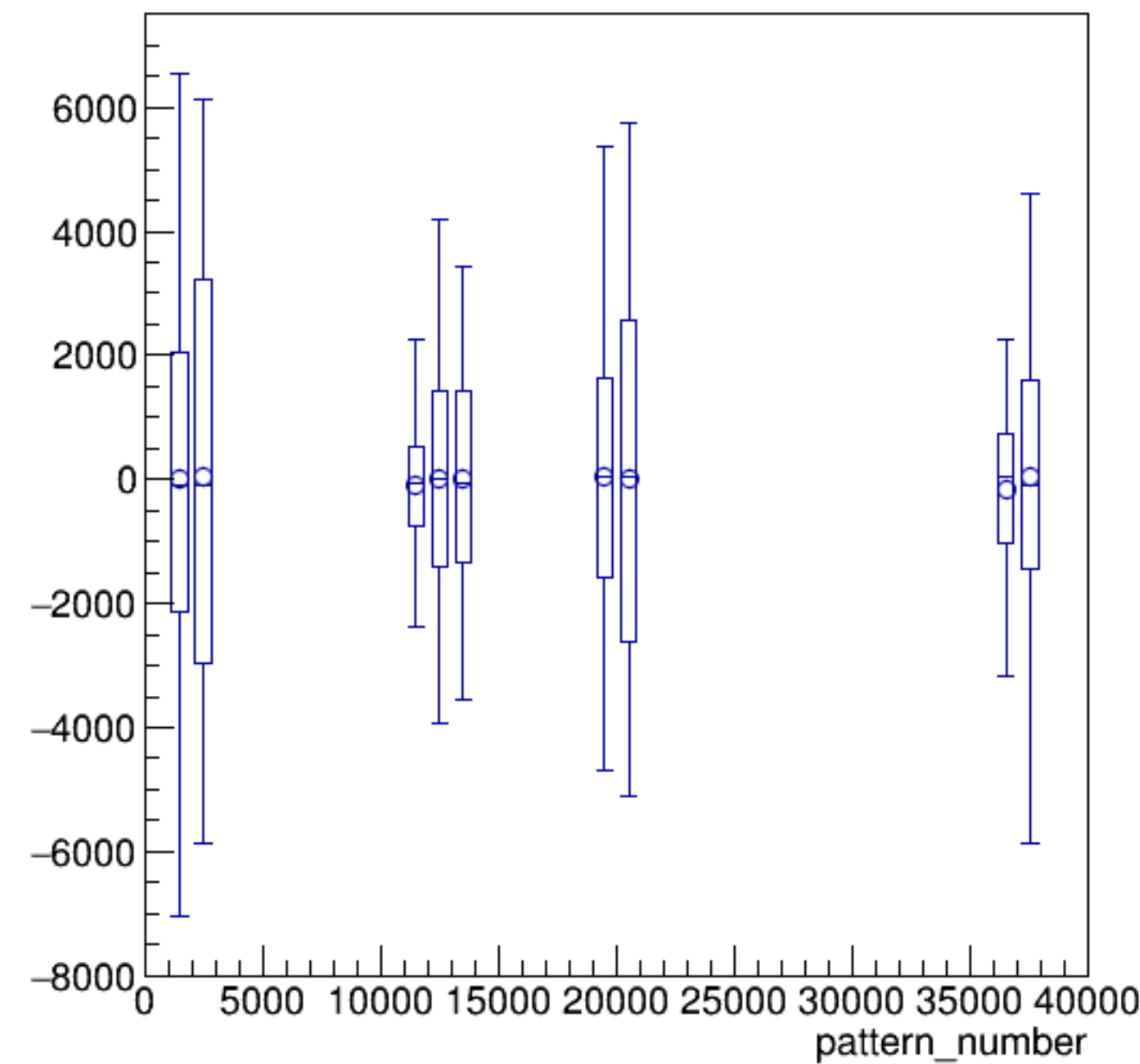
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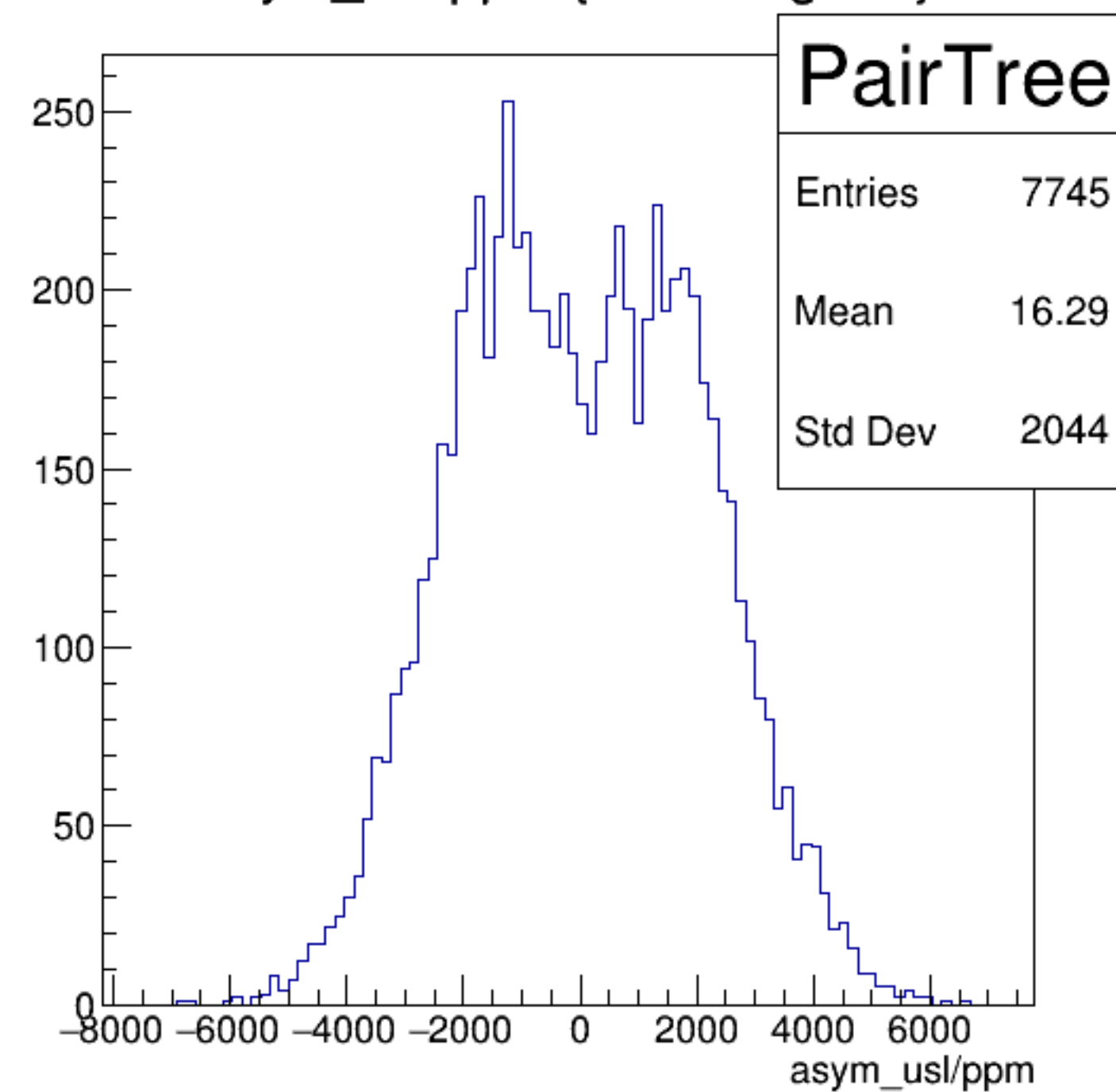
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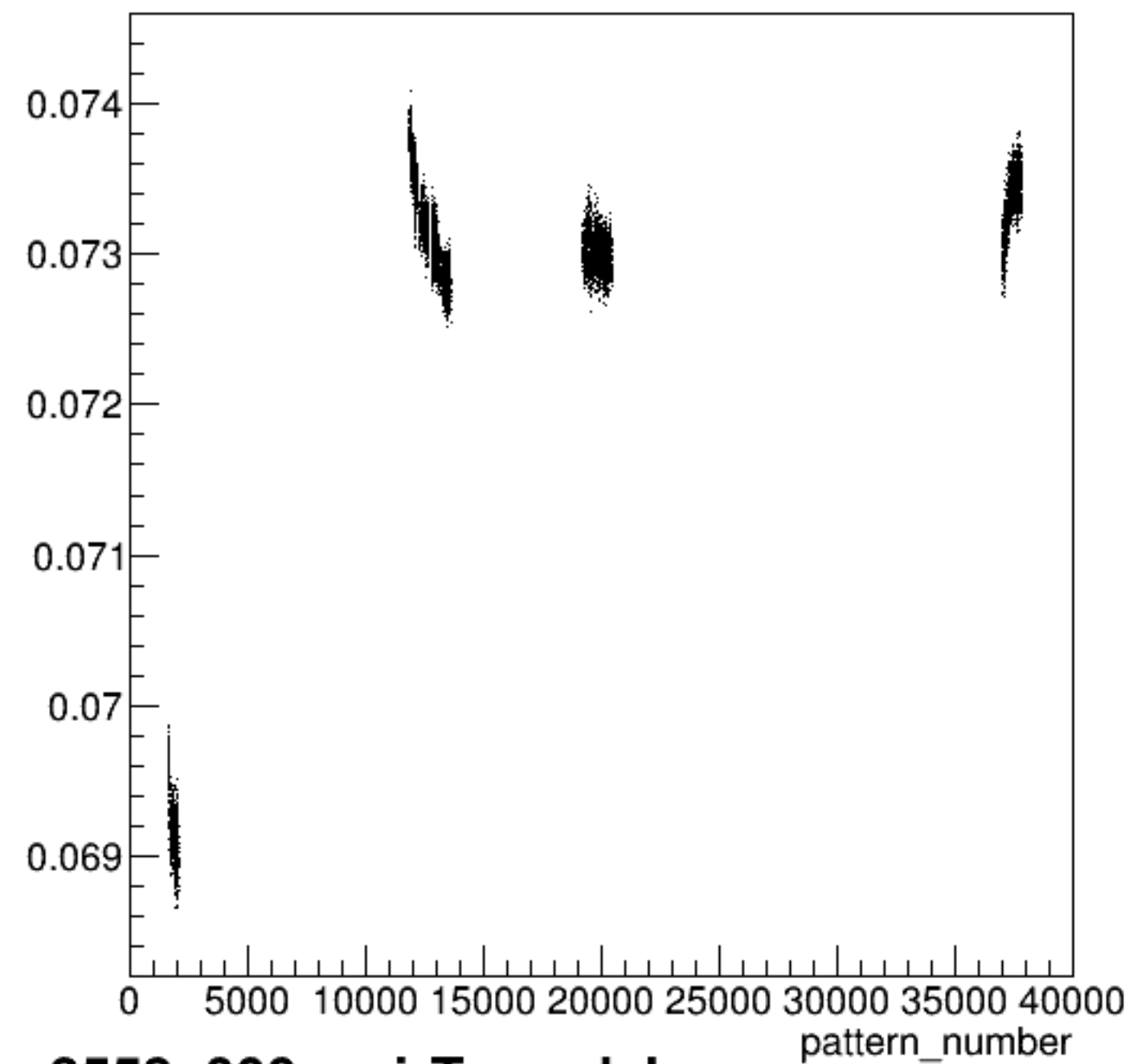
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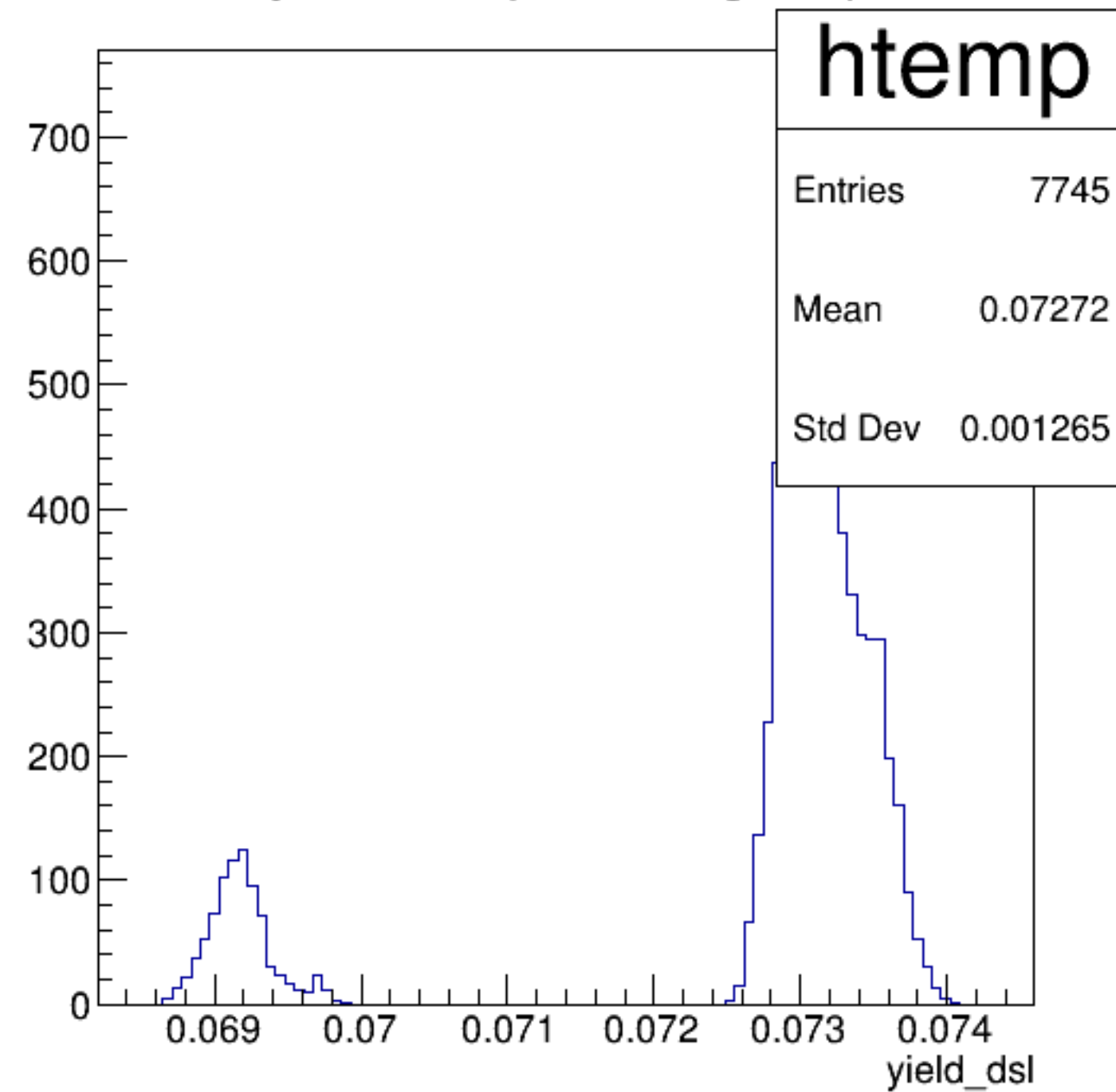
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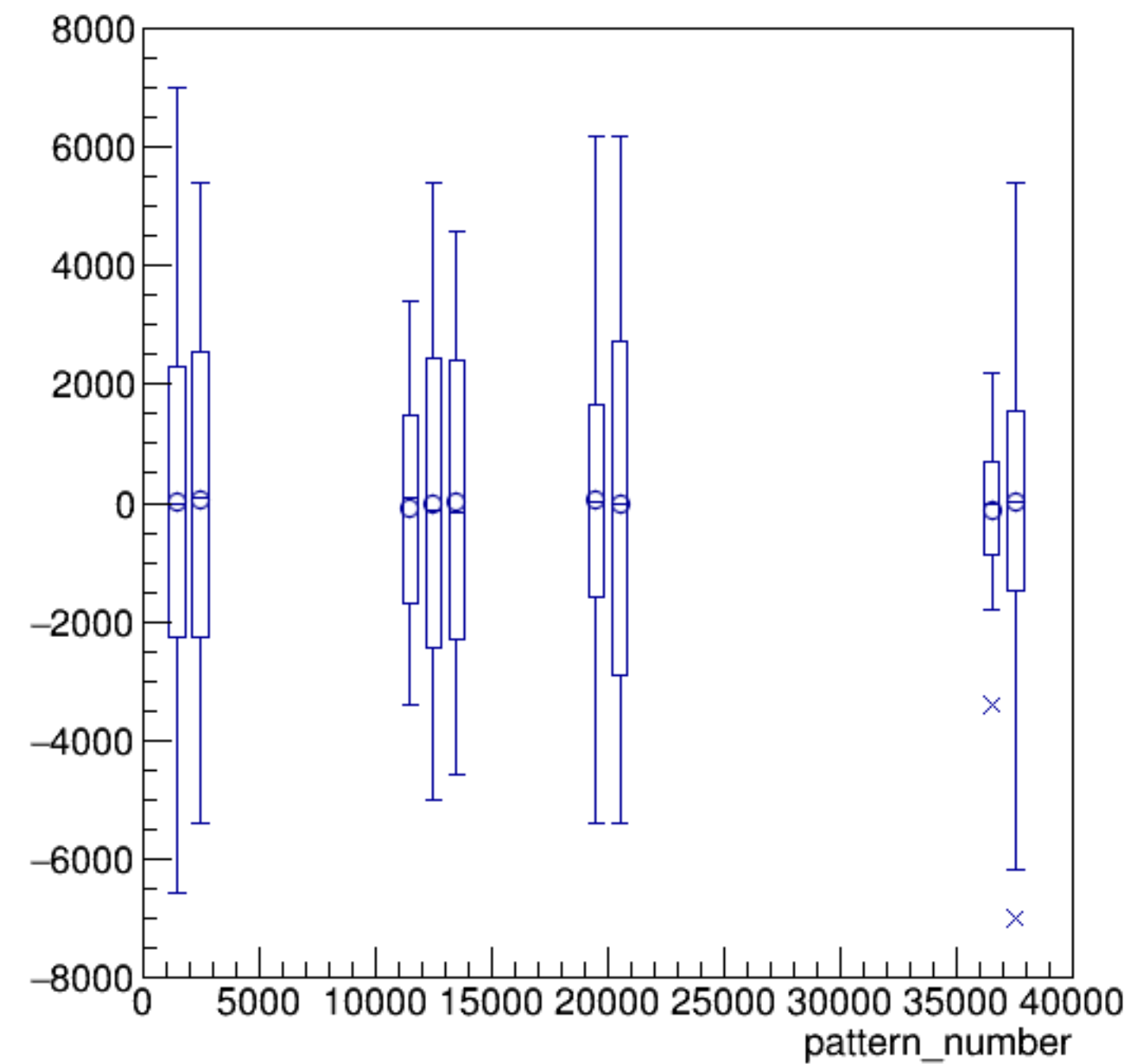
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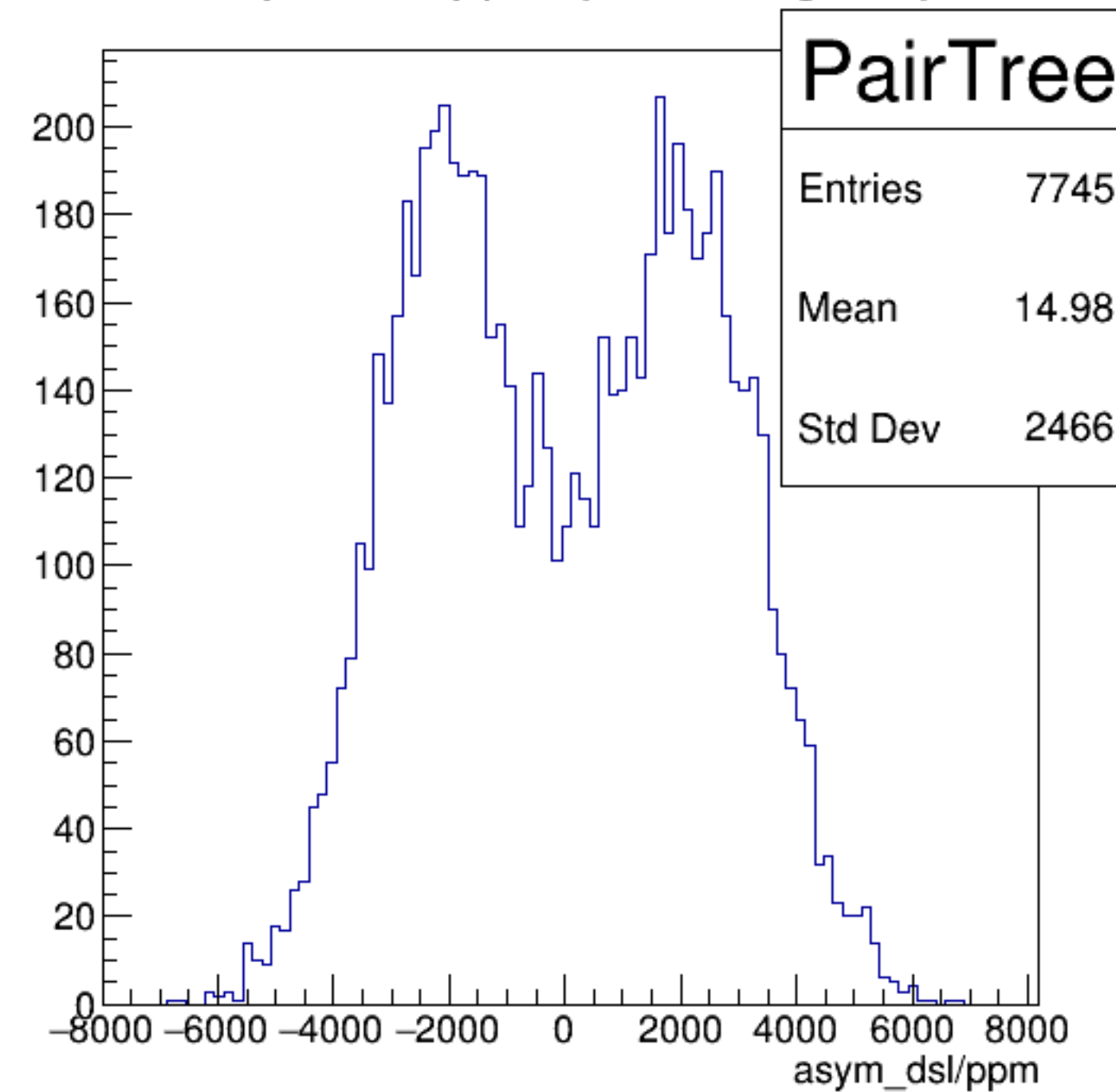
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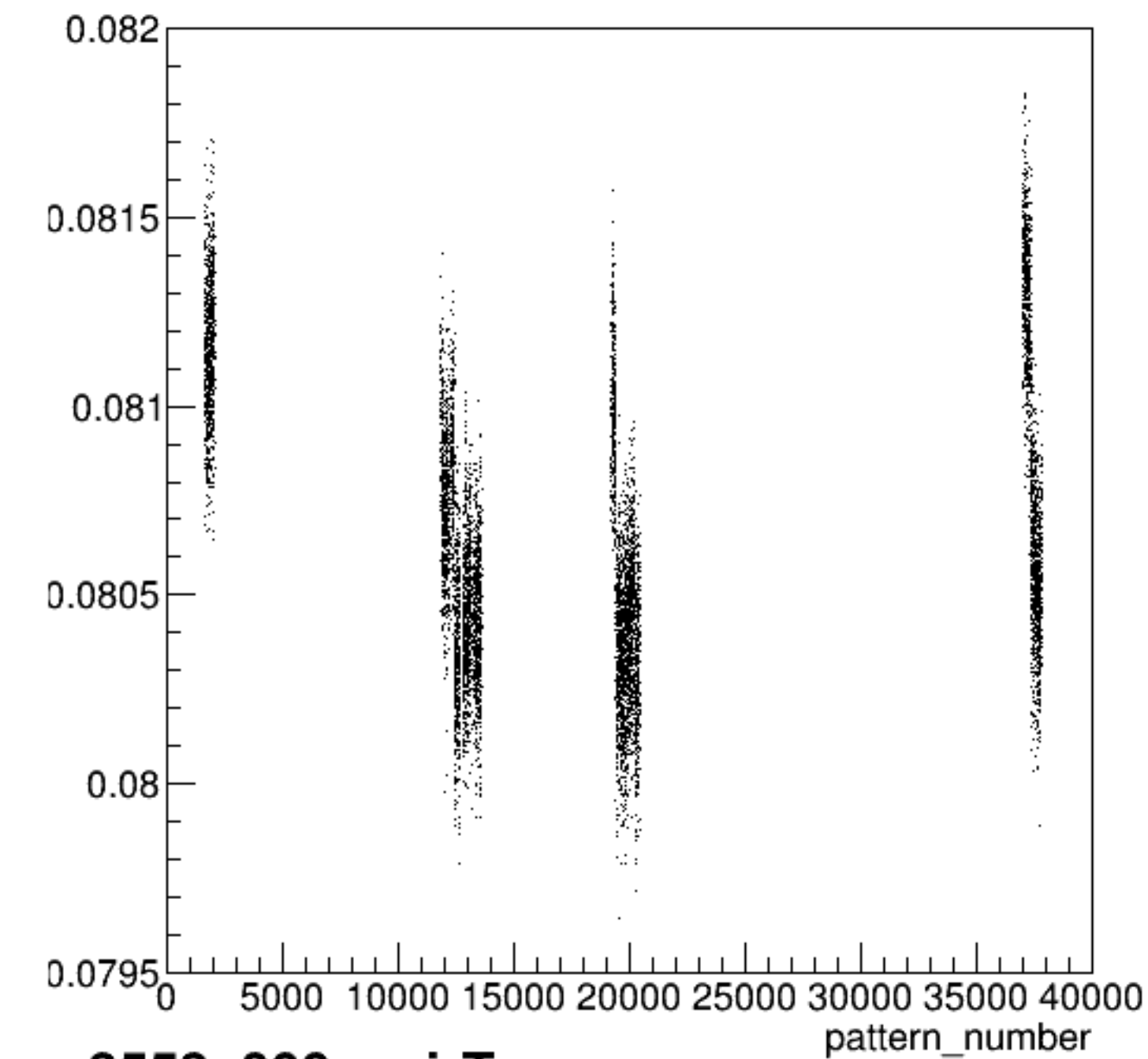
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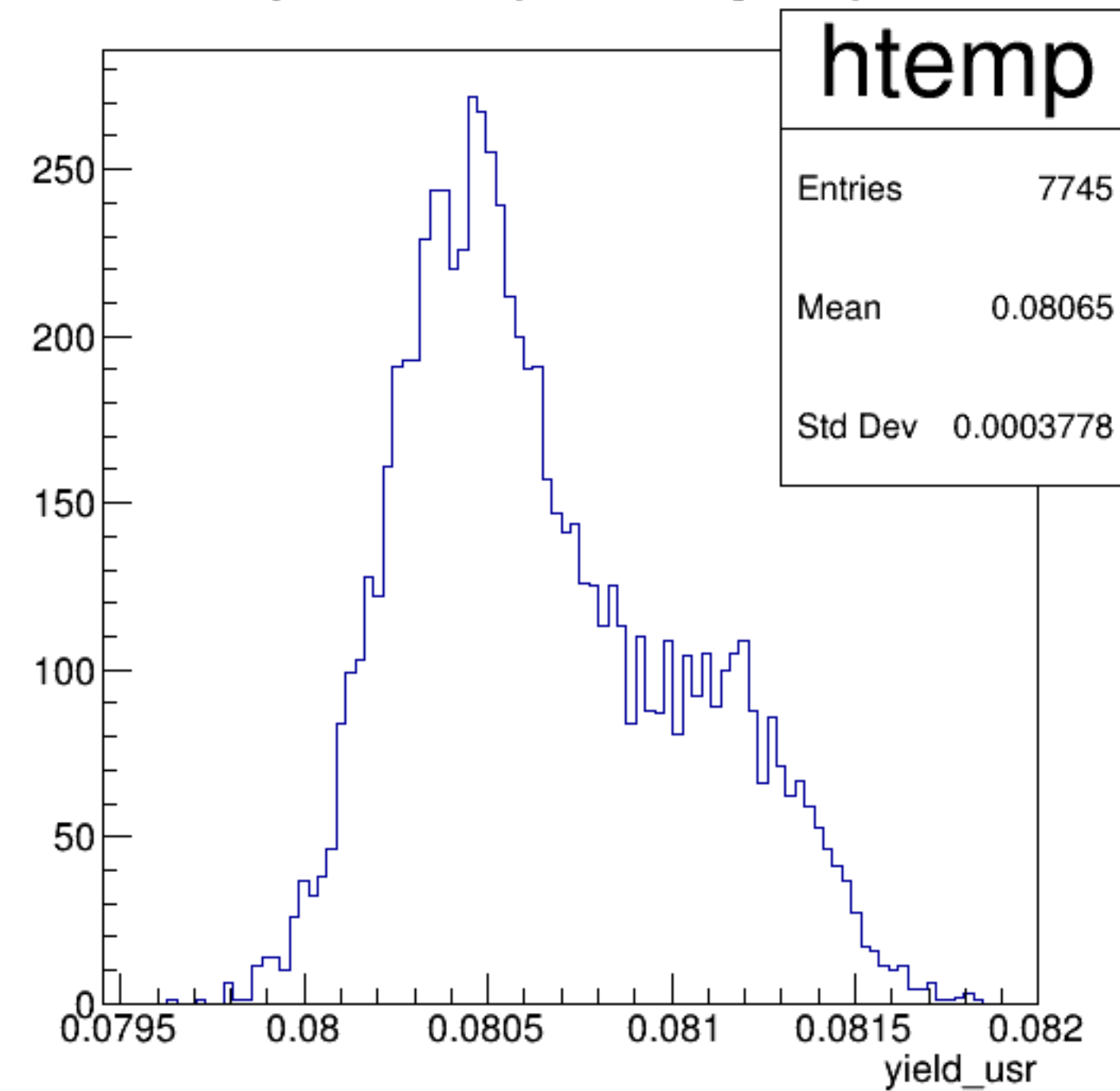
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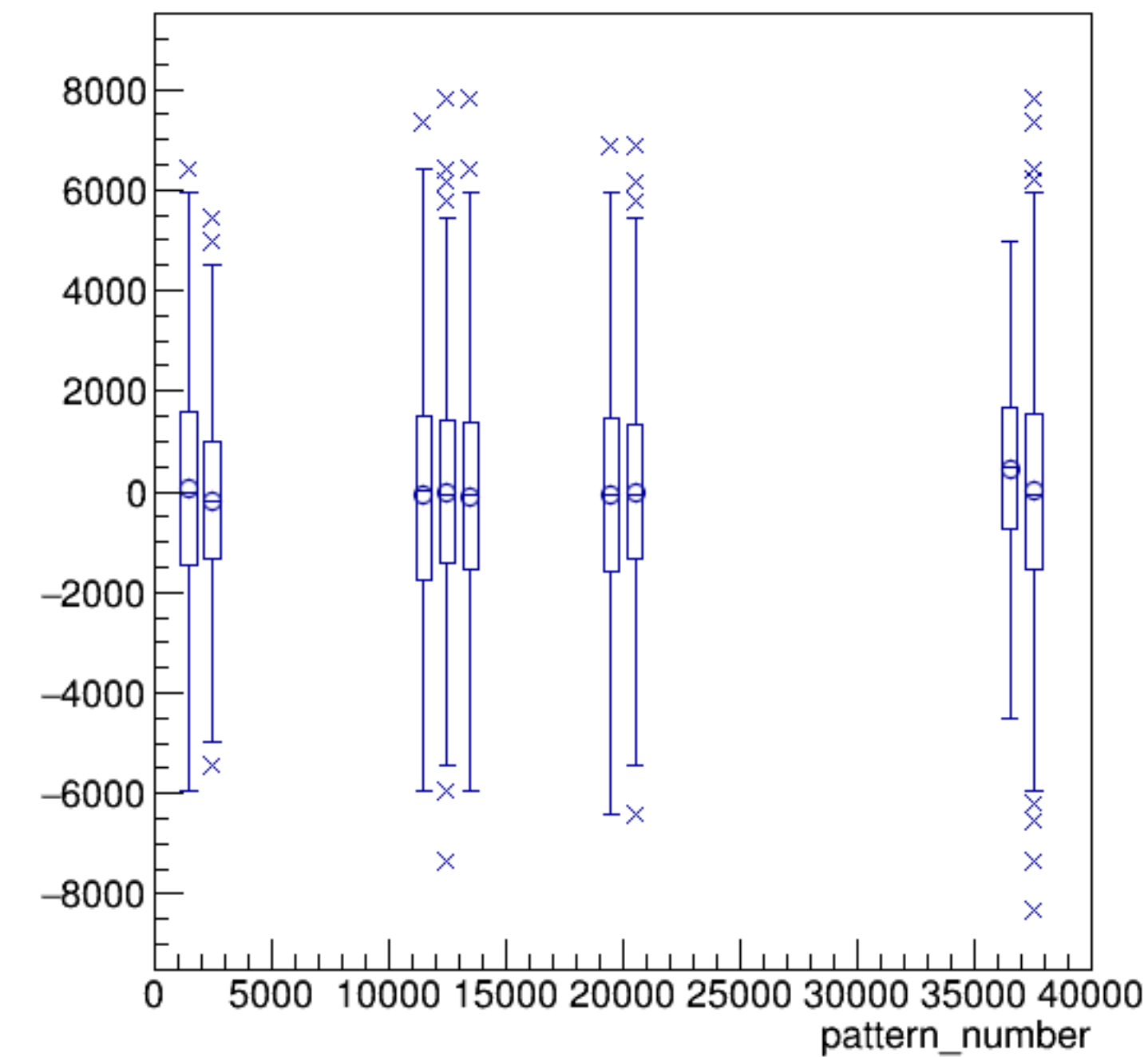
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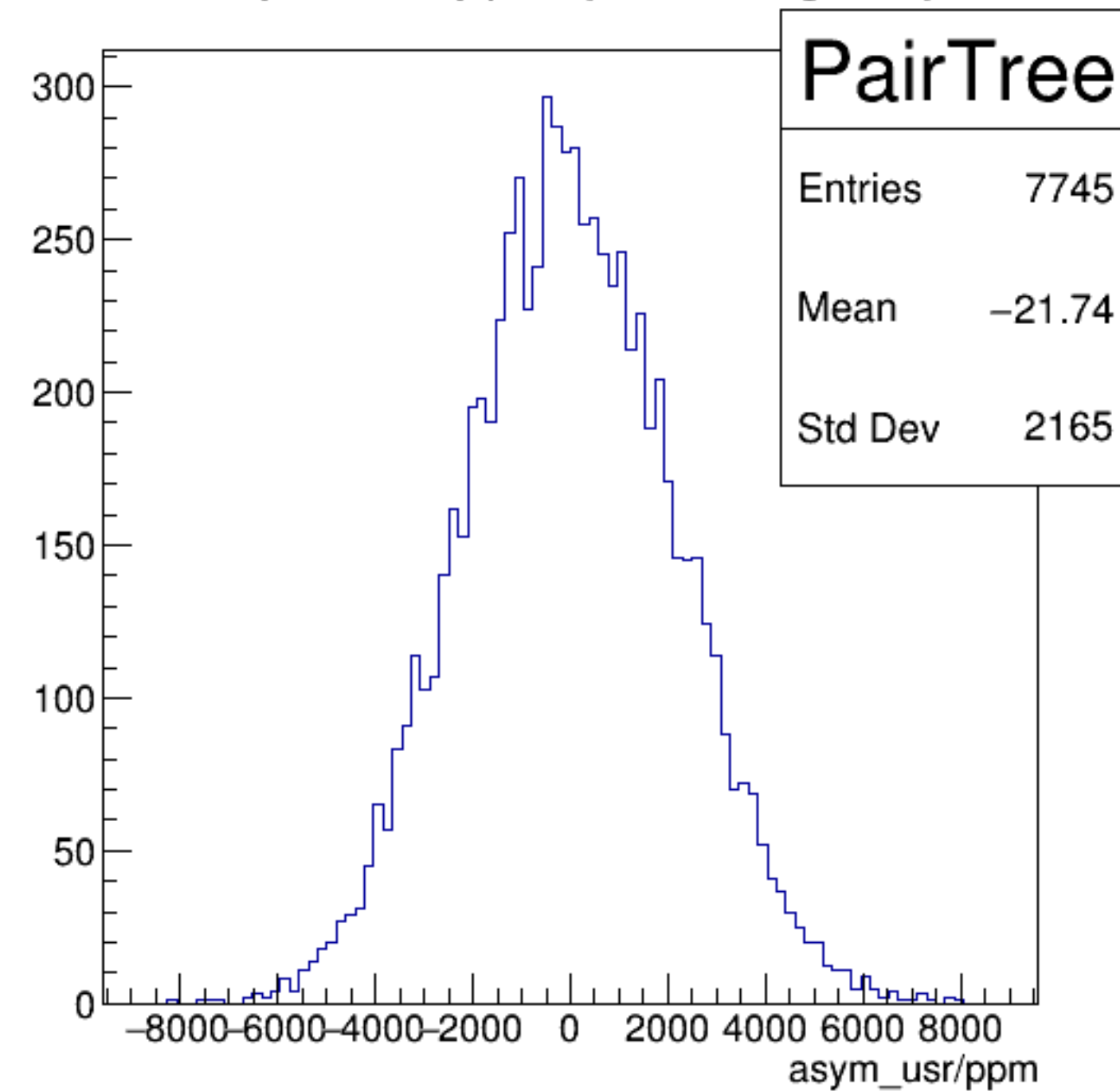
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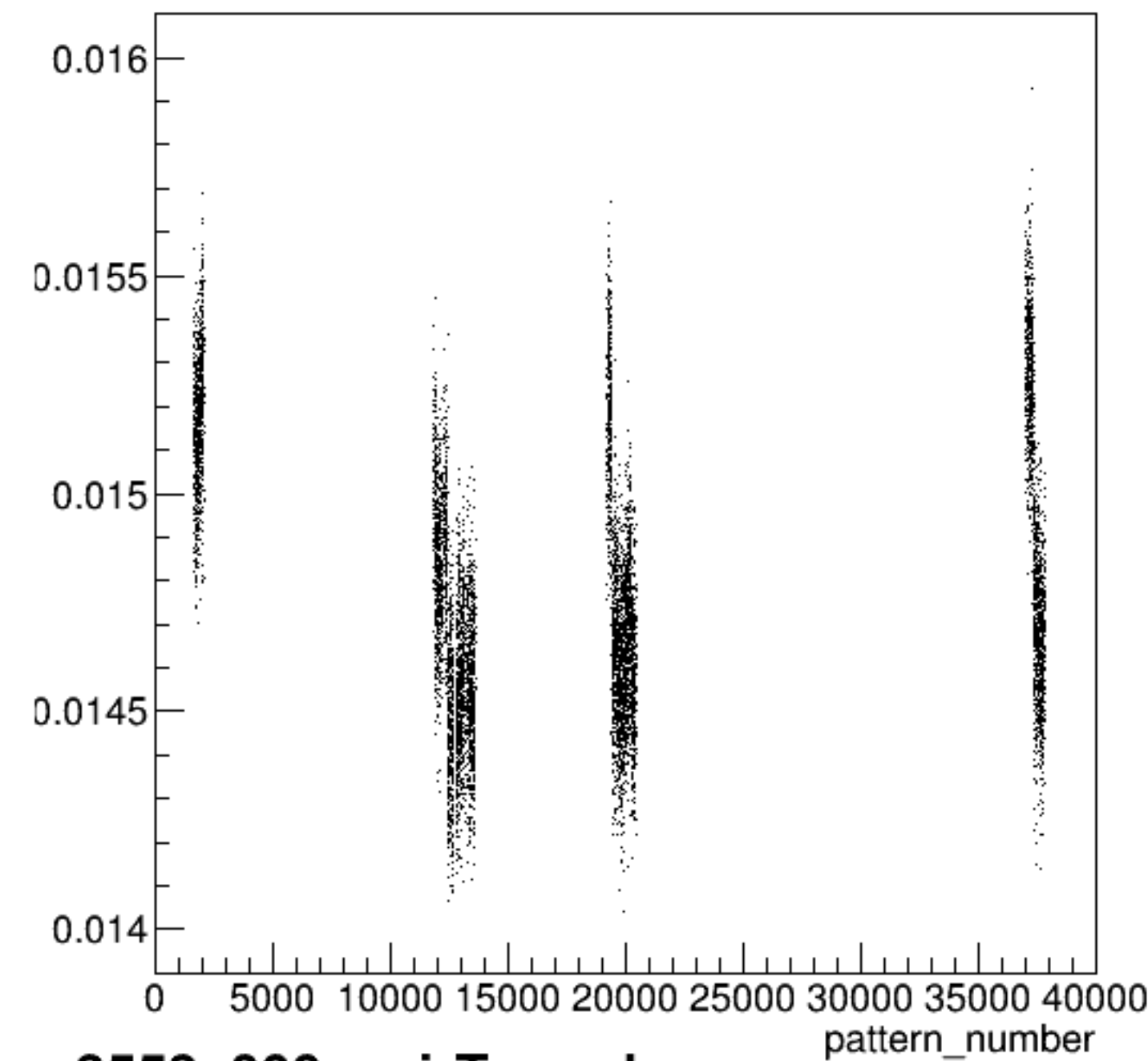
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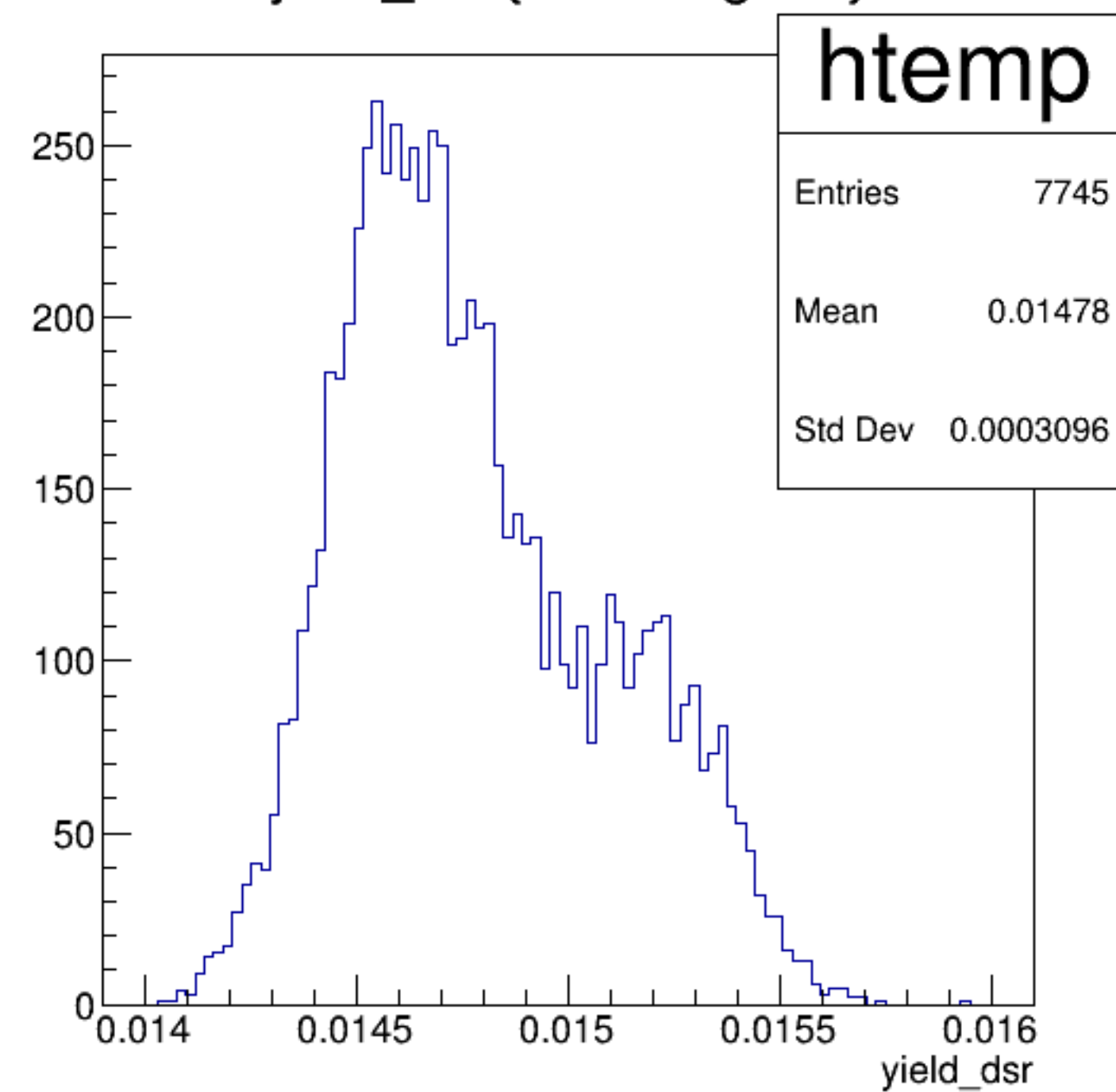
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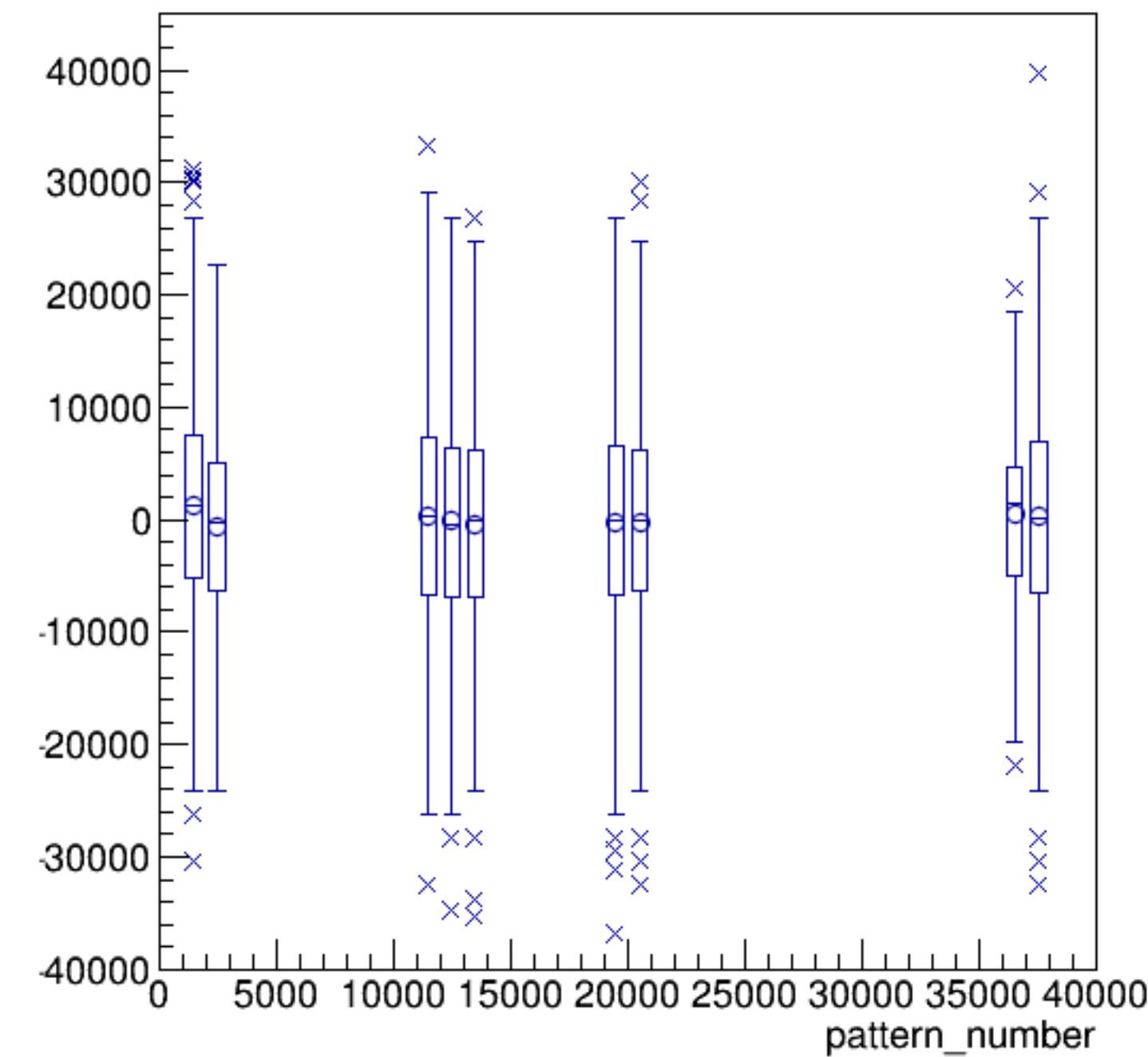
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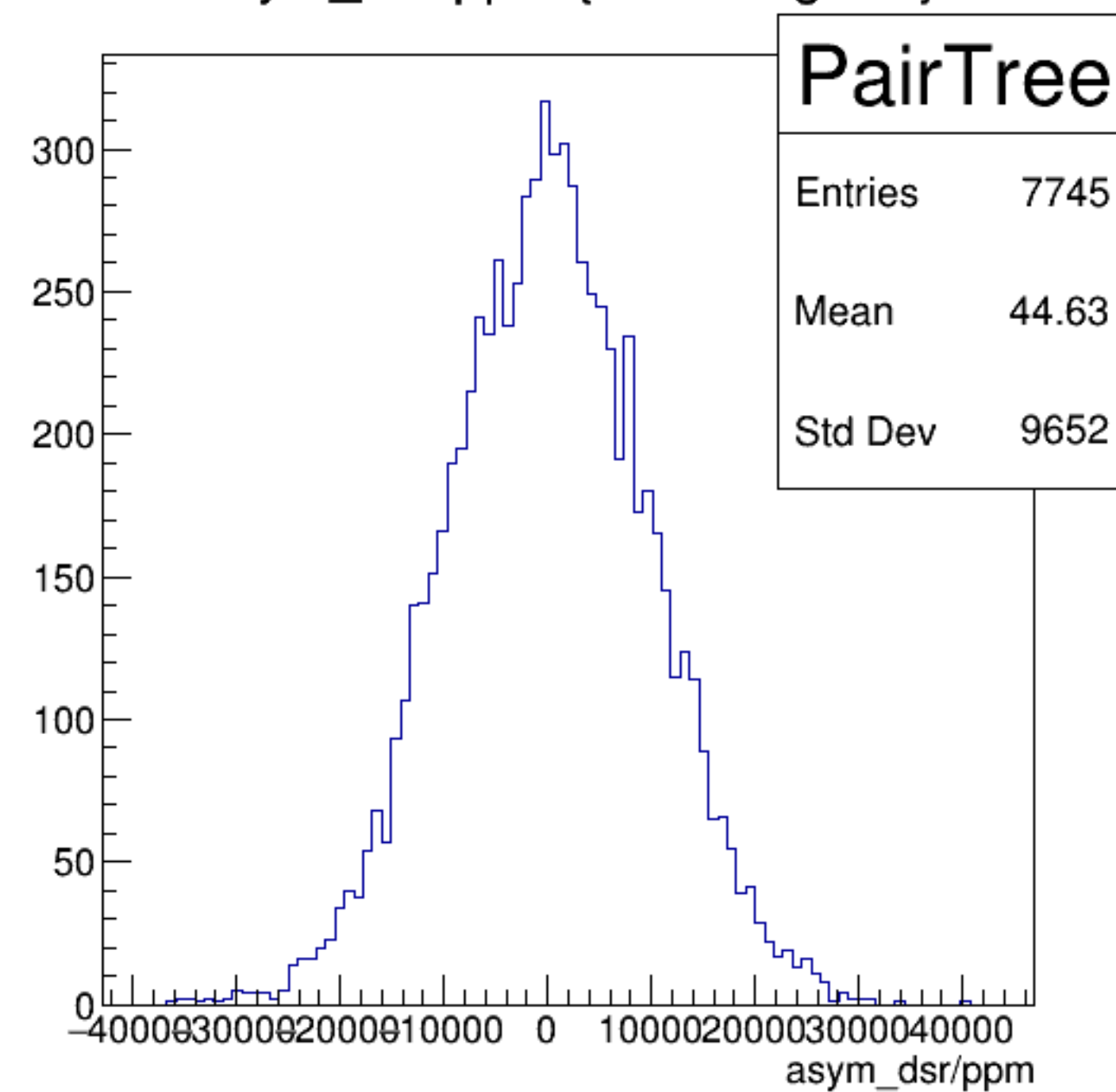
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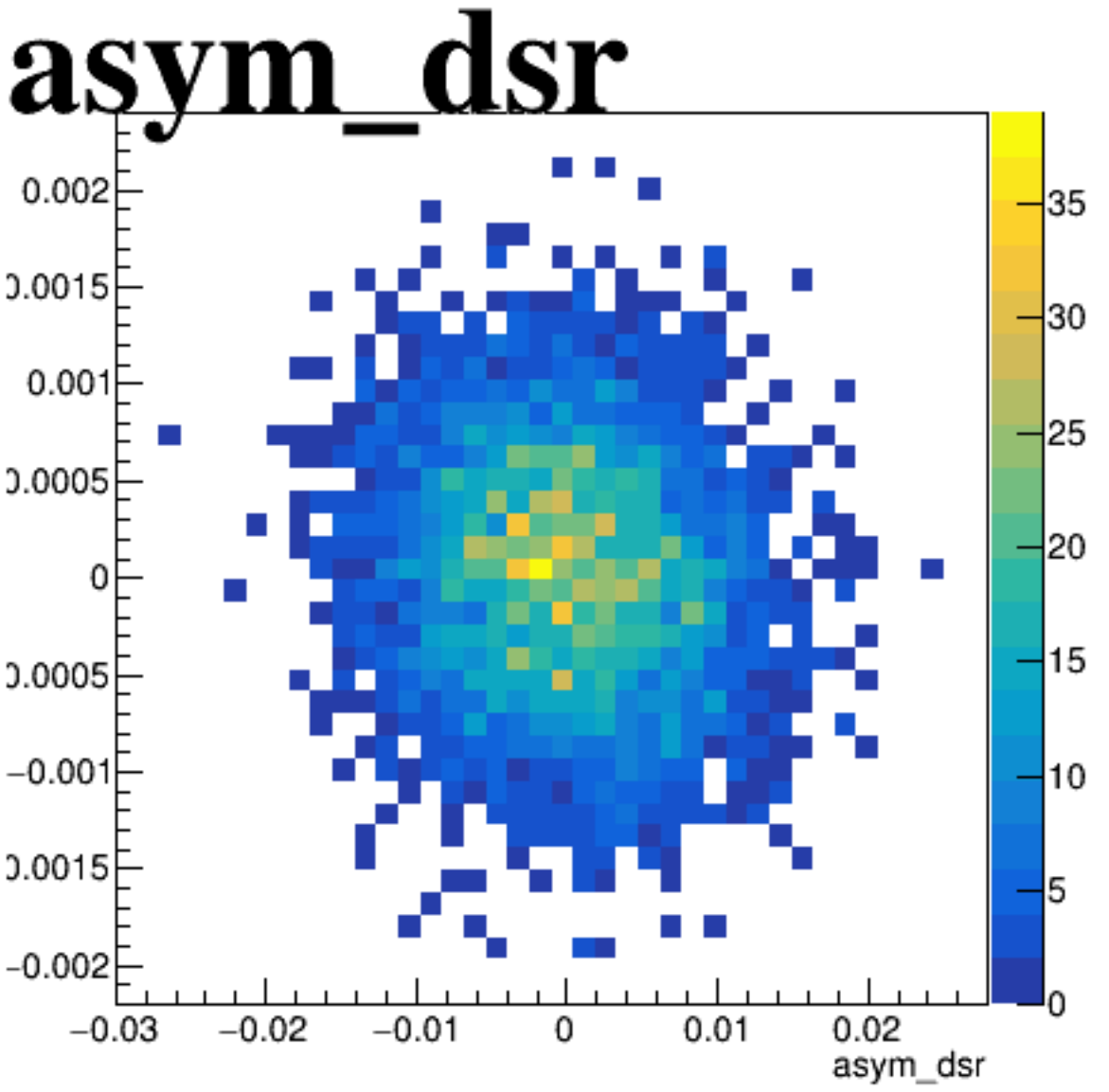
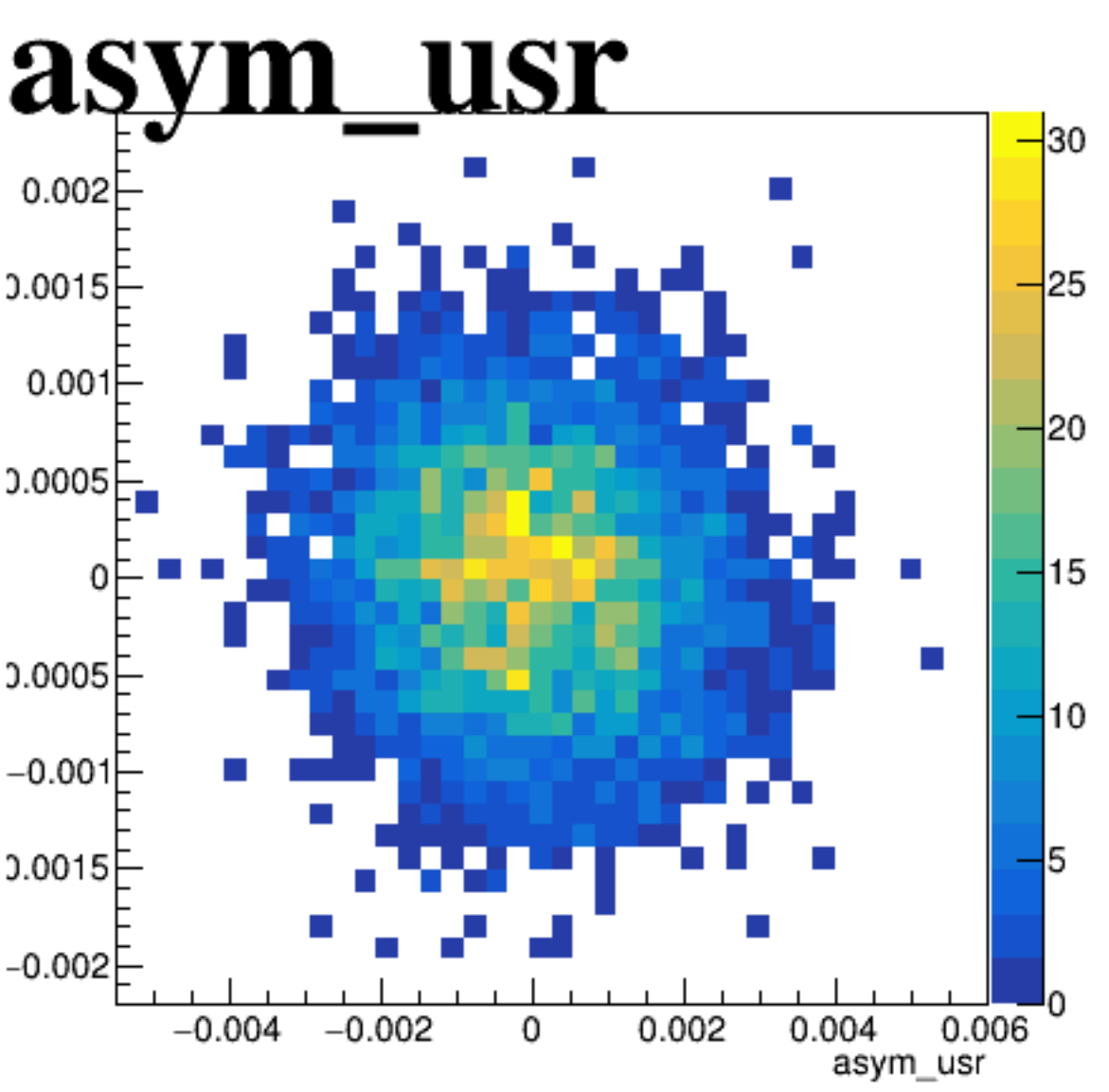
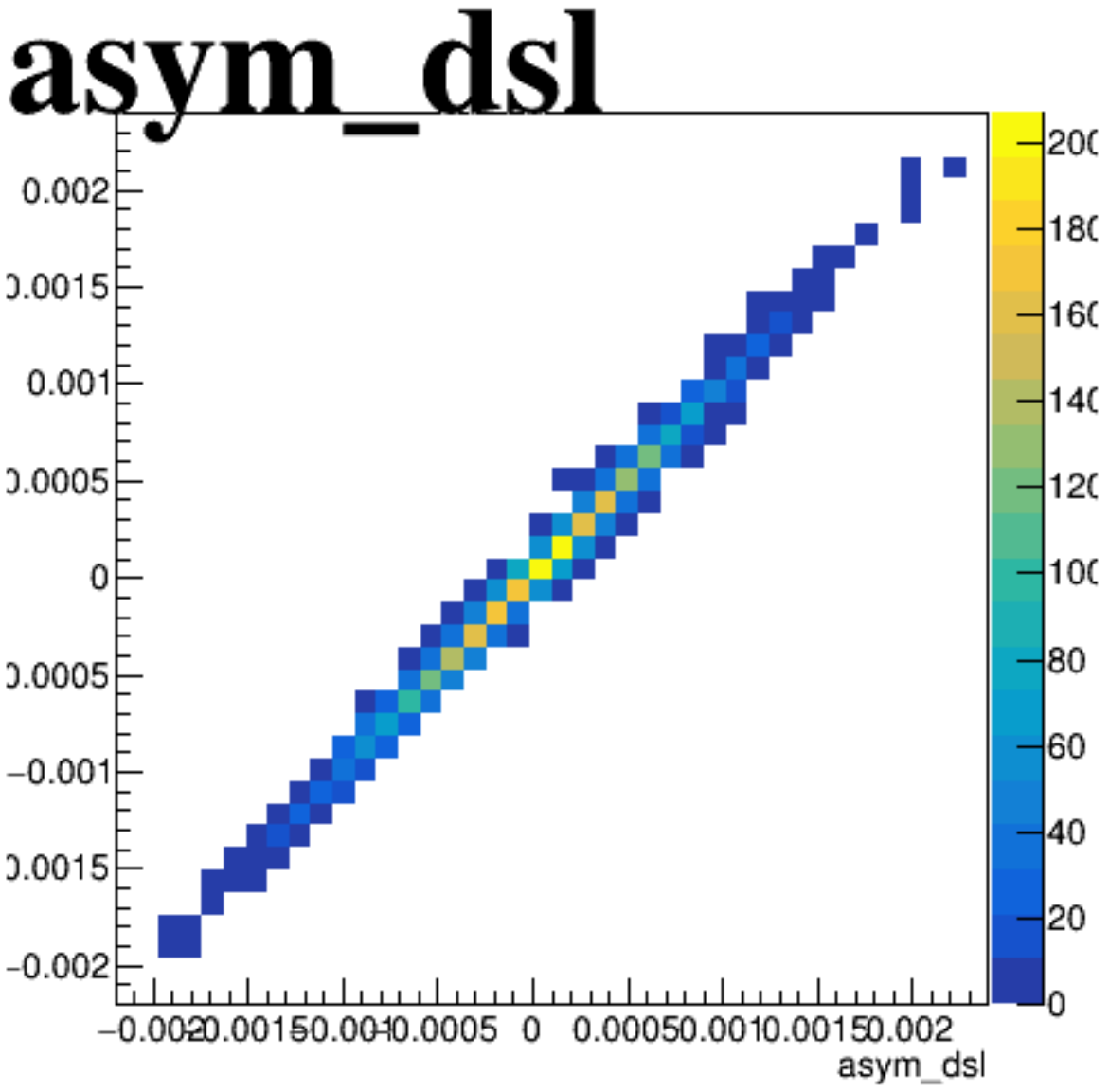
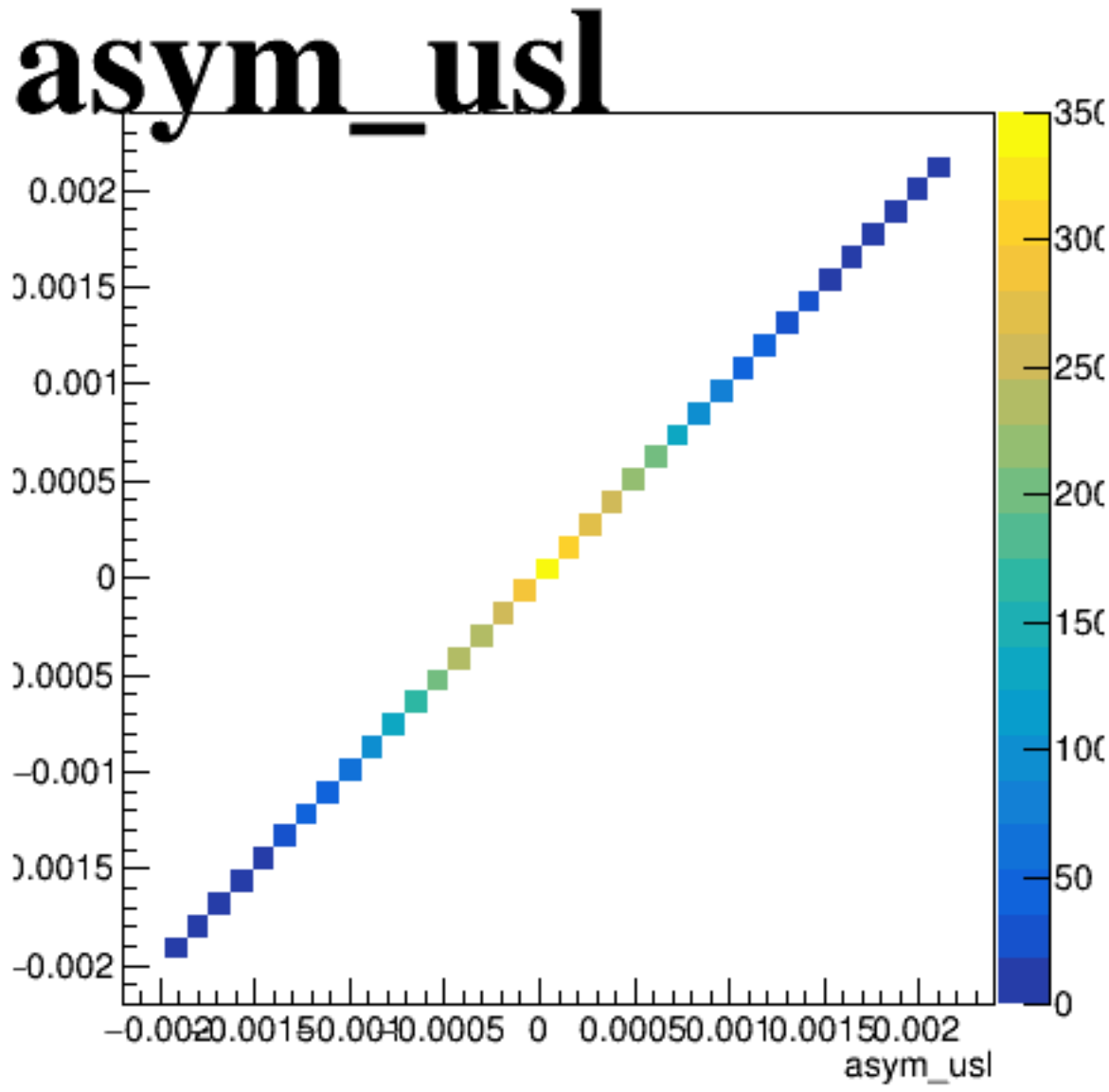
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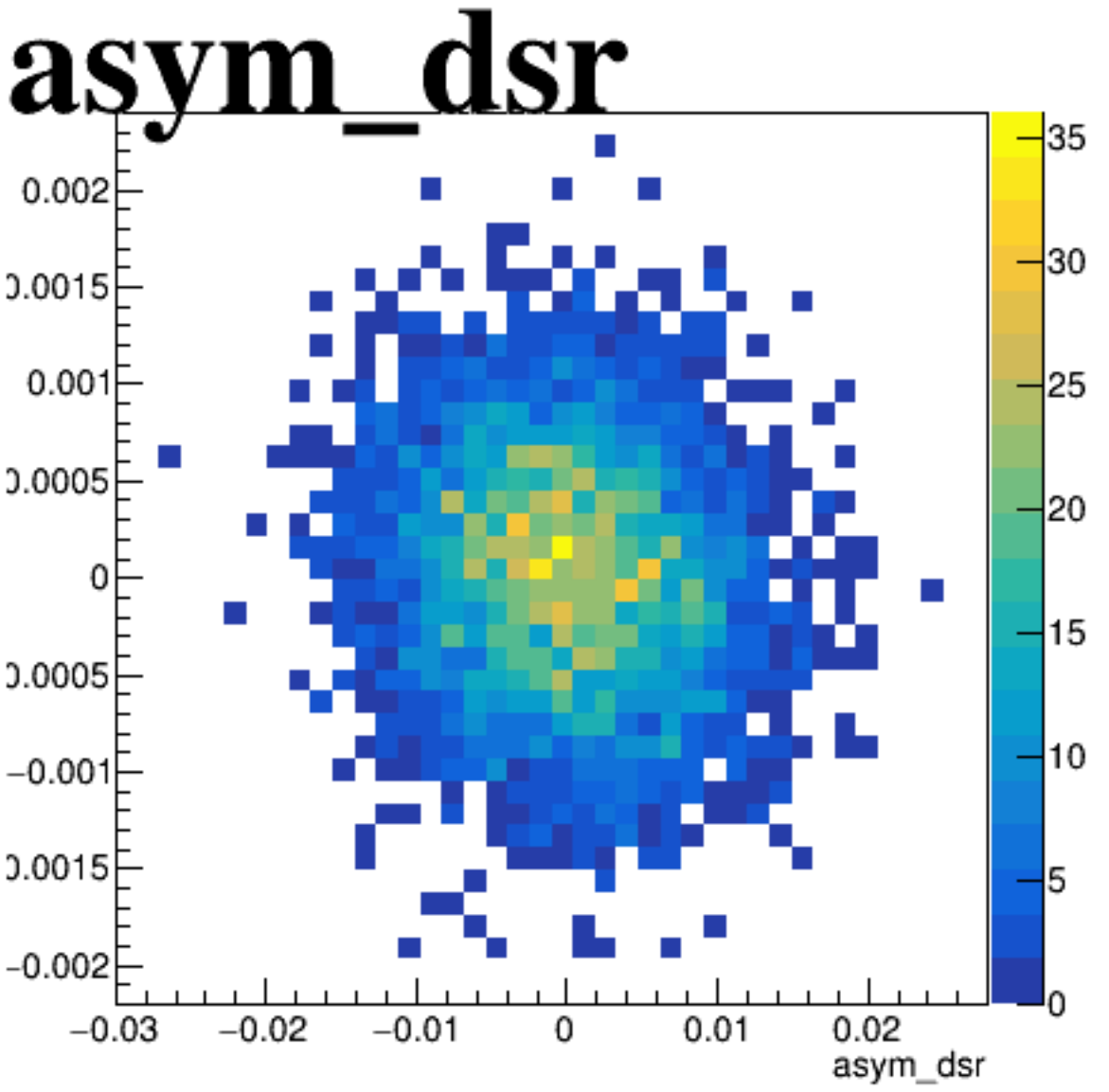
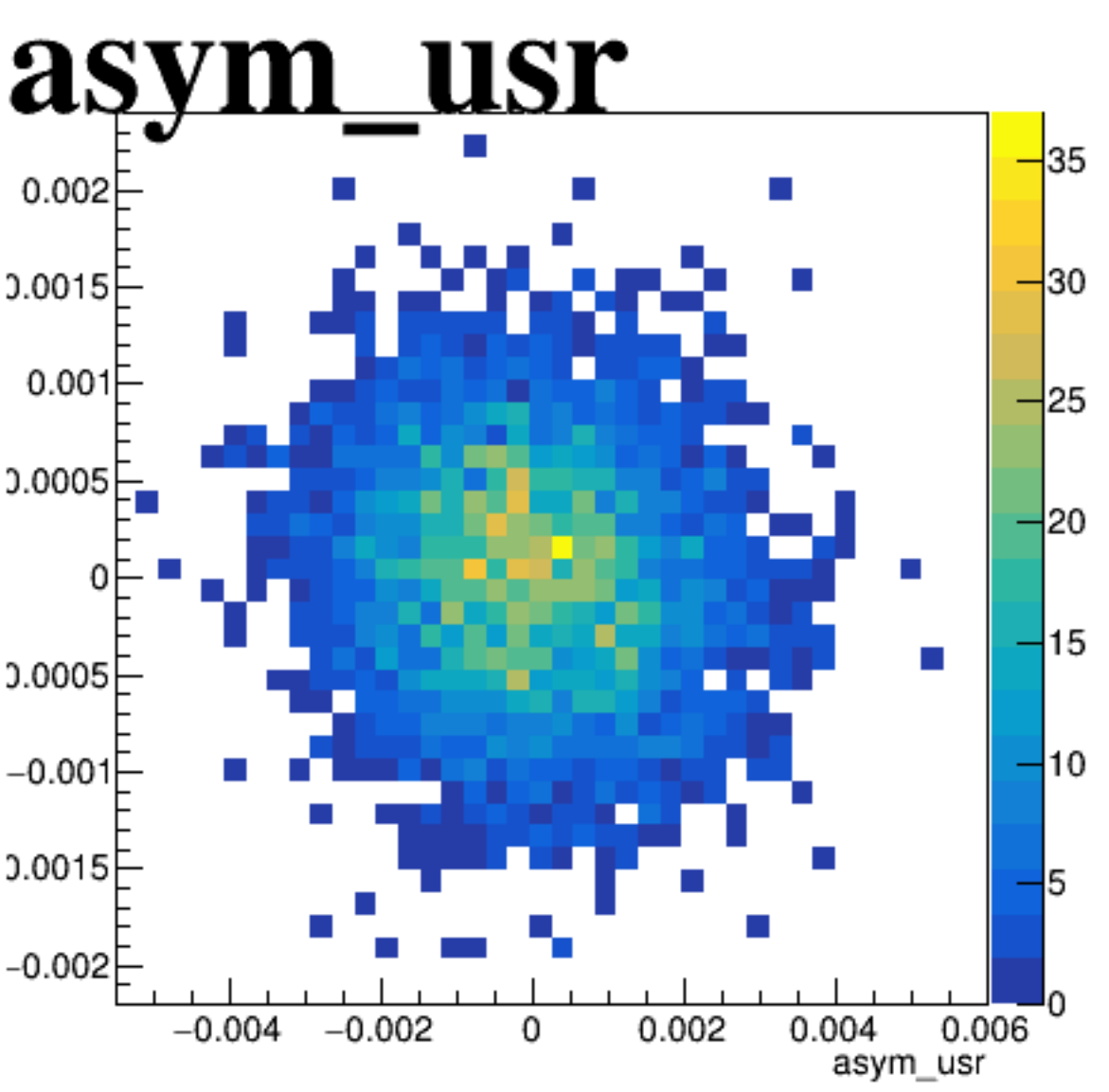
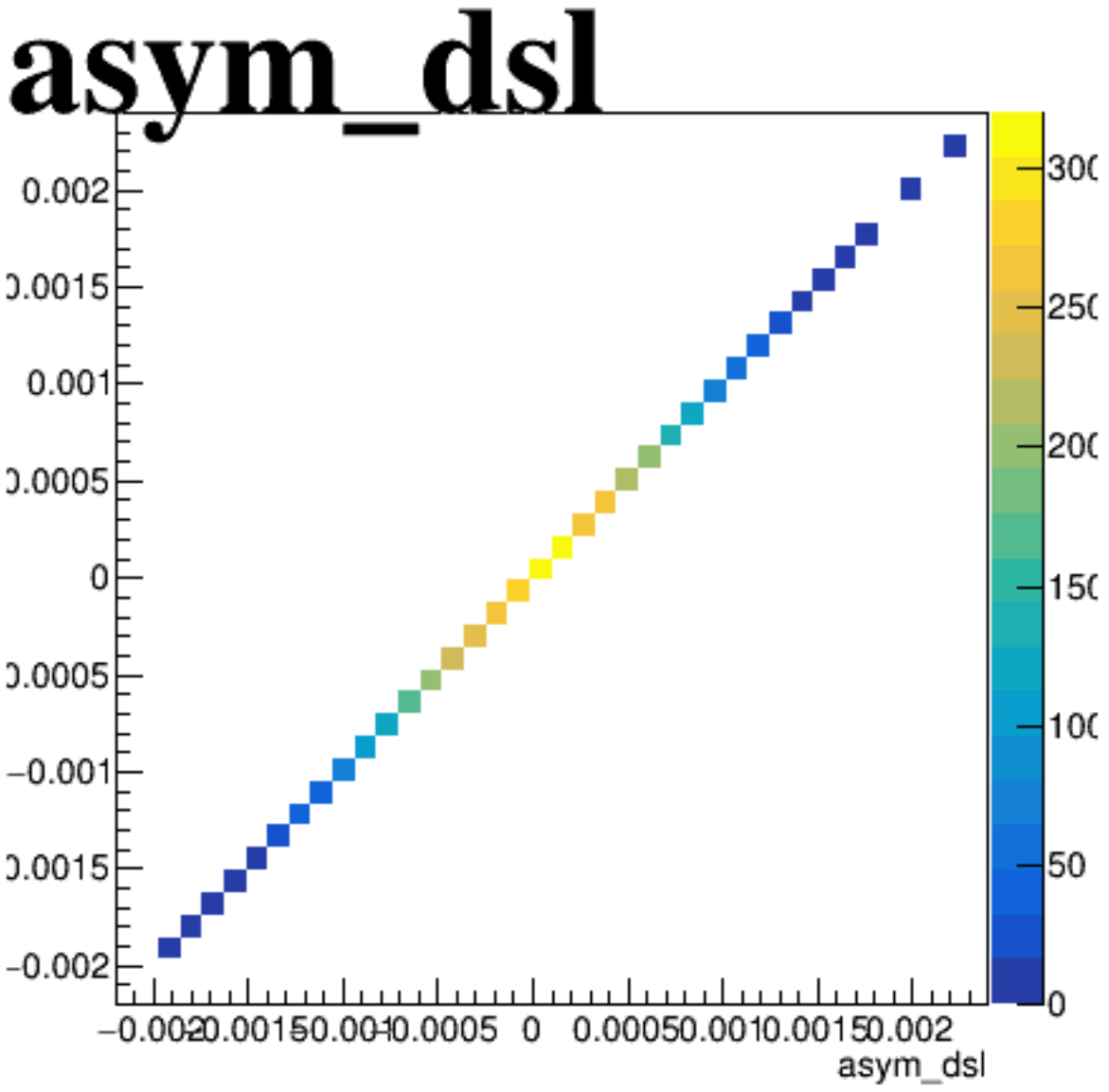
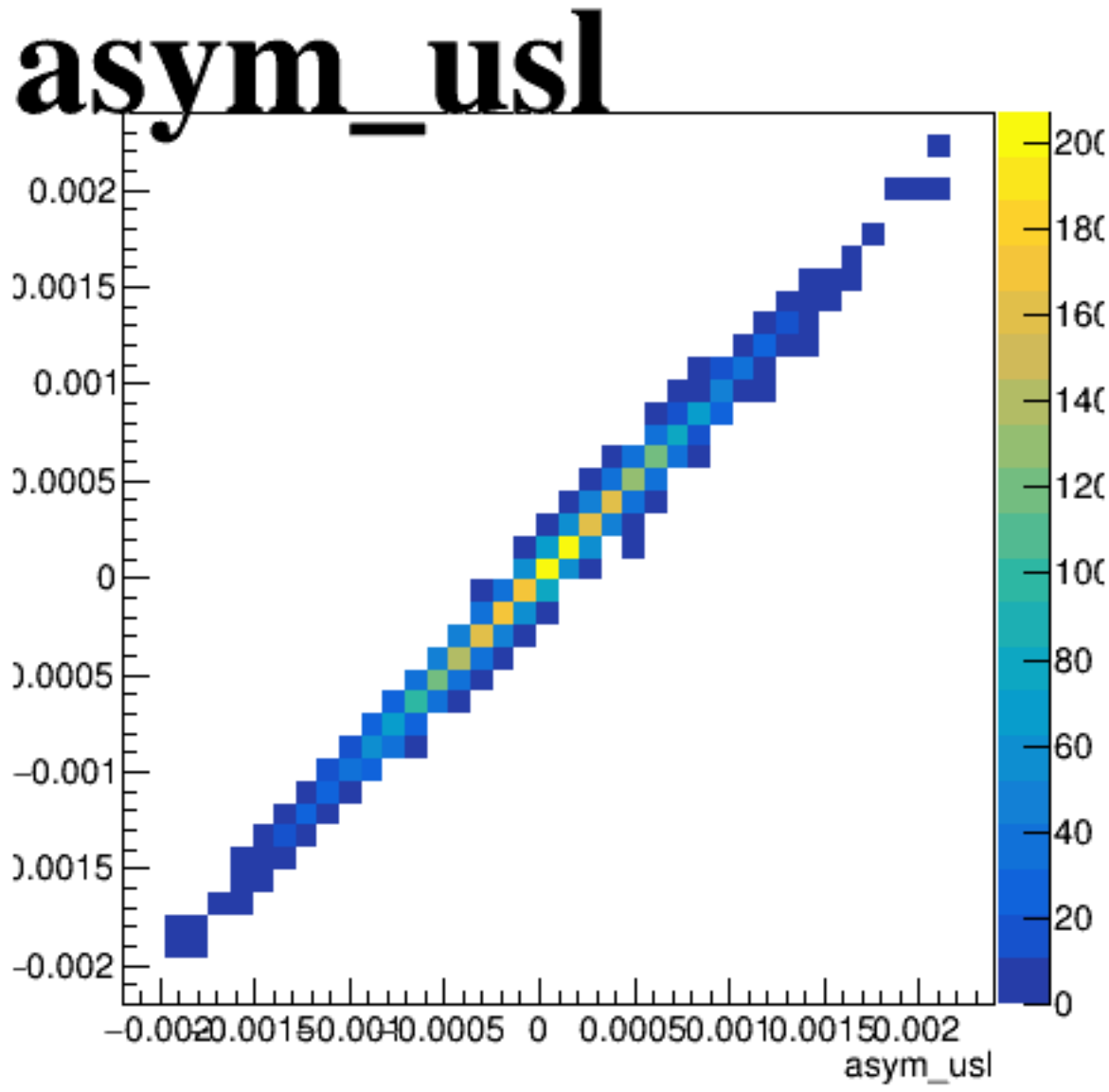
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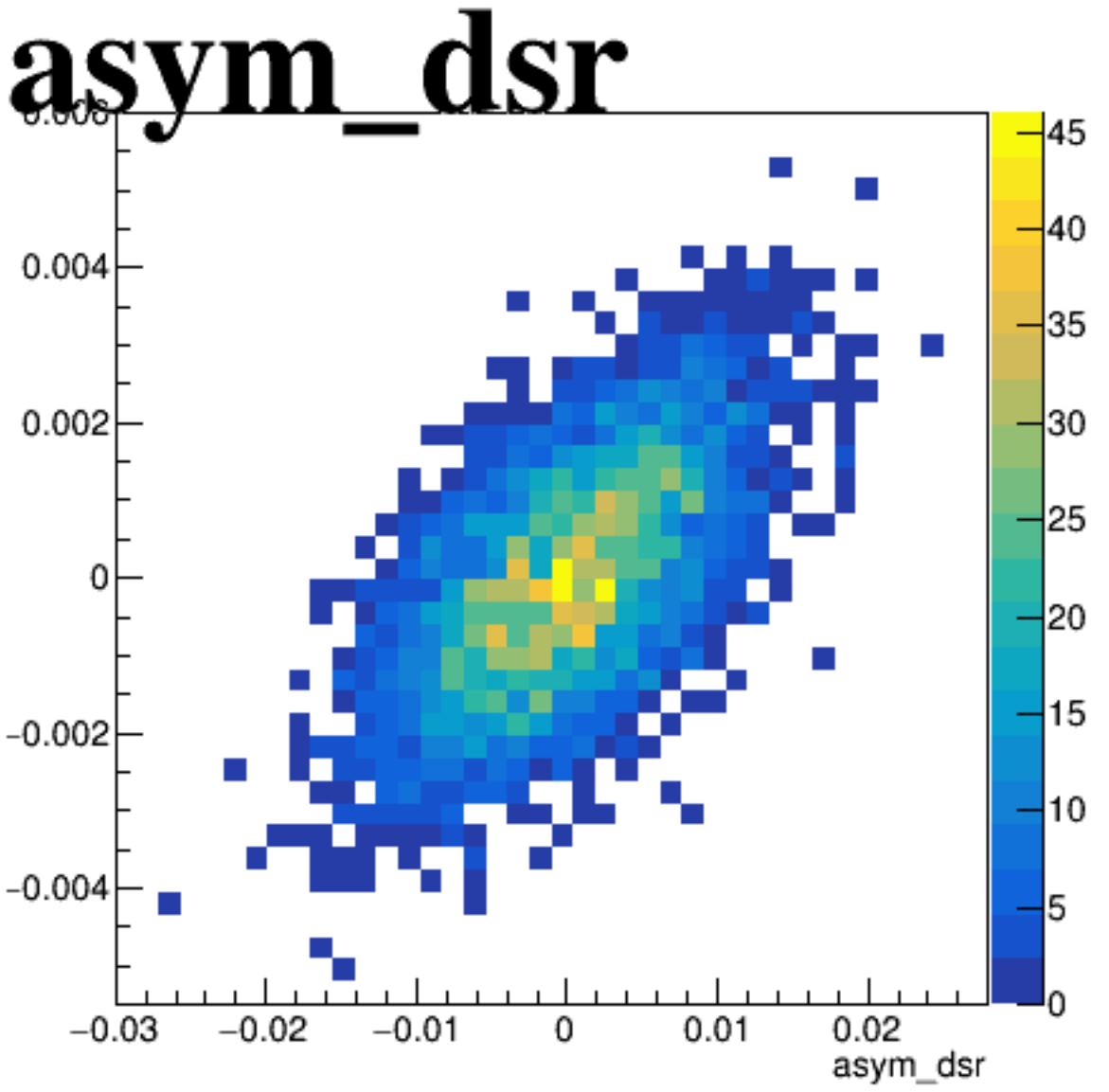
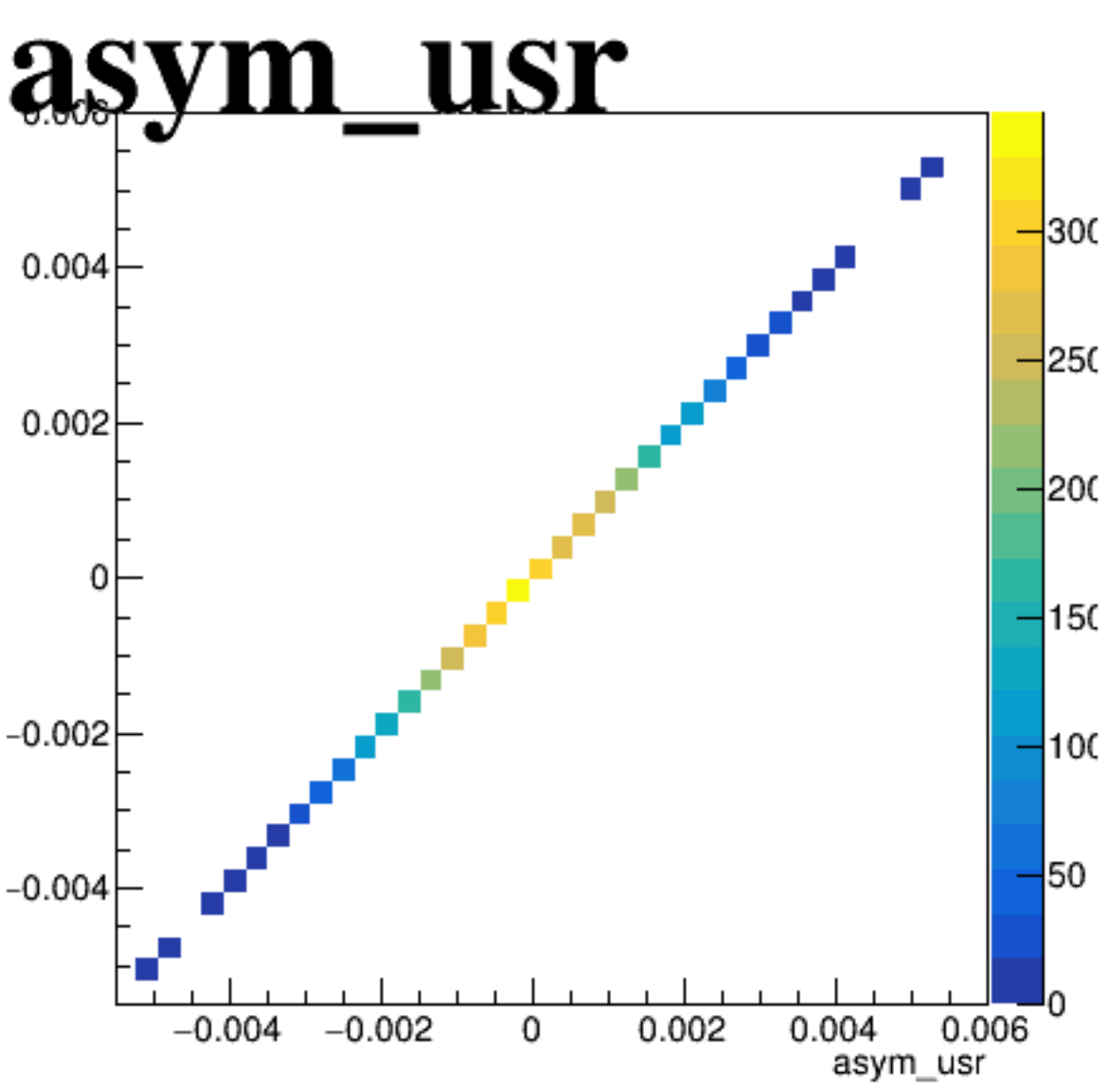
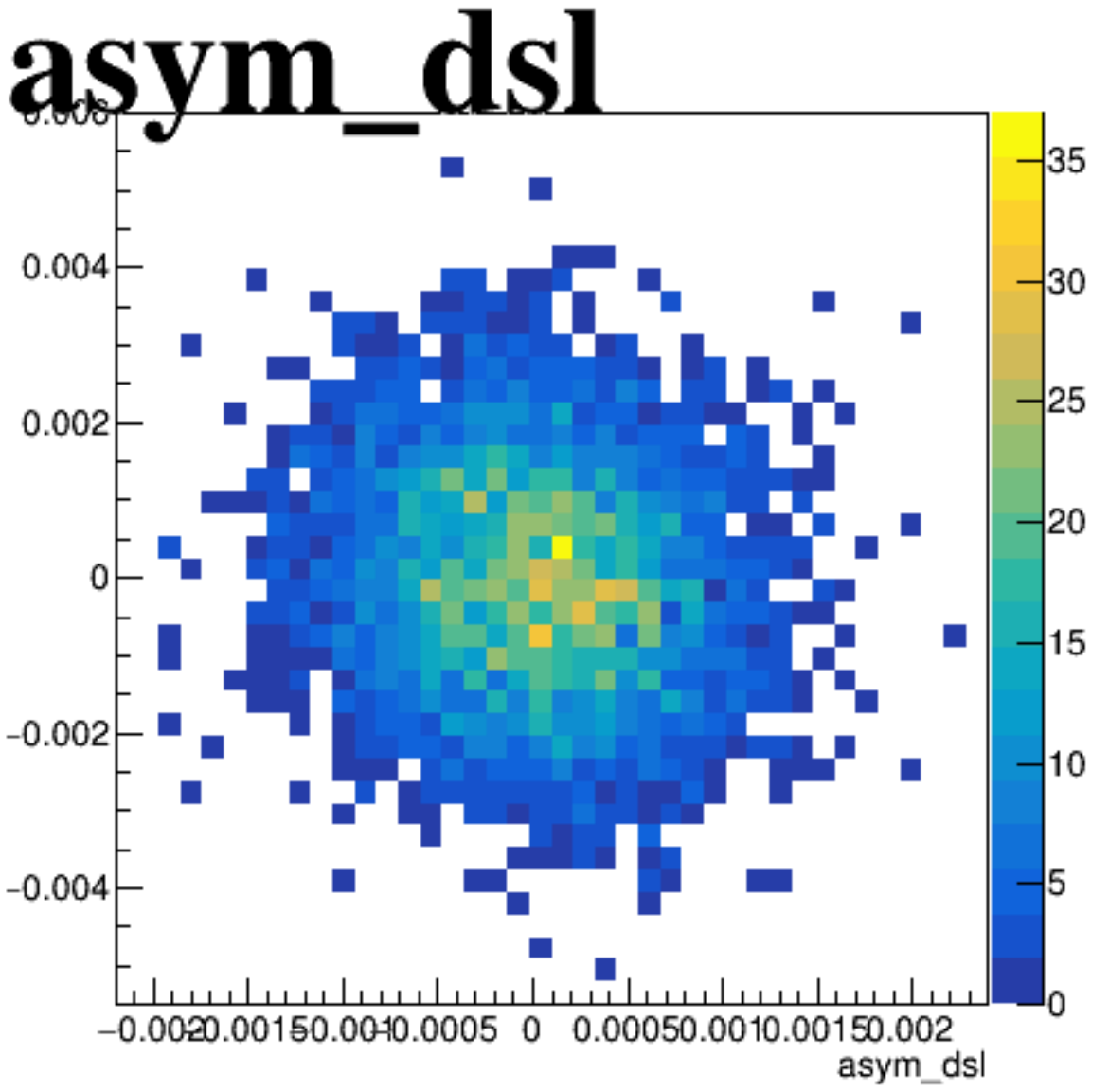
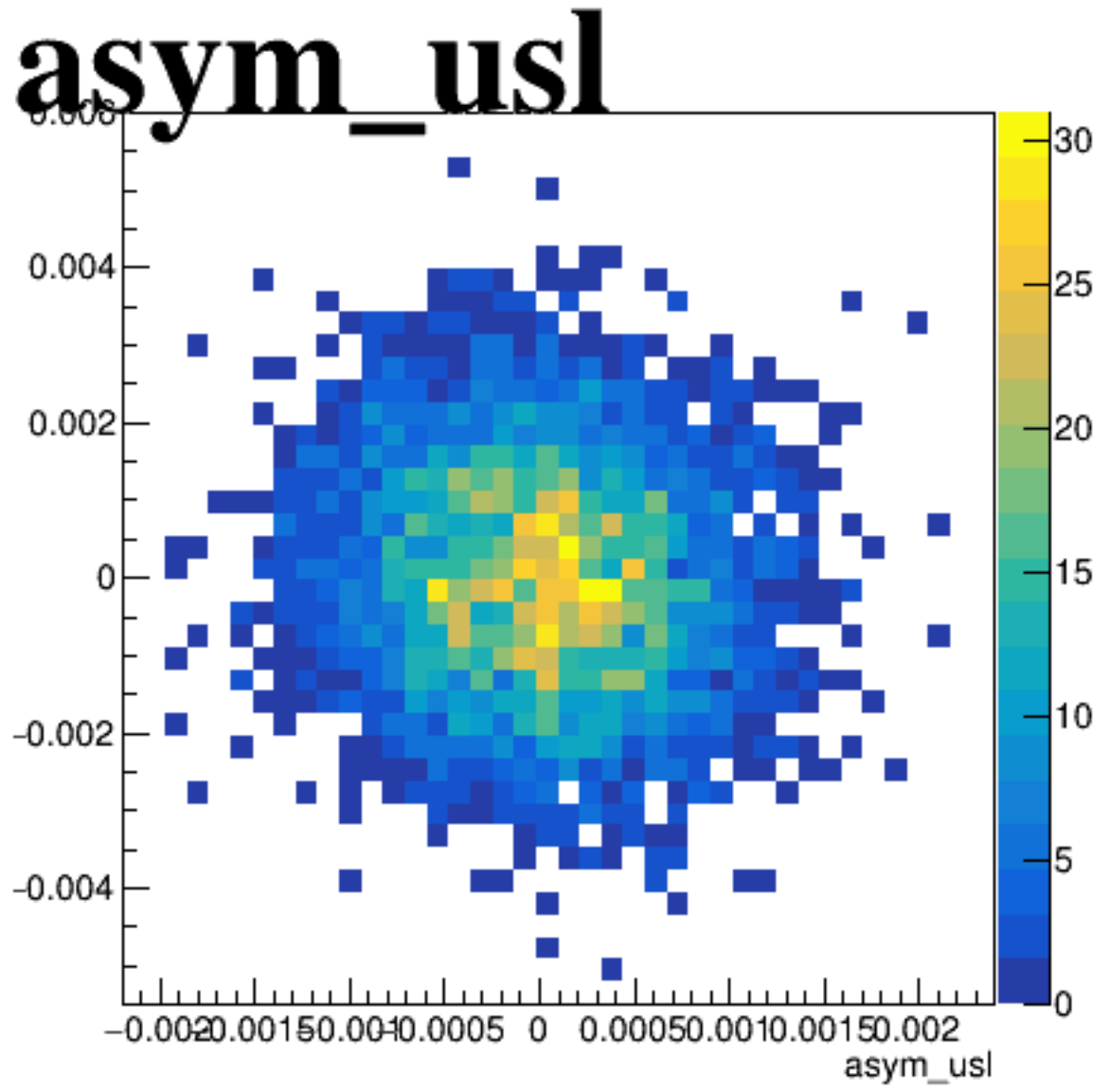
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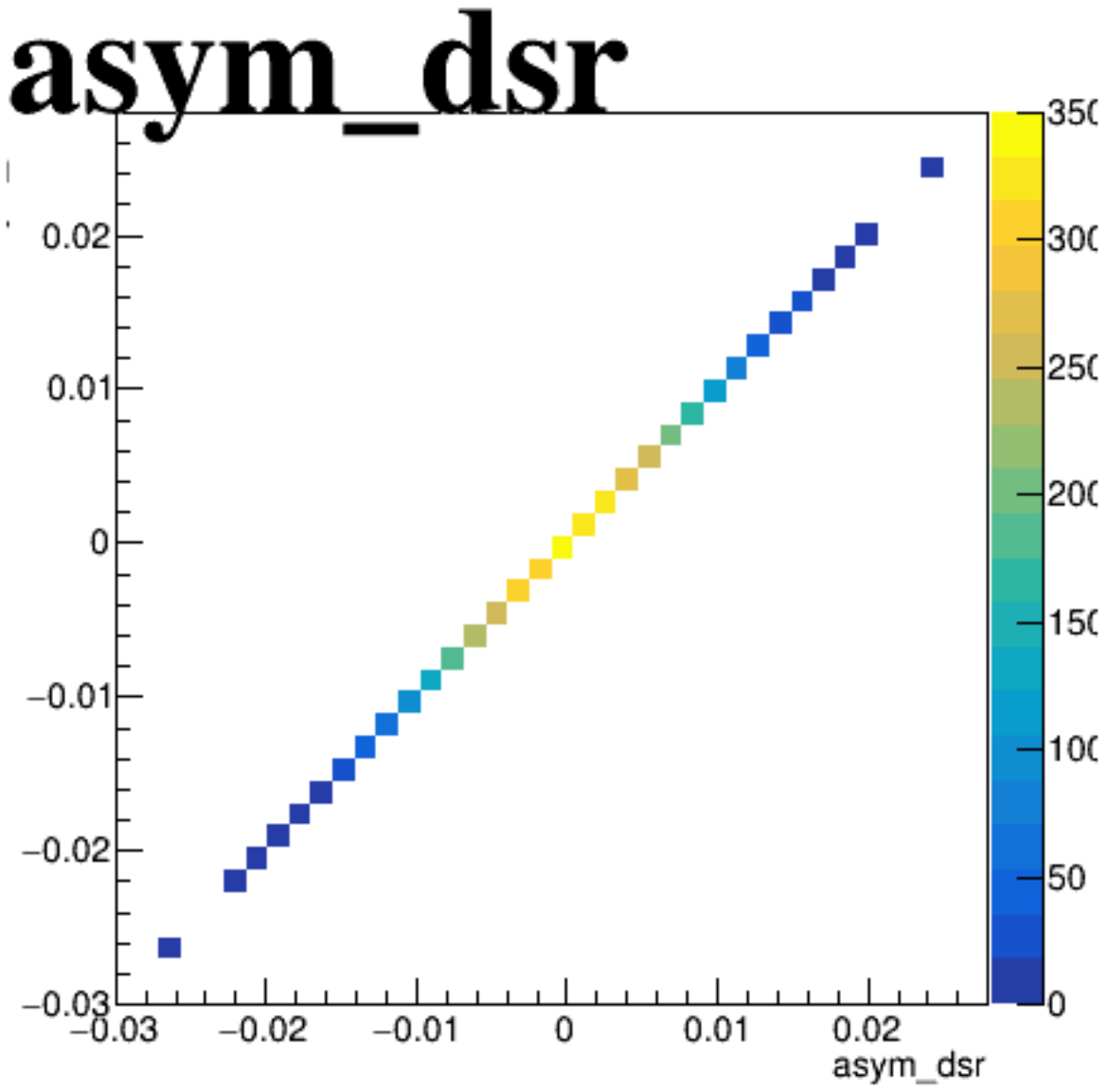
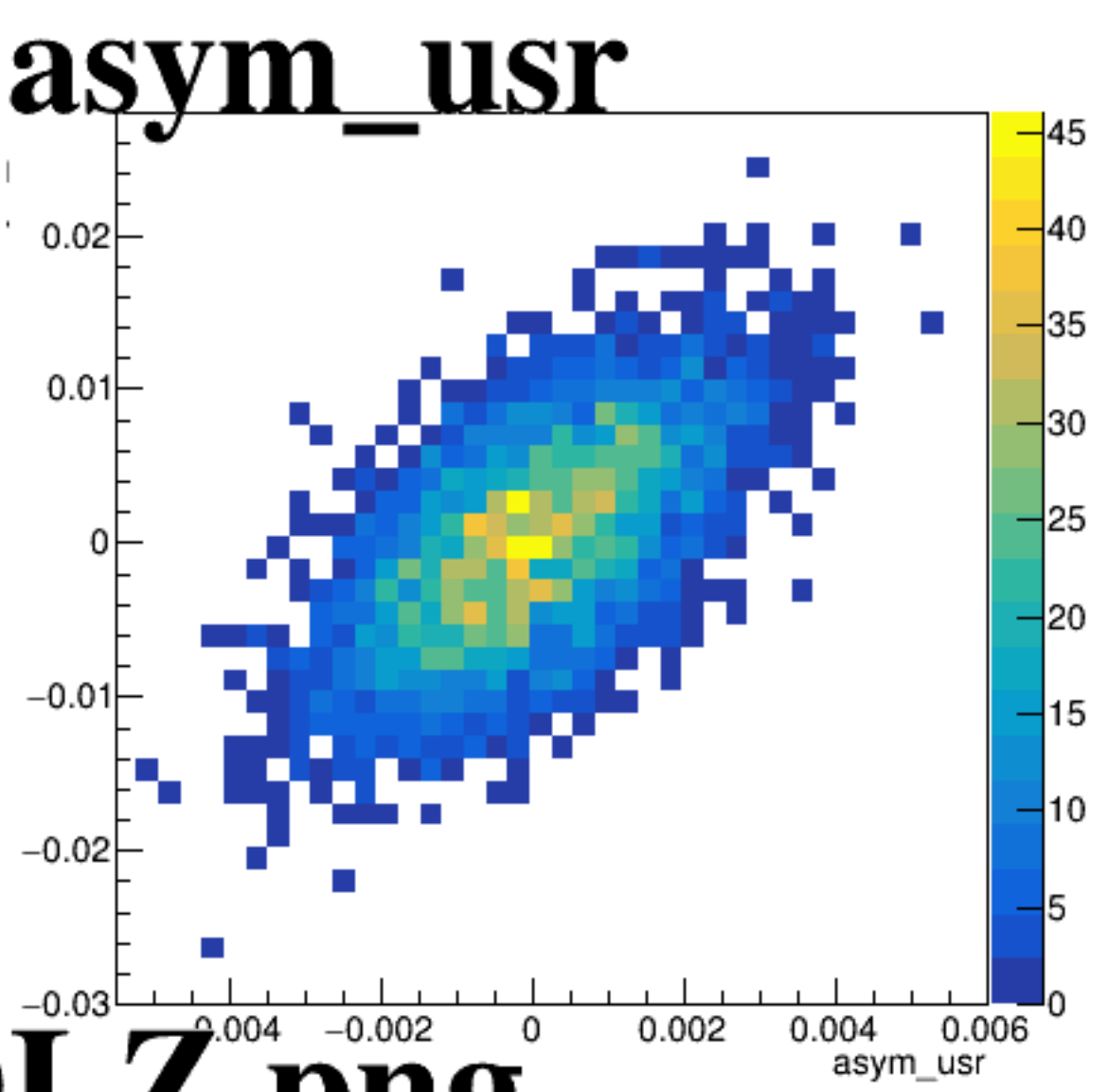
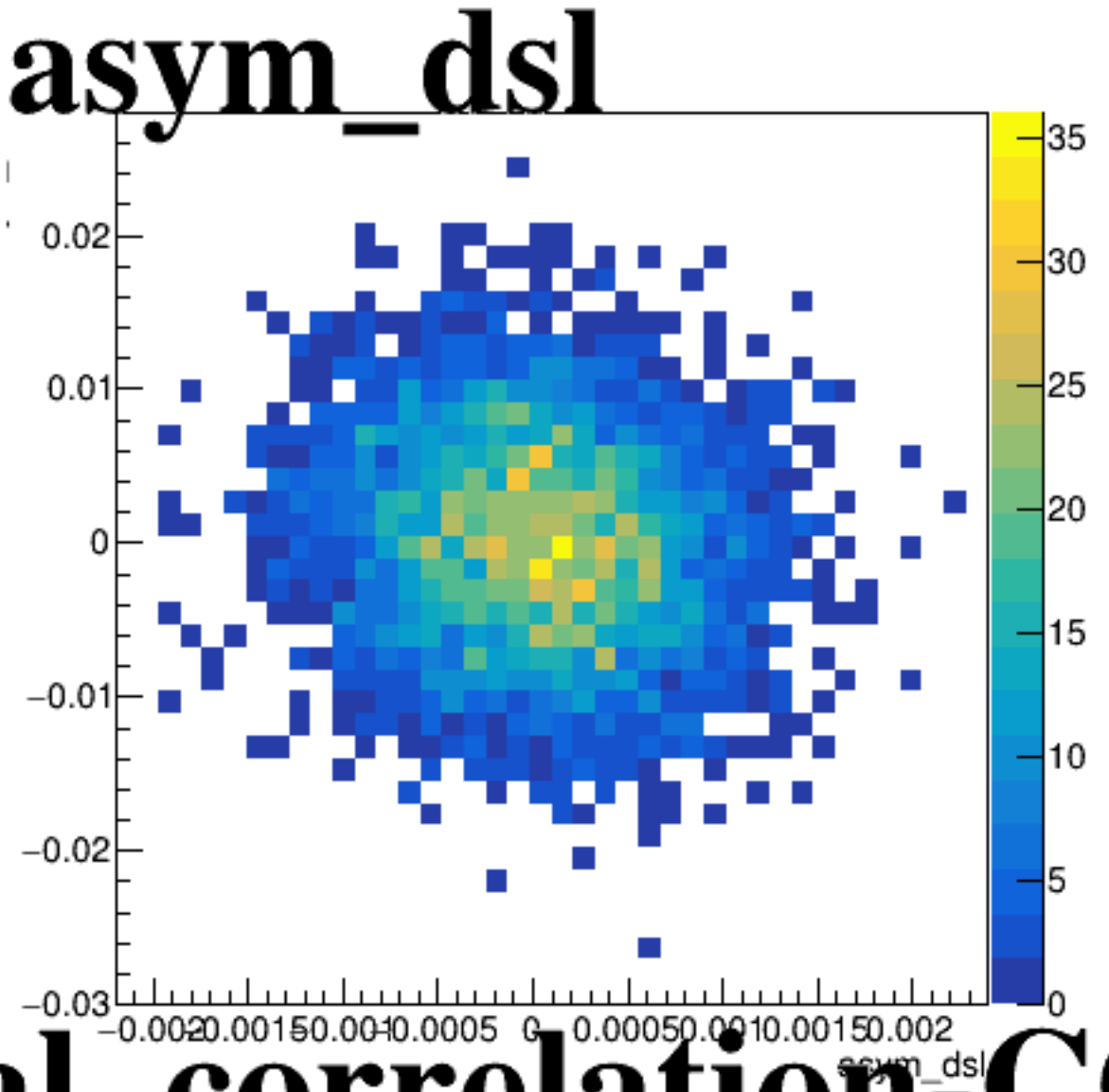
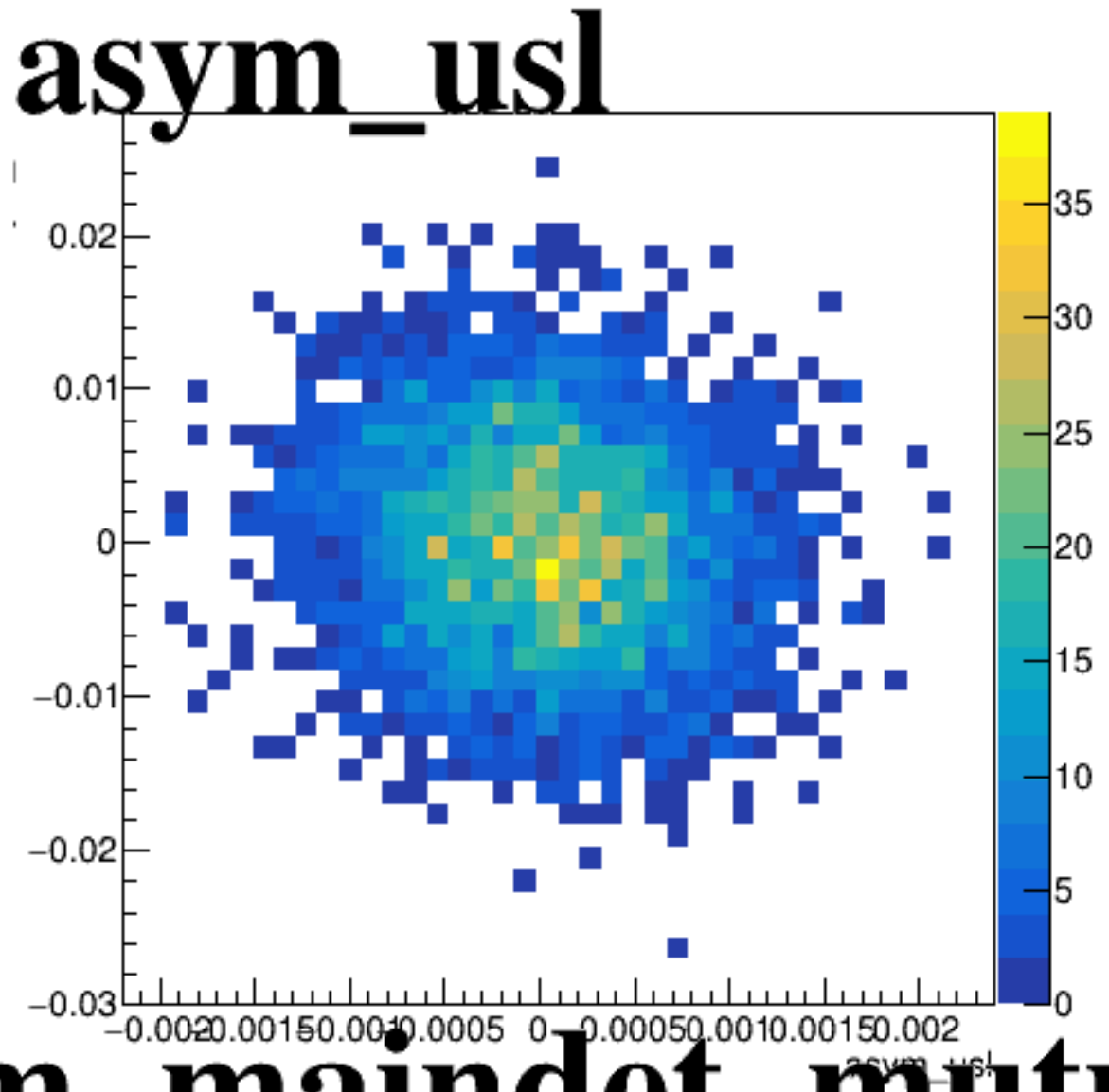
asym_dsl



asym_usr

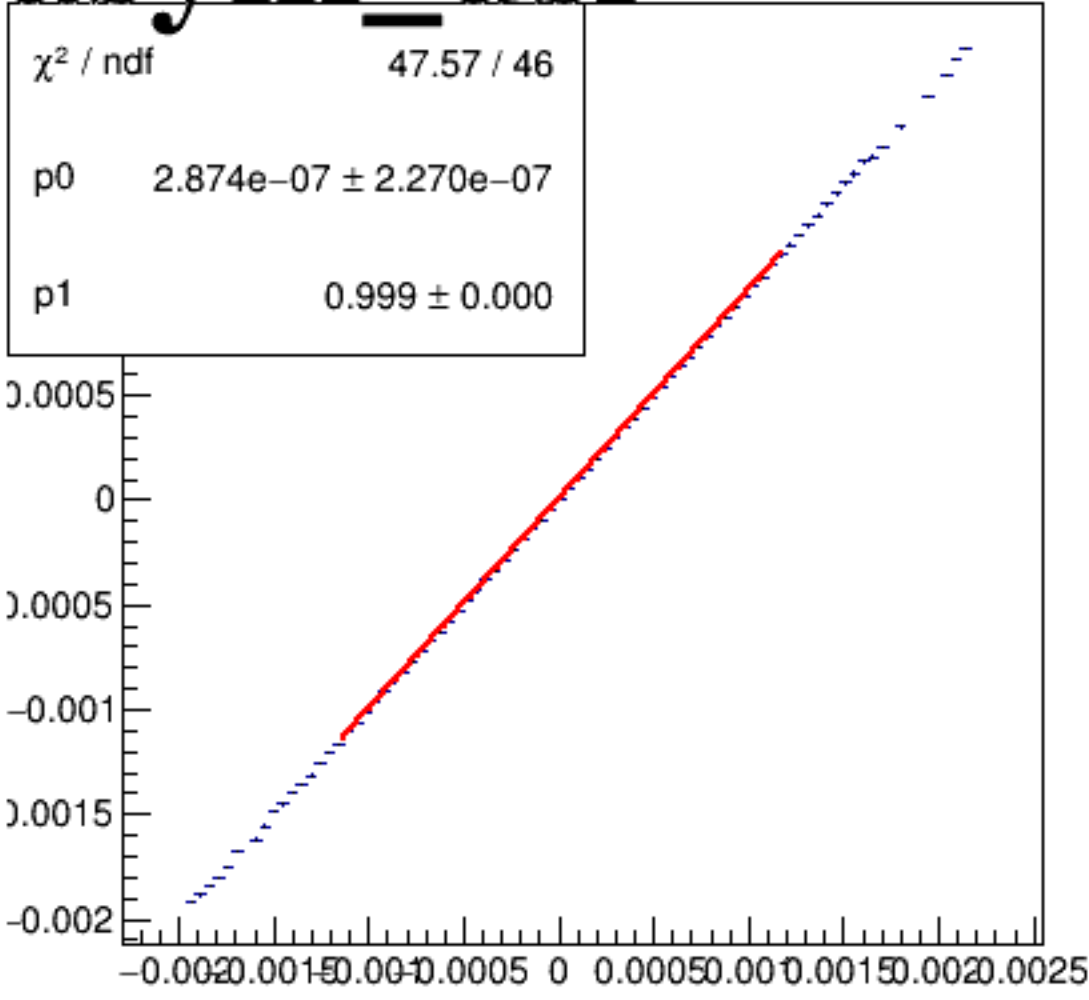


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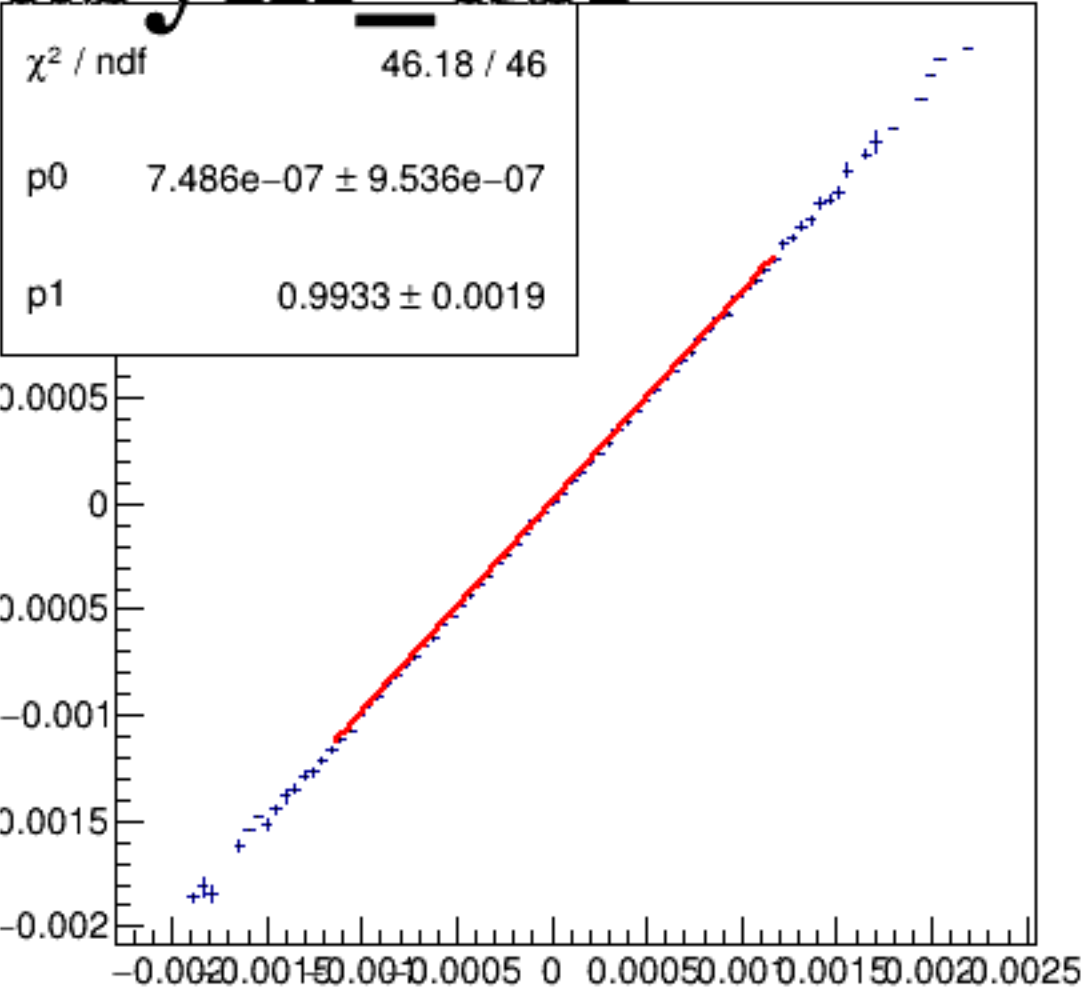


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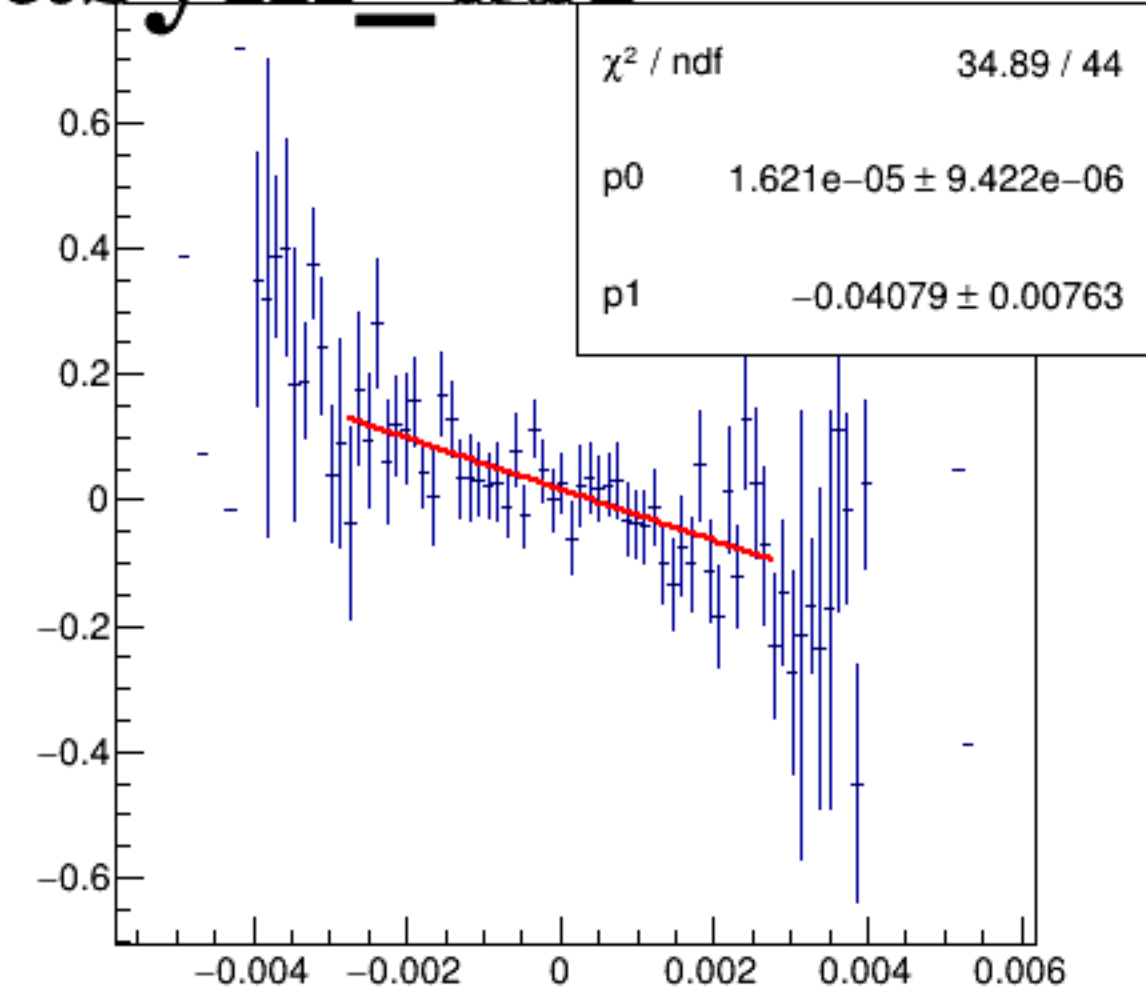
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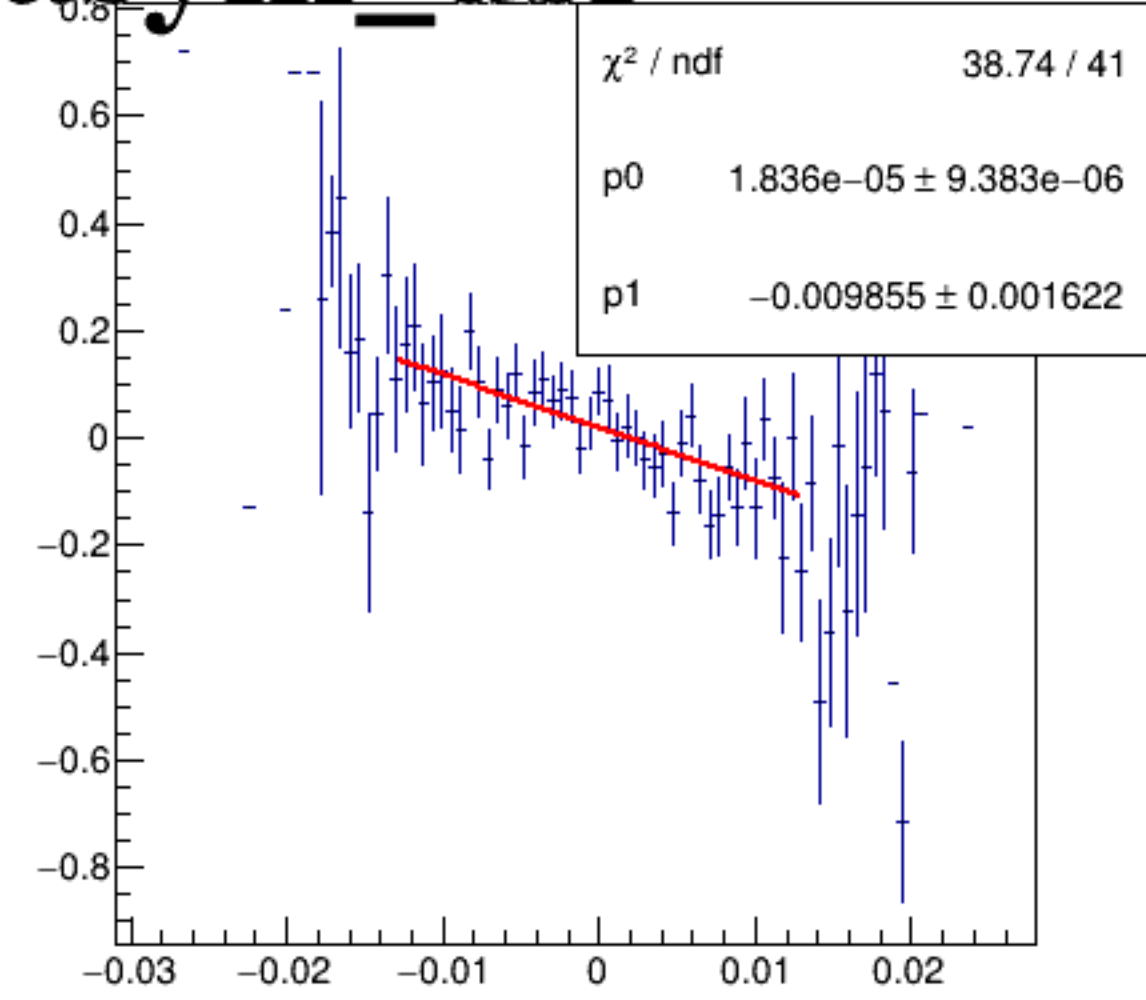
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asym_usr

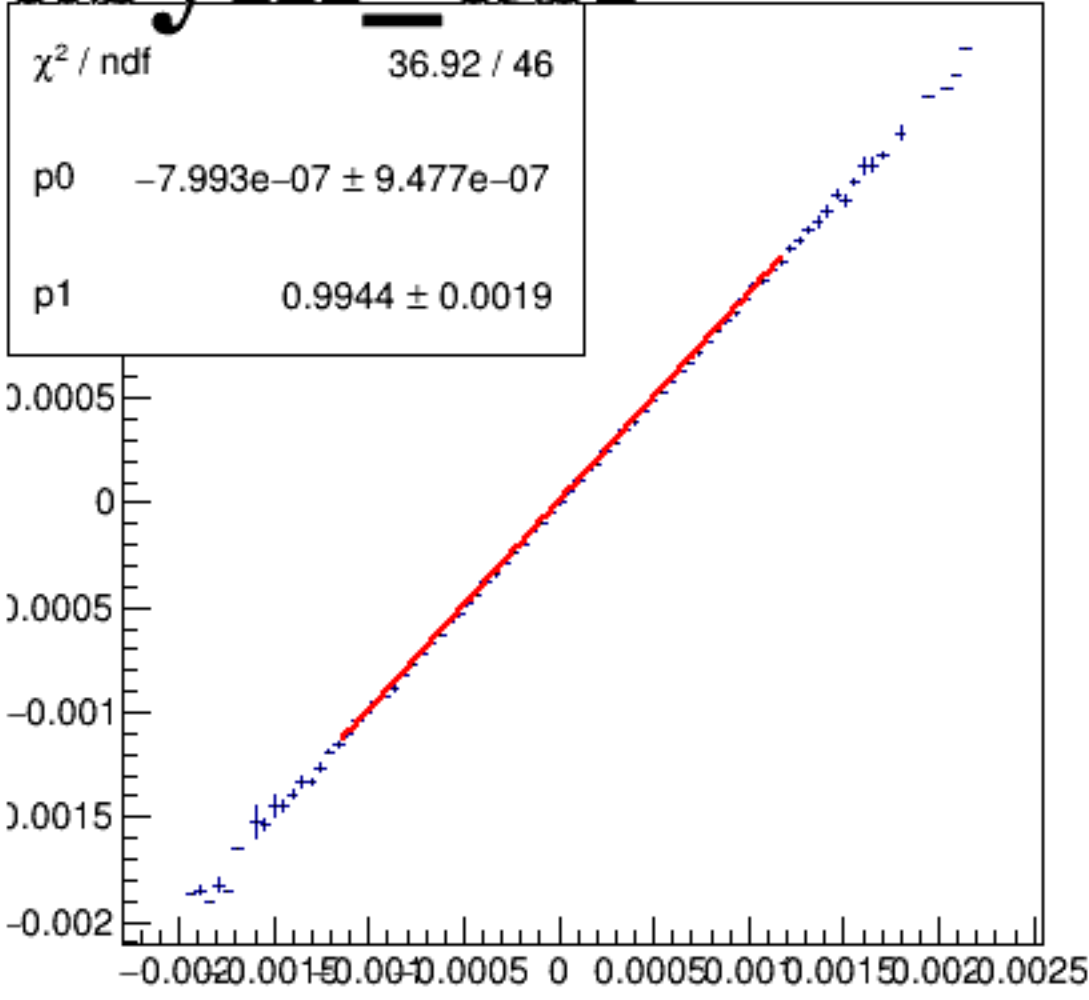


asym_dsr

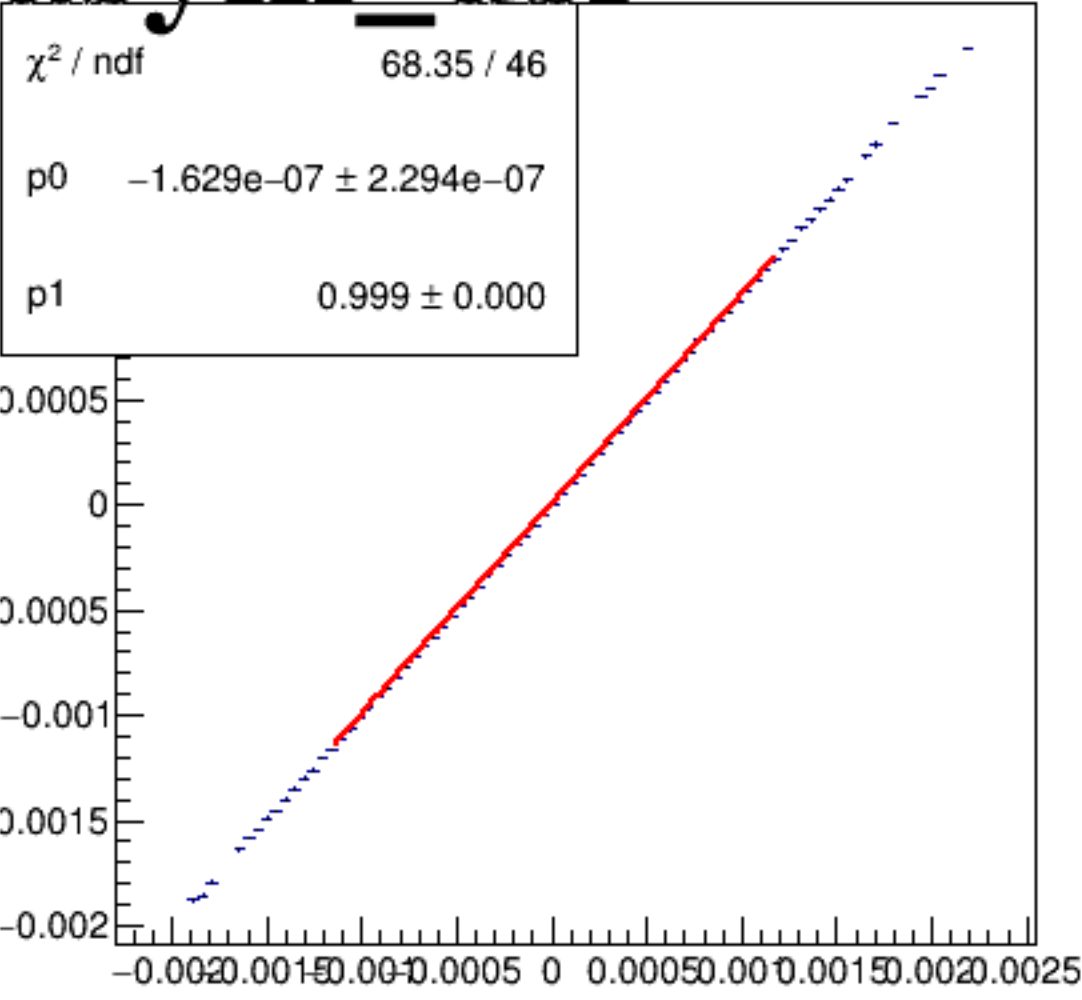


asym_dsl

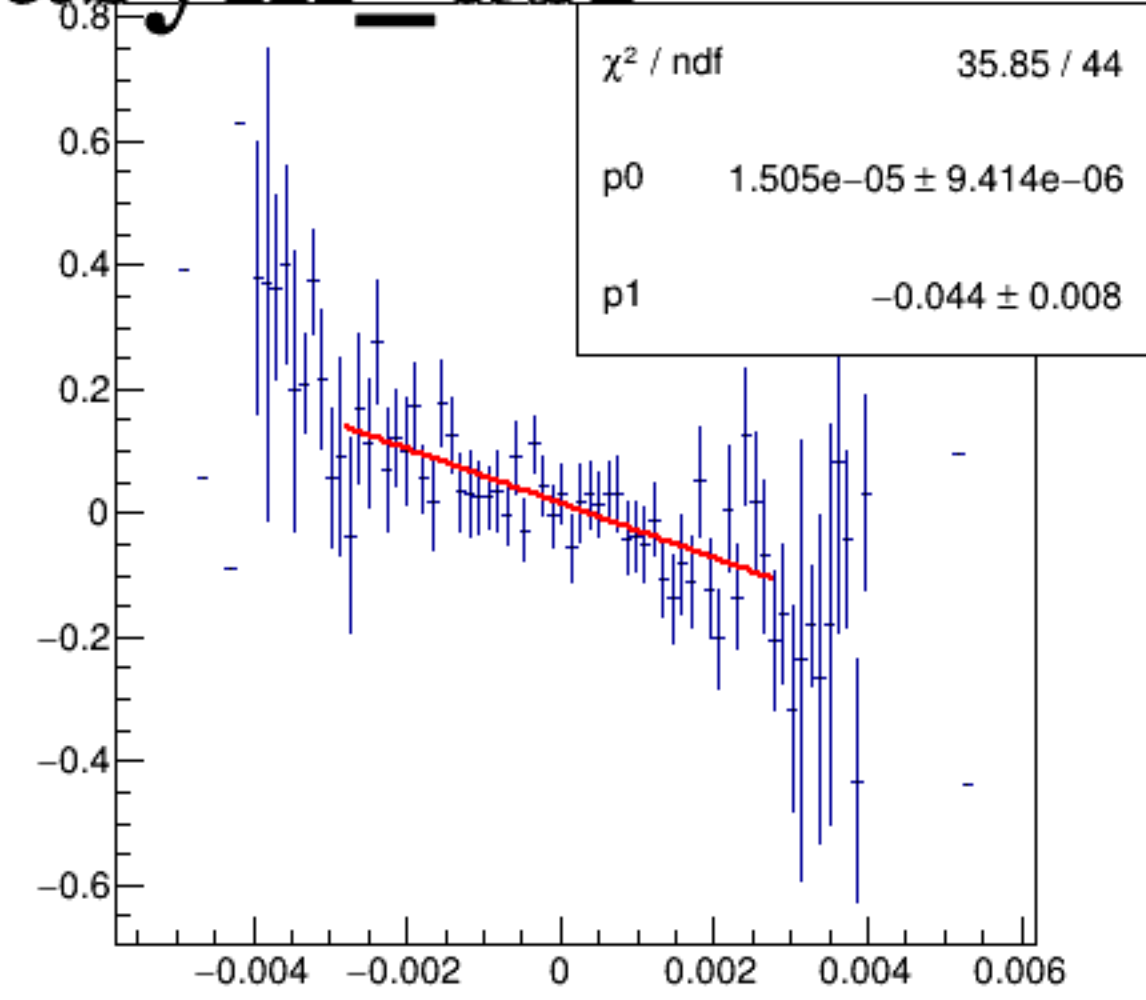
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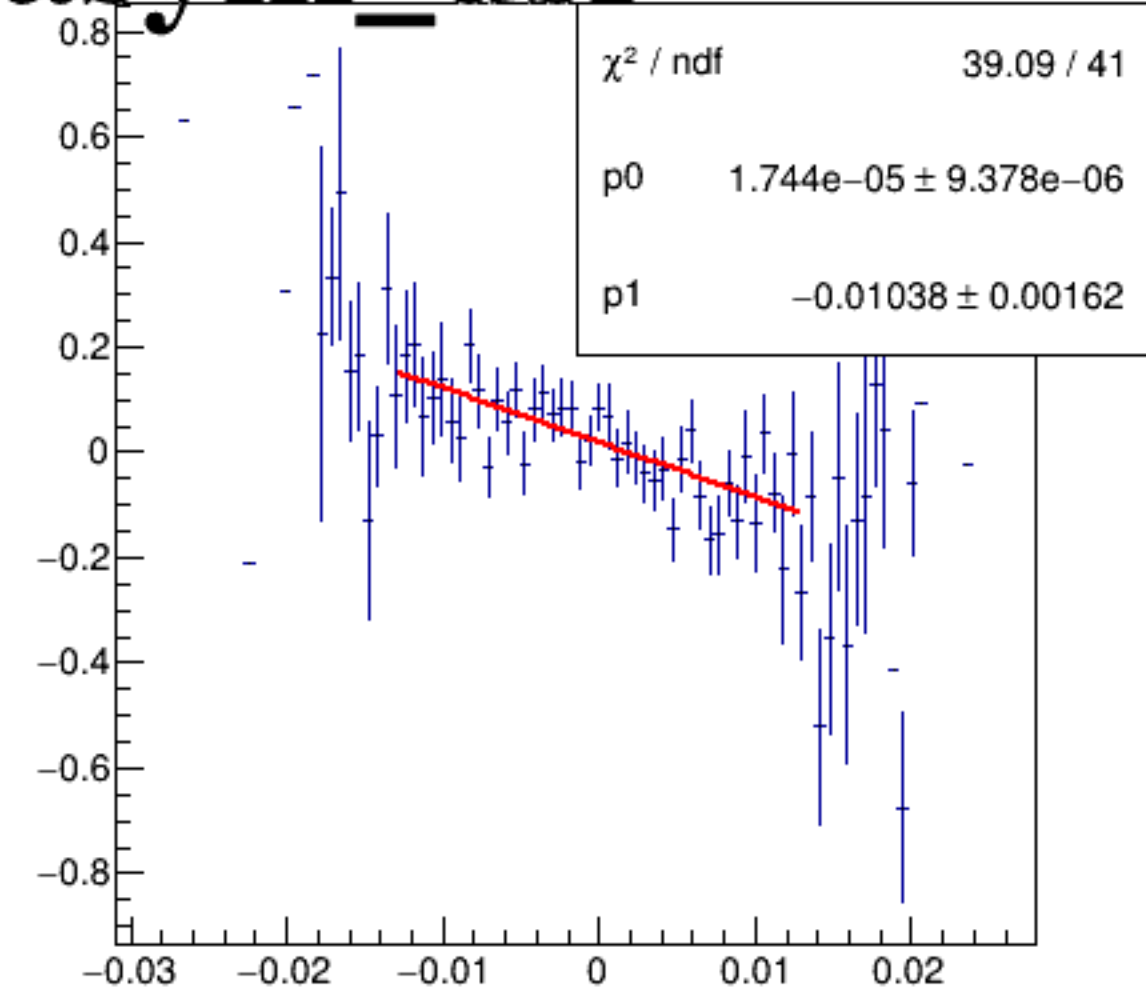
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asym_usr

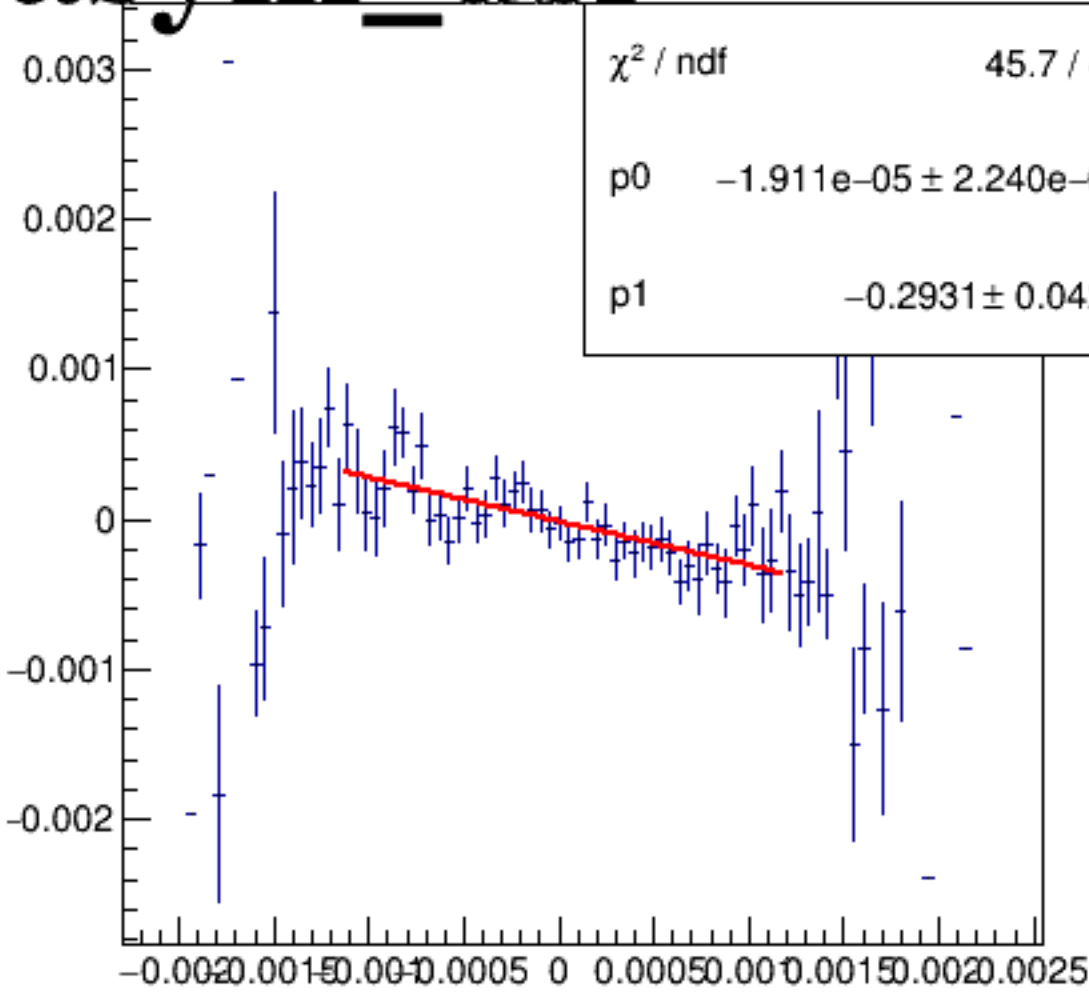


asym_dsr

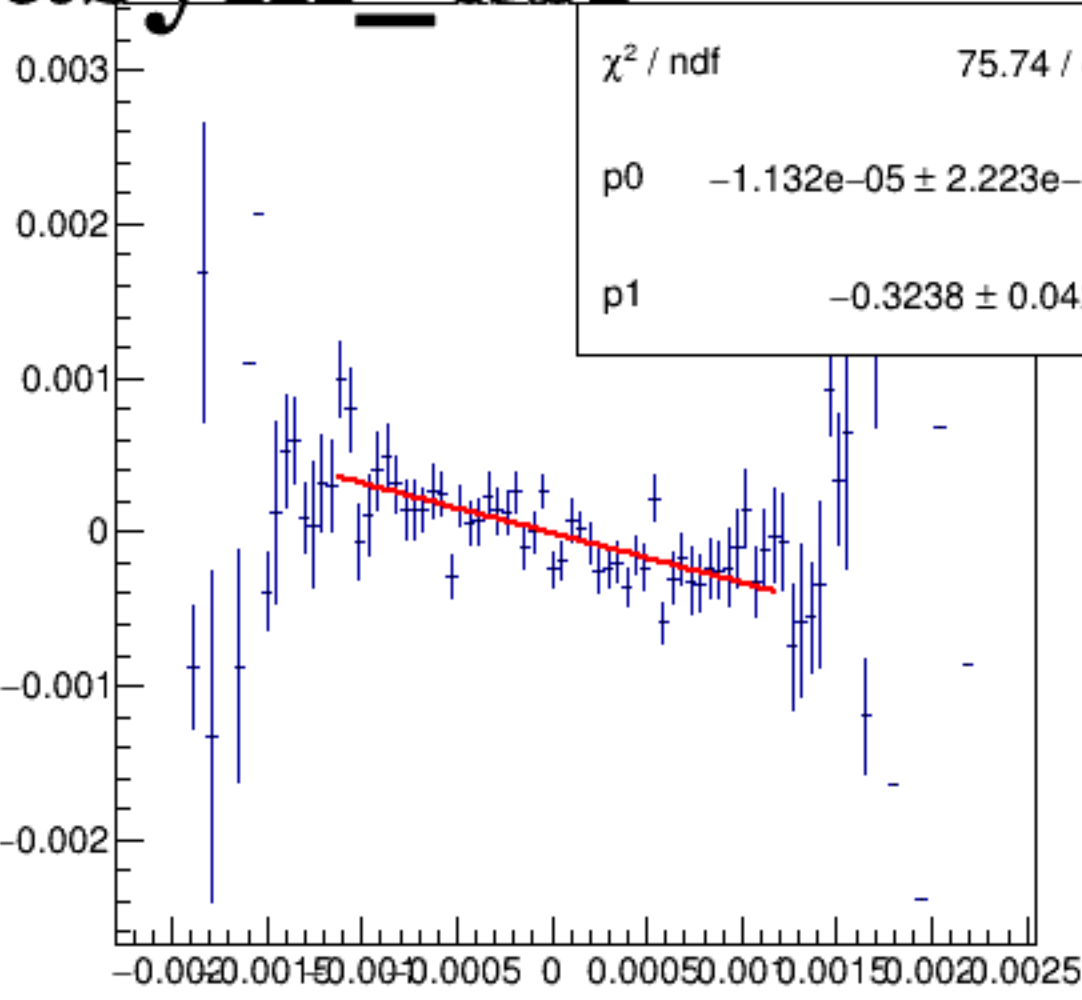


asym_usr

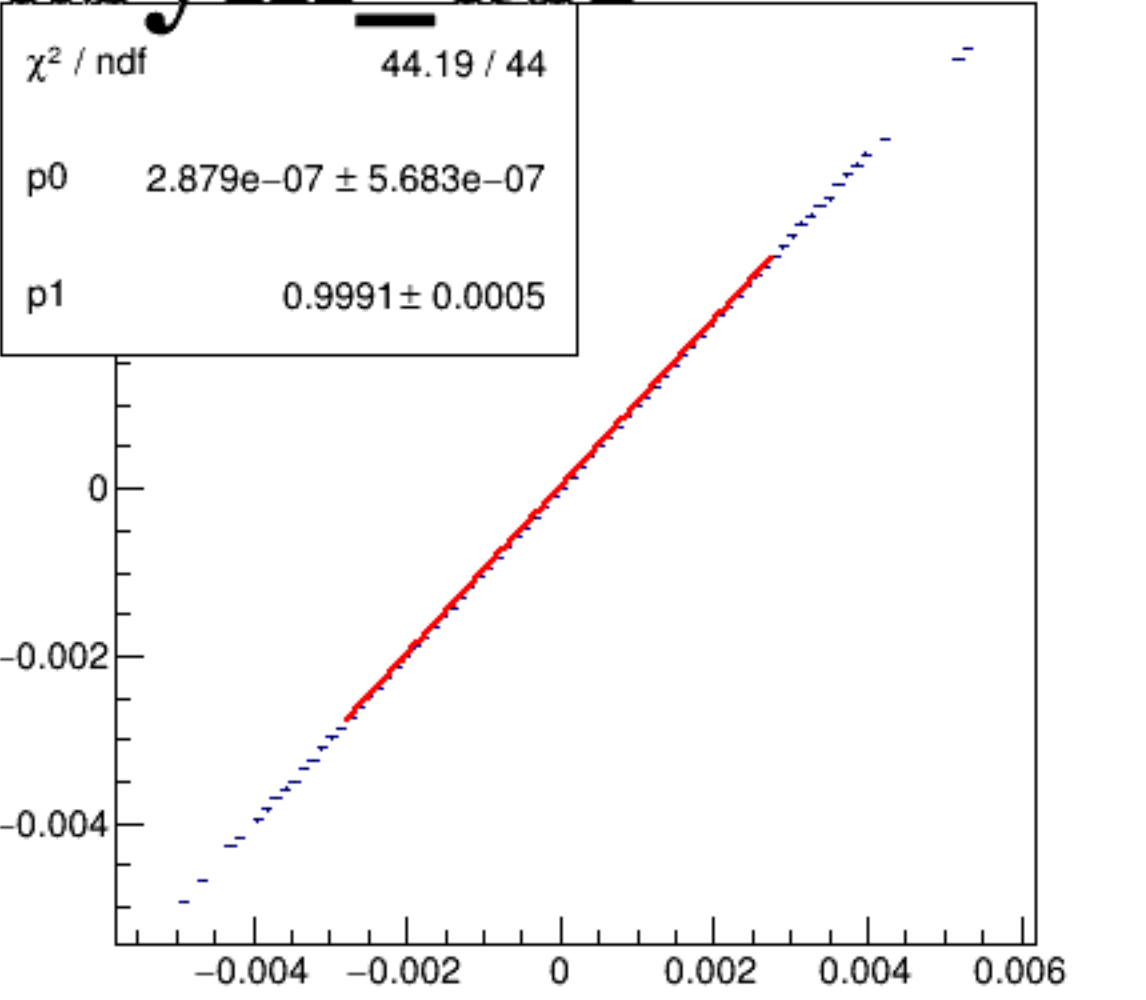
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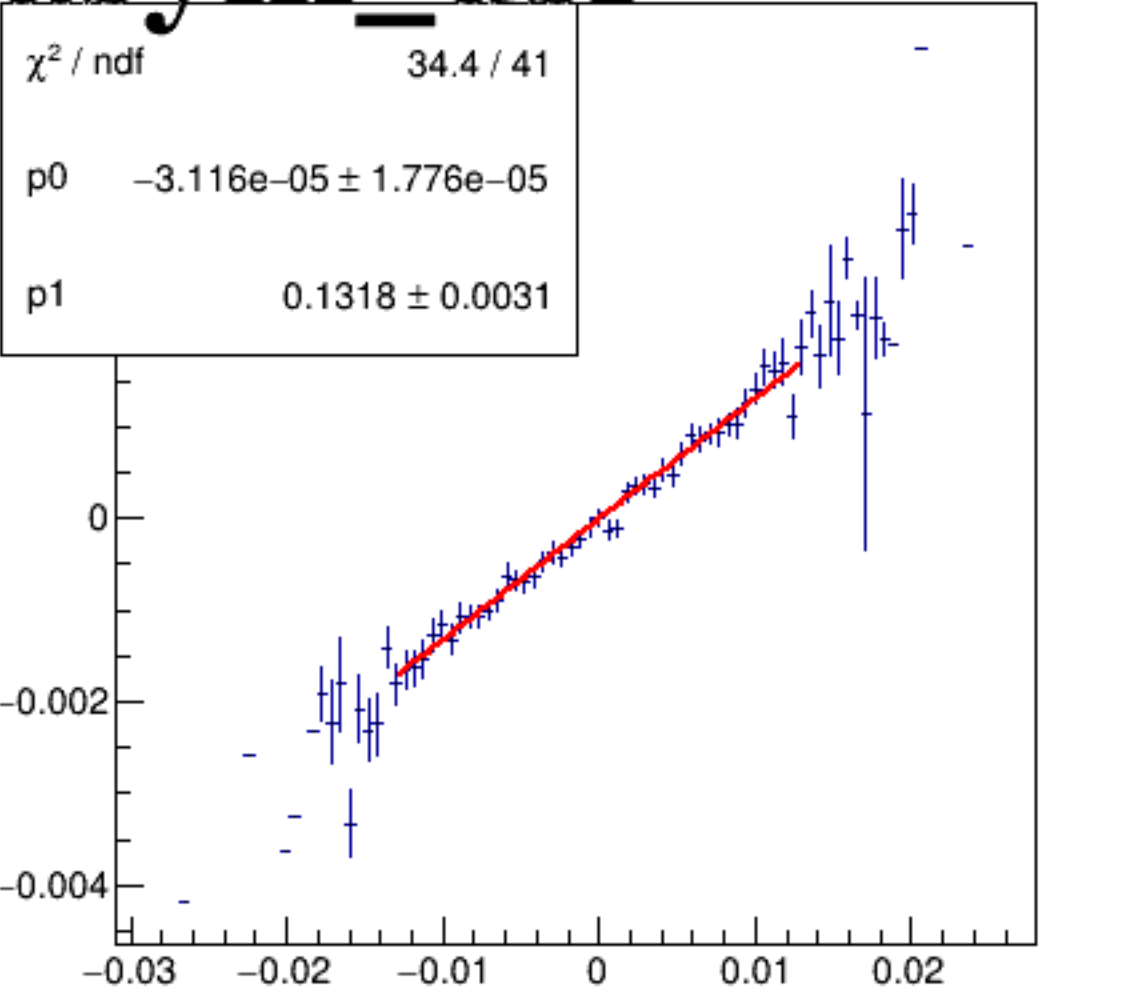
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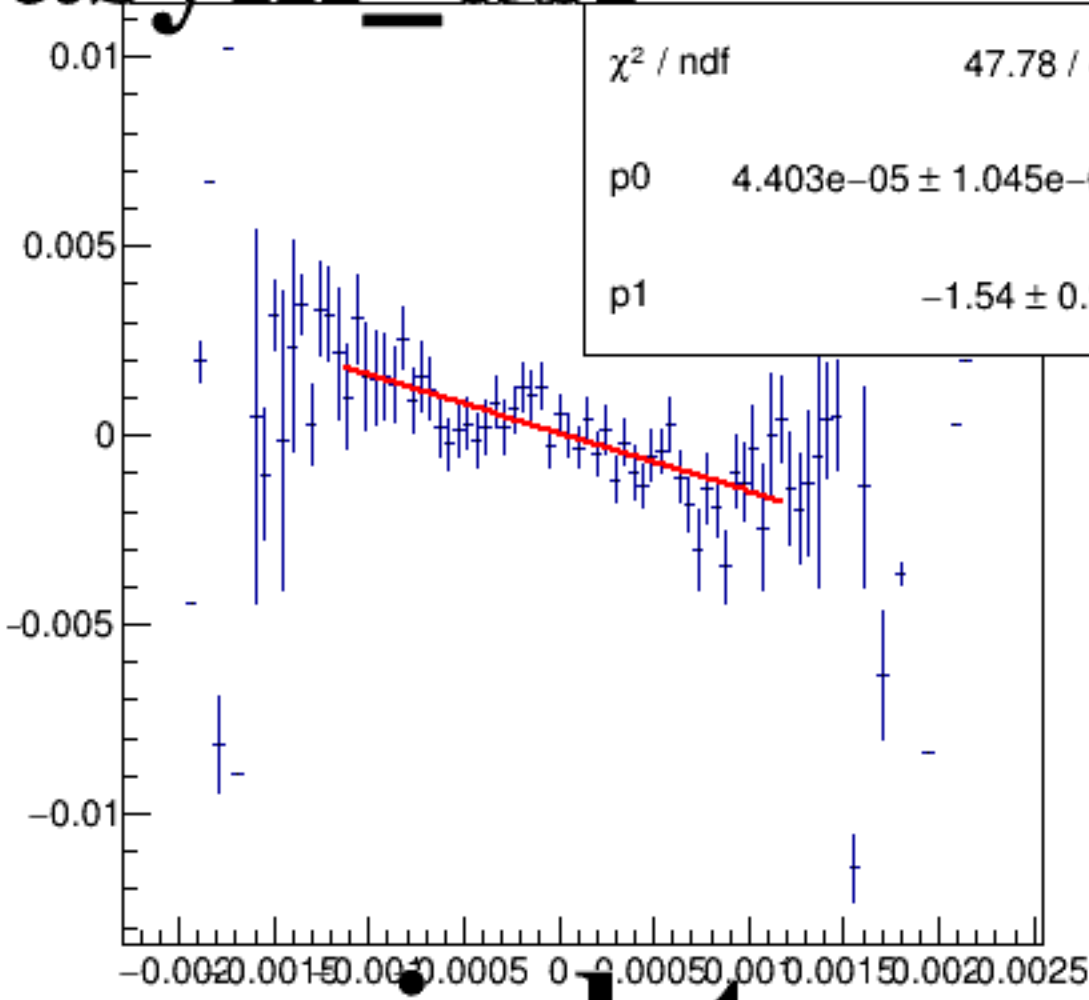


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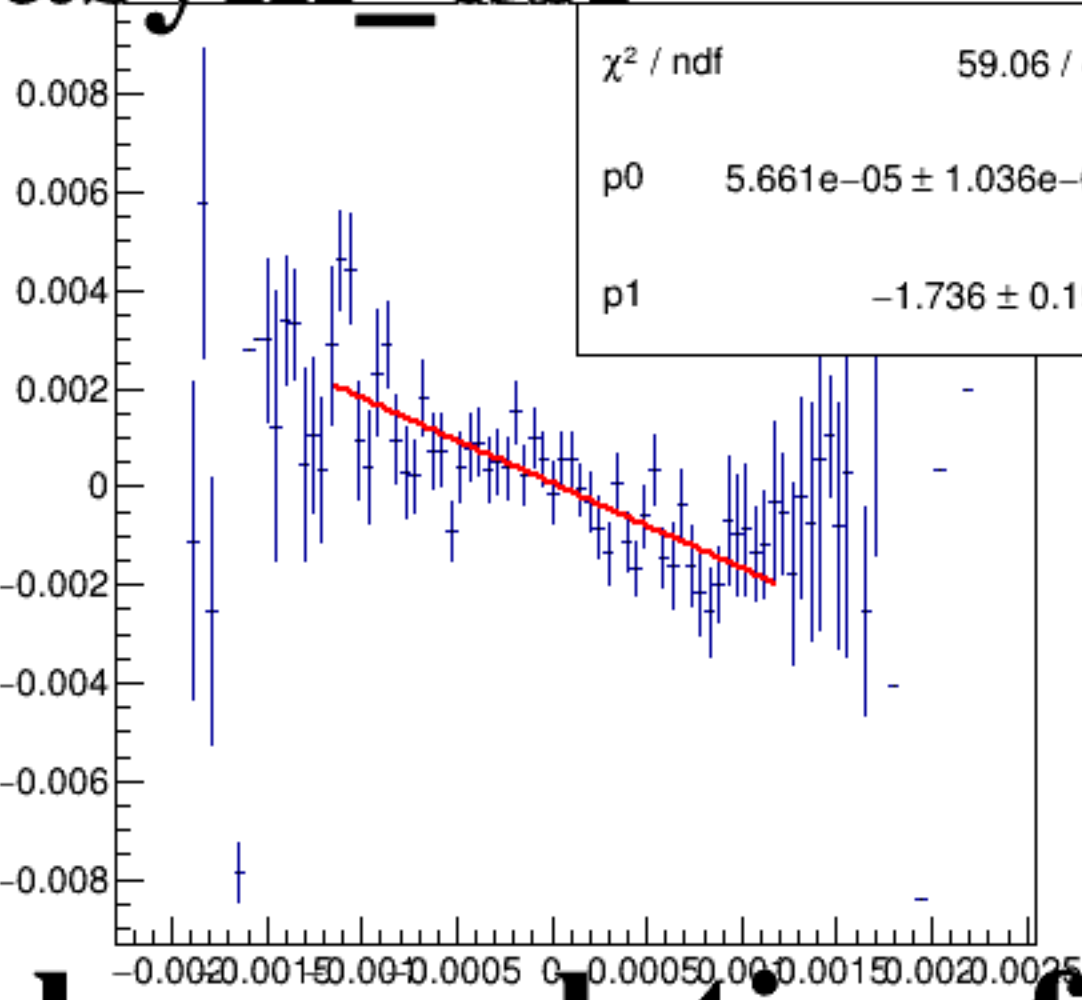


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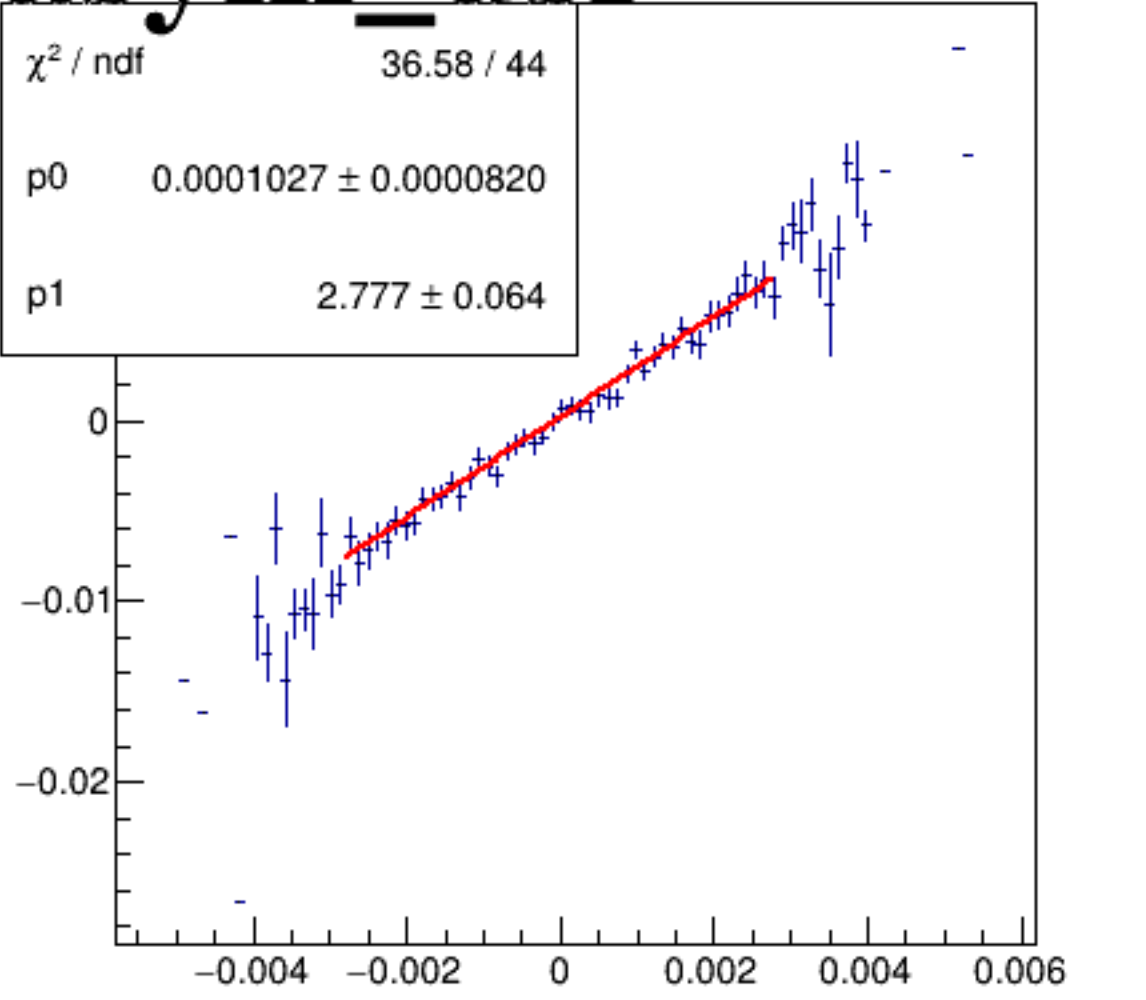
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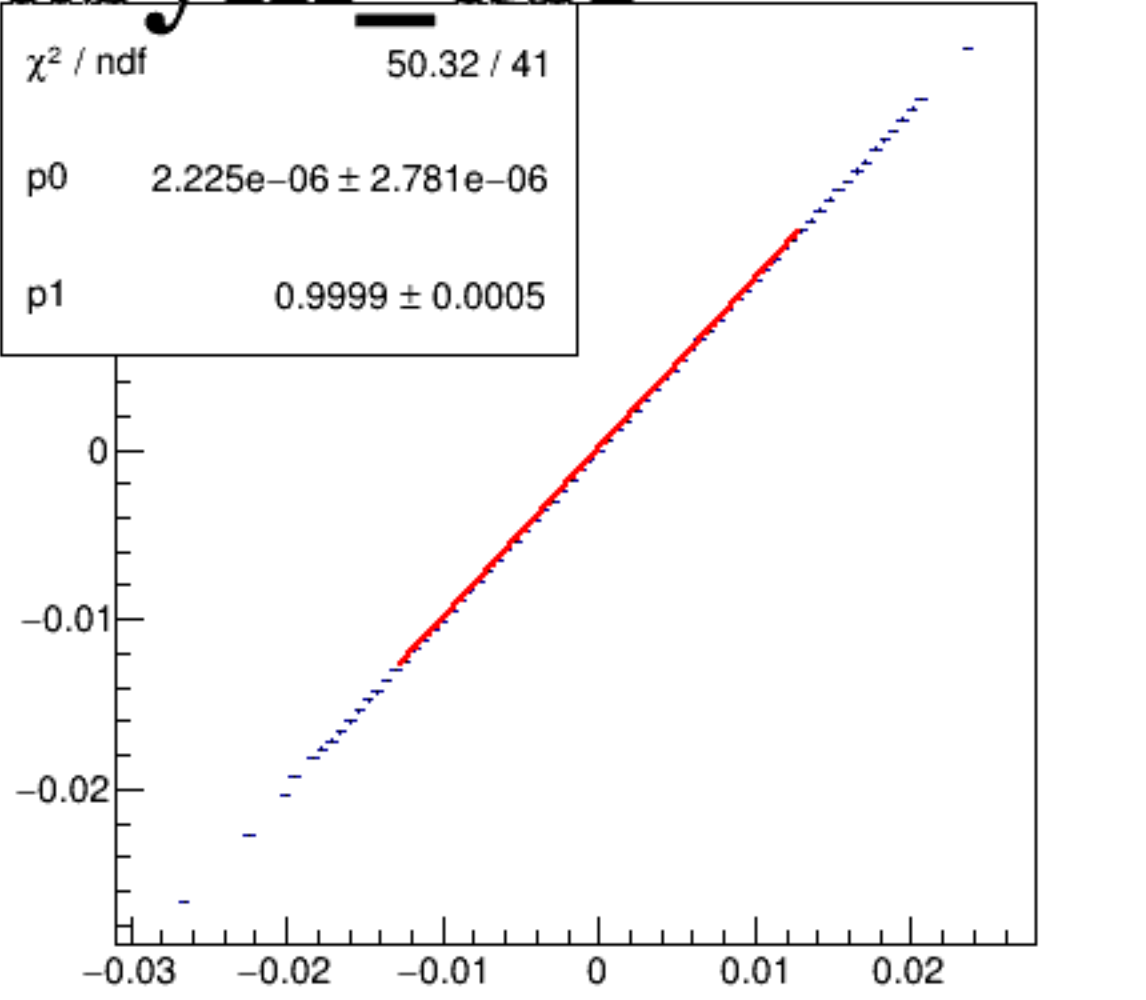
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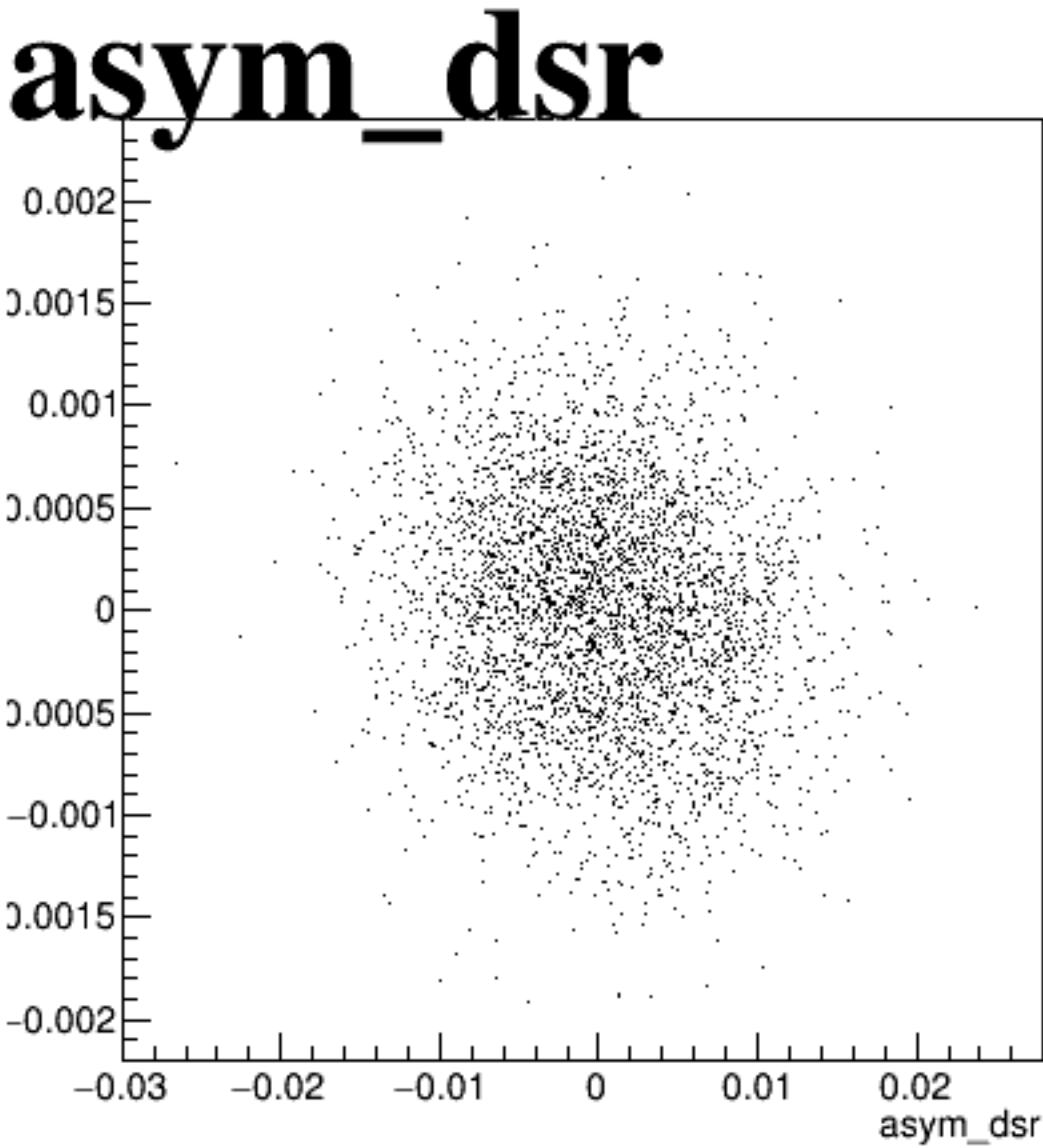
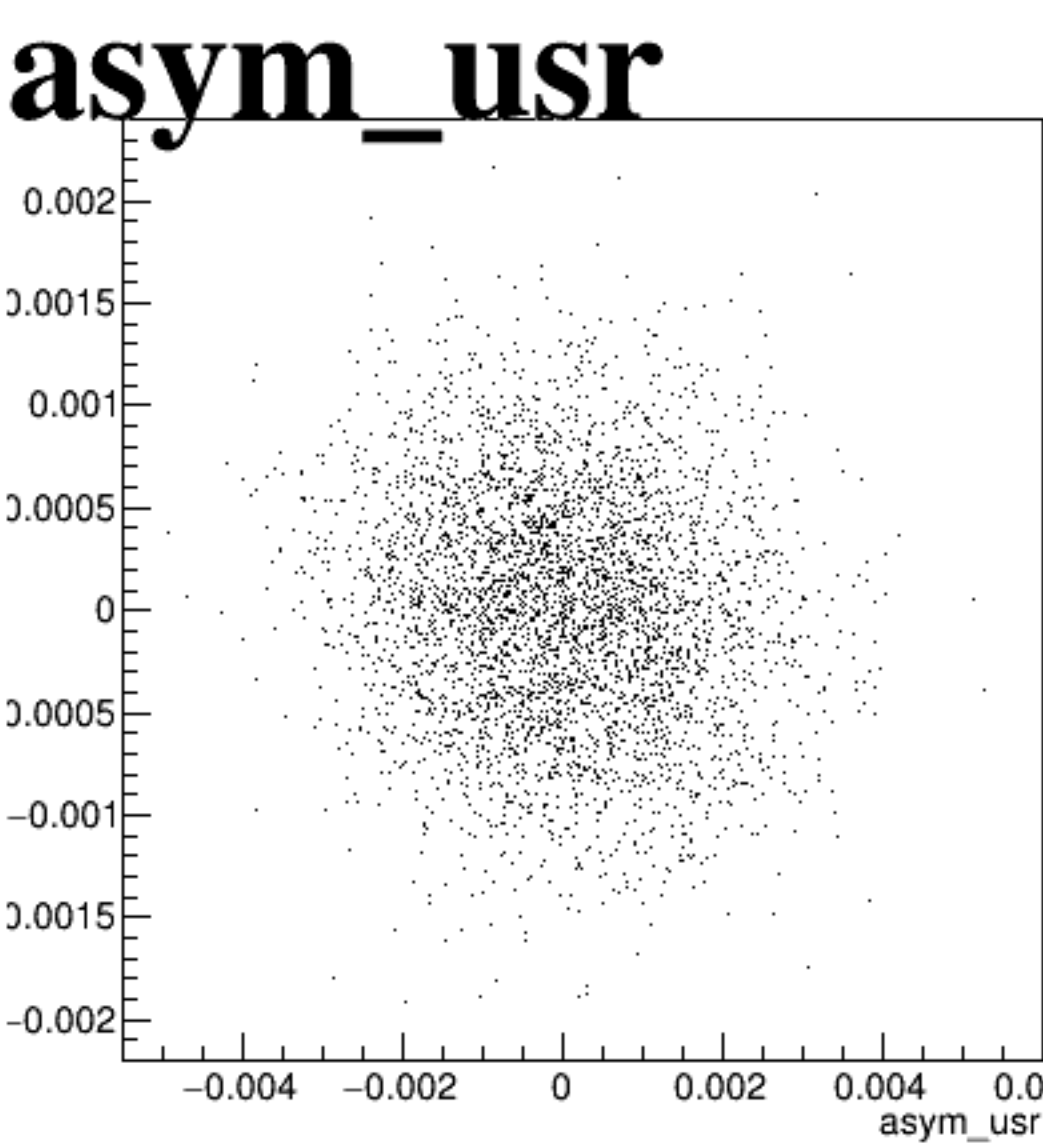
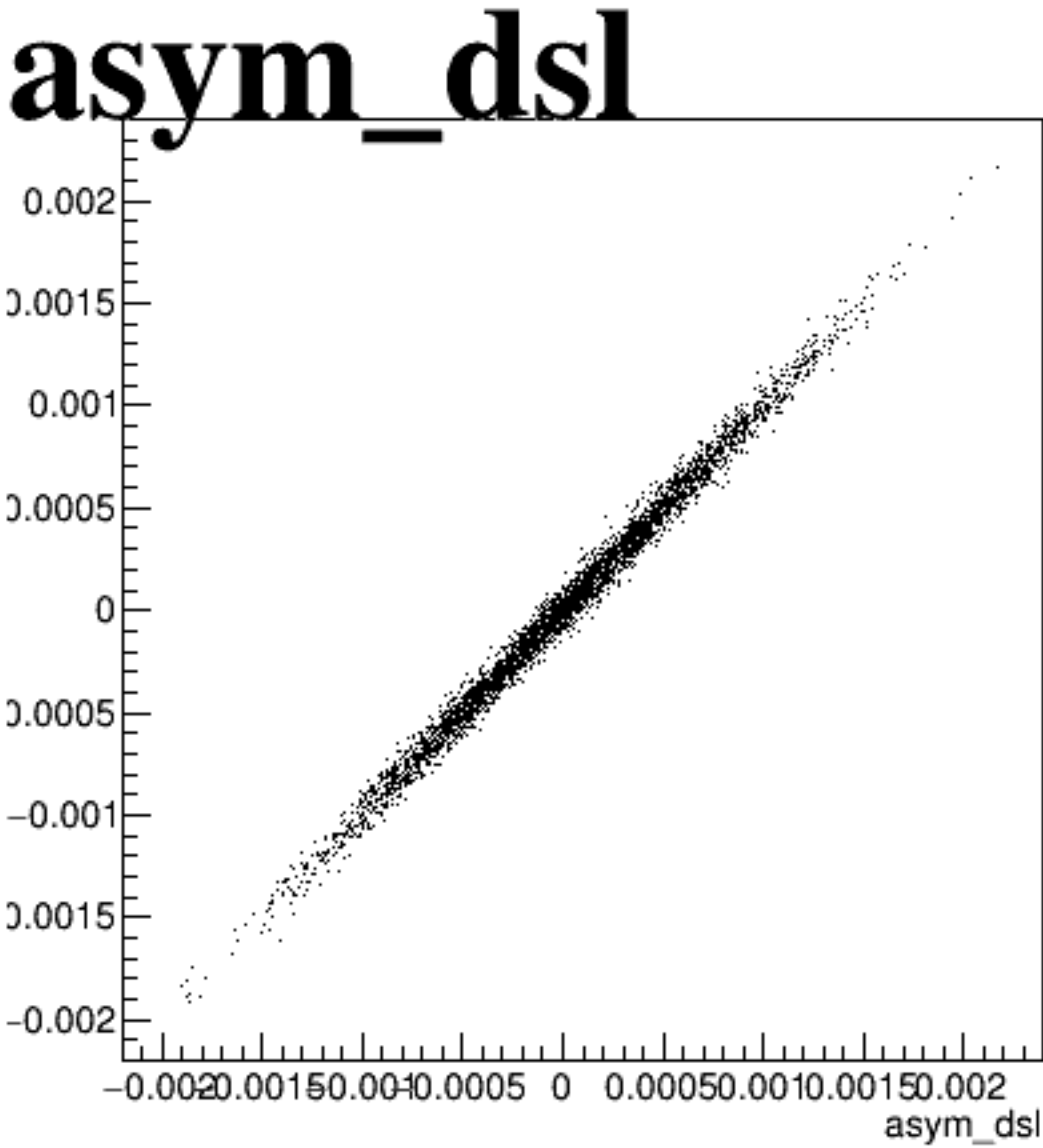
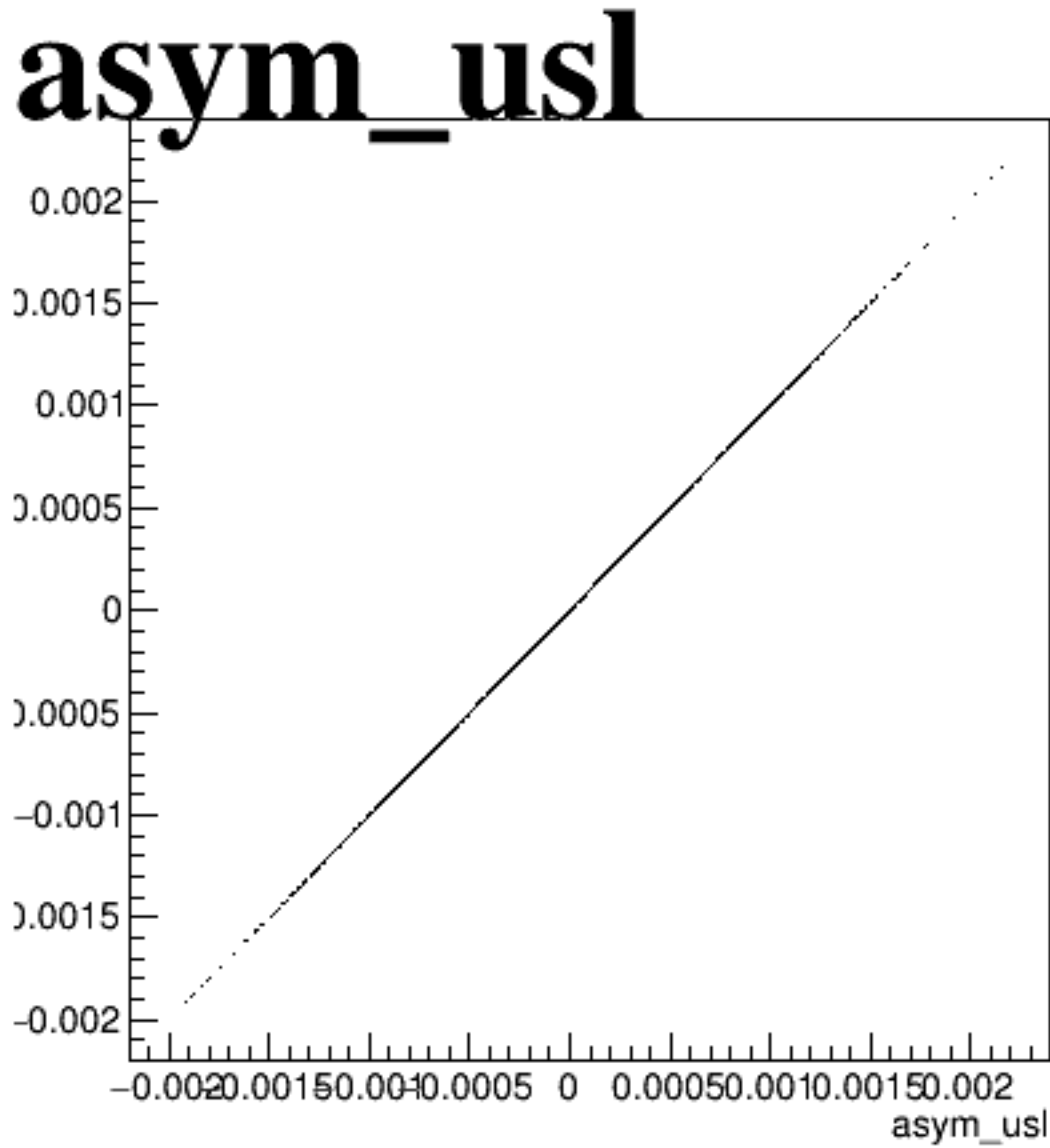
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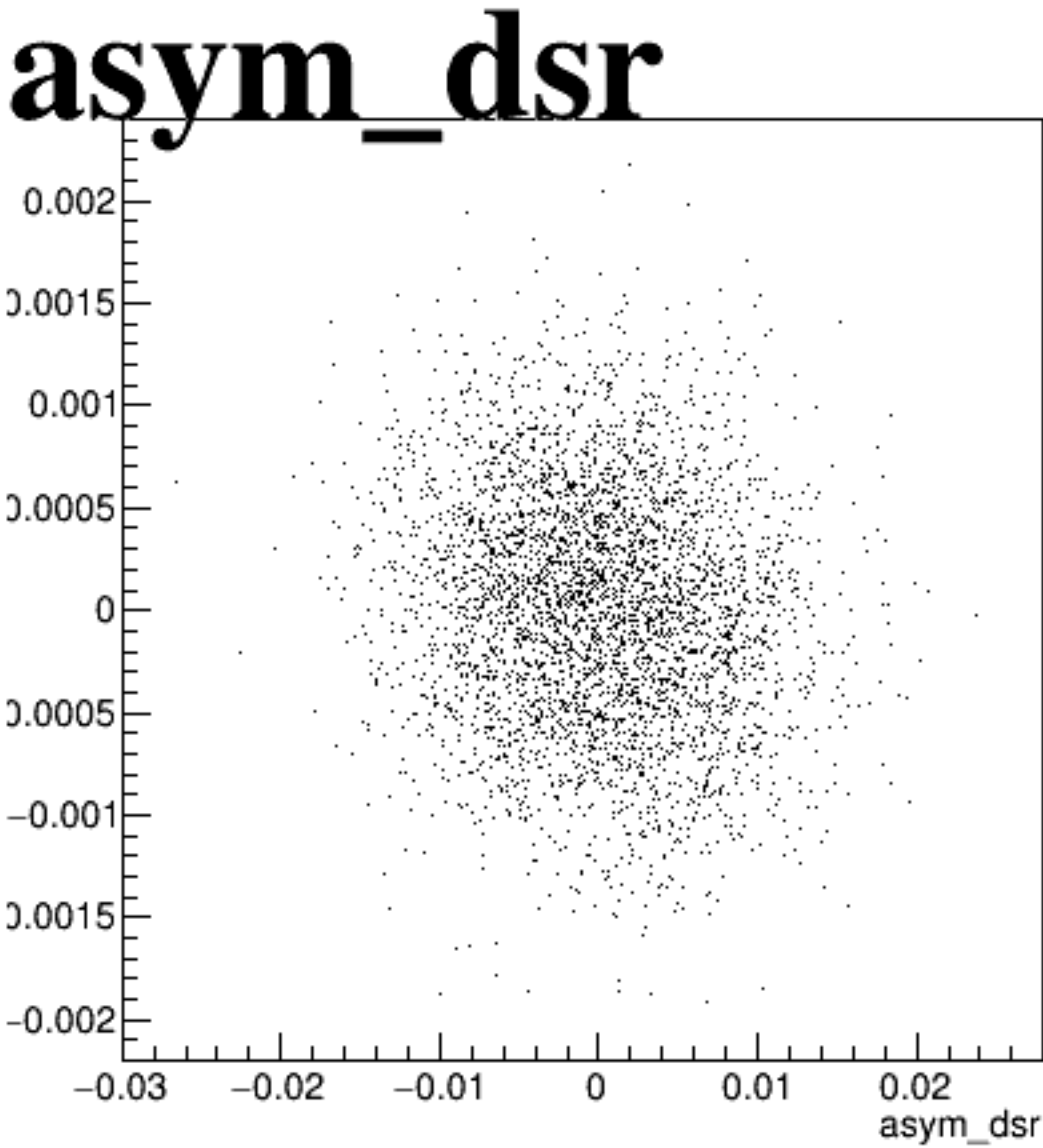
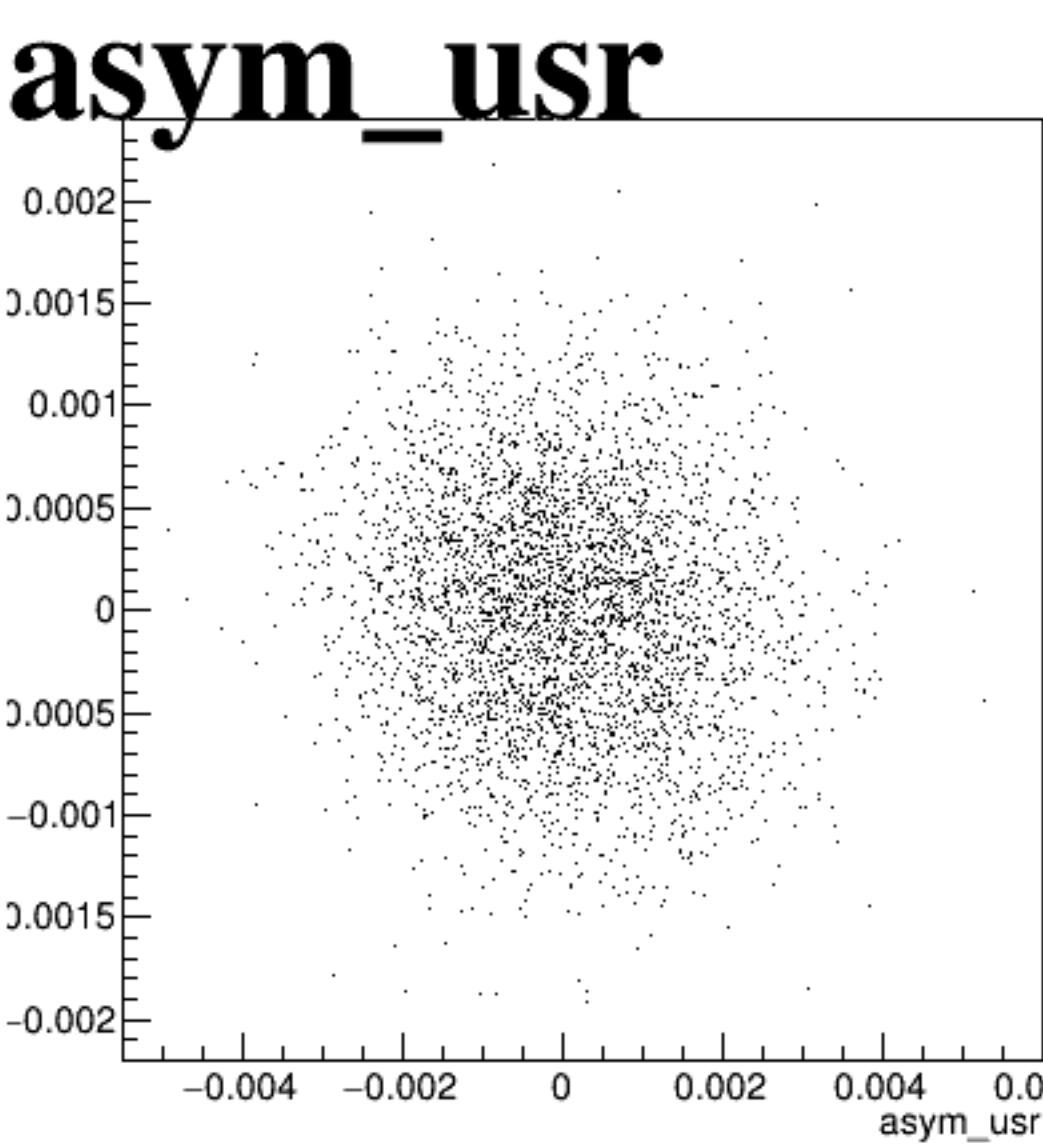
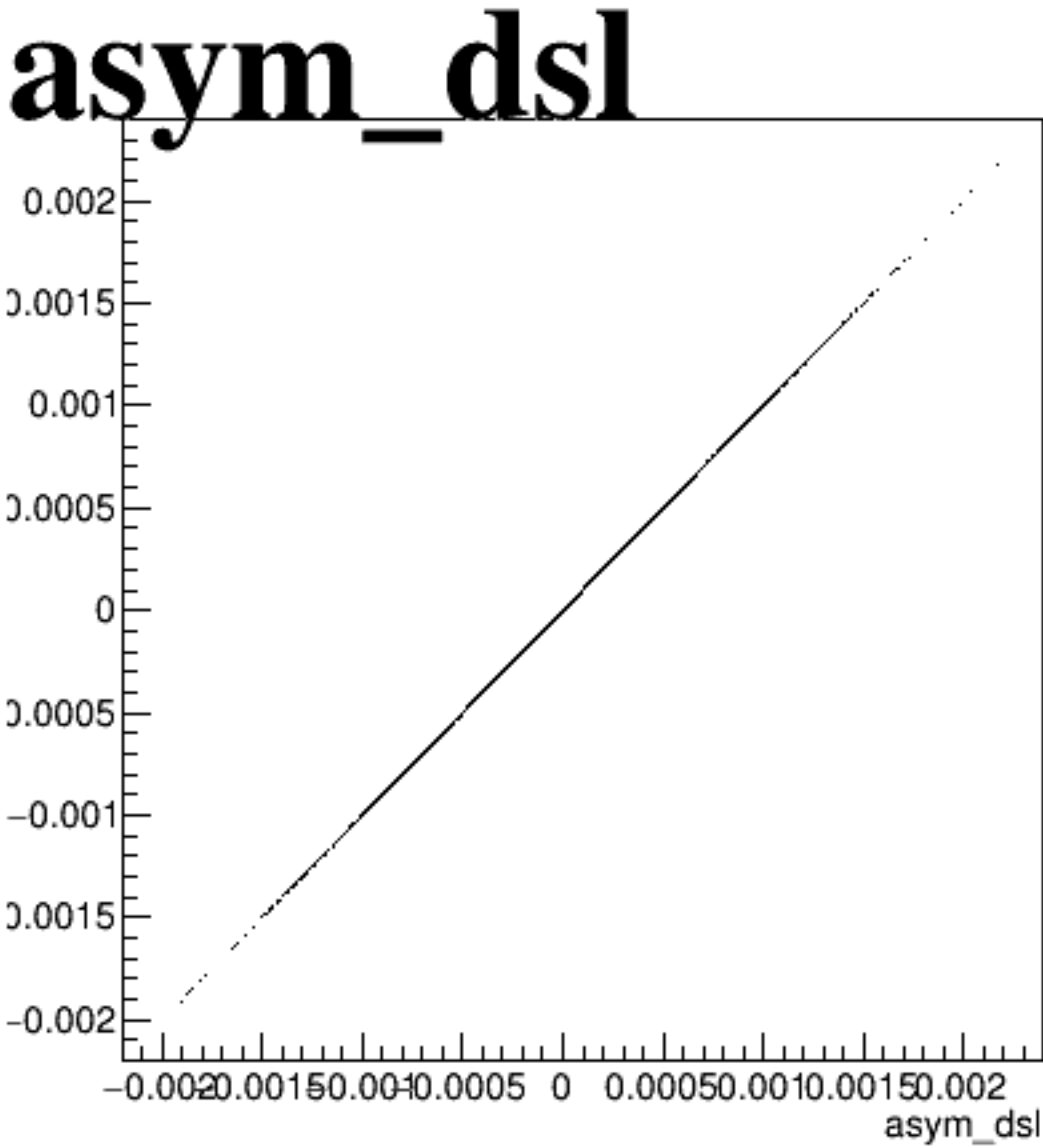
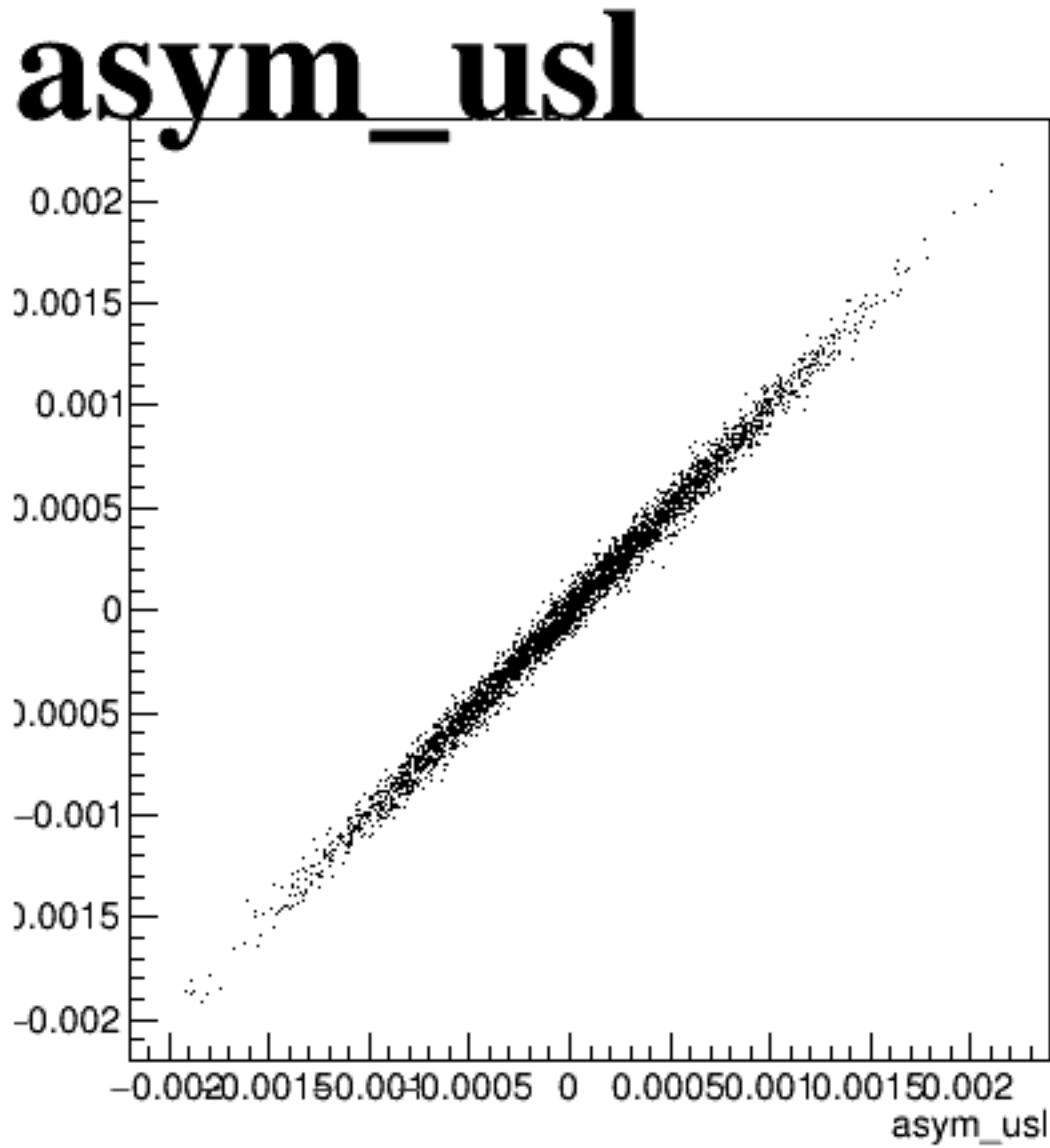
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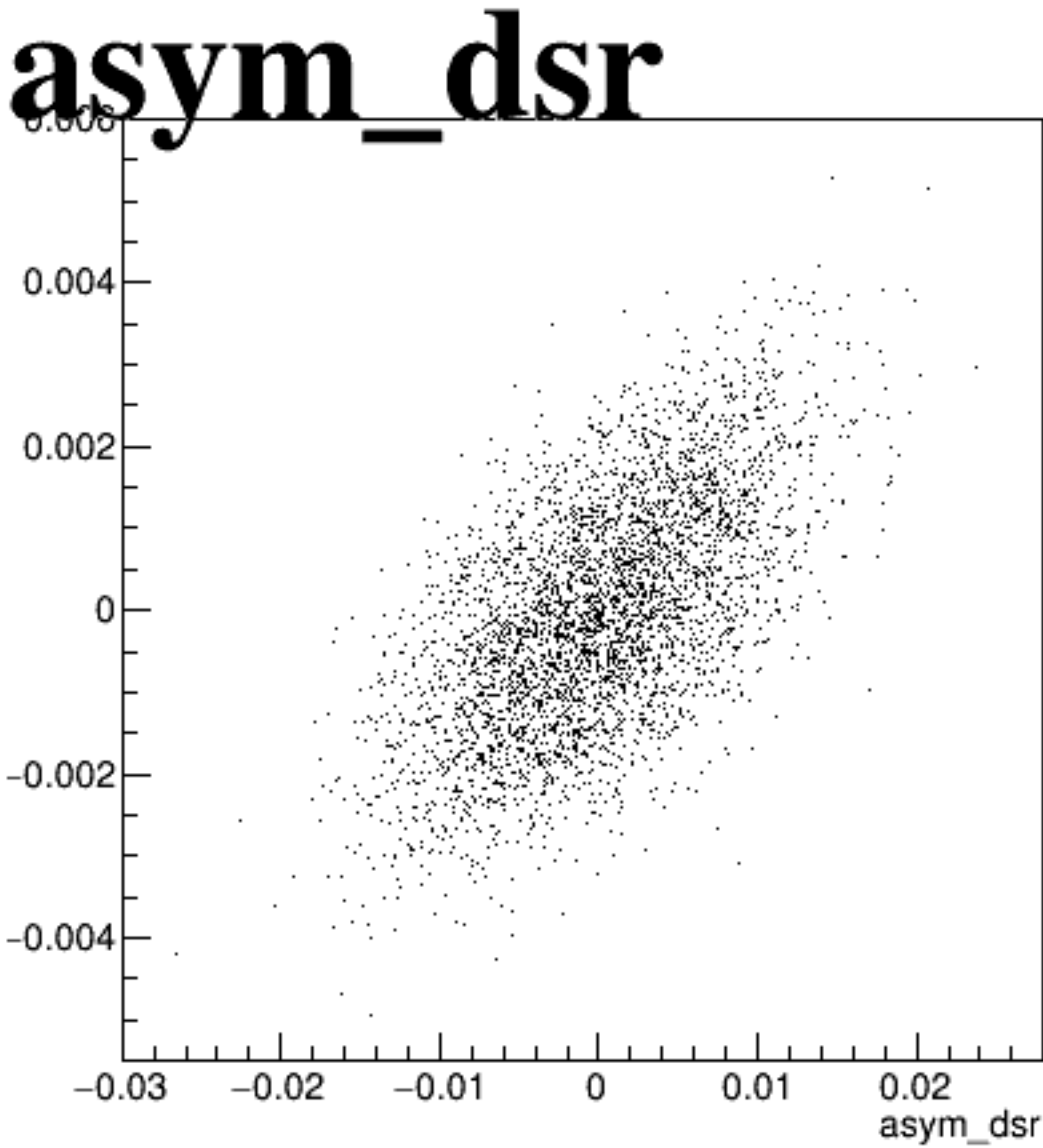
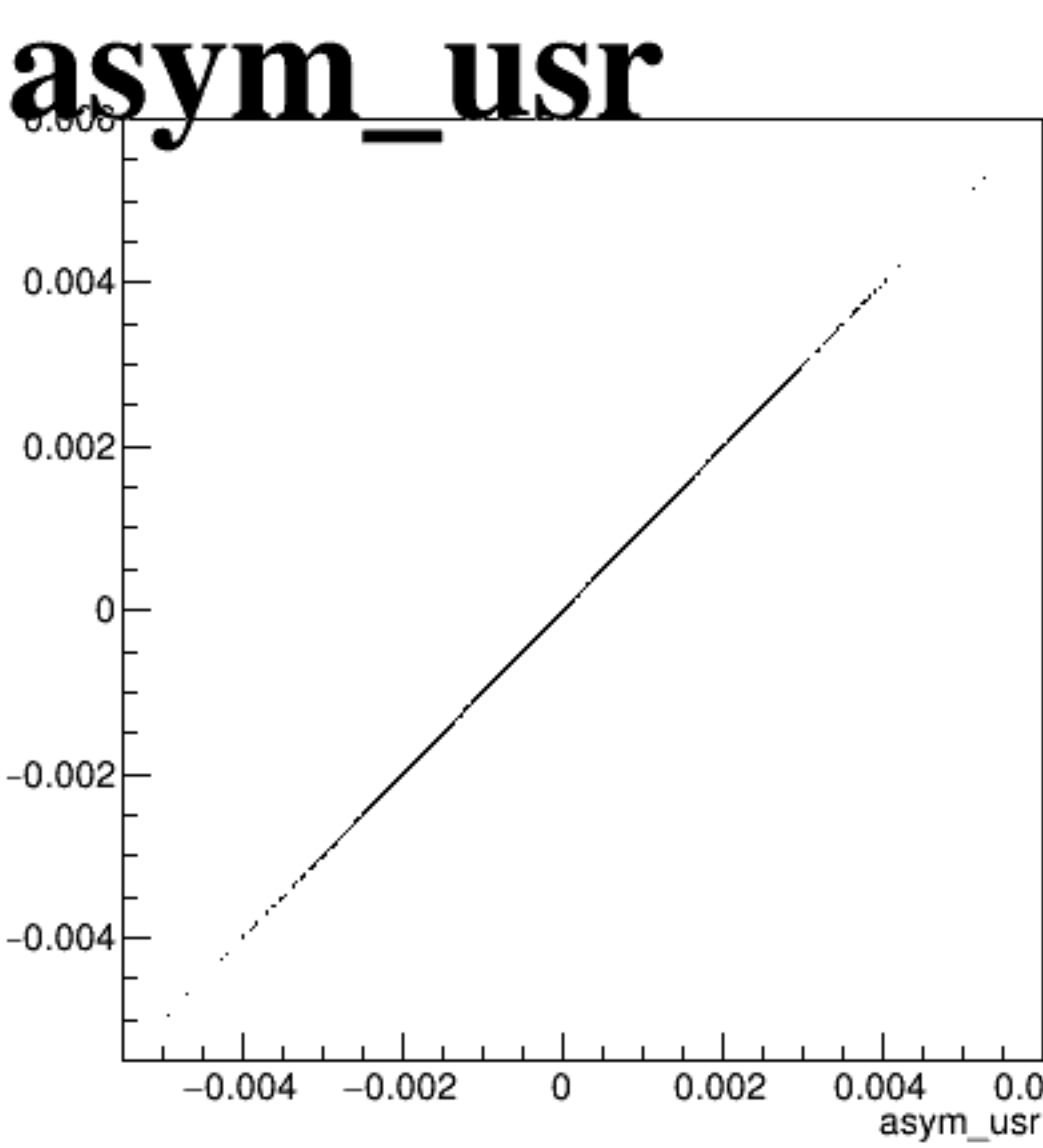
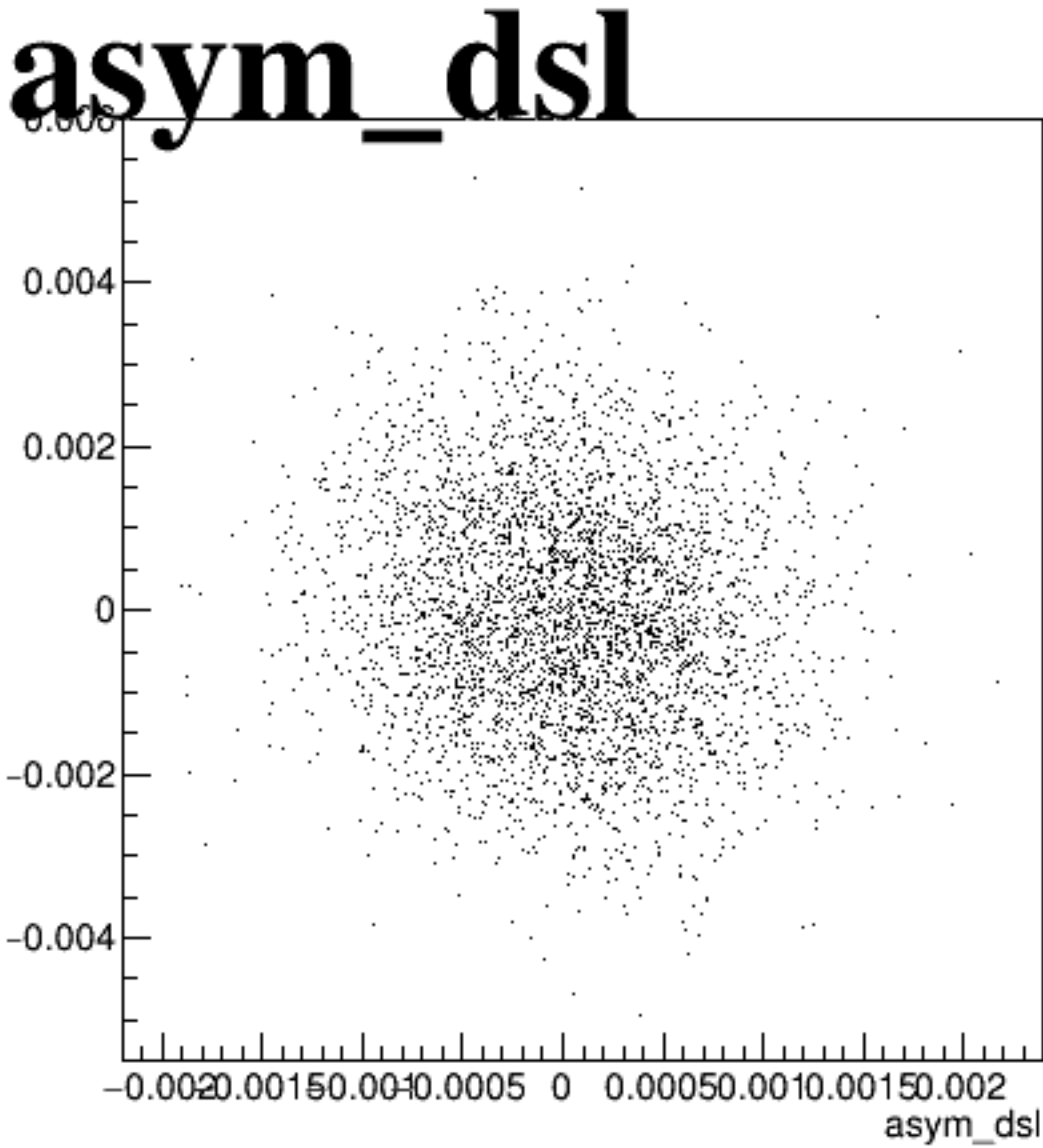
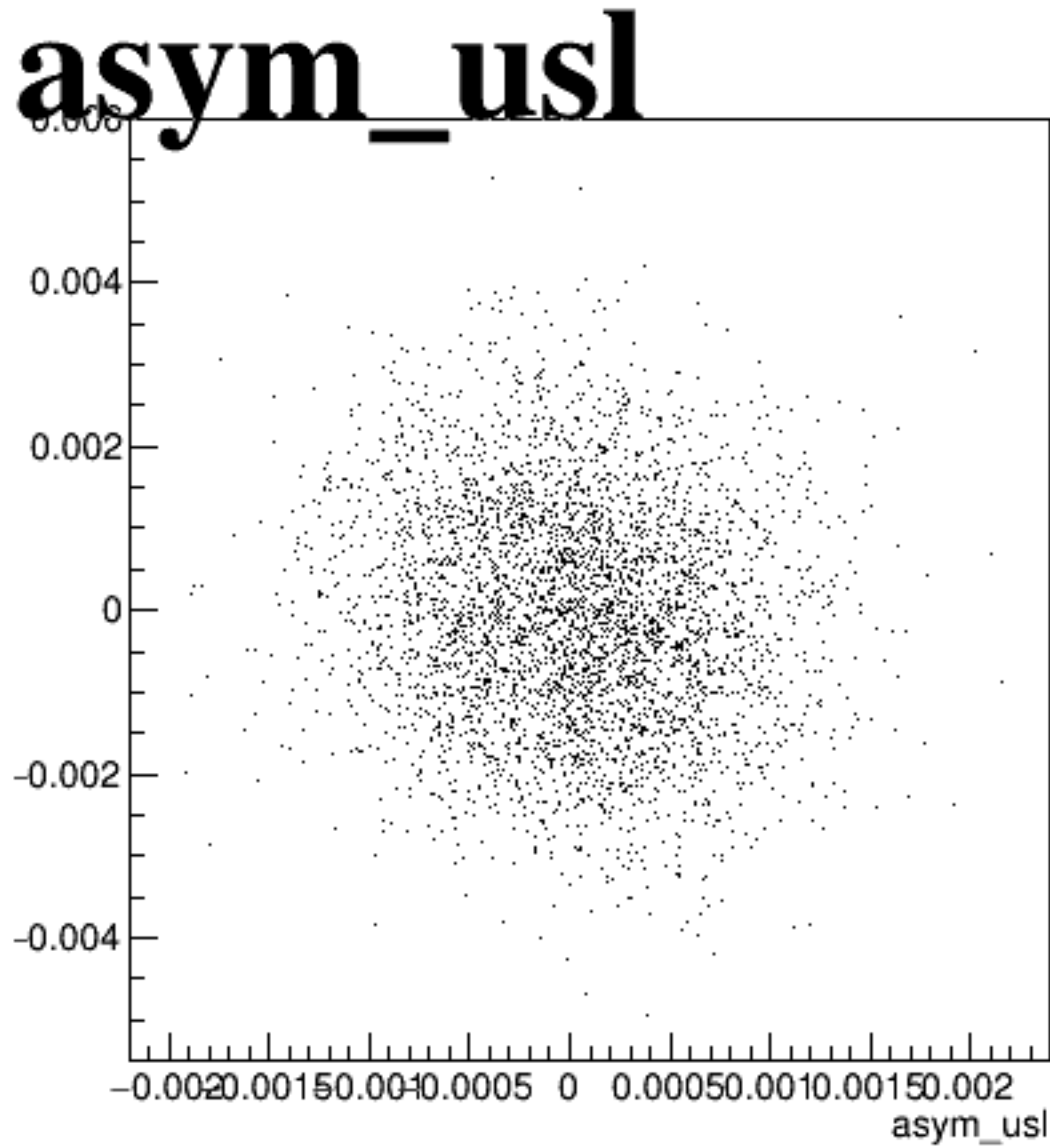
asym_usl



asym_dsl



asym_usr



asym_dsr

