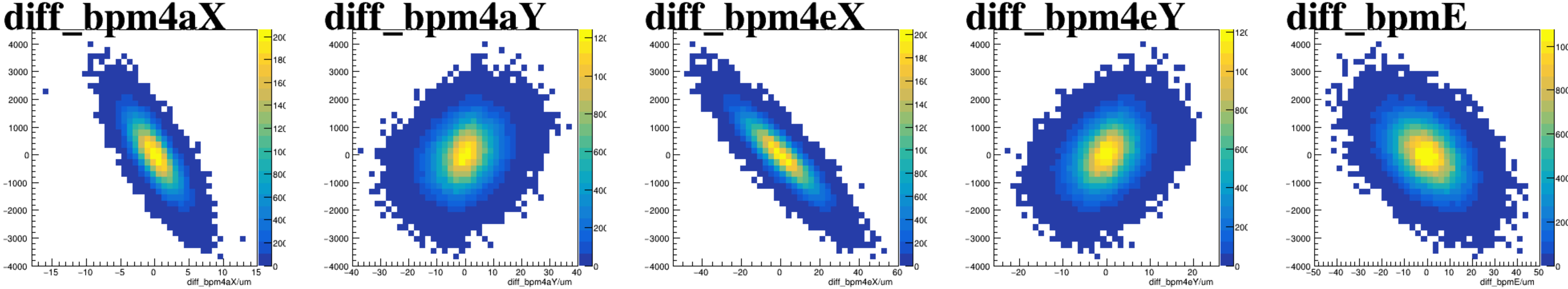
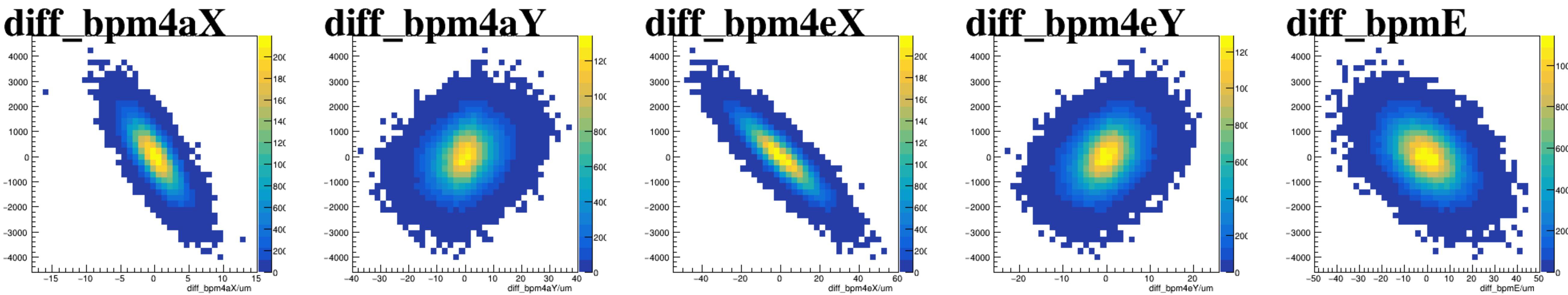


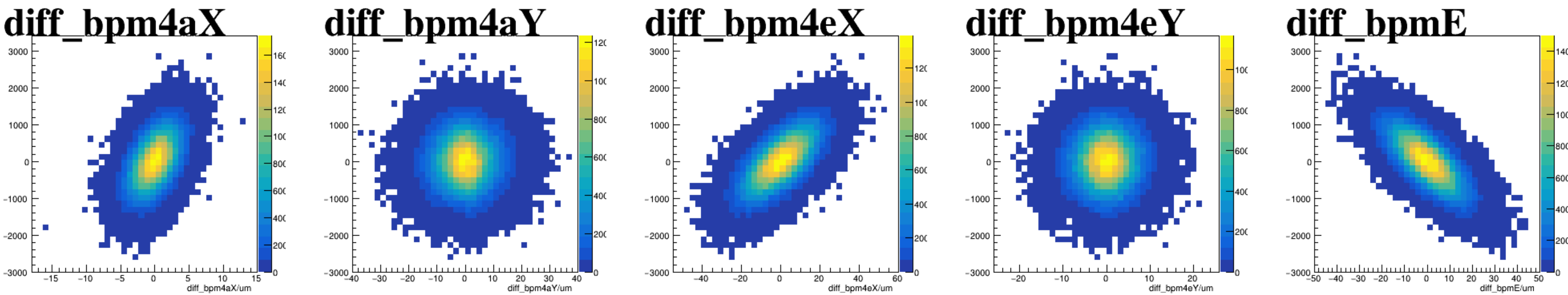
asym_usl



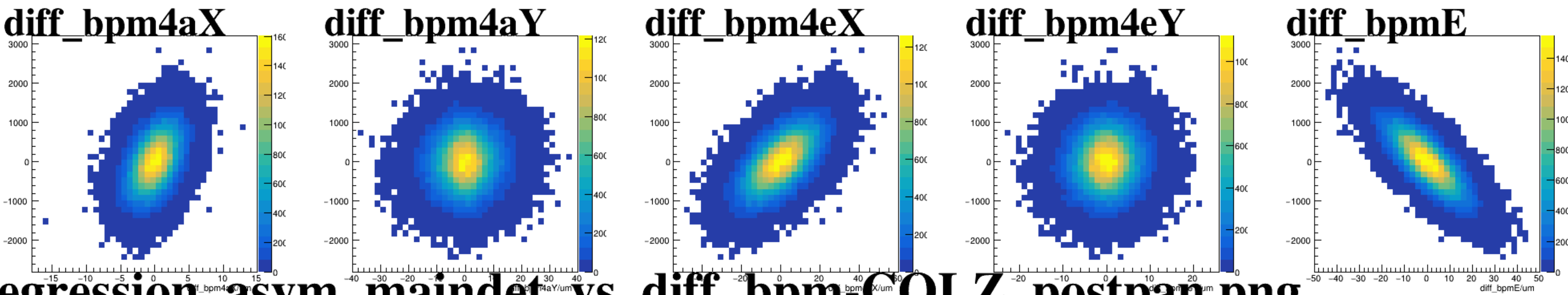
asym_dsl



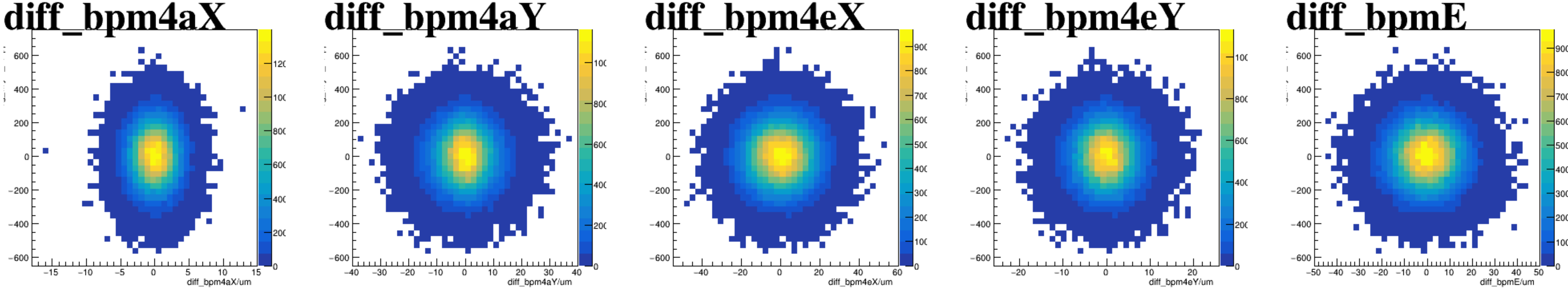
asym_usr



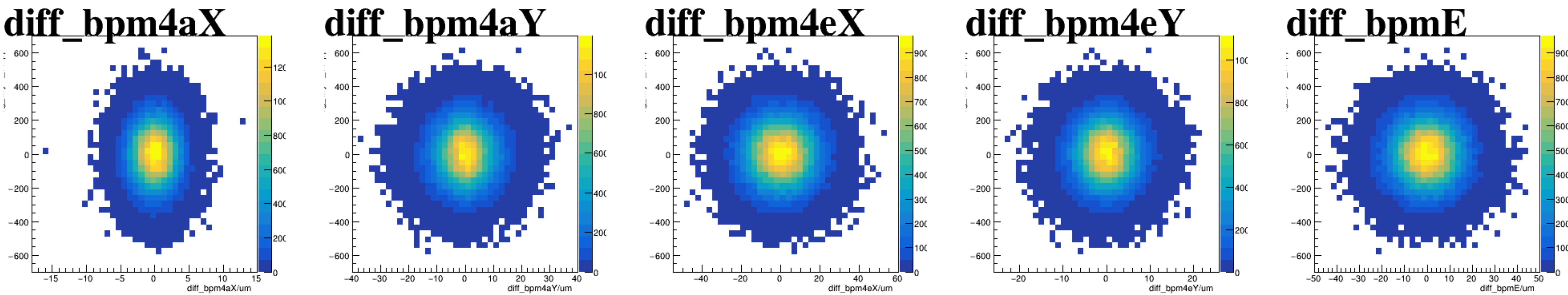
asym_dsr



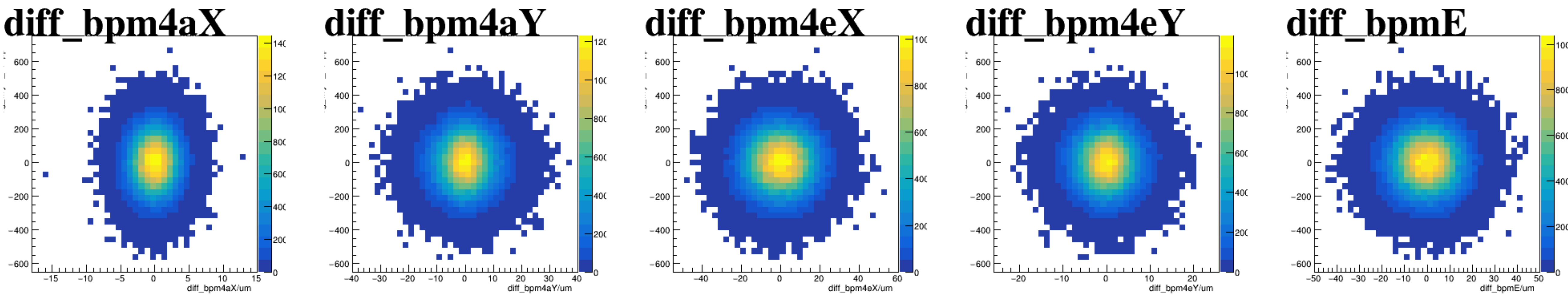
reg_asym_usl



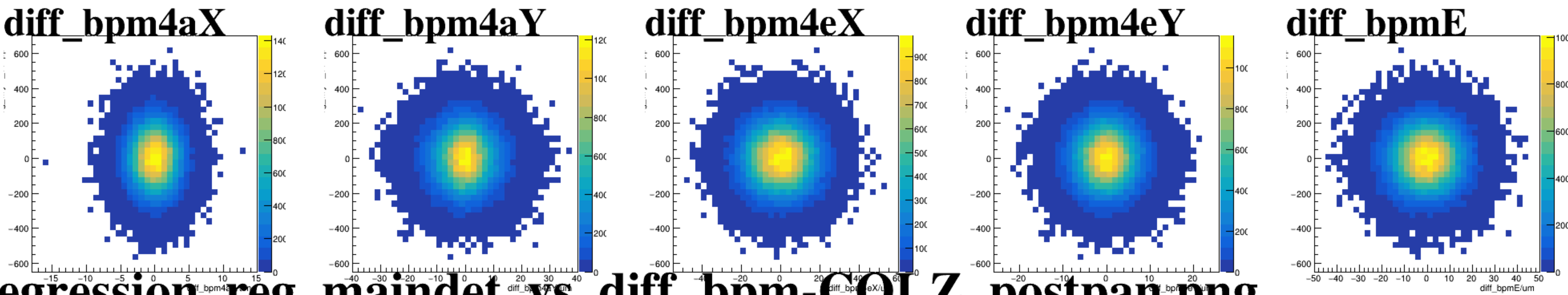
reg_asym_dsl



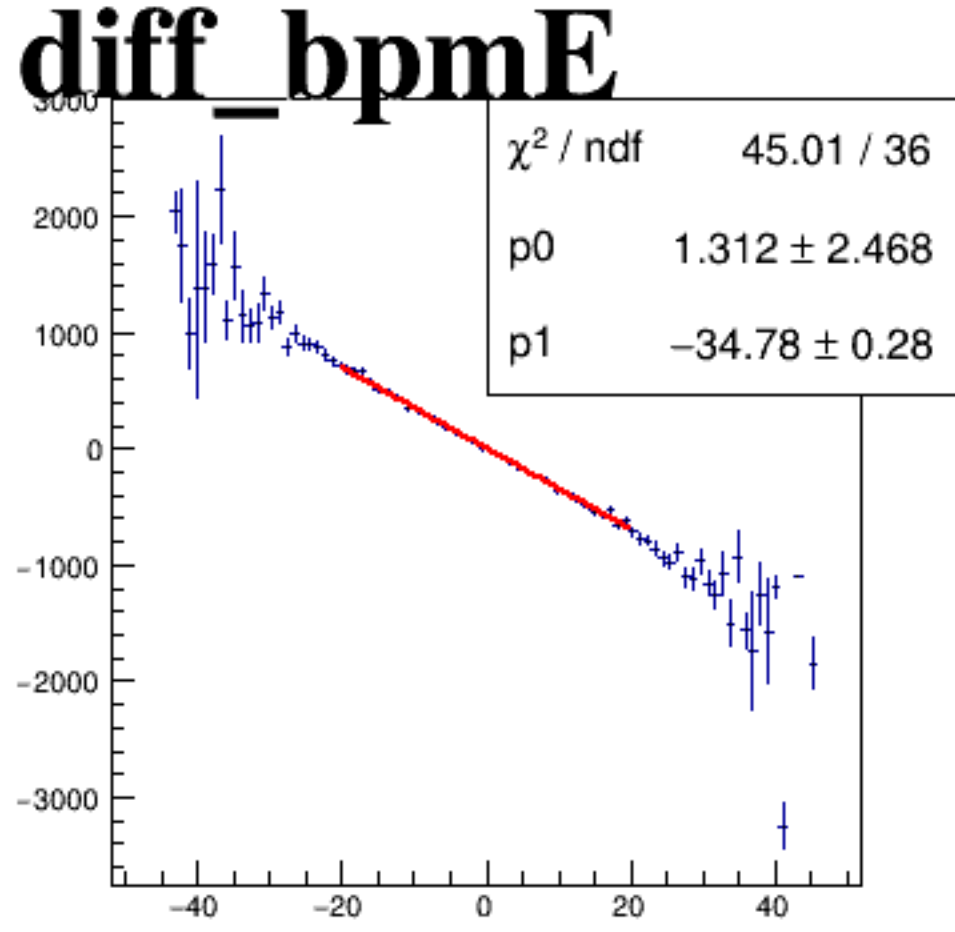
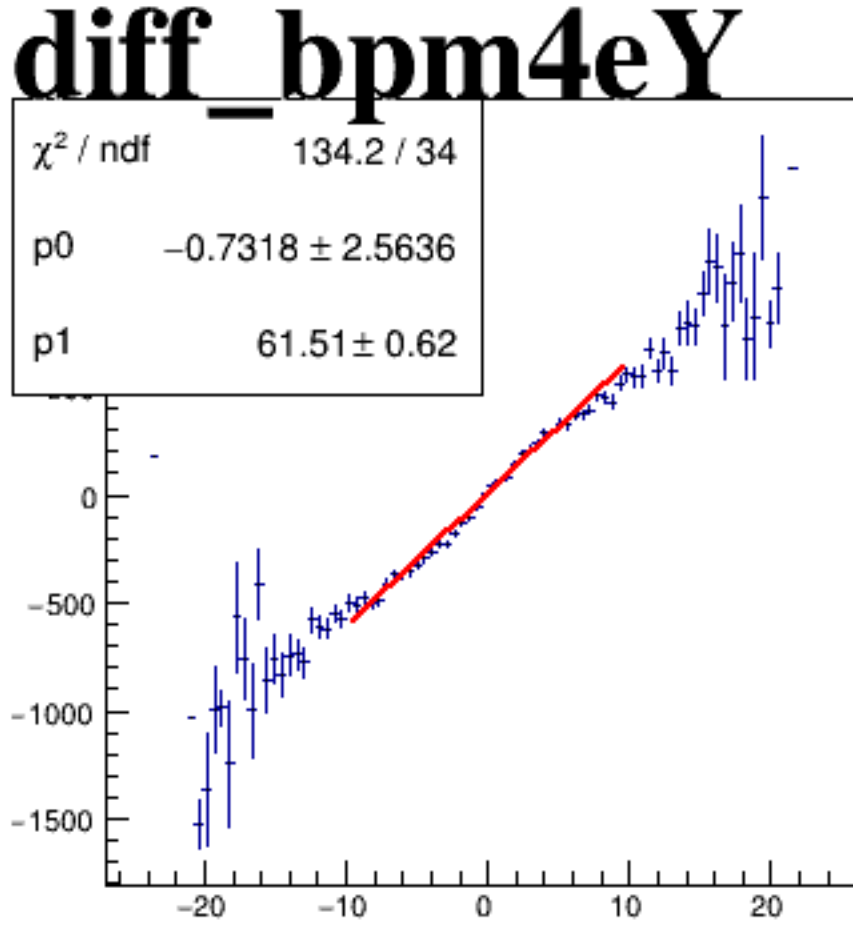
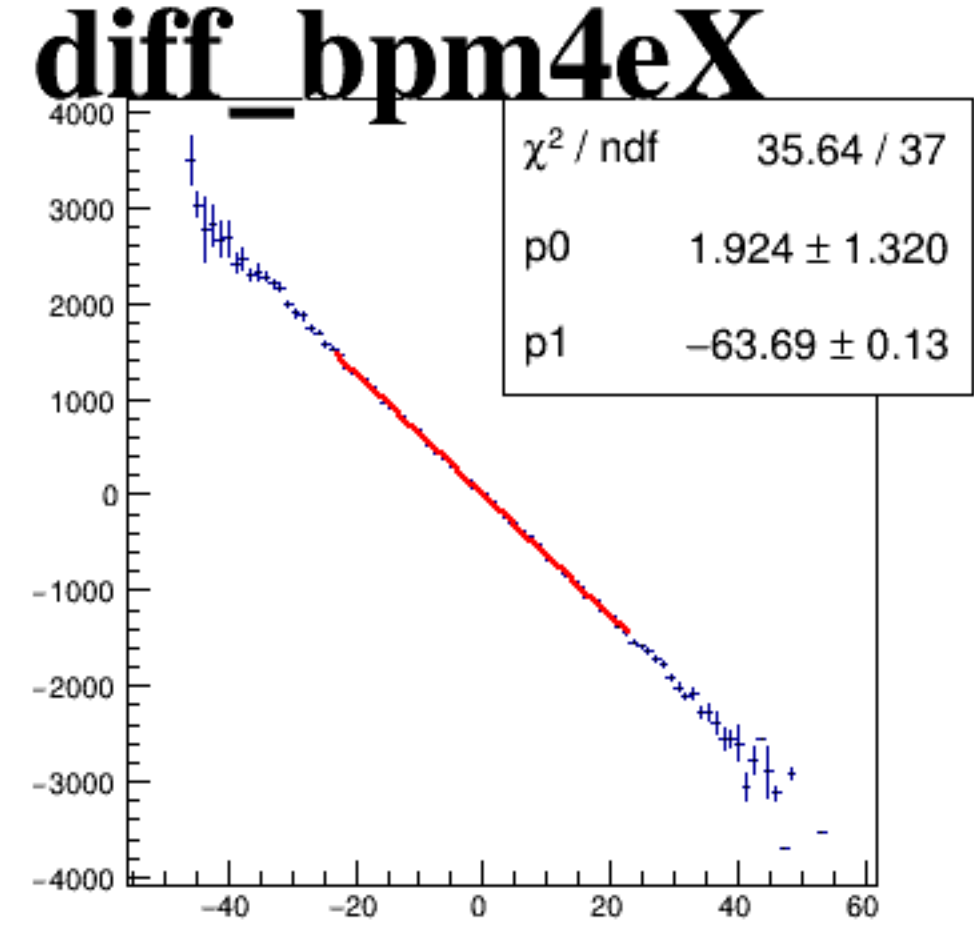
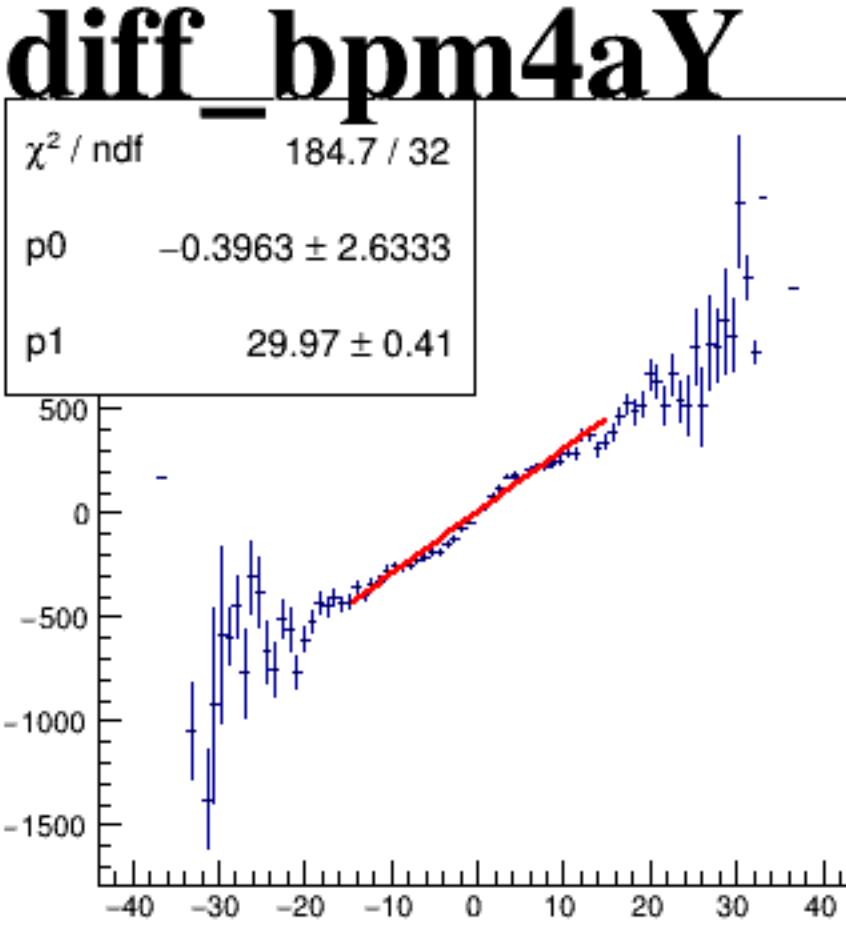
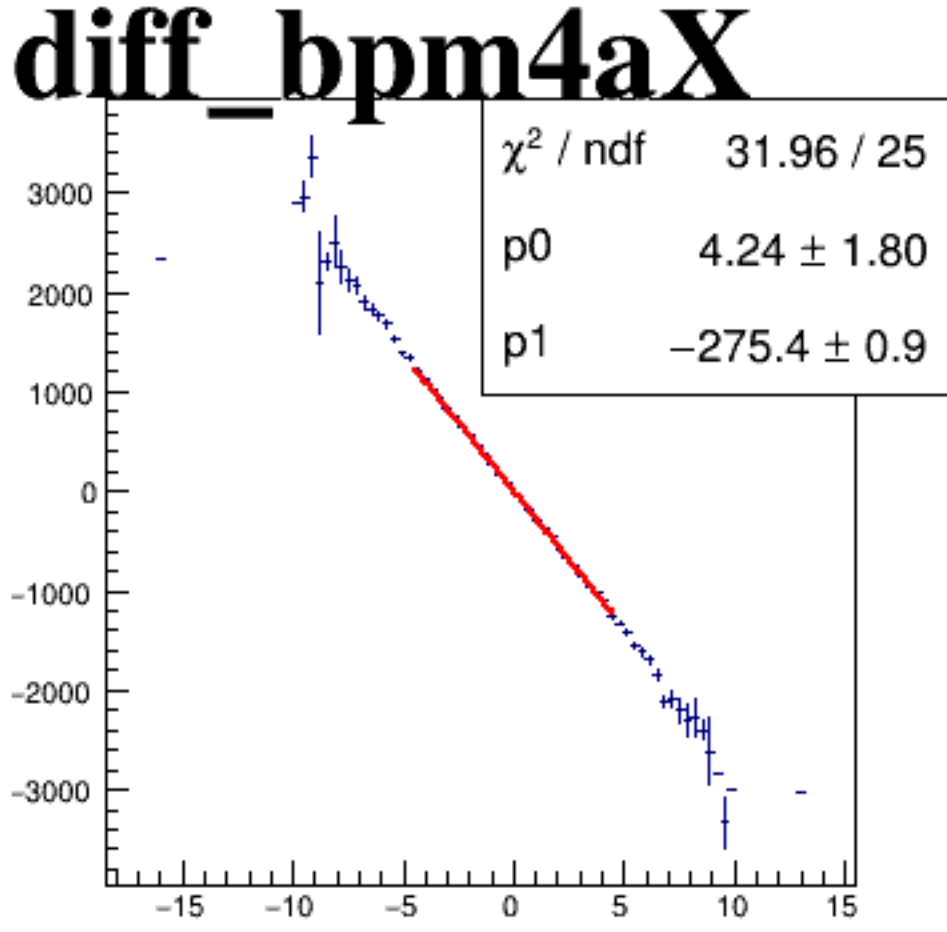
reg_asym_usr



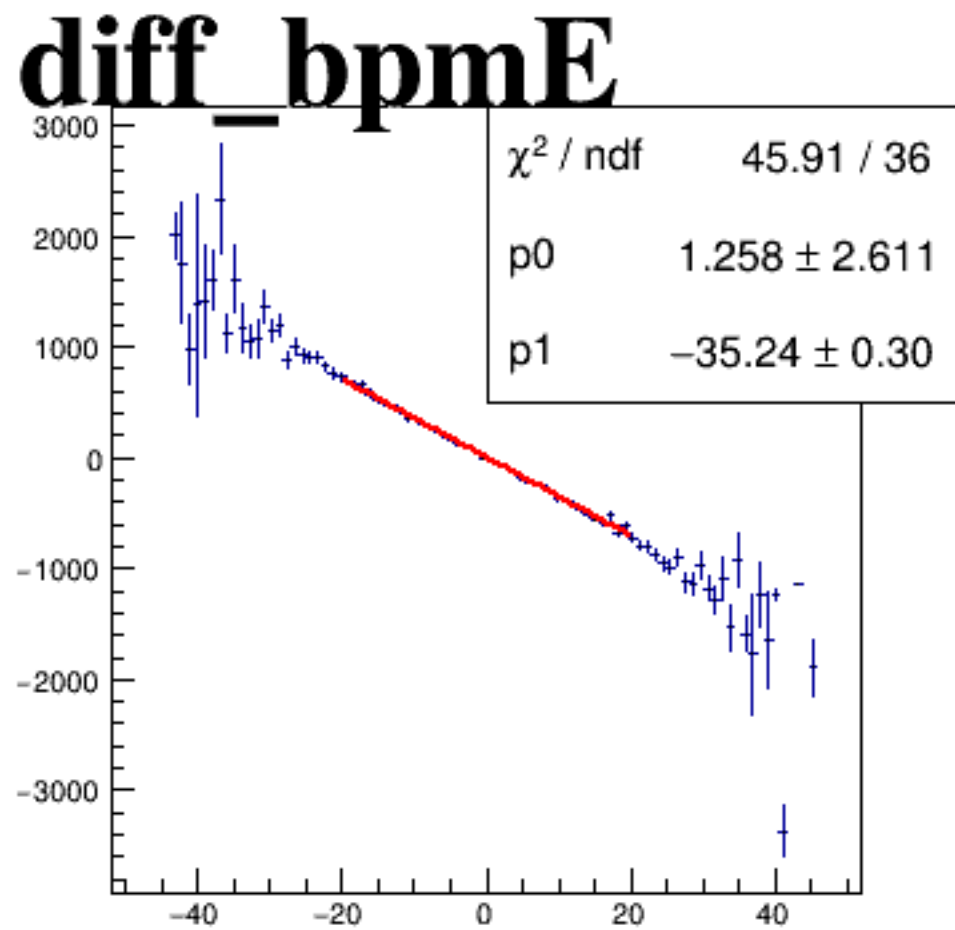
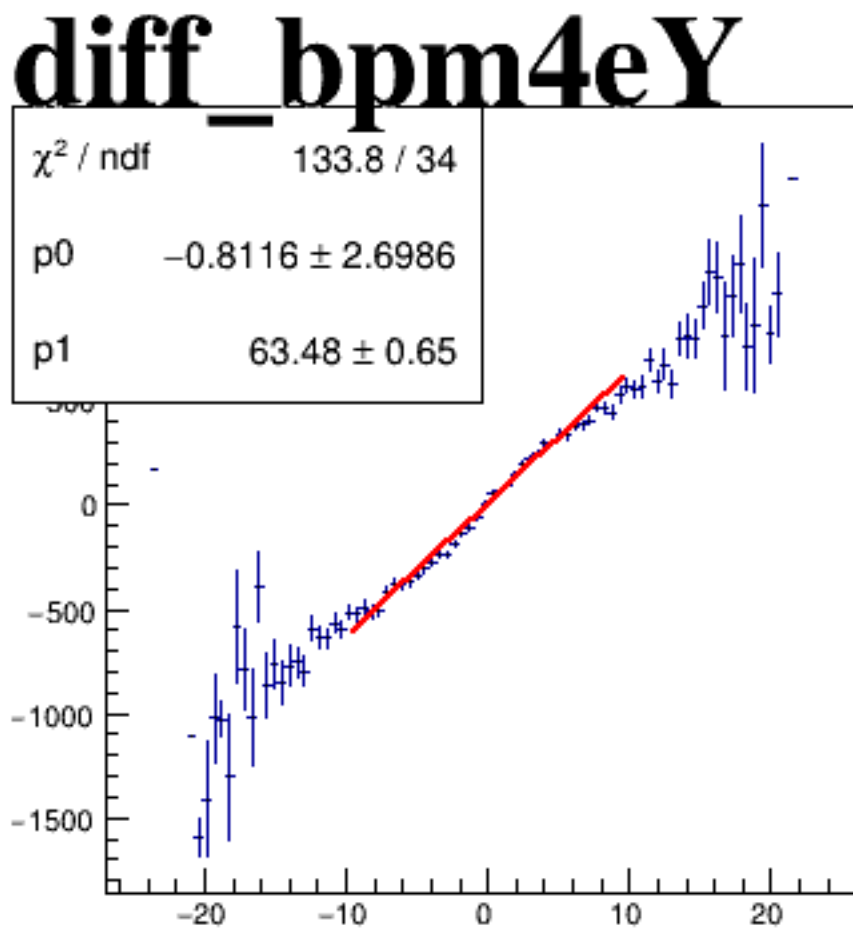
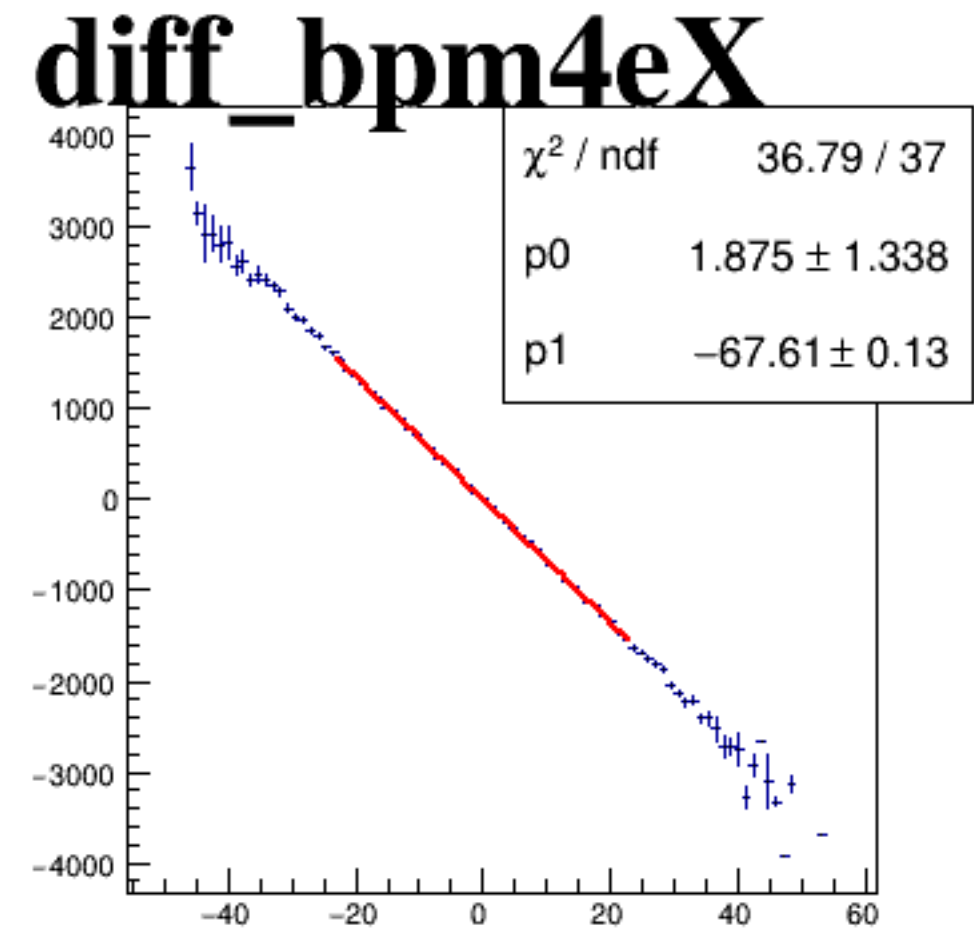
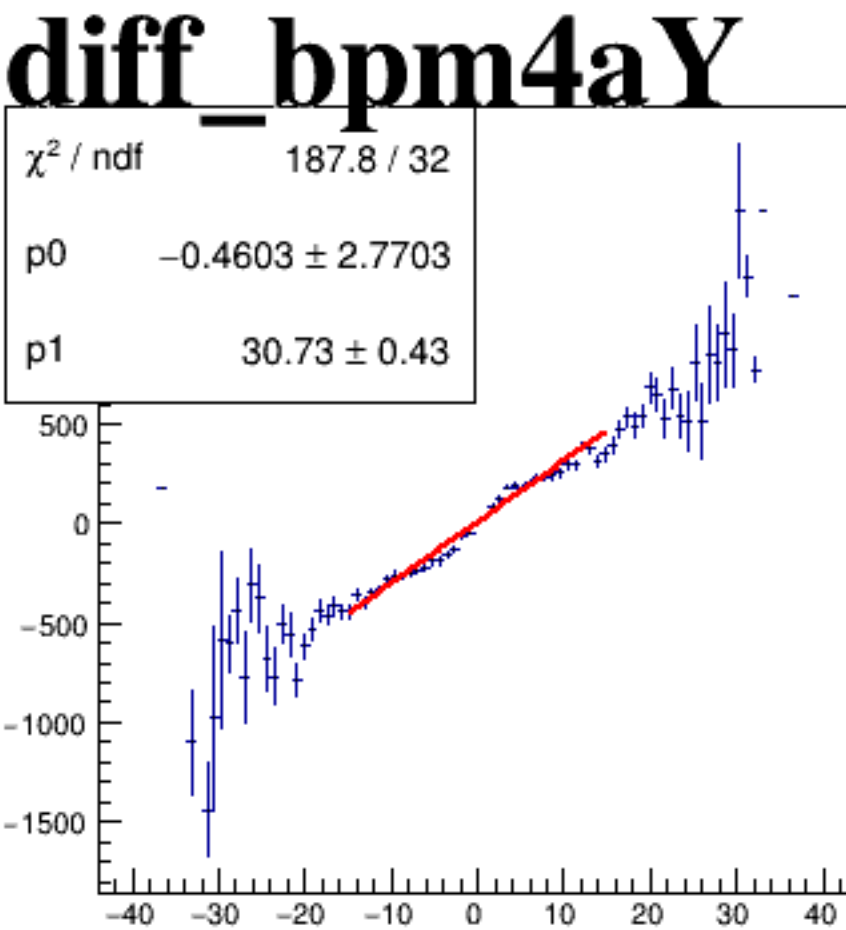
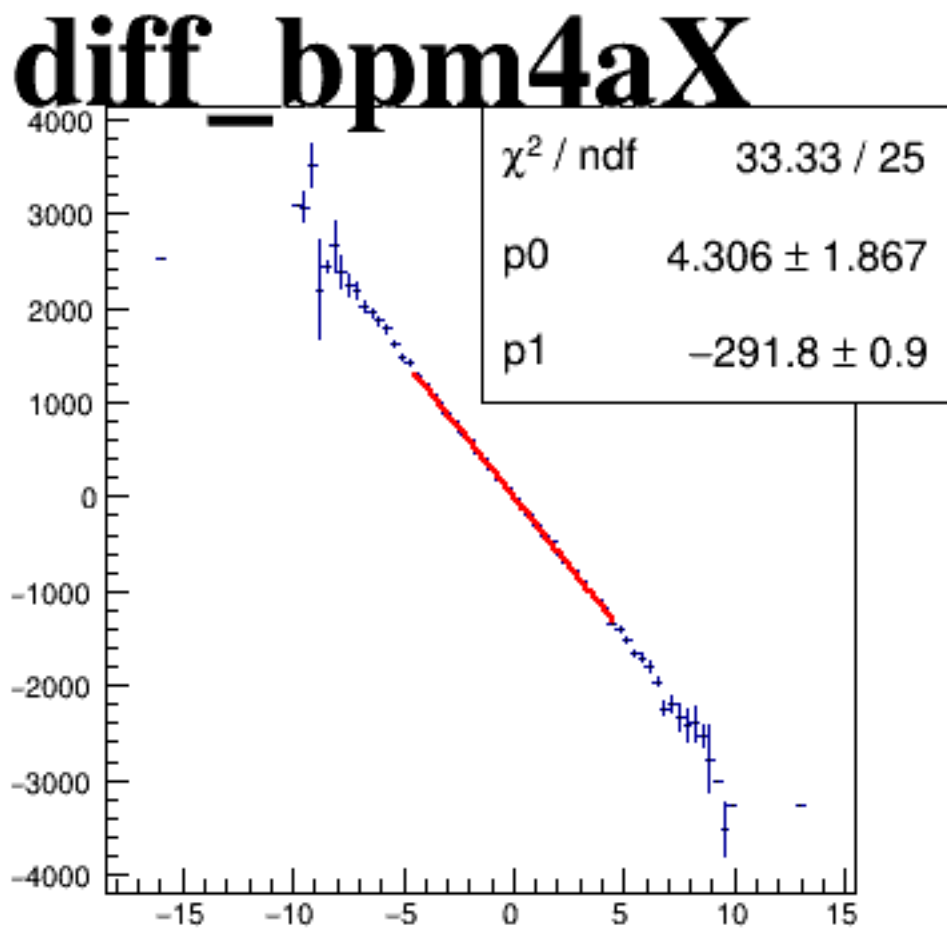
reg_asym_dsr



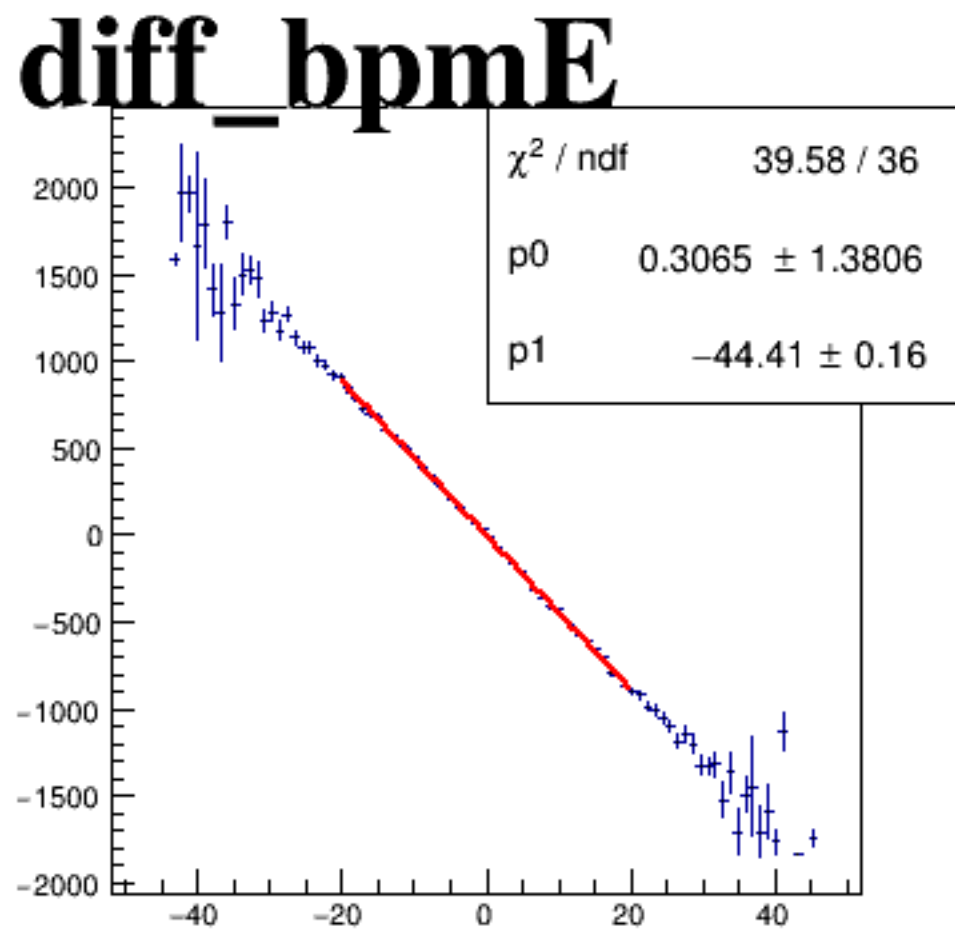
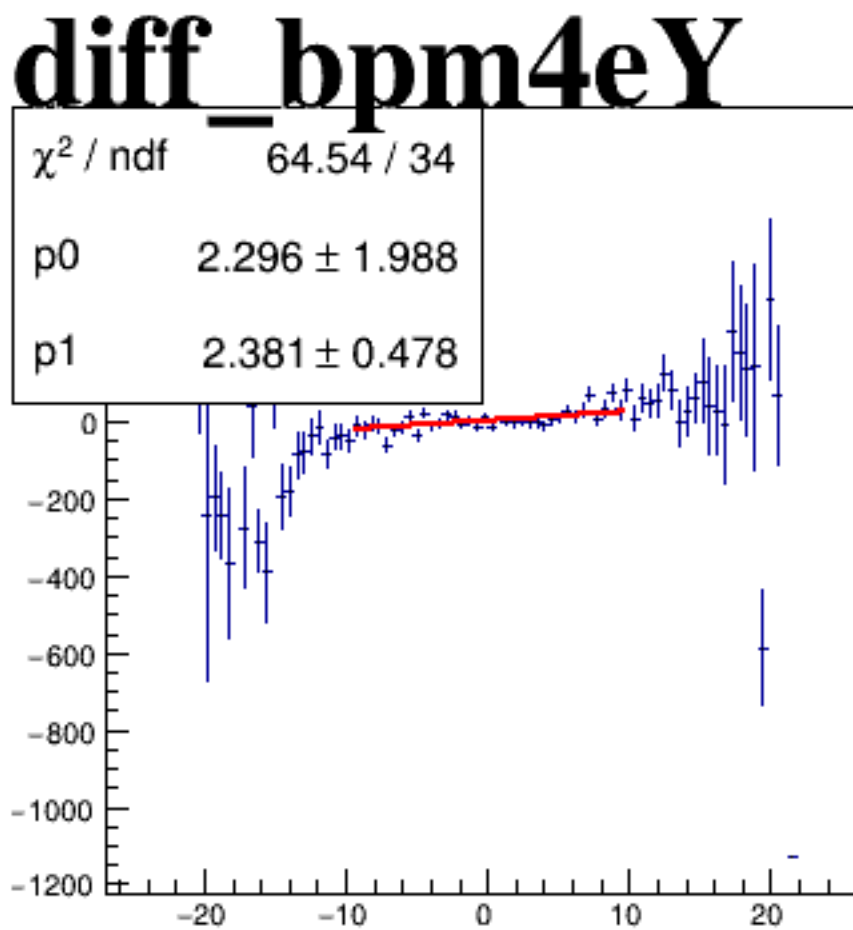
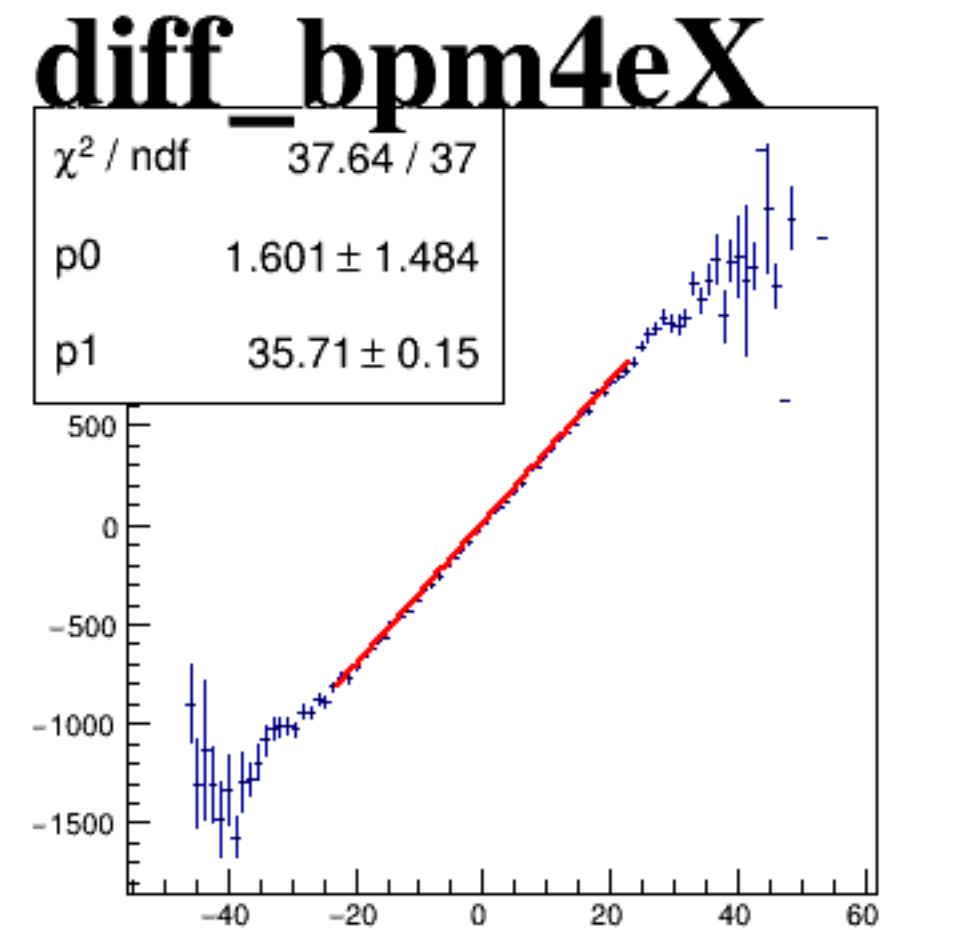
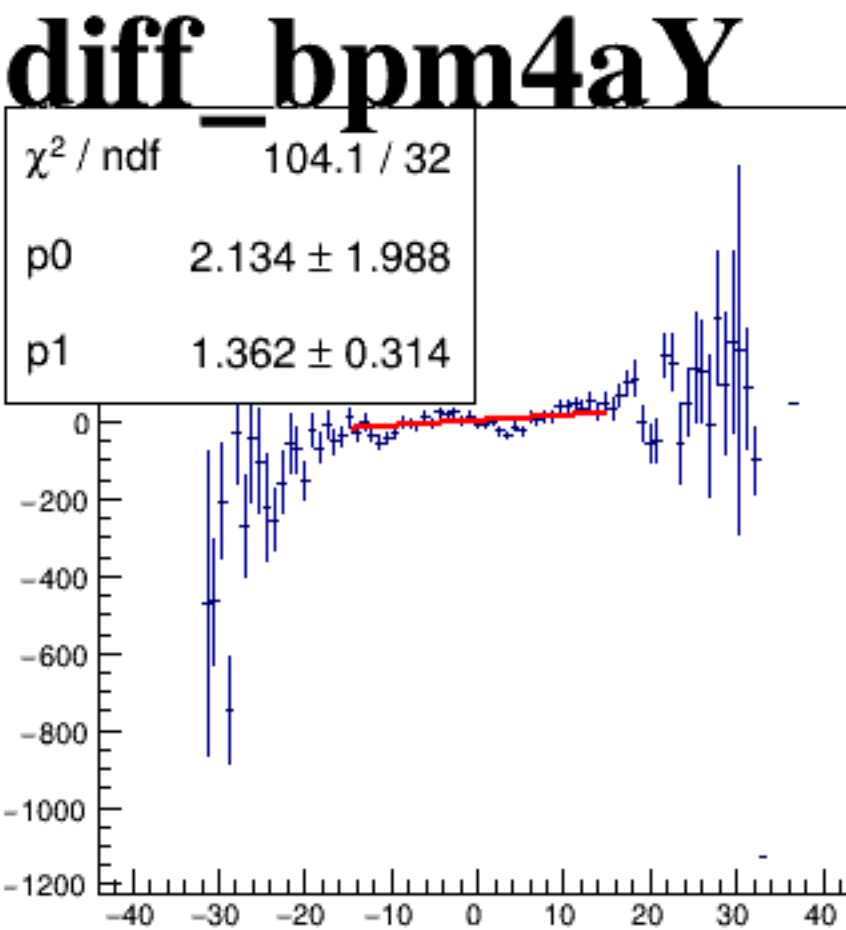
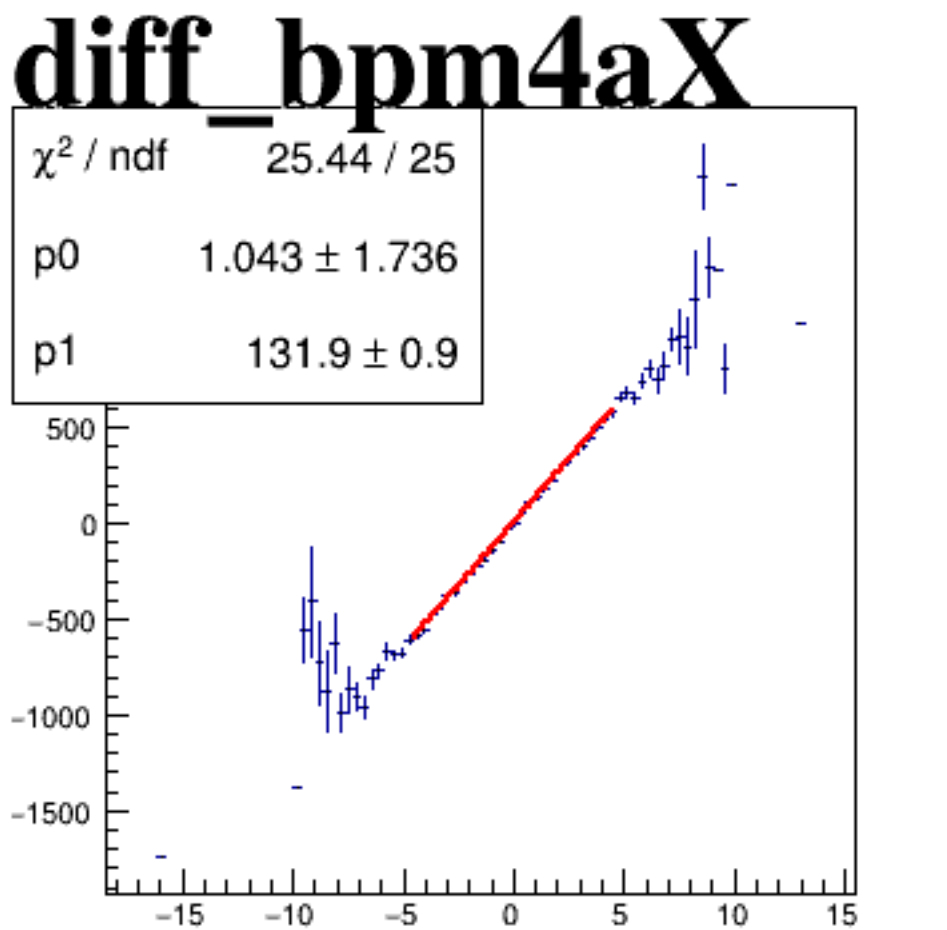
asym_usl



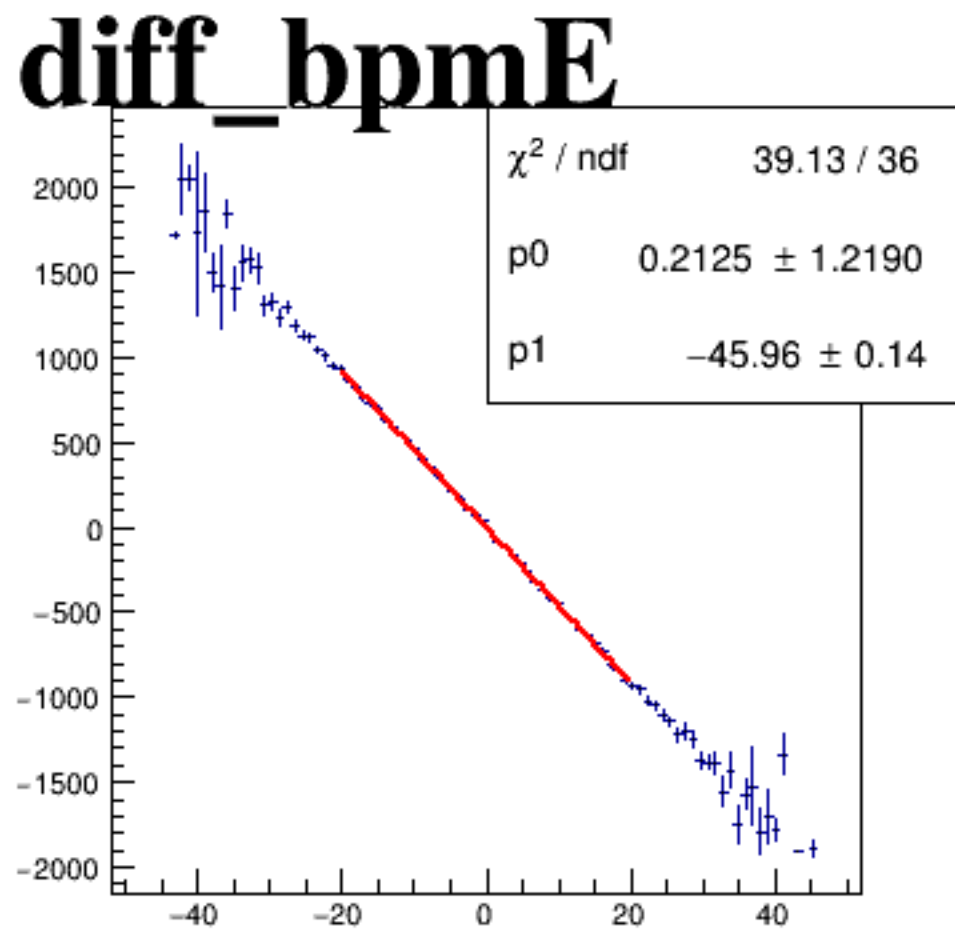
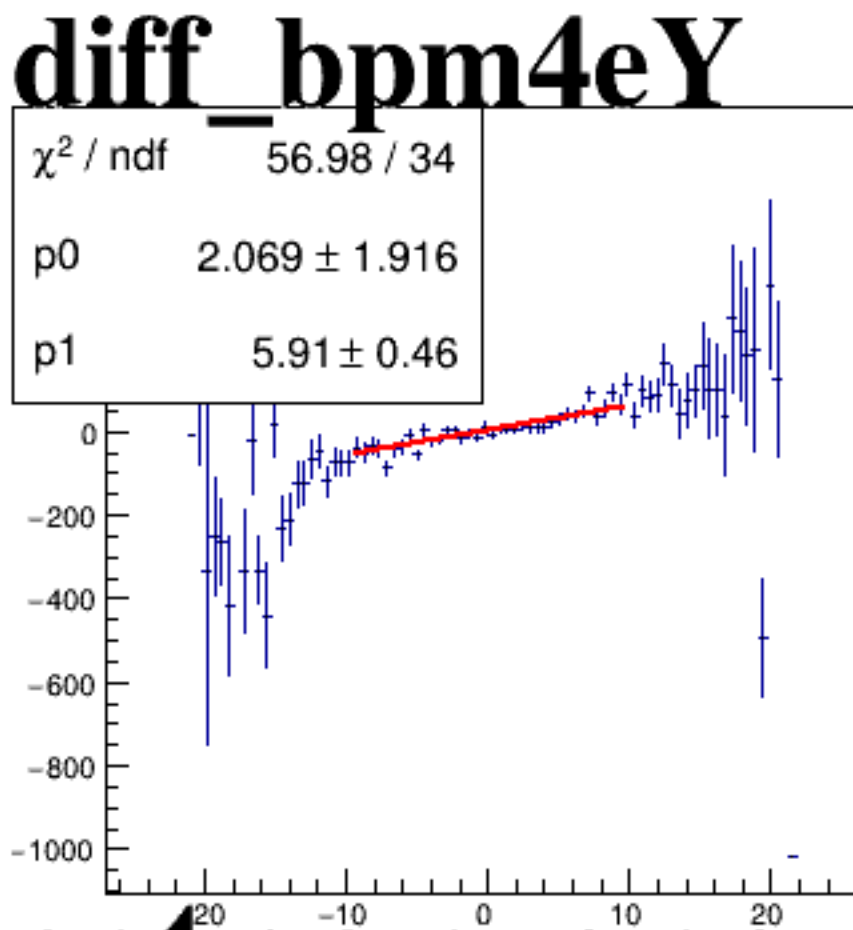
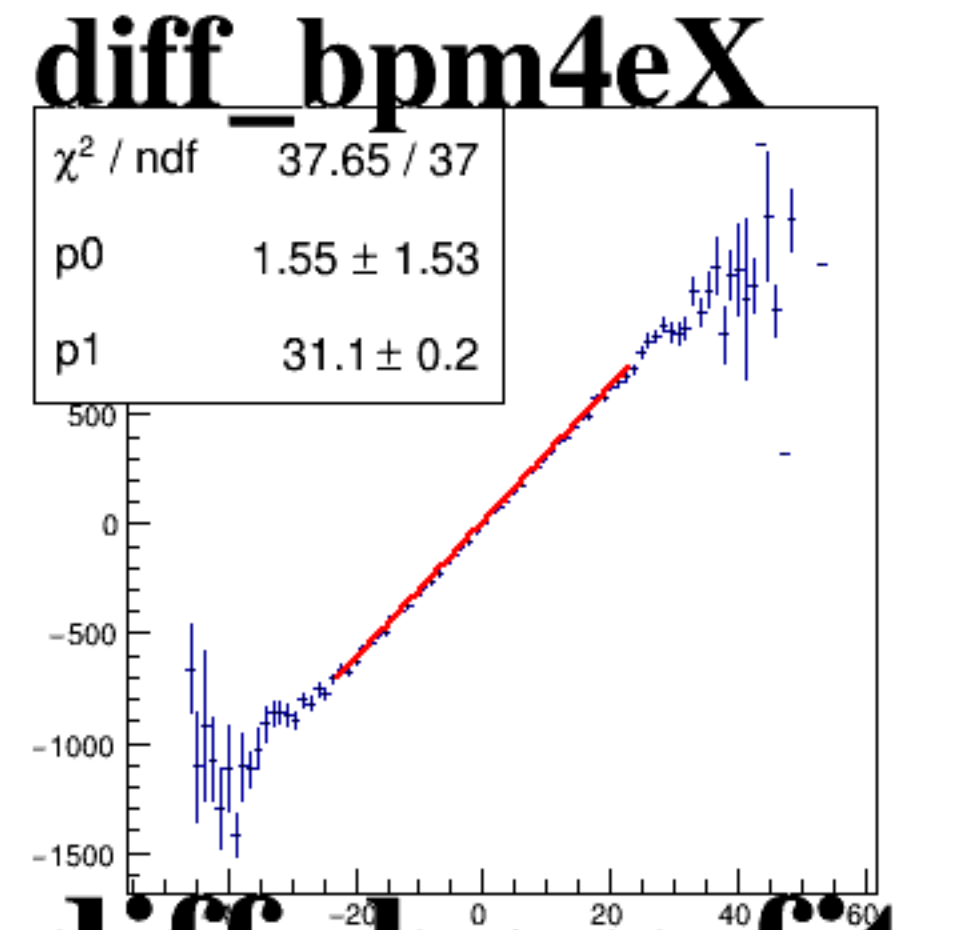
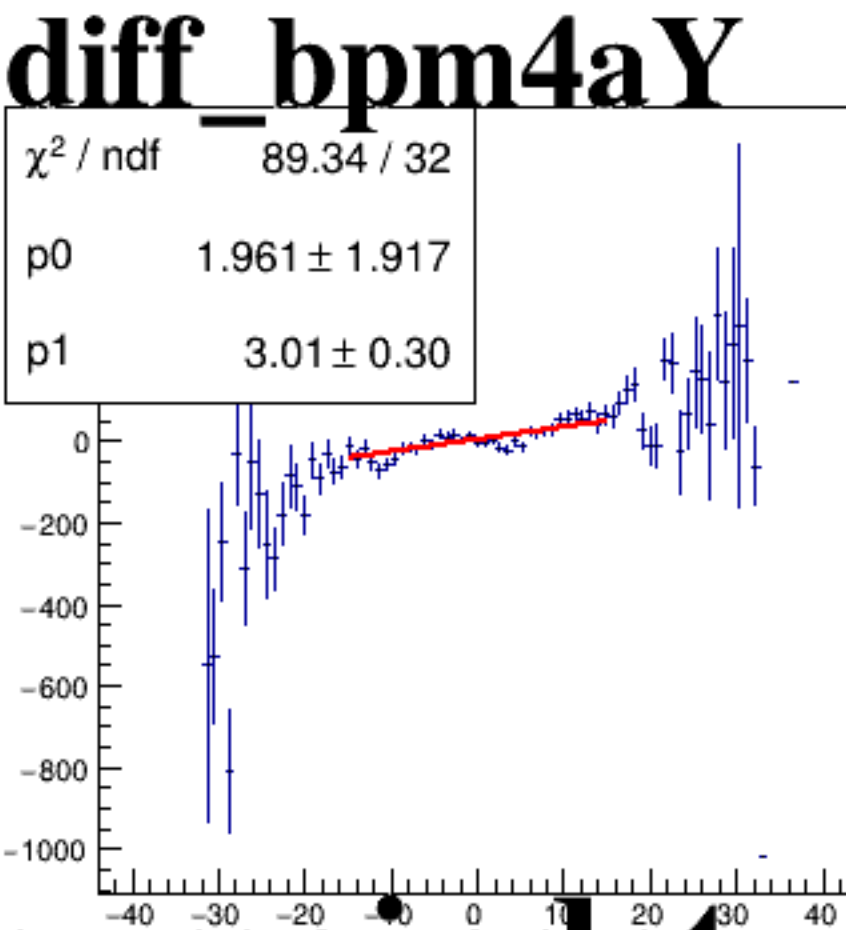
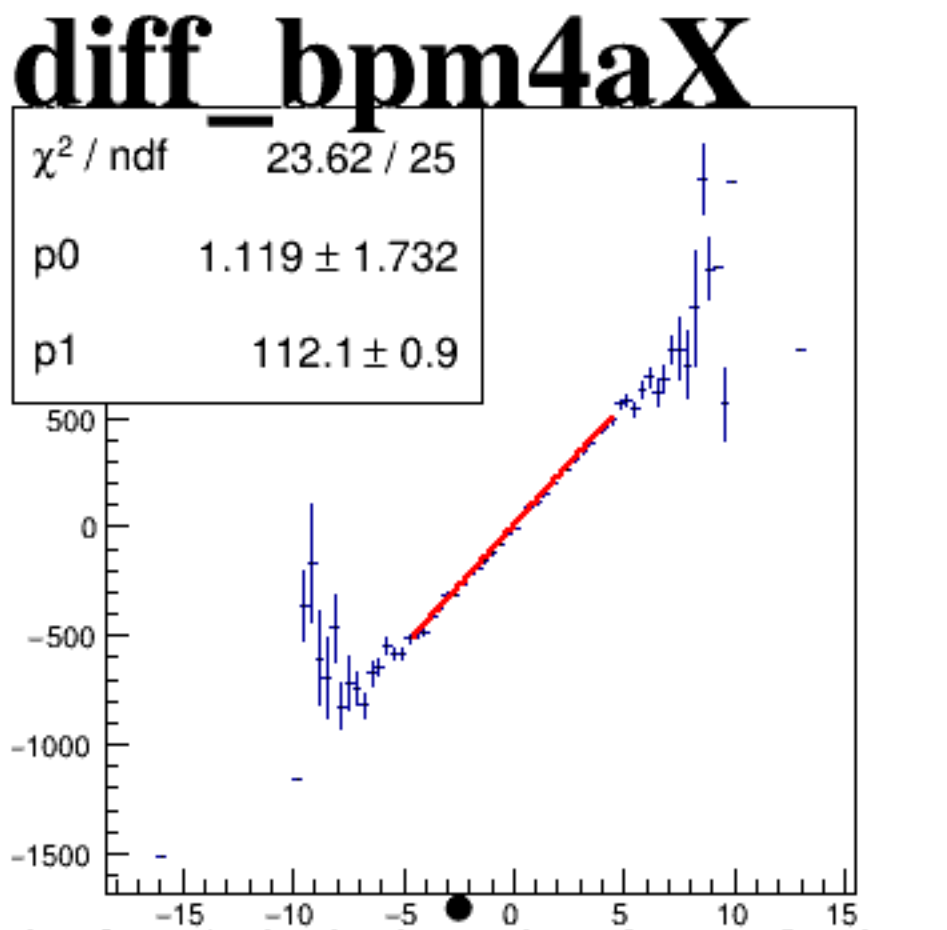
asym_dsl



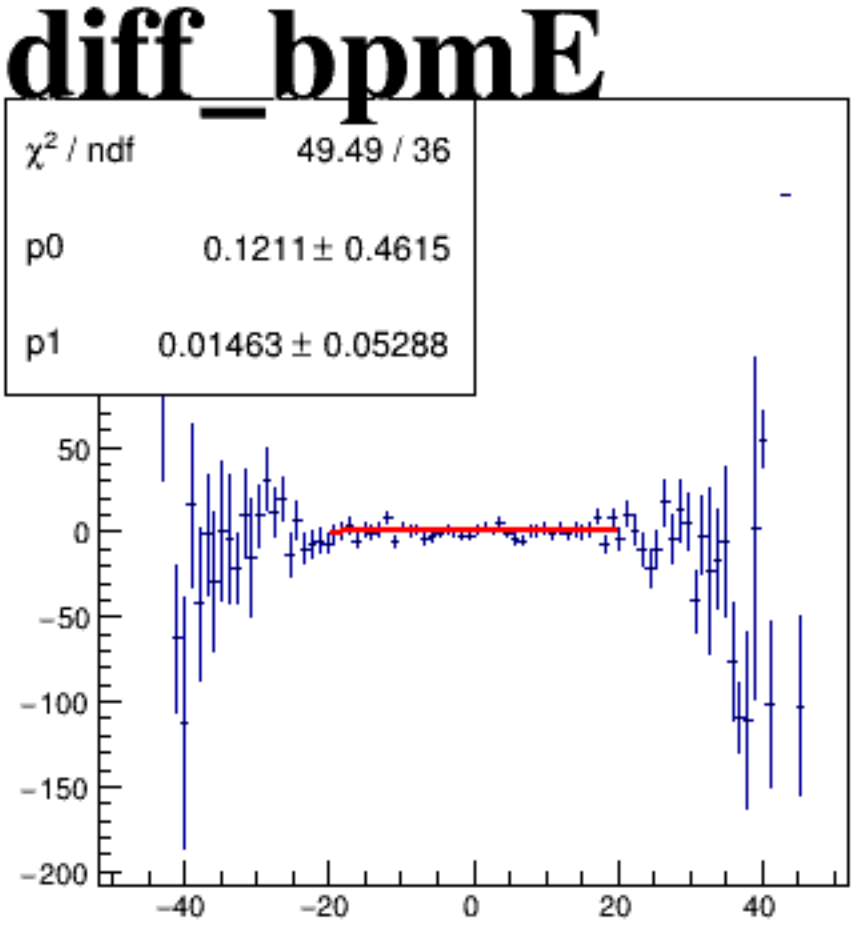
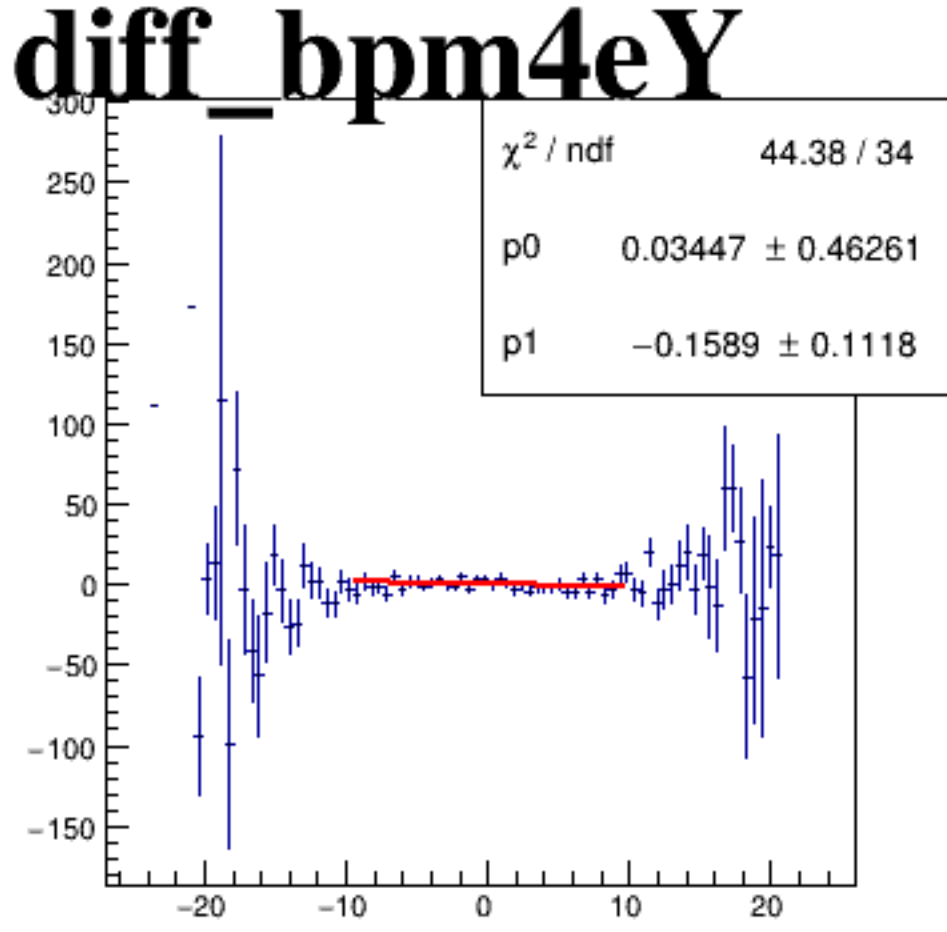
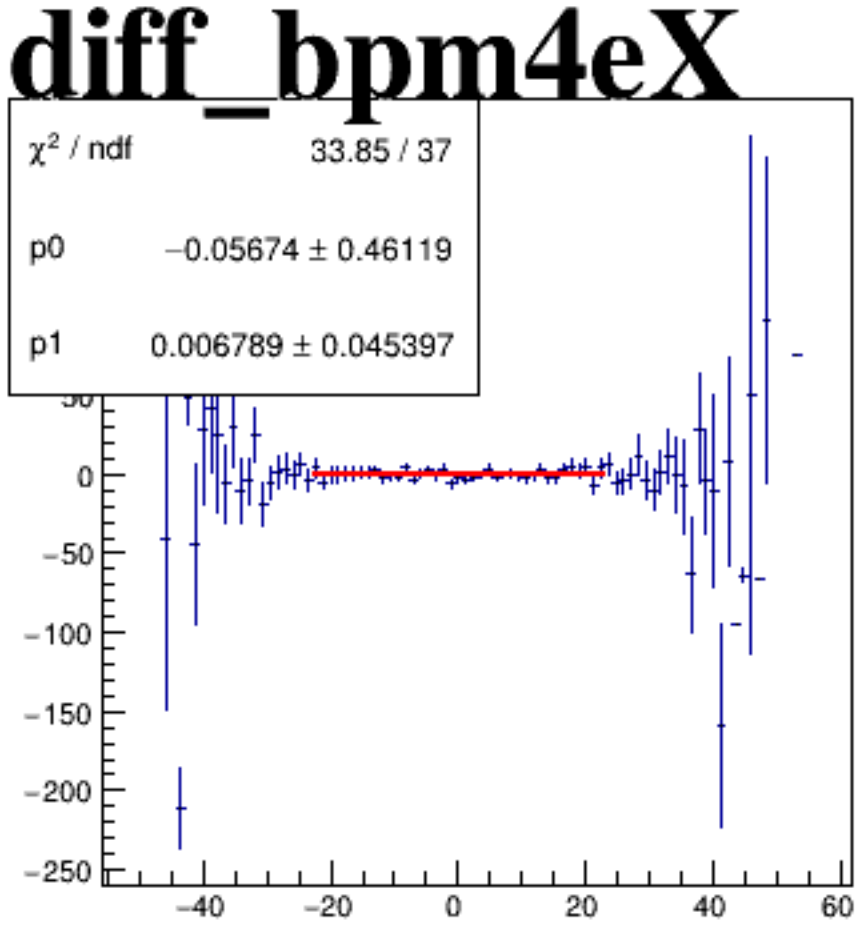
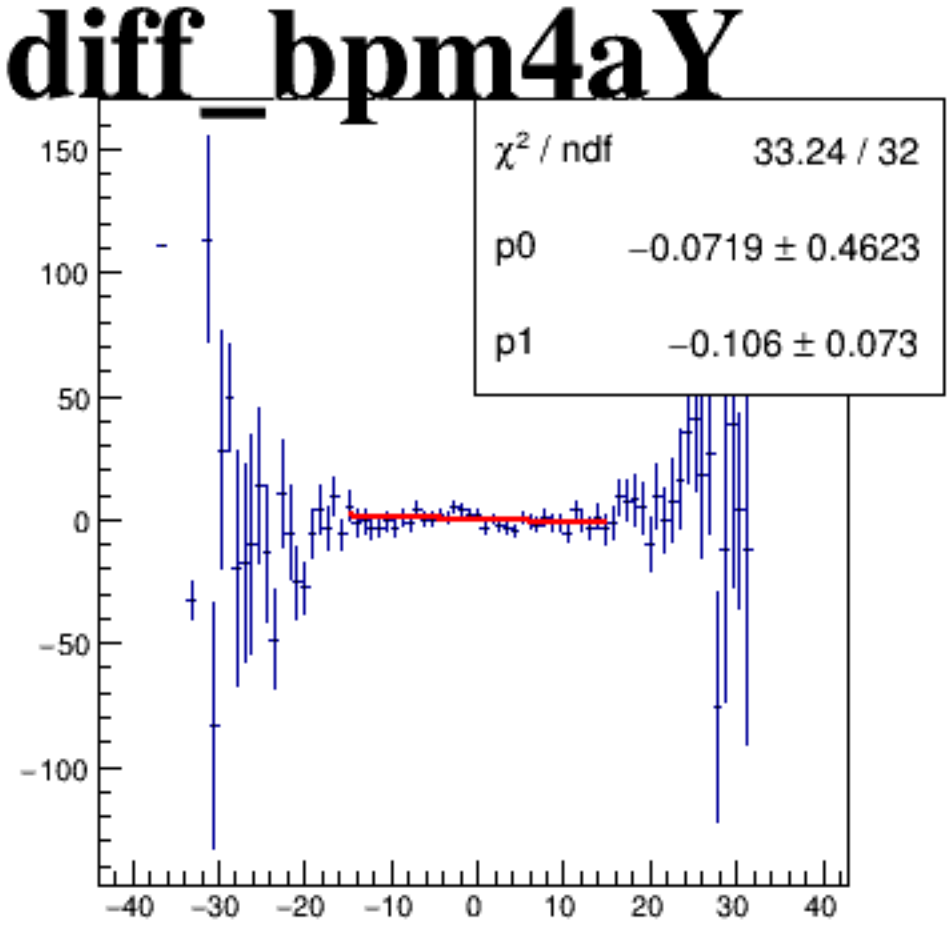
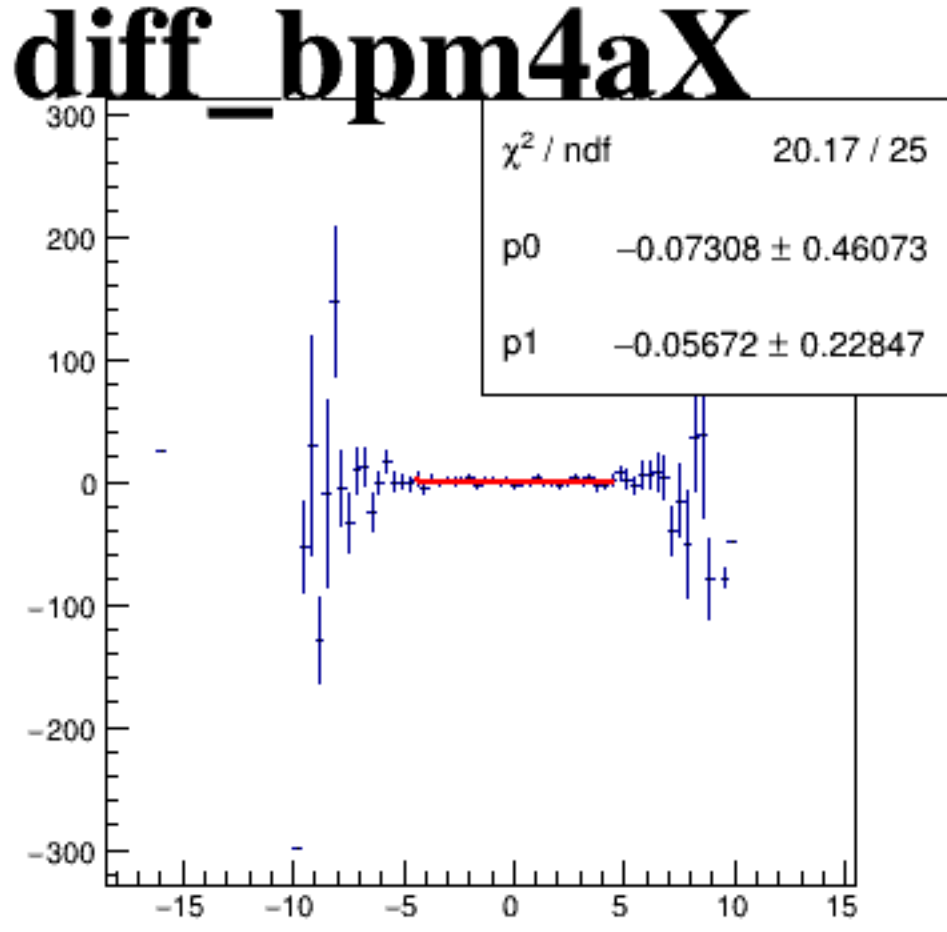
asym_usr



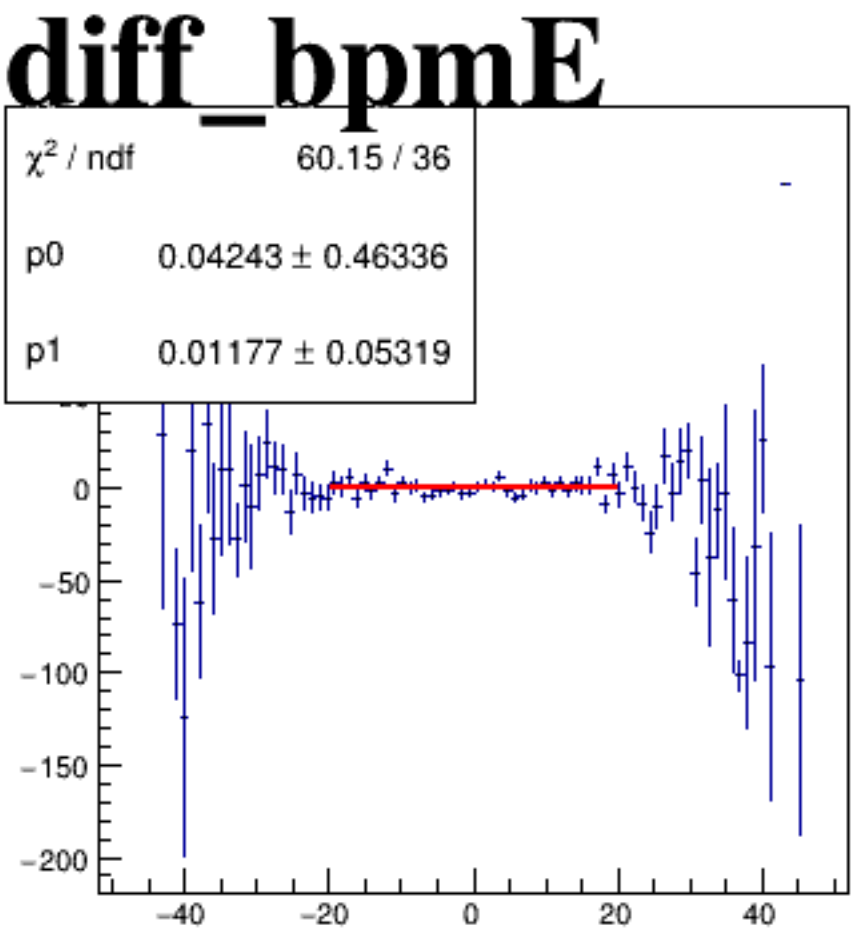
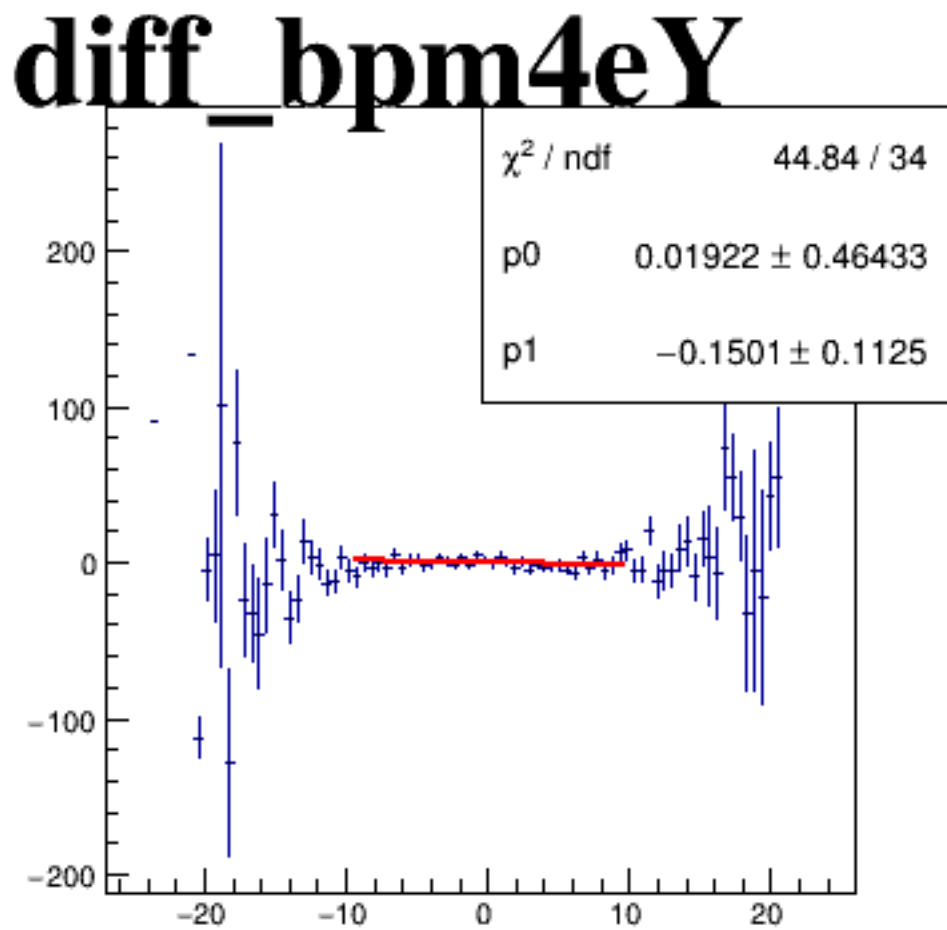
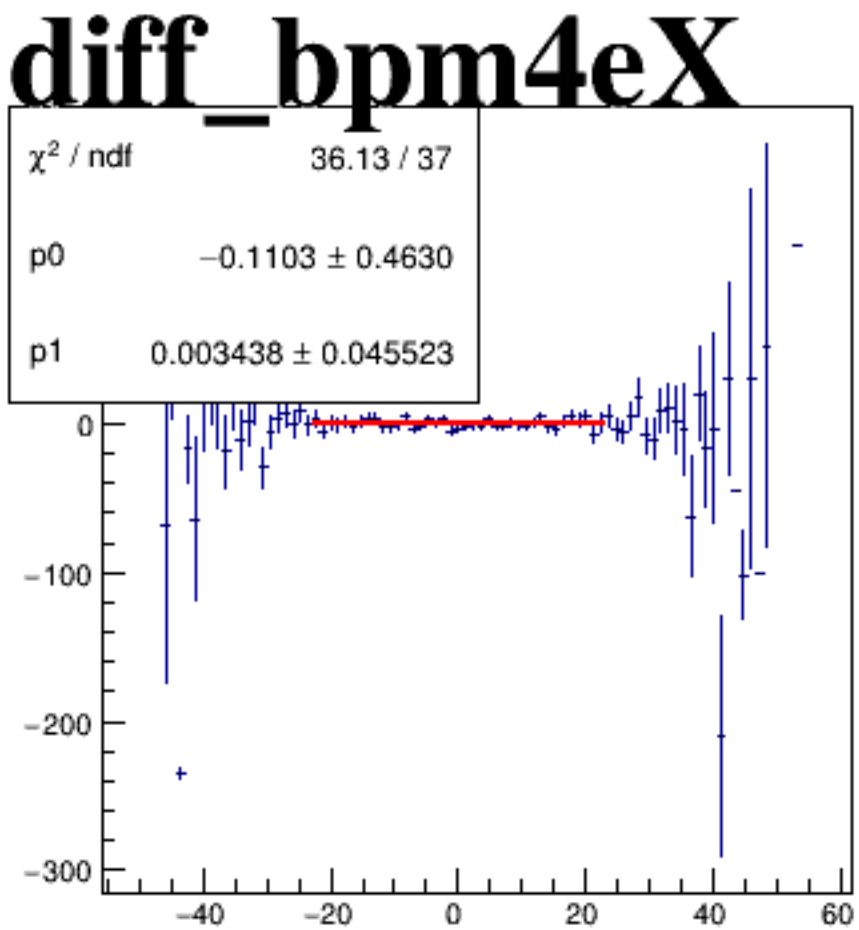
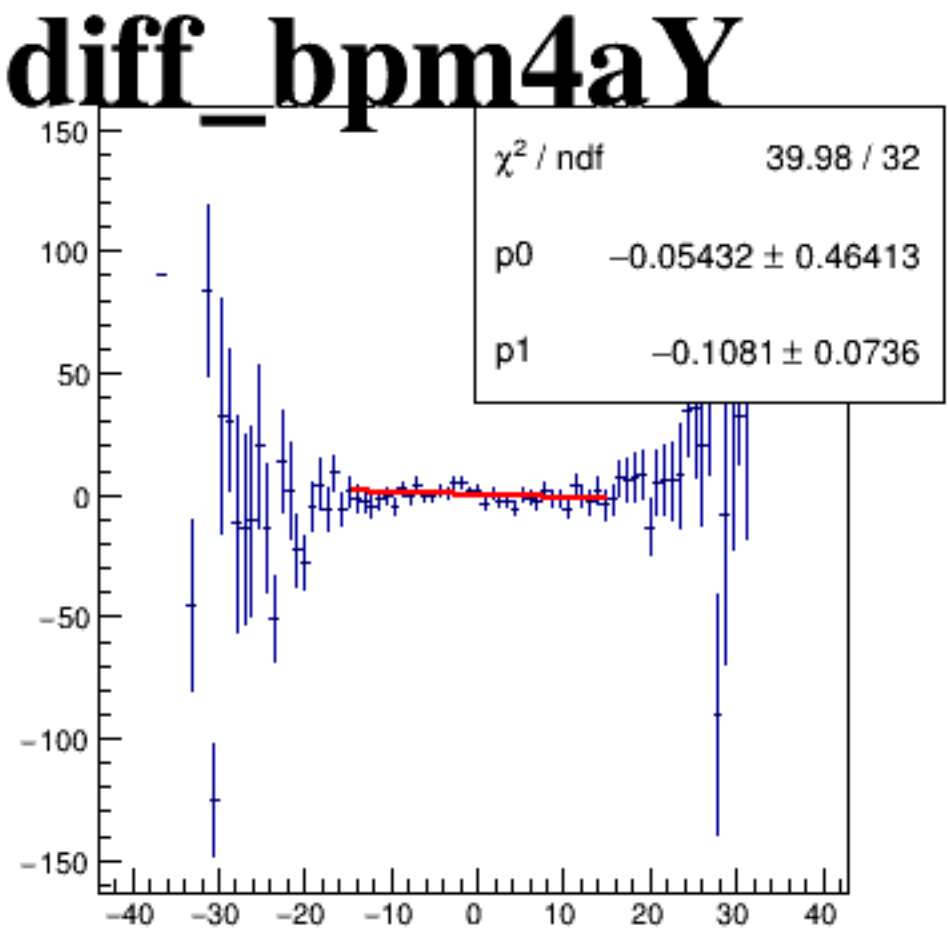
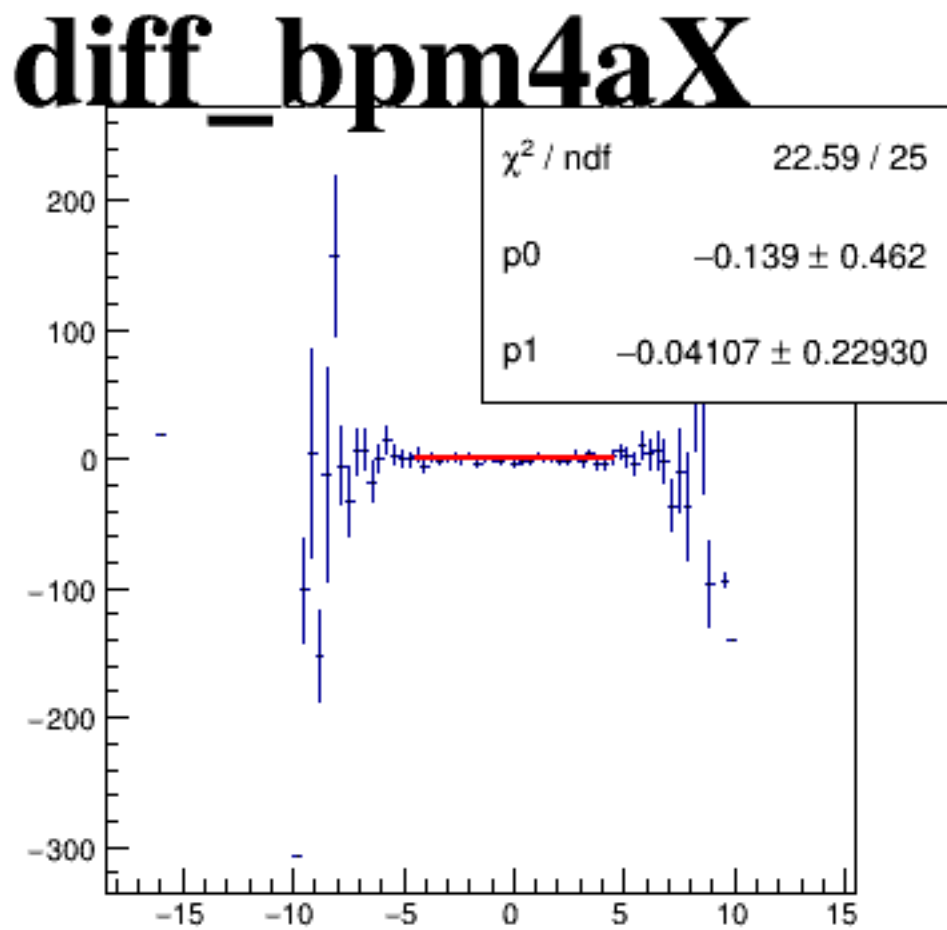
asym_dsr



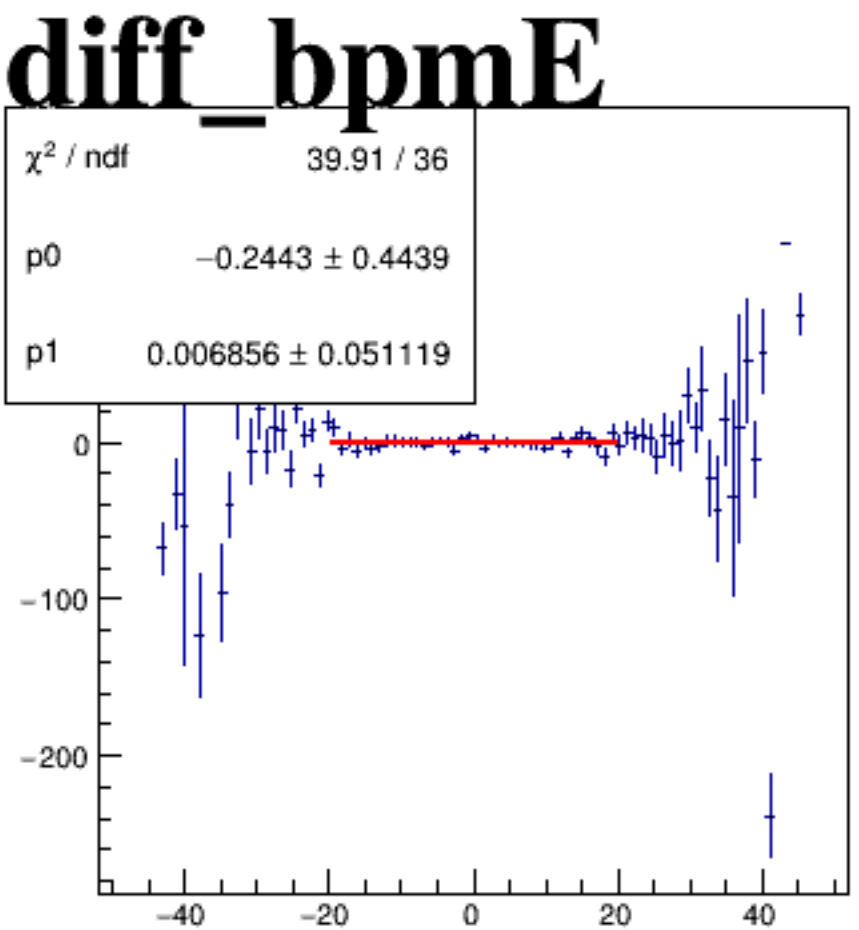
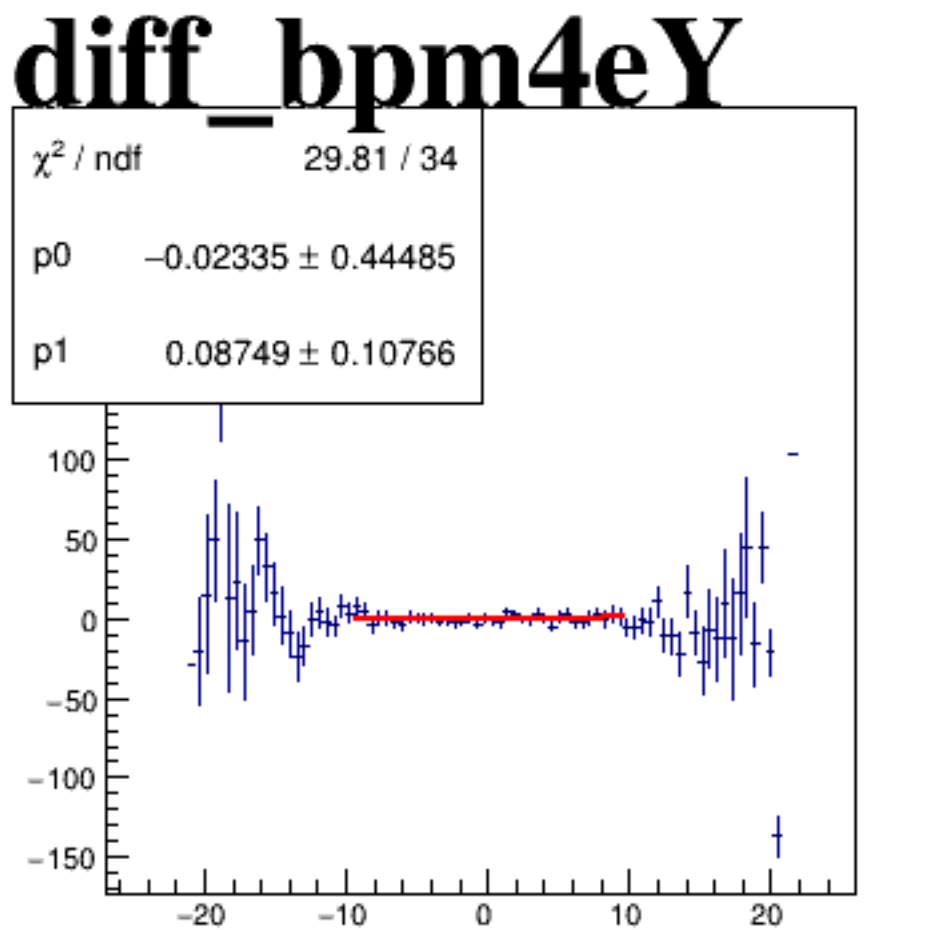
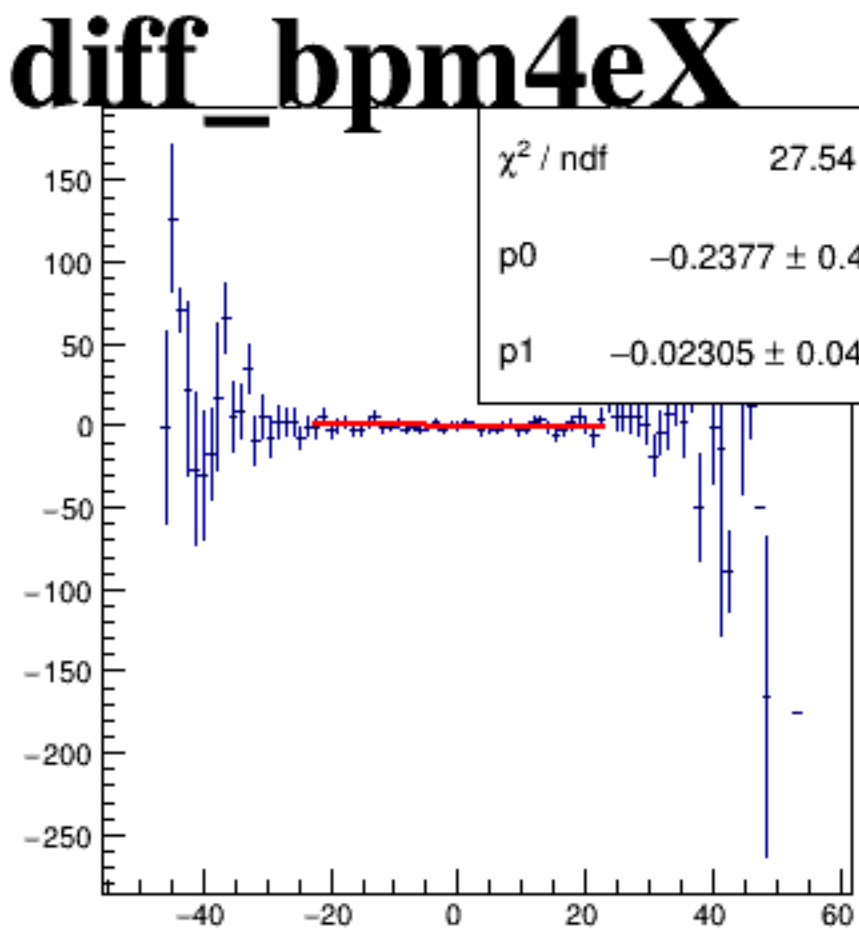
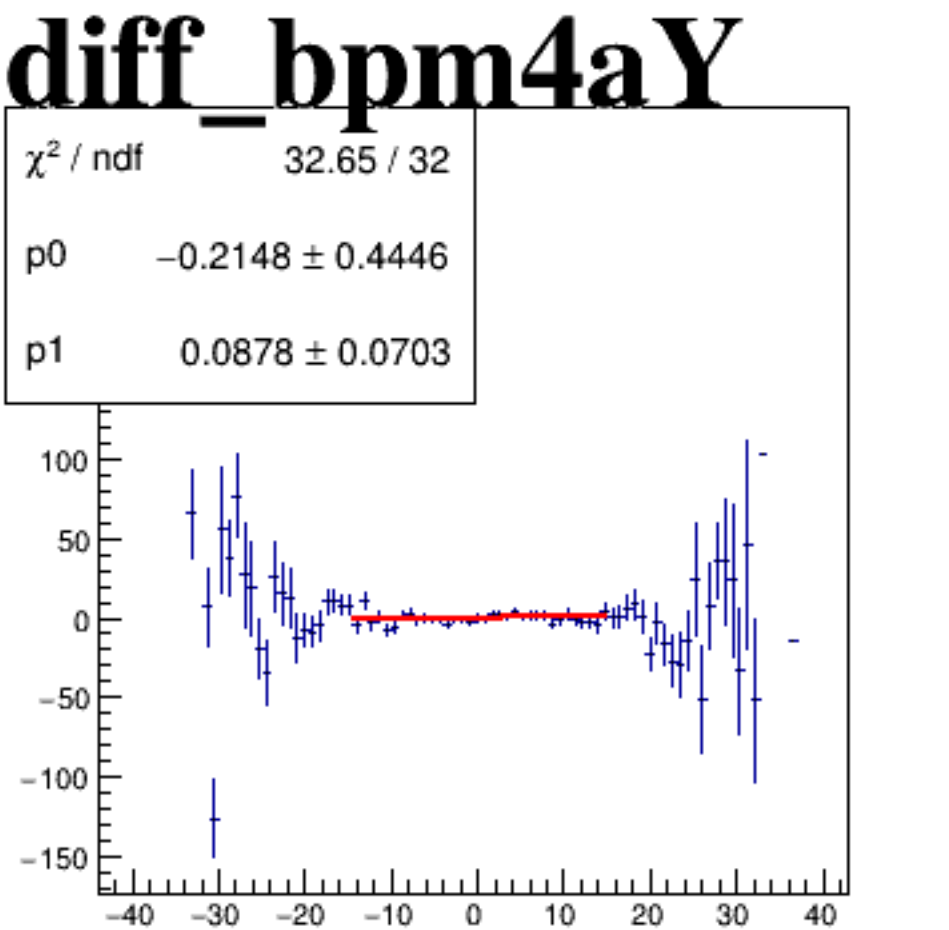
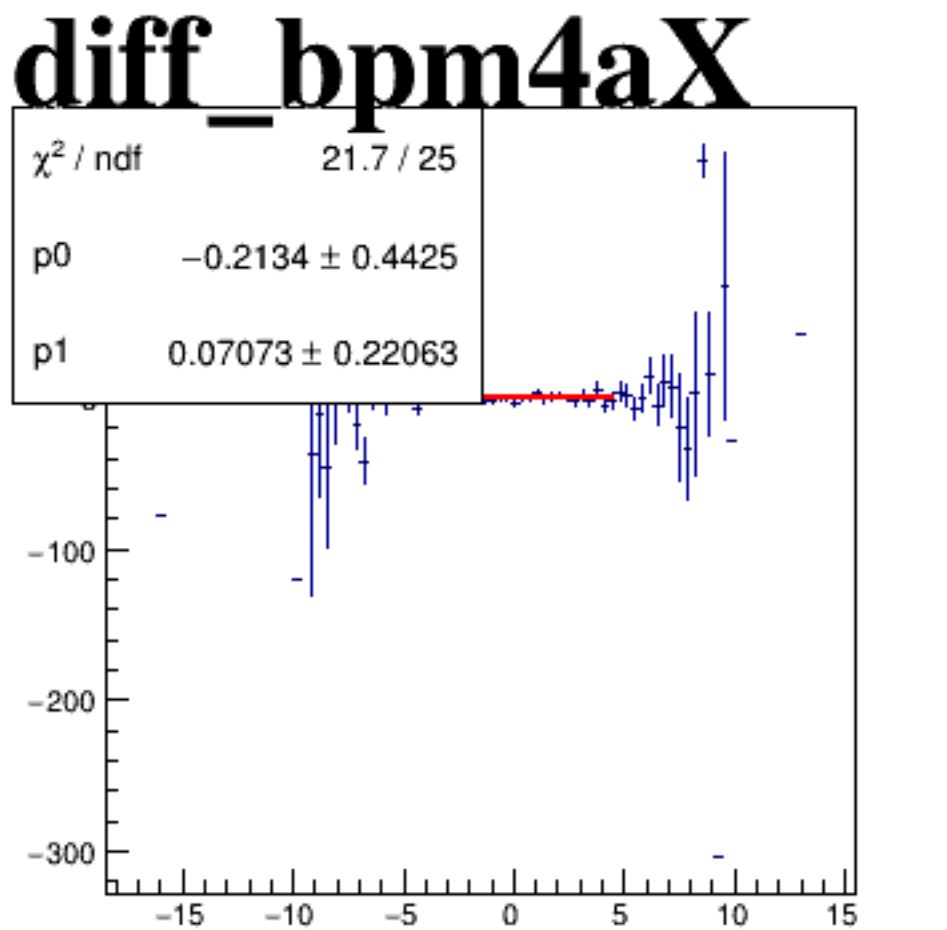
reg_asym_usl



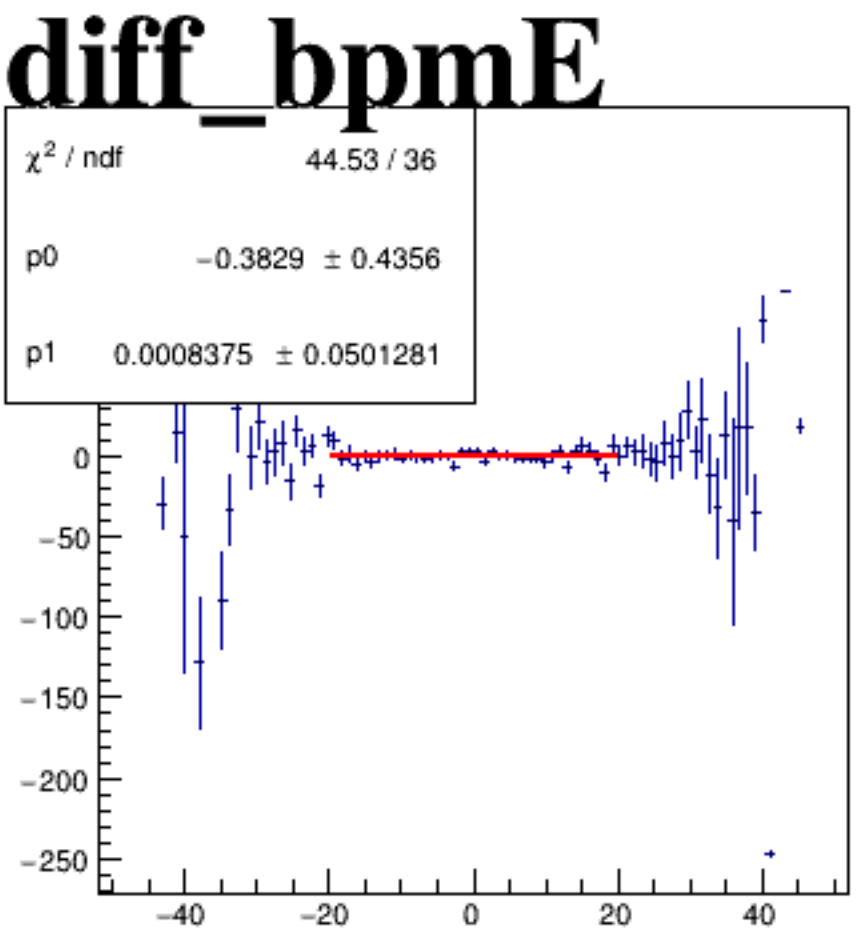
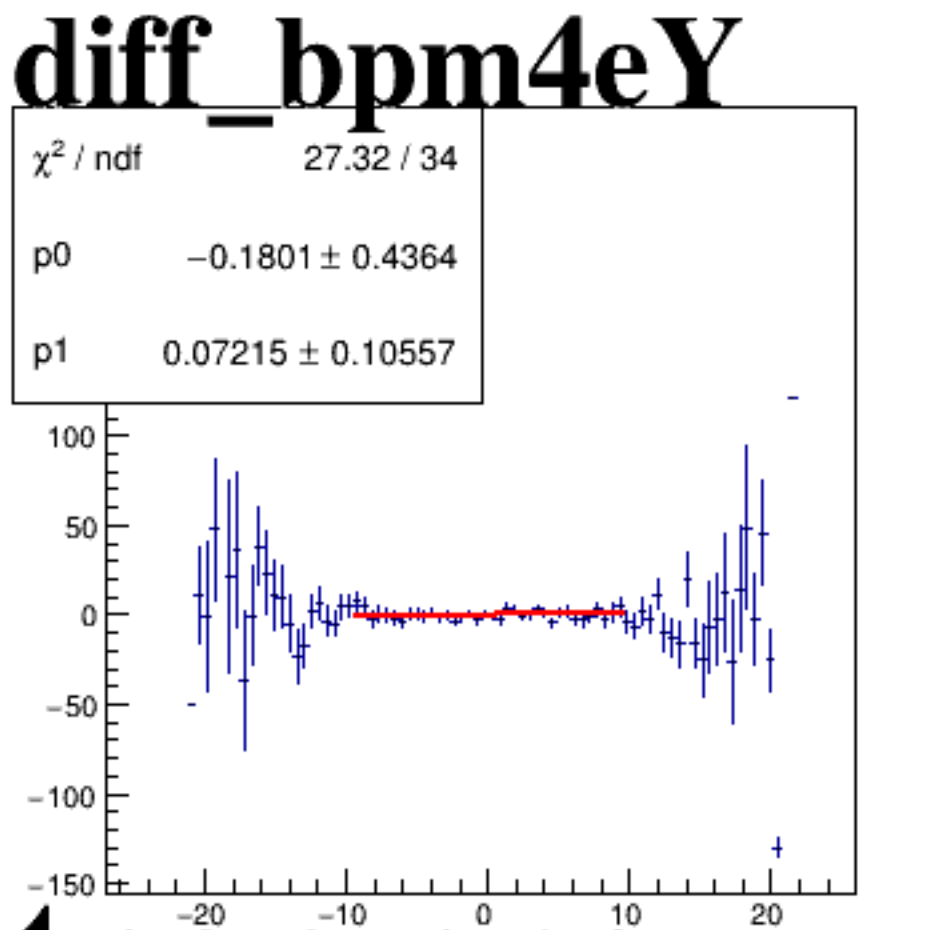
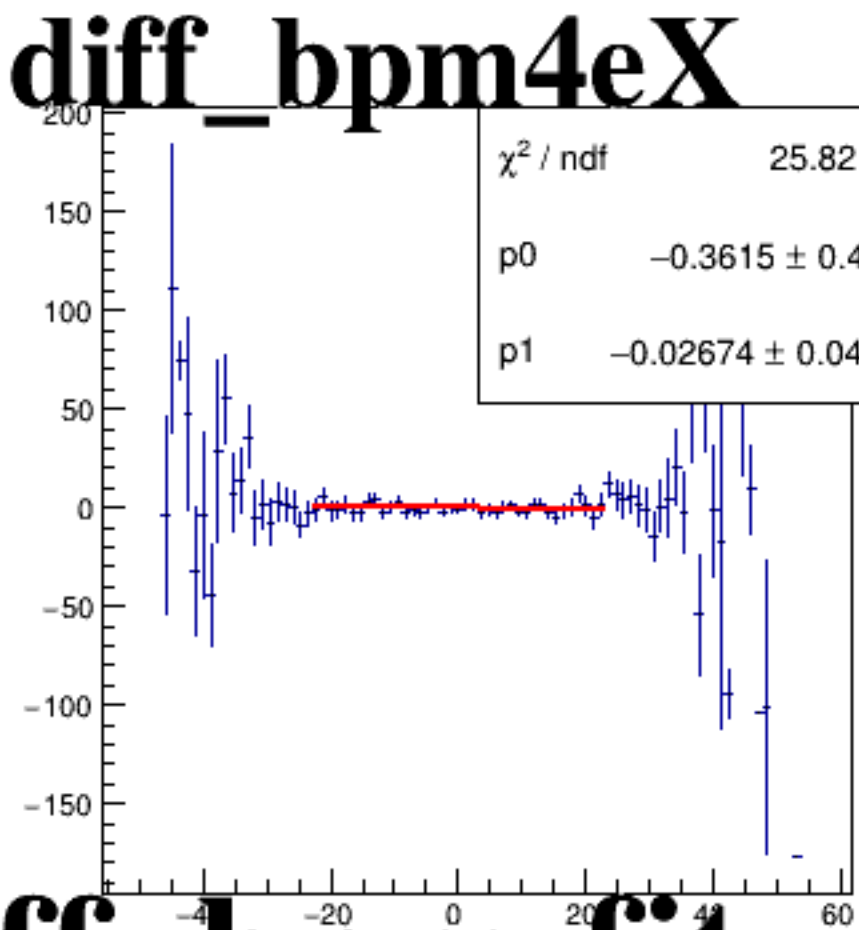
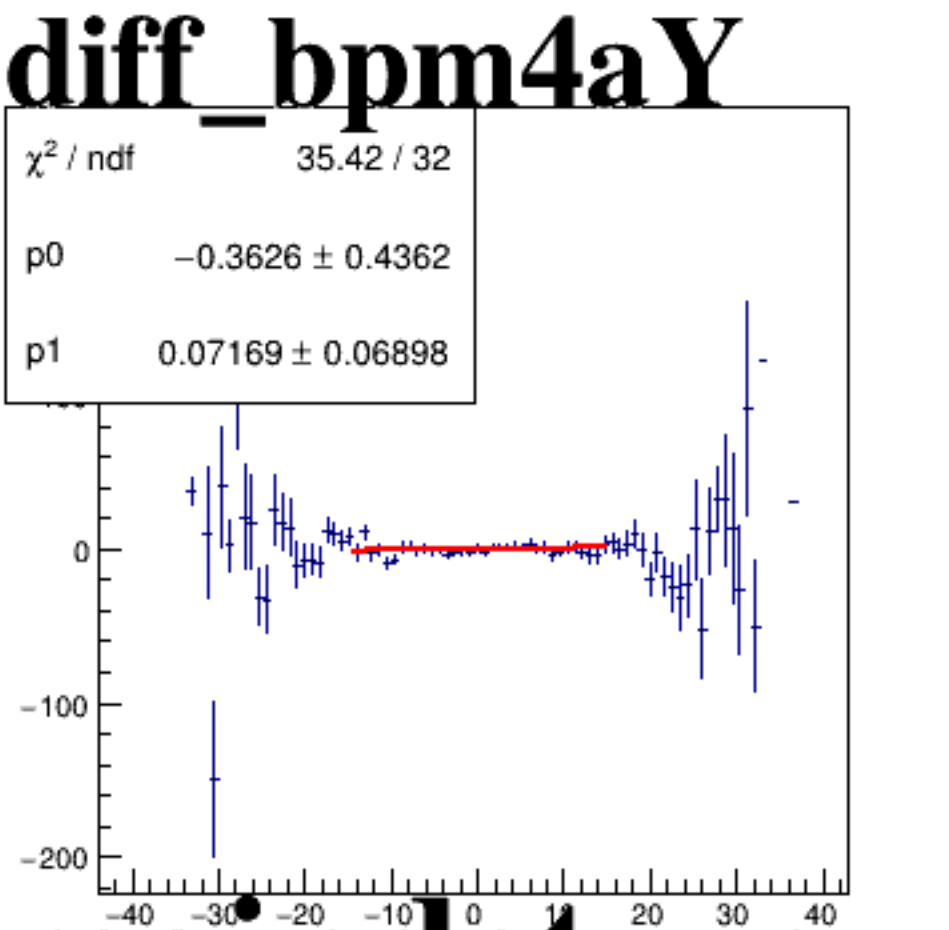
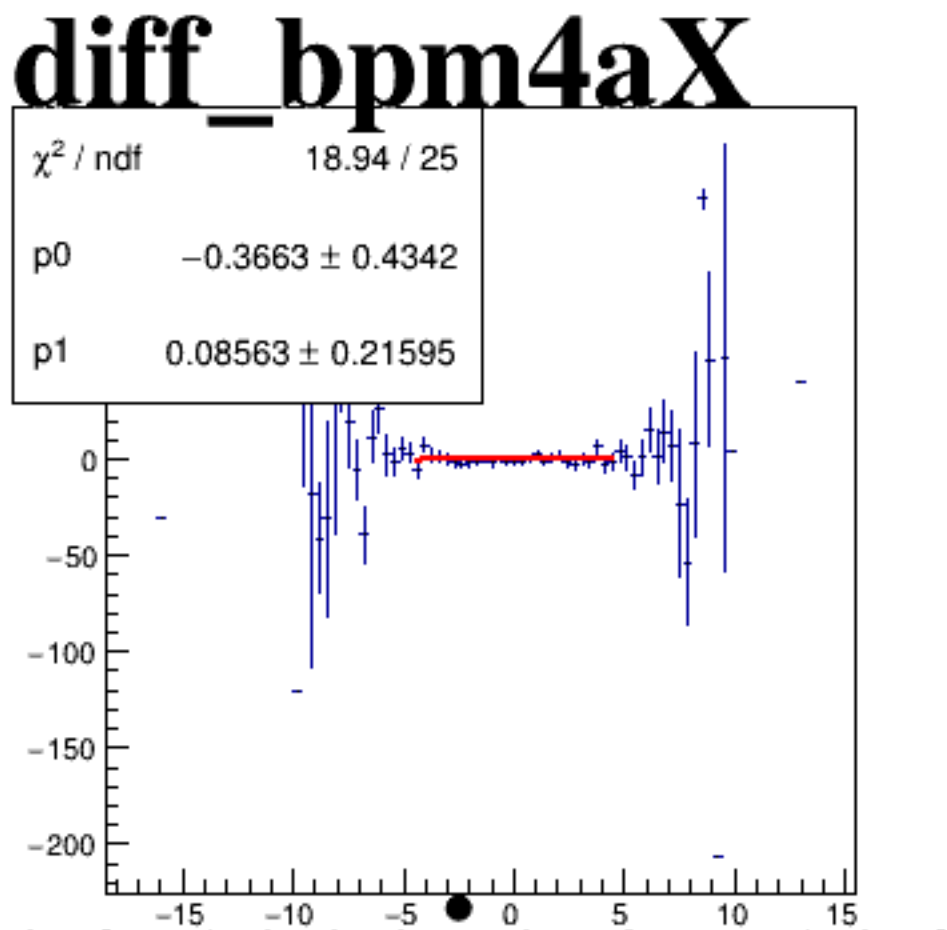
reg_asym_dsl



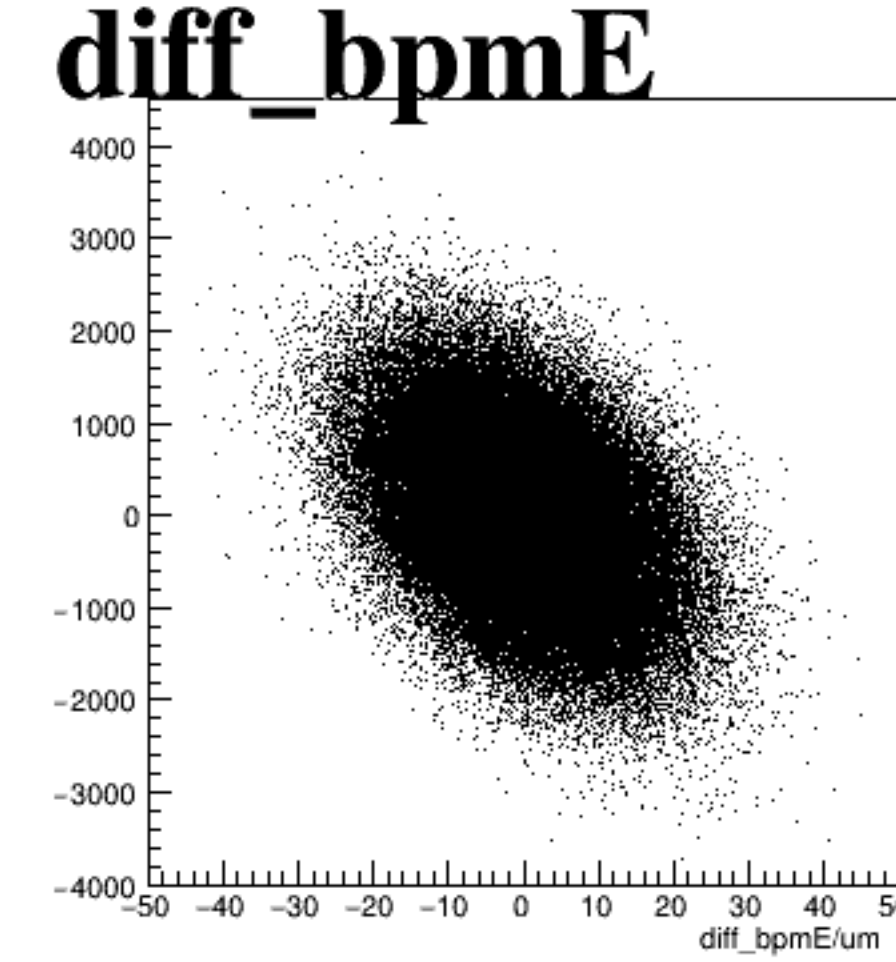
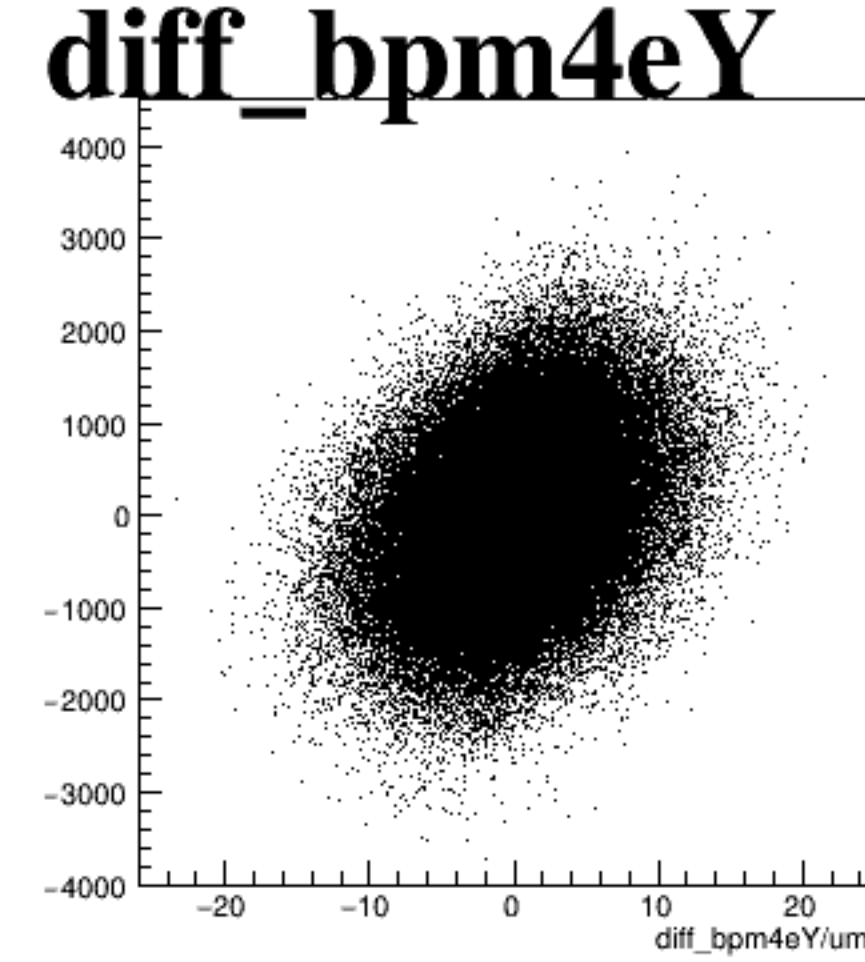
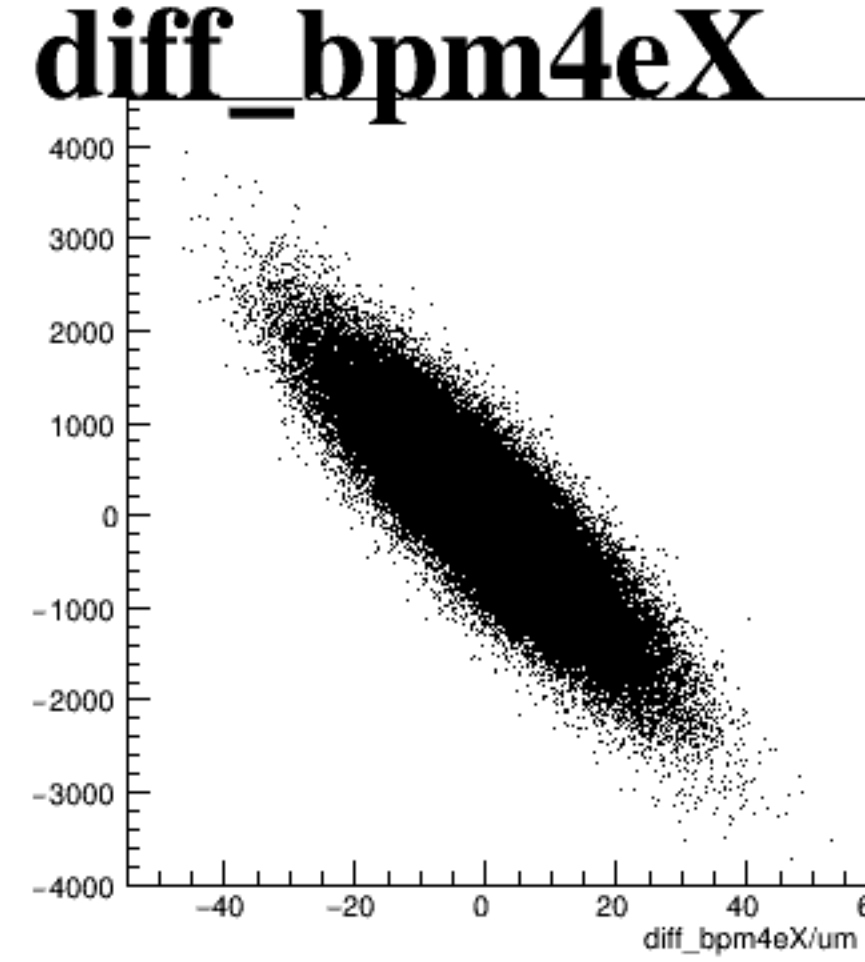
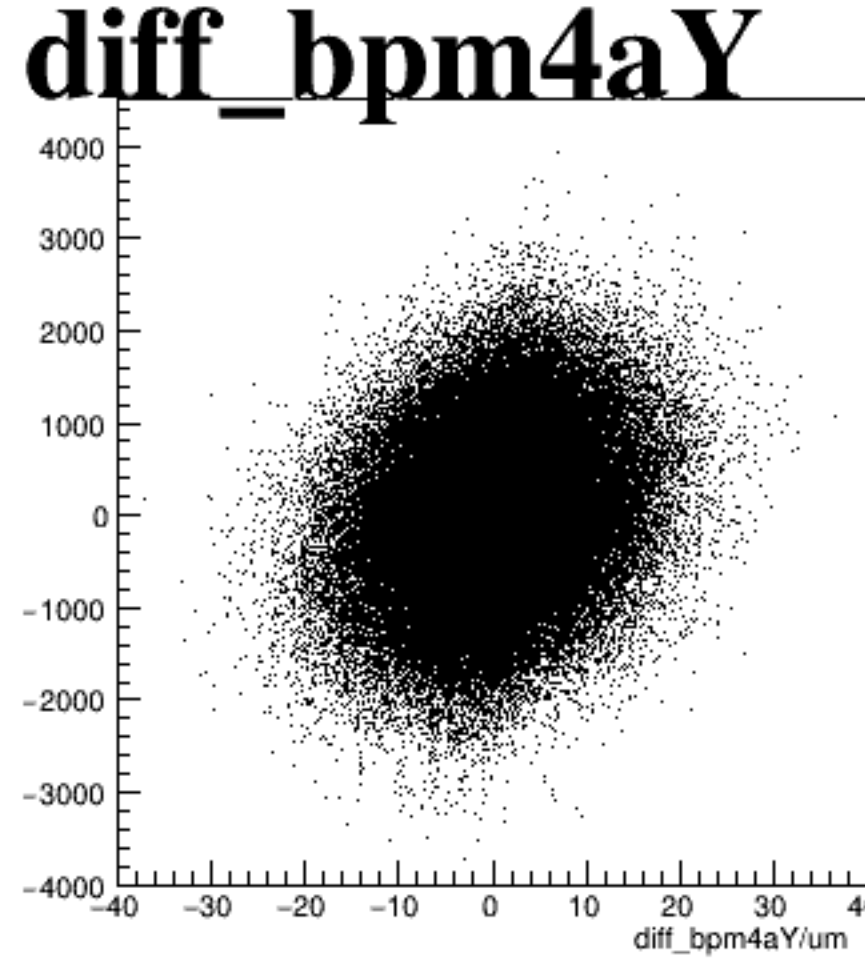
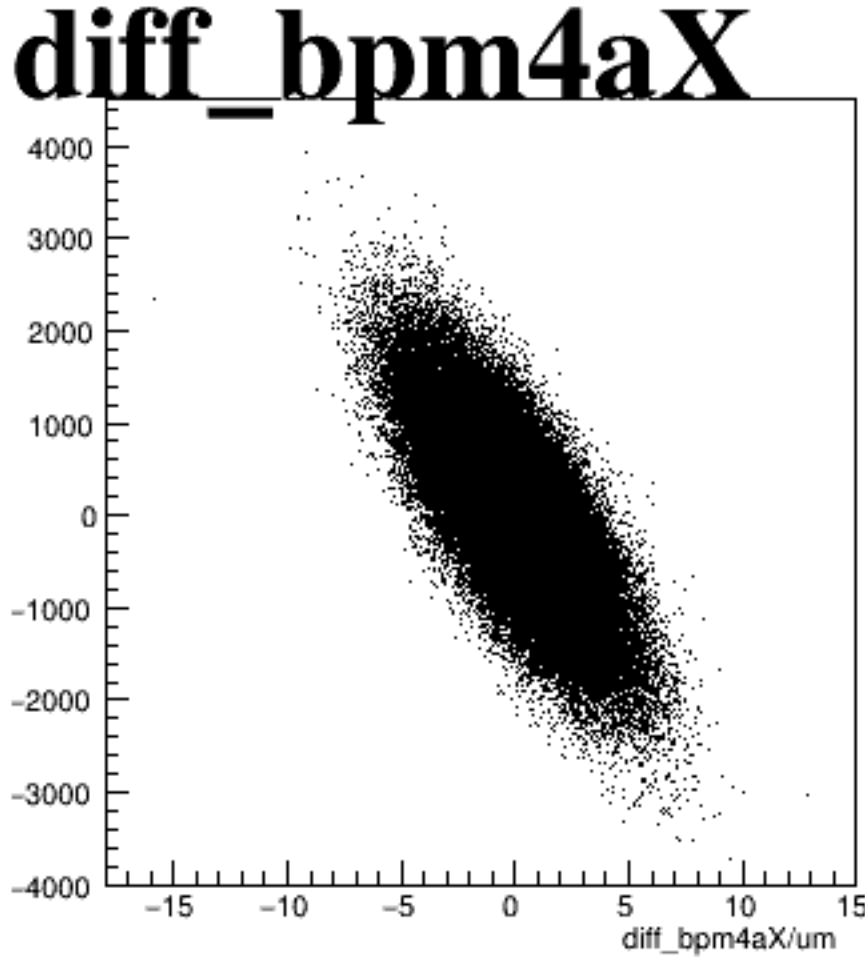
reg_asym_usr



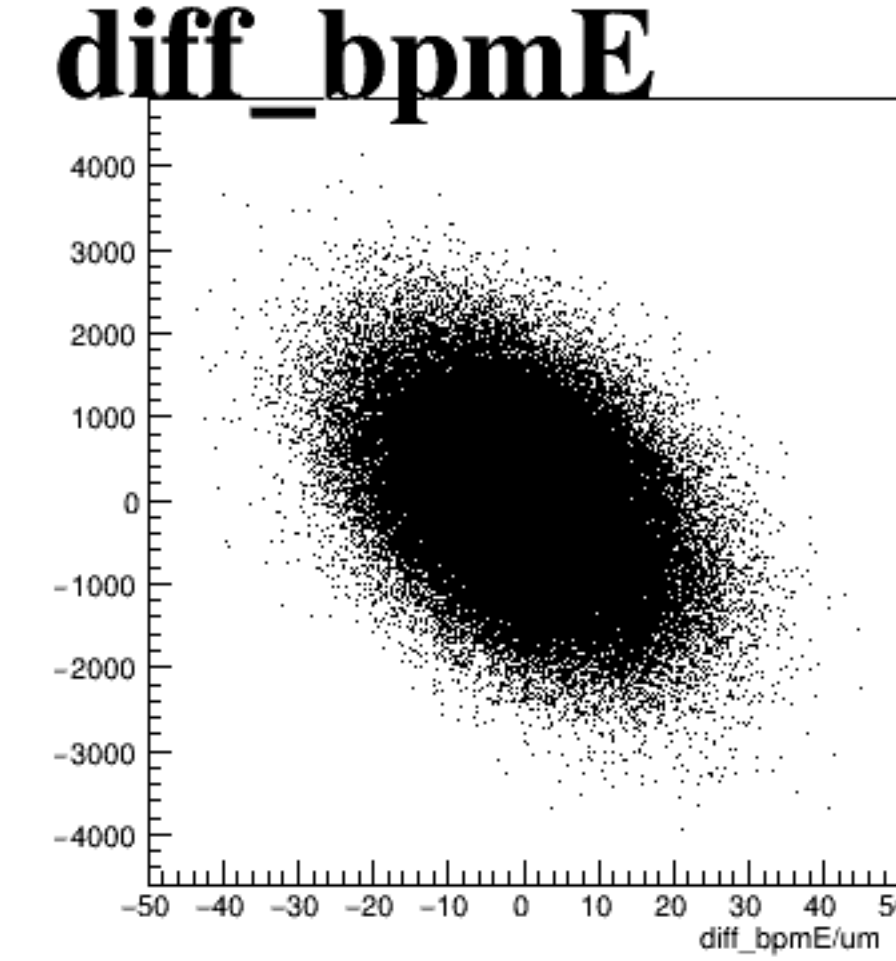
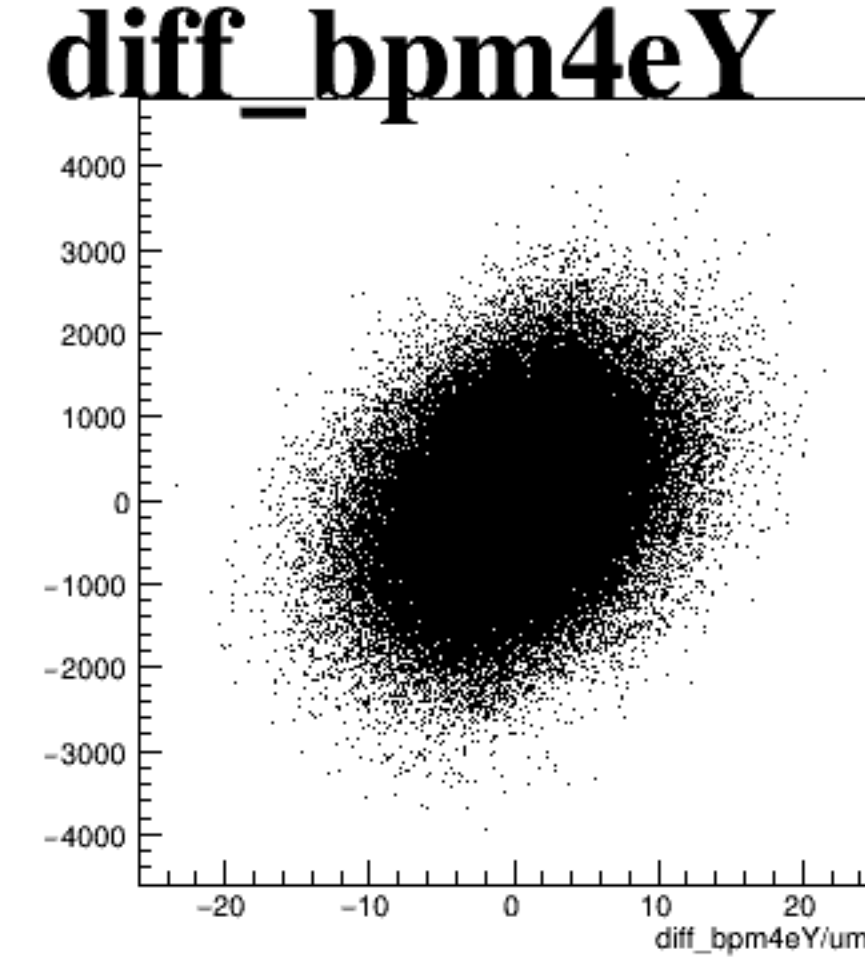
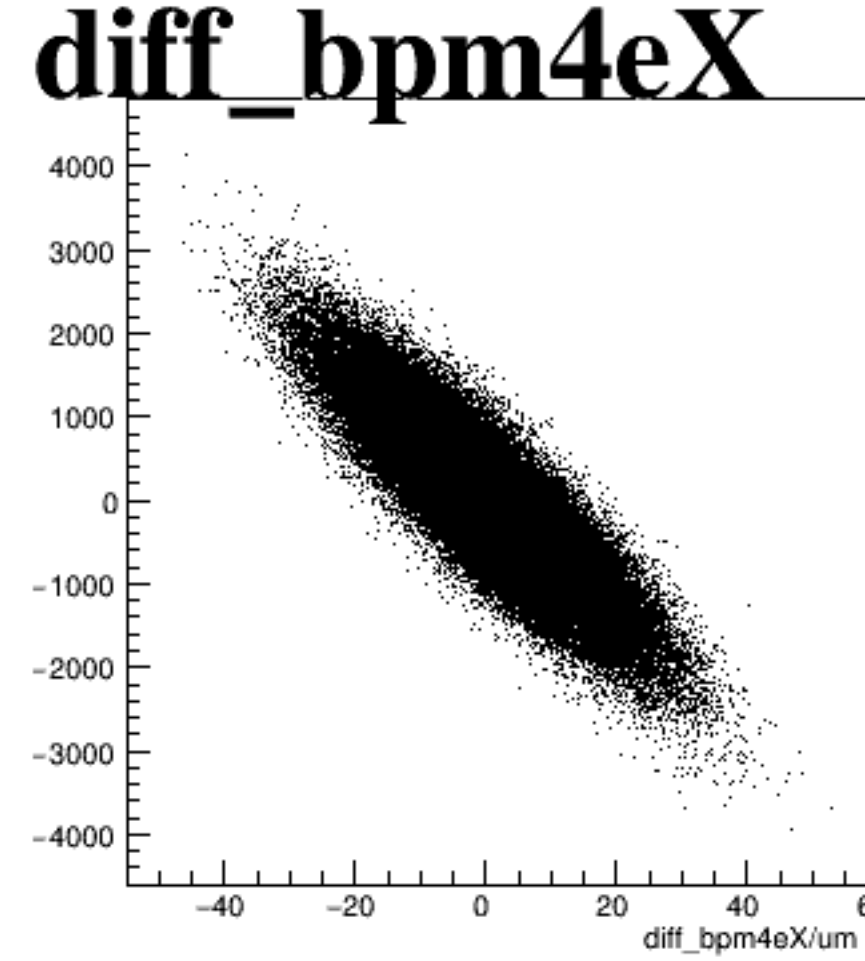
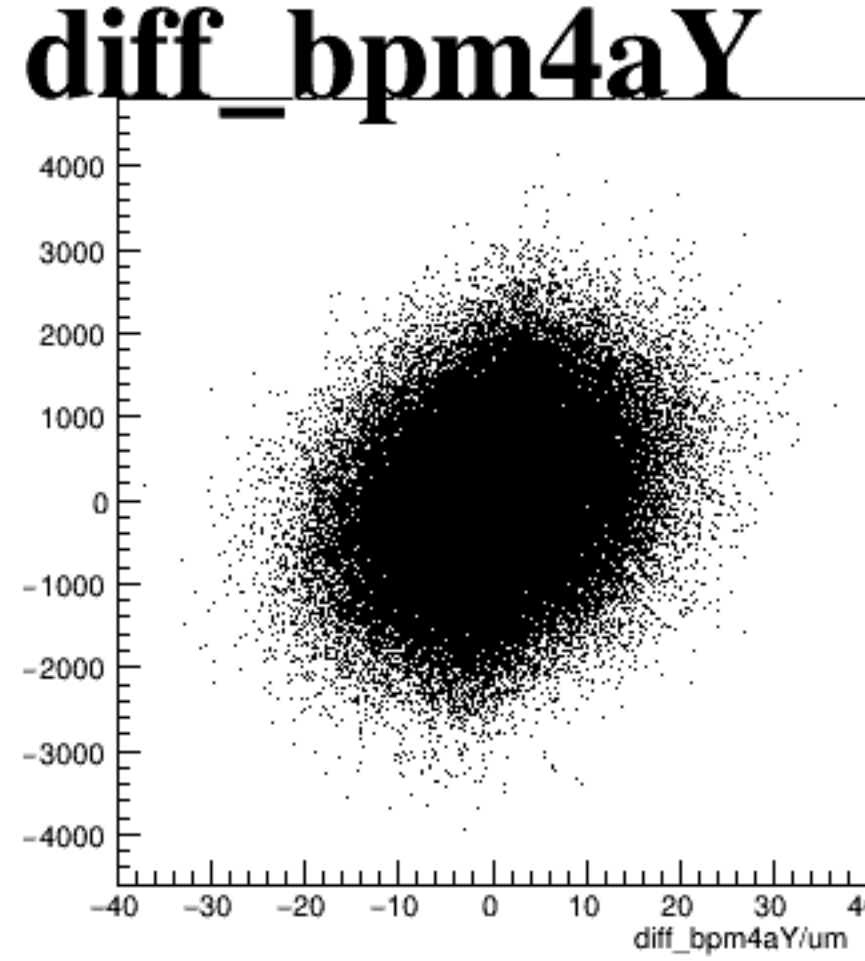
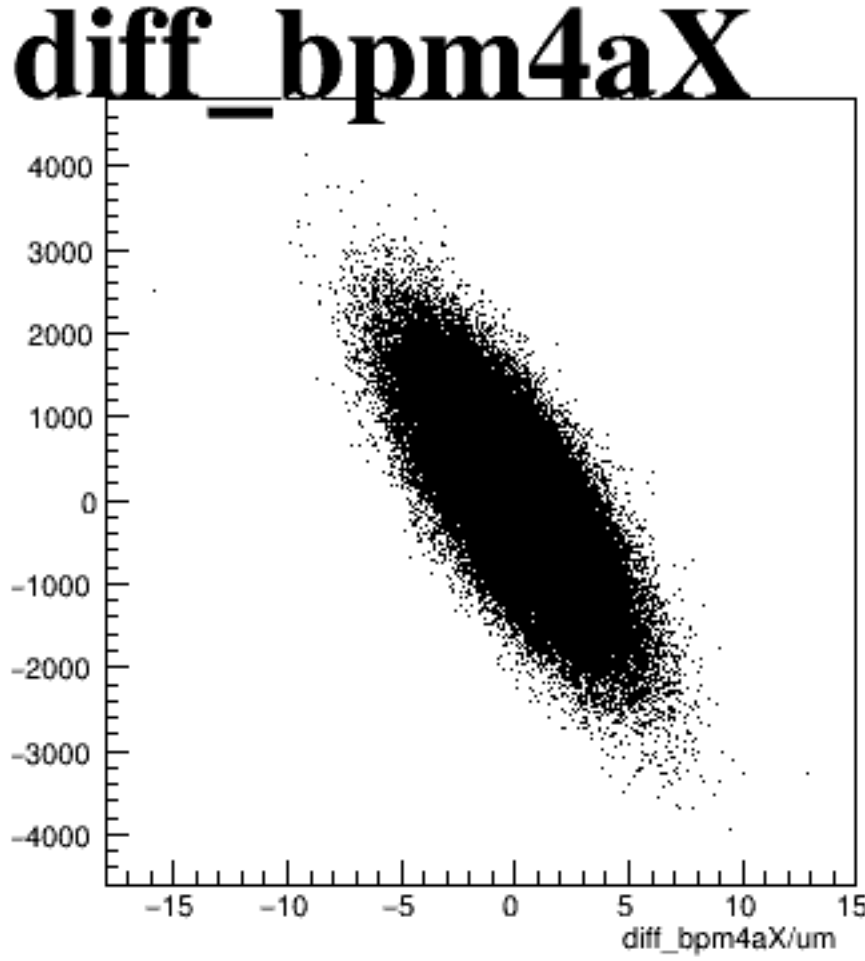
reg_asym_dsr



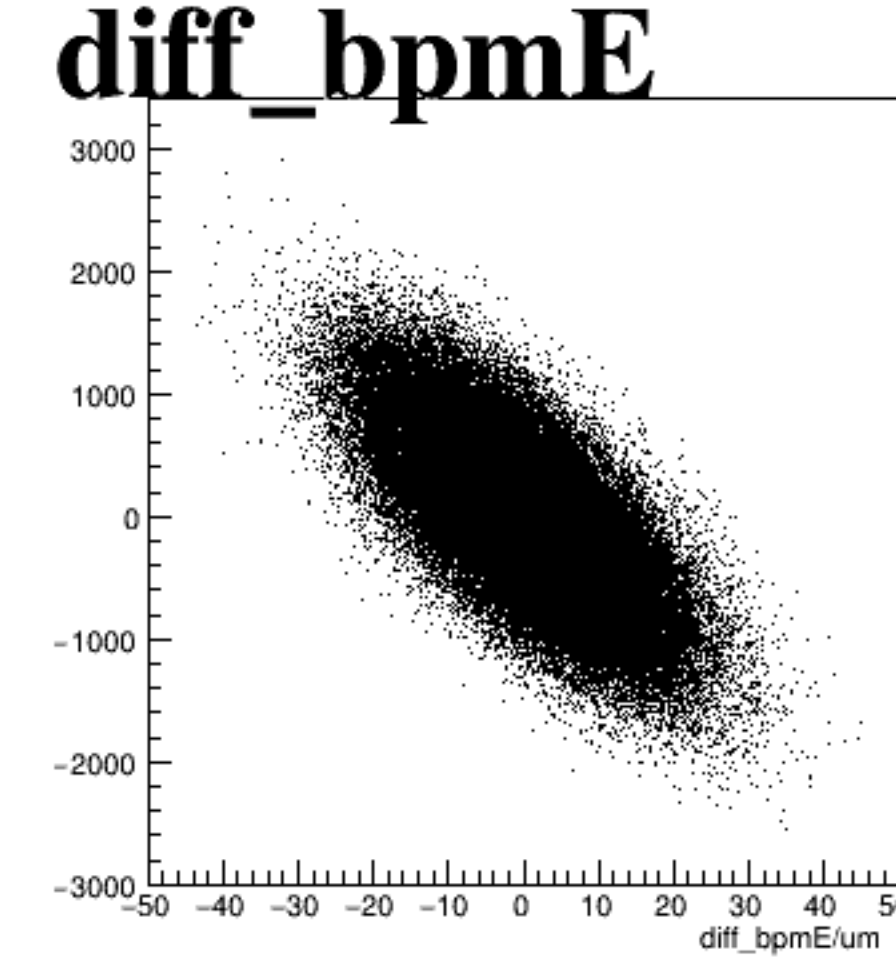
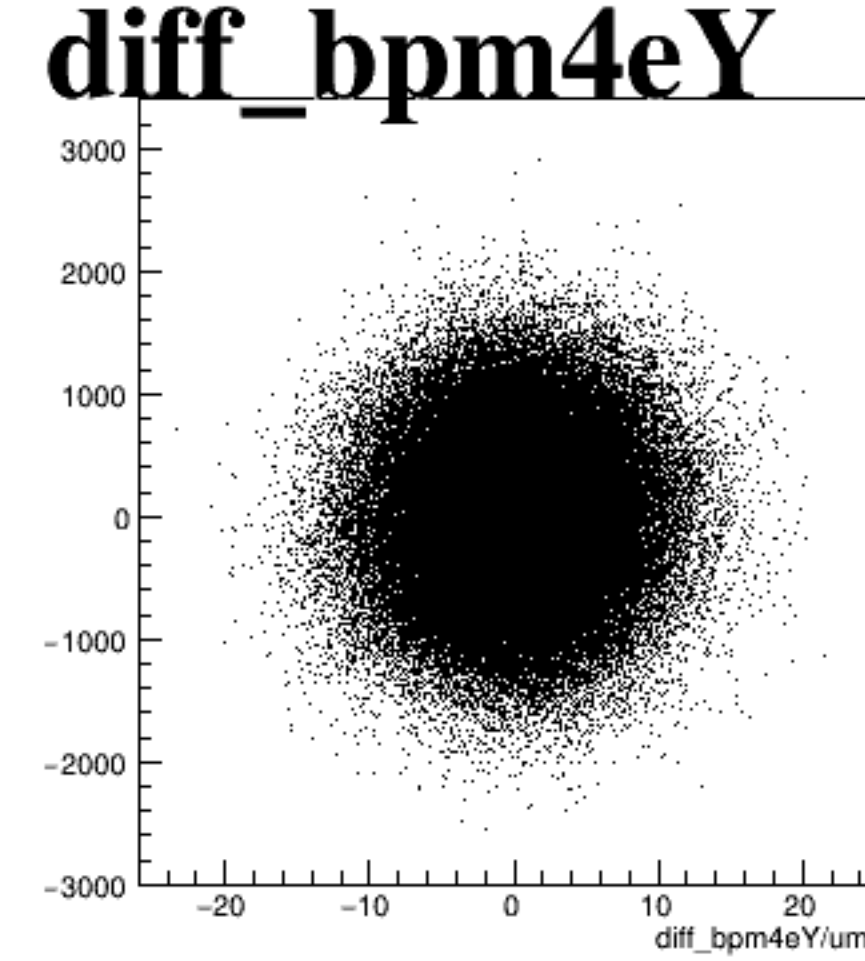
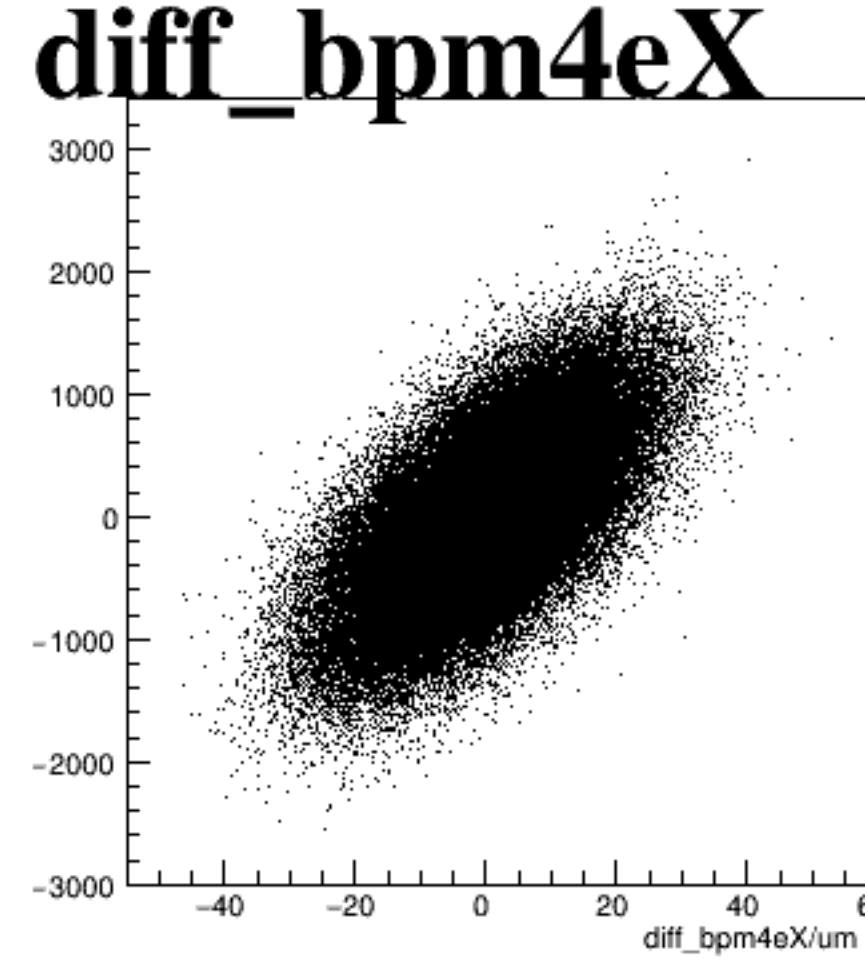
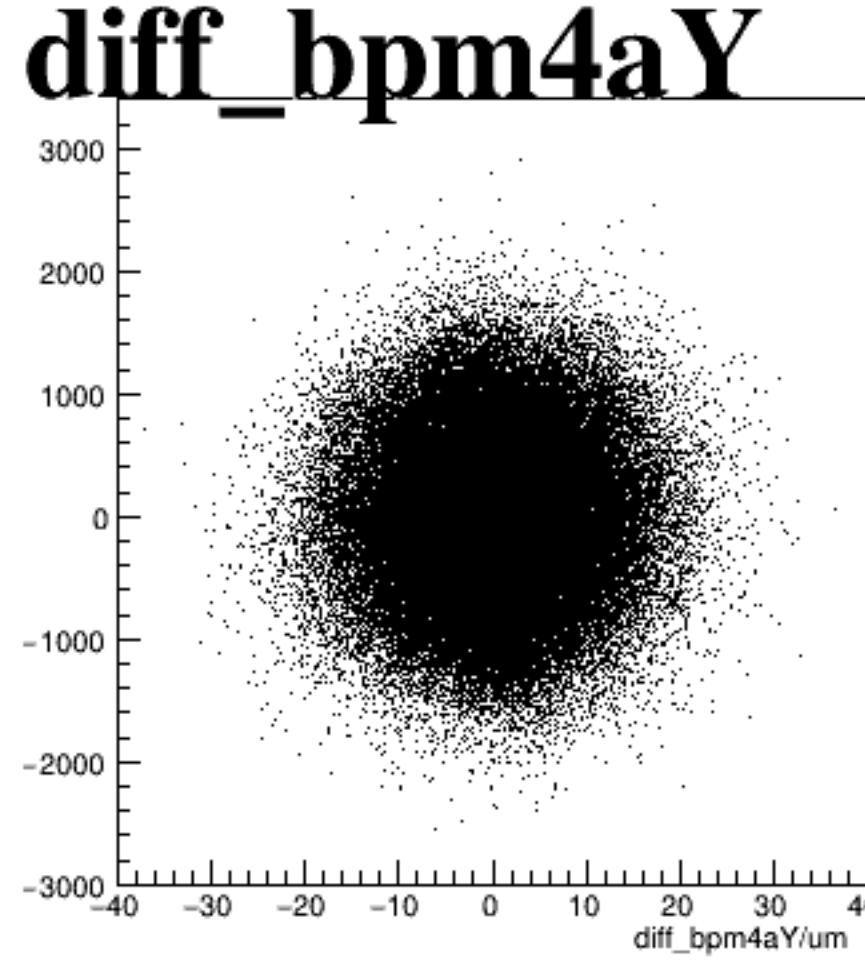
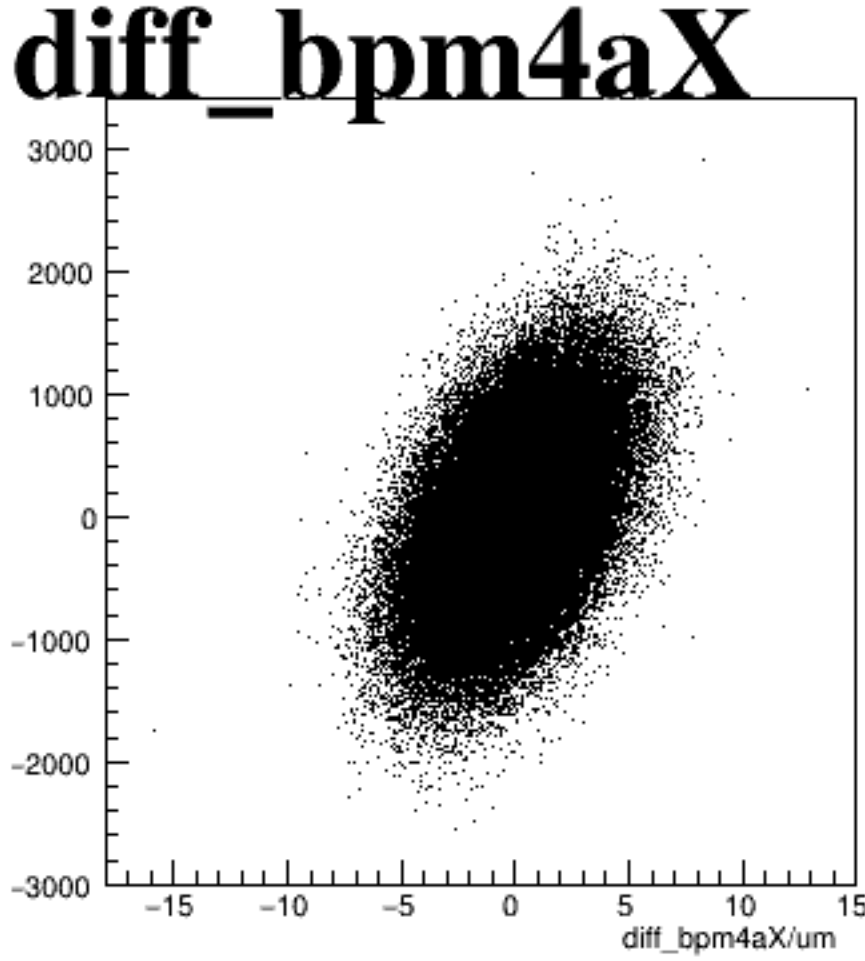
asym_usl



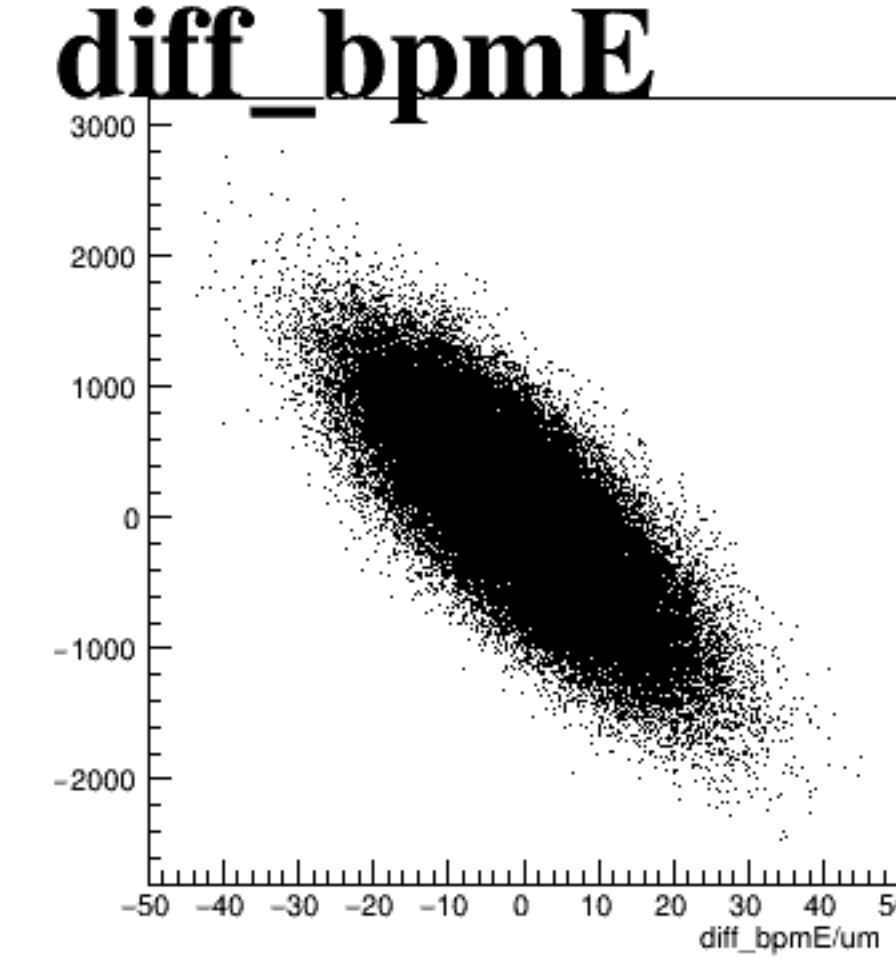
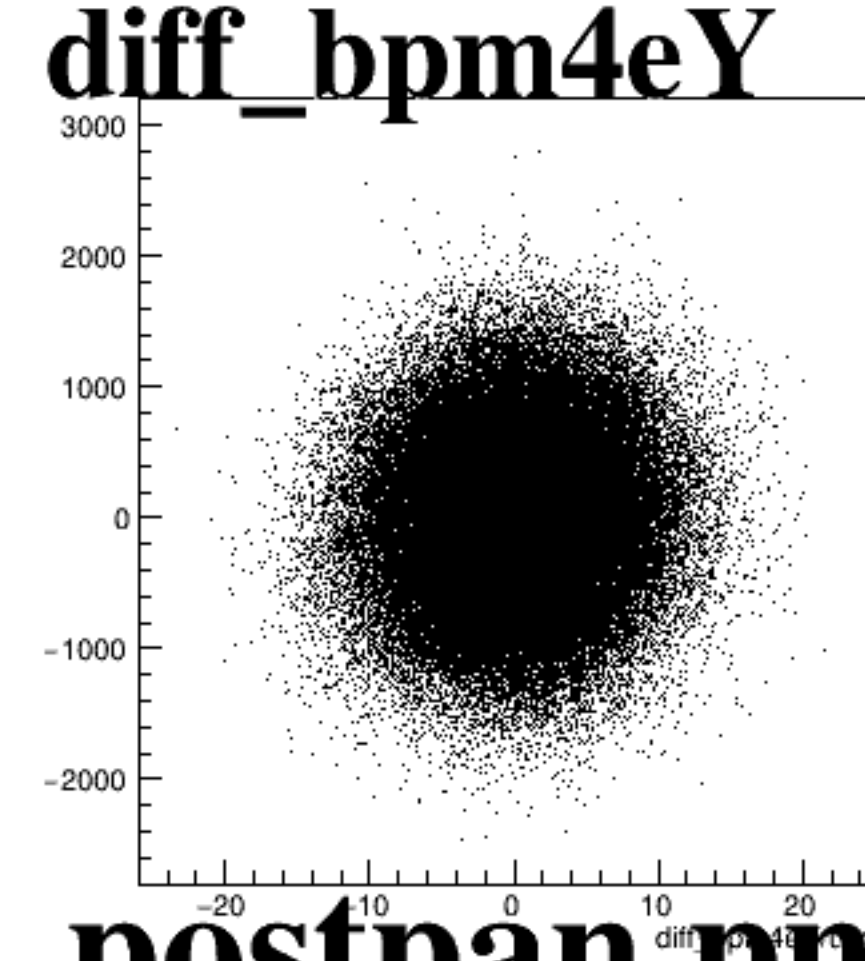
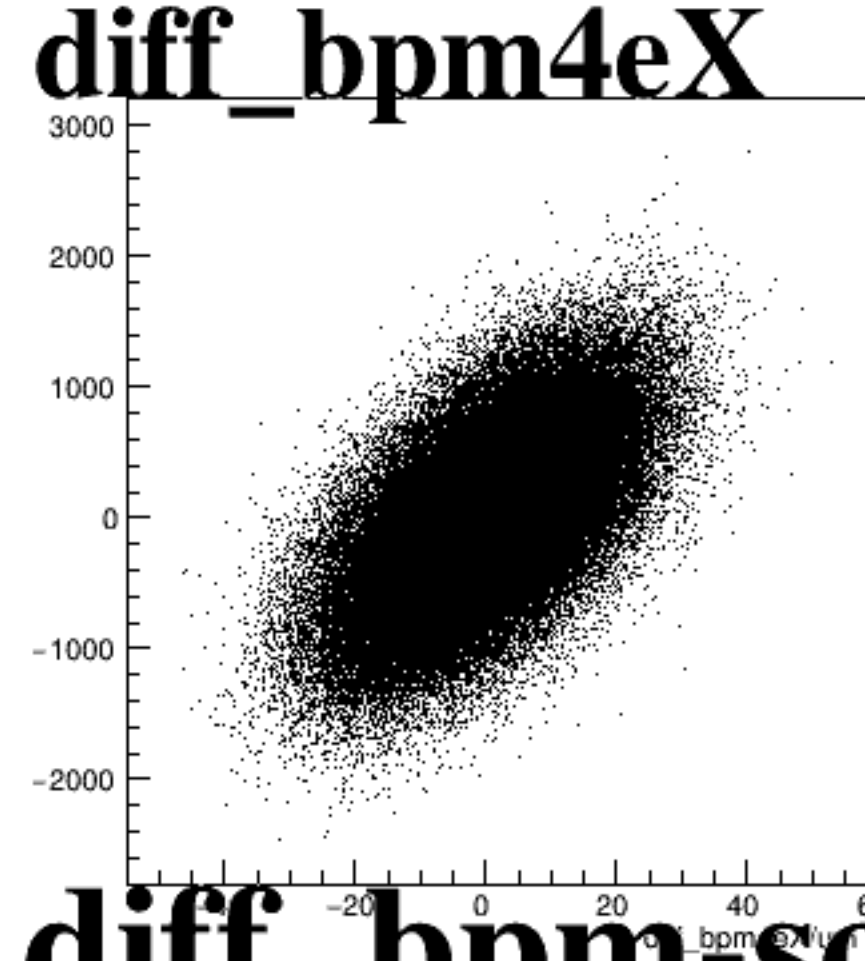
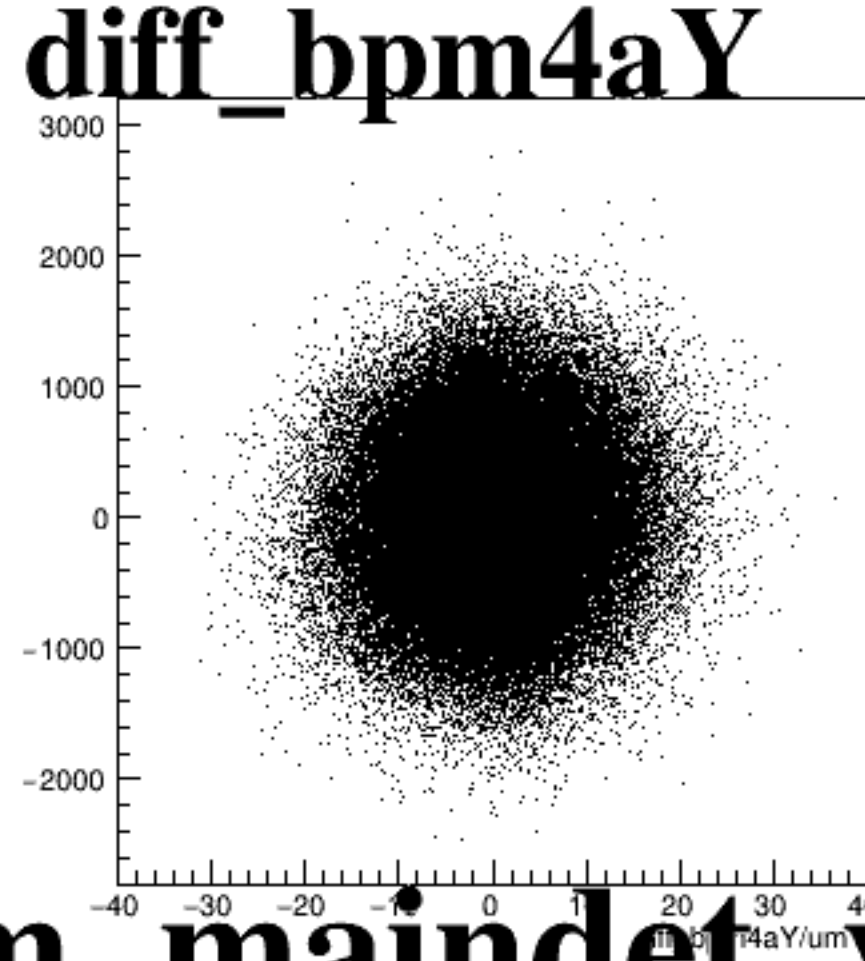
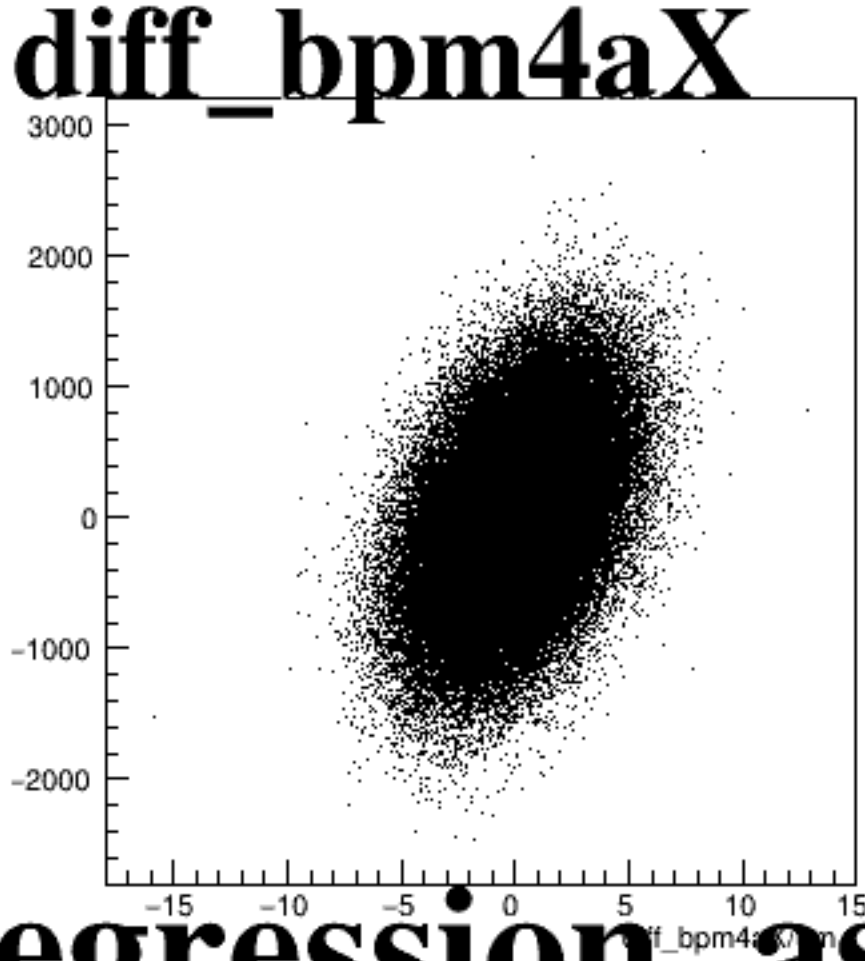
asym_dsl



asym_usr



asym_dsr



run3605_000_regression_reg_maindet_vs_diff_bpm-scat_postpan.png