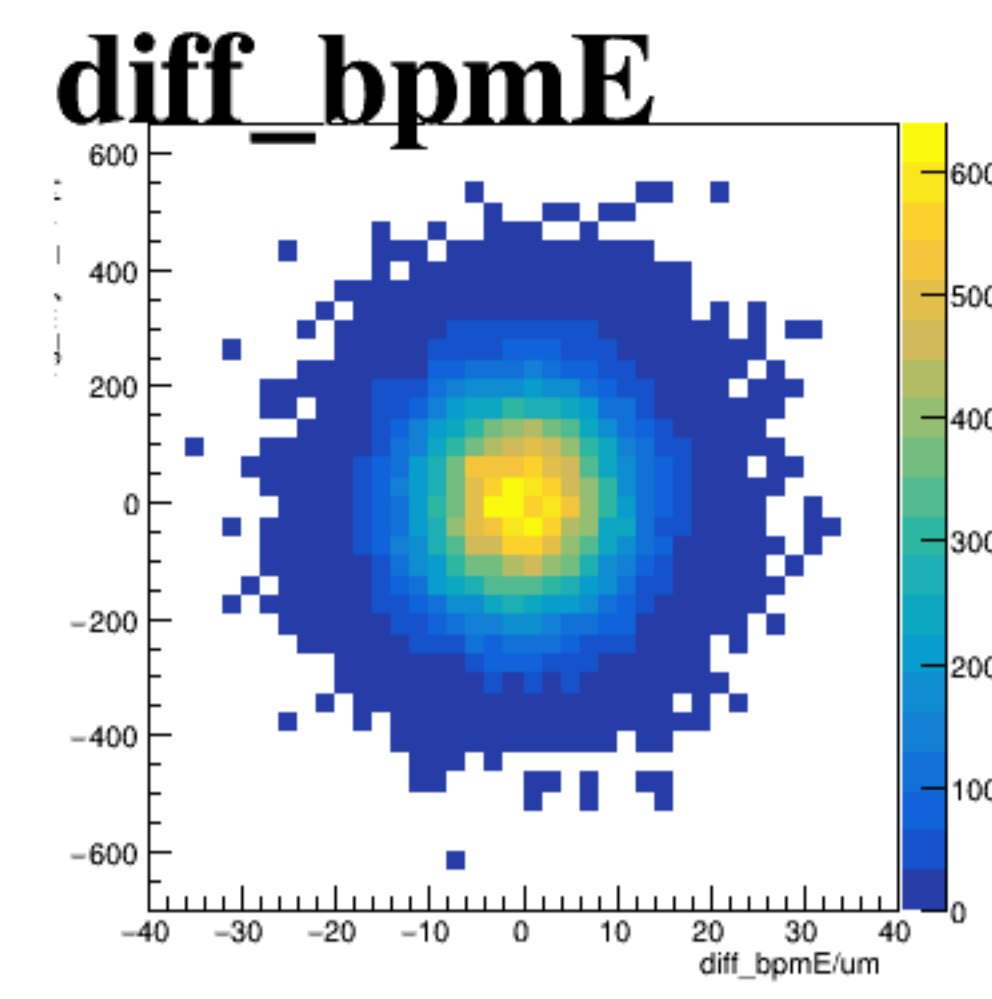
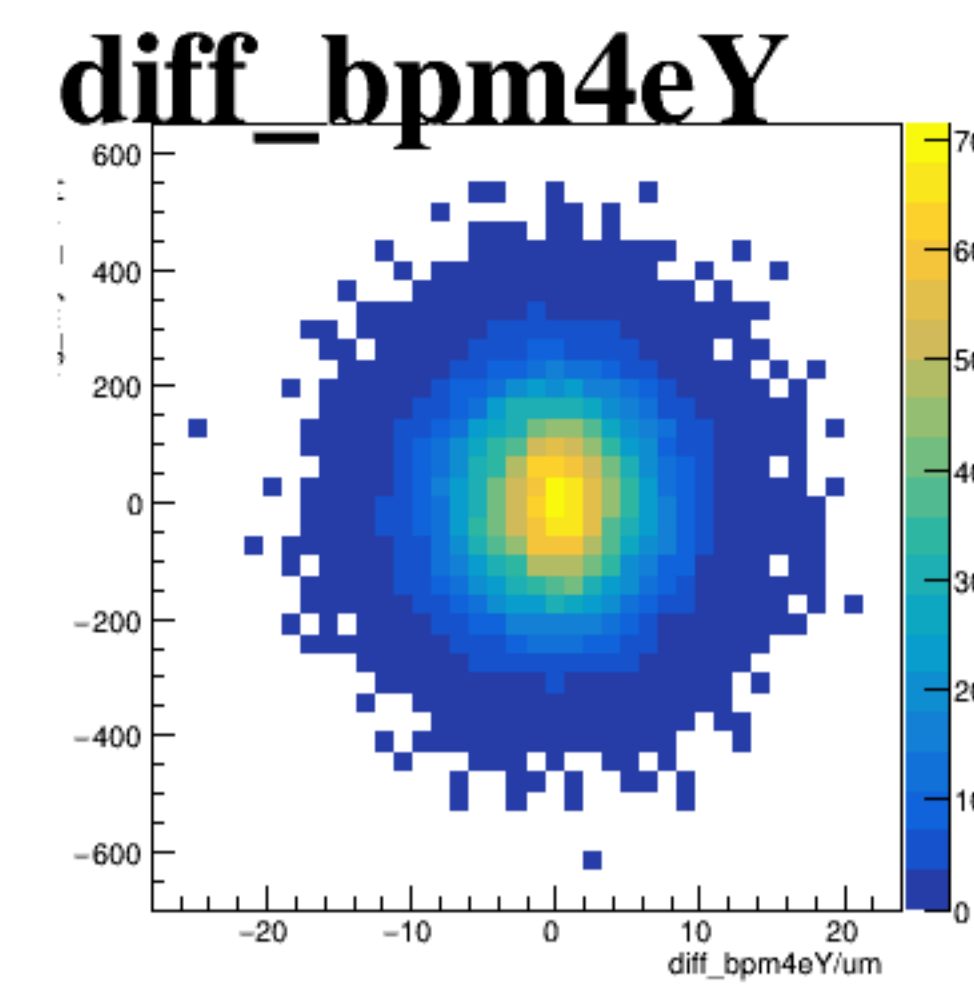
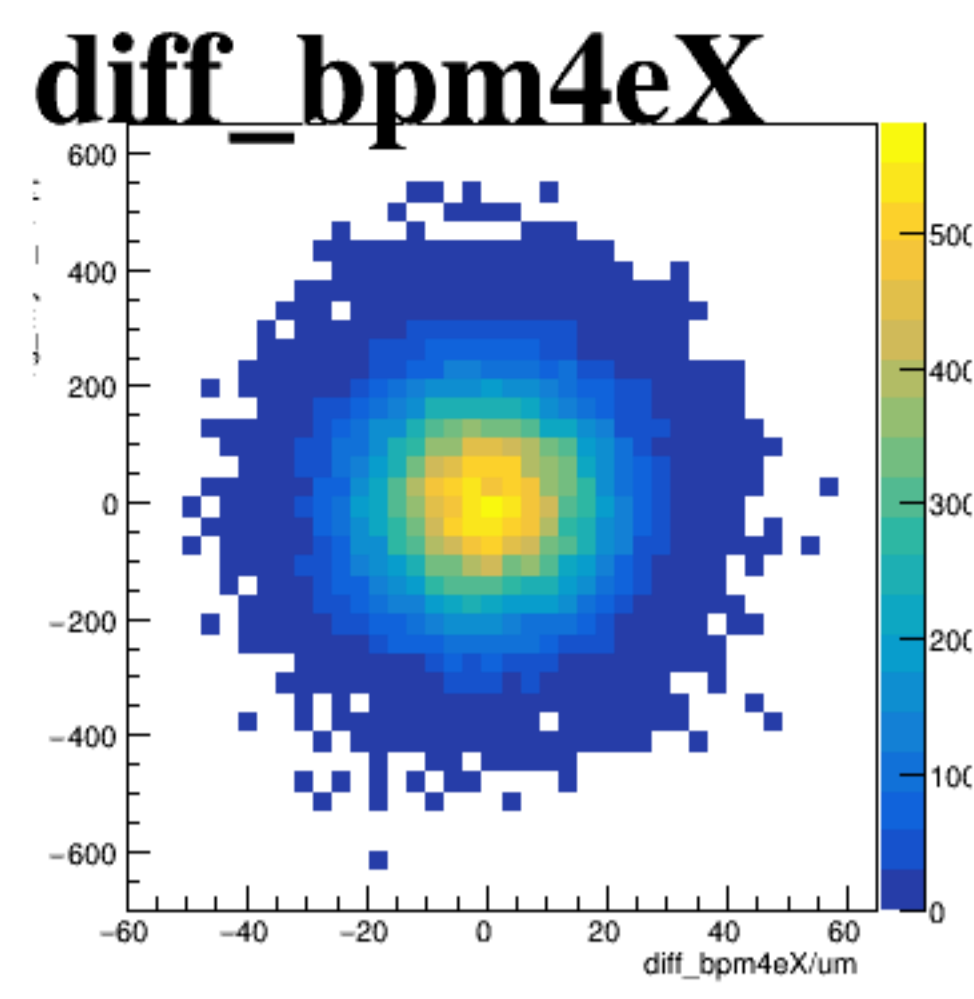
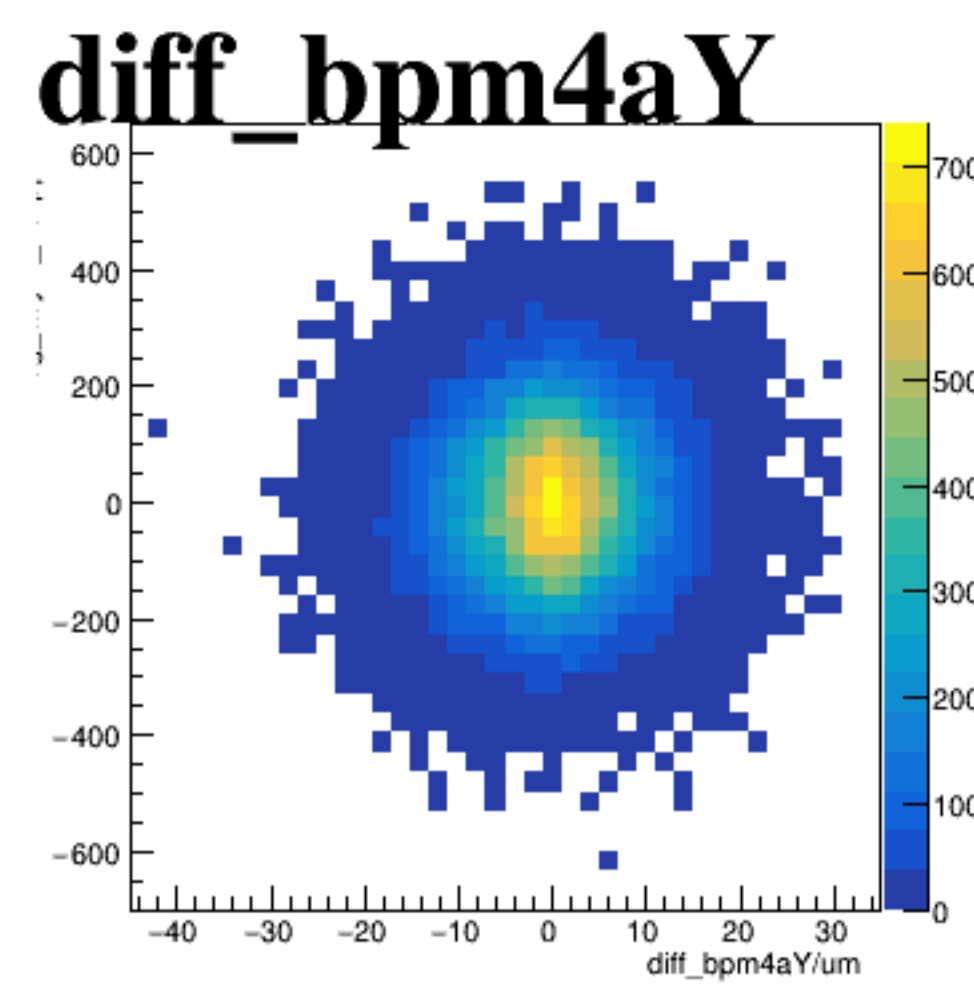
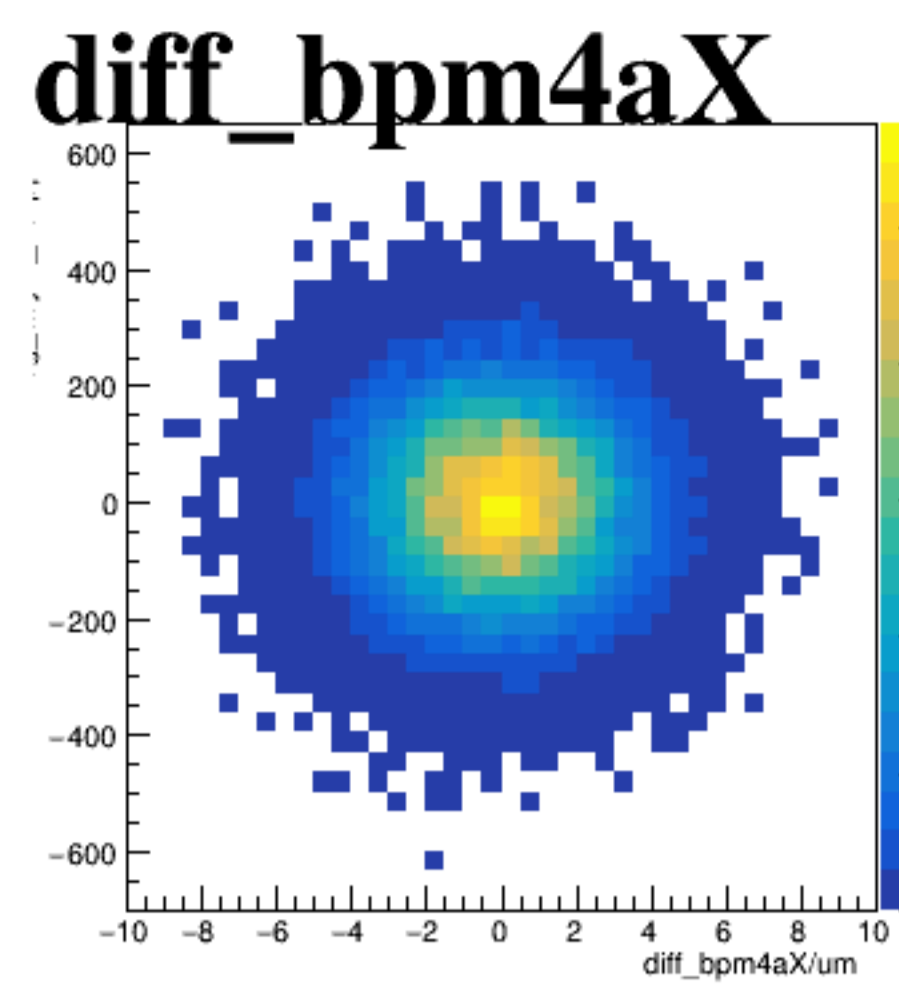
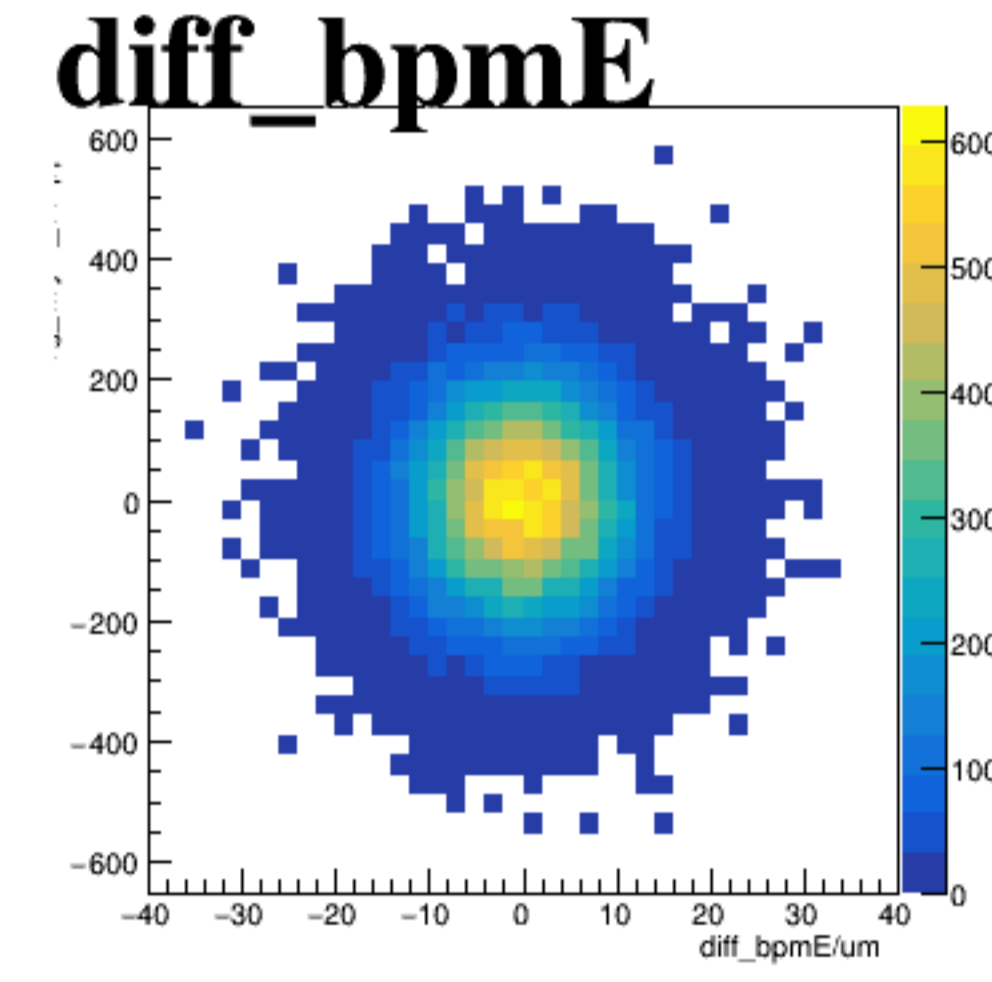
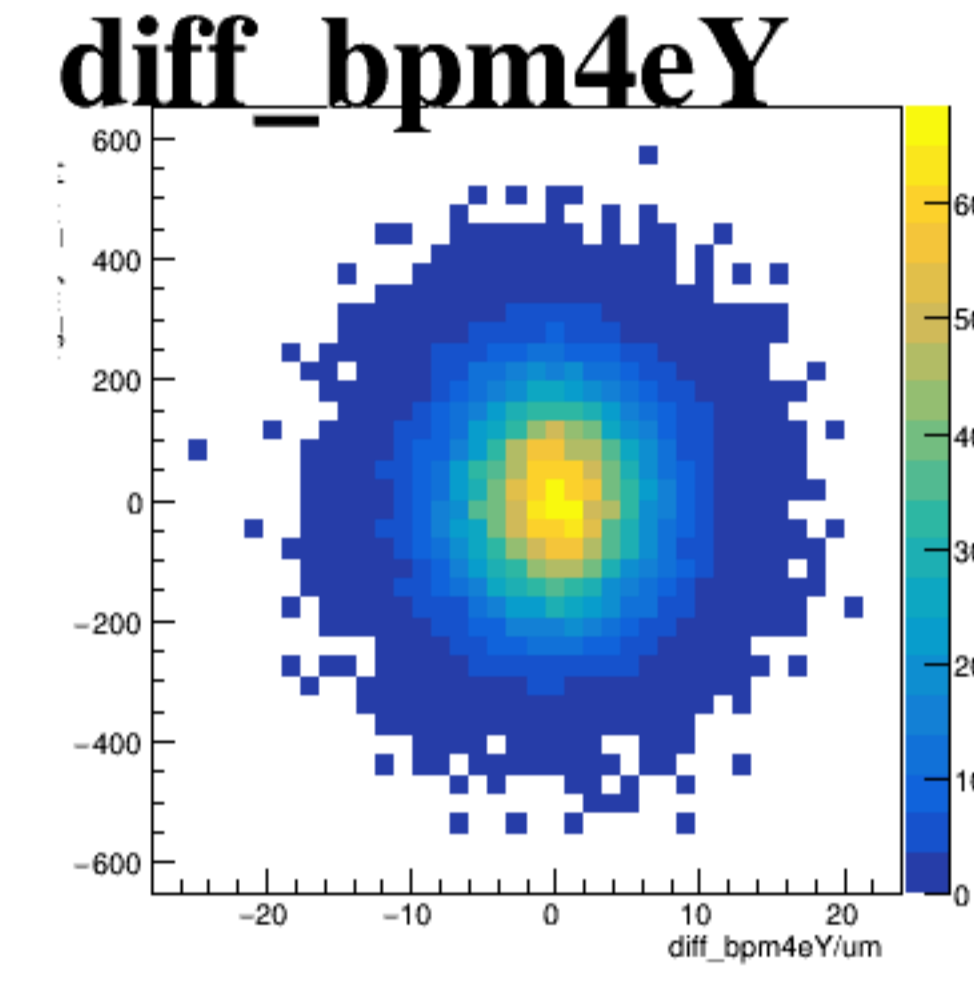
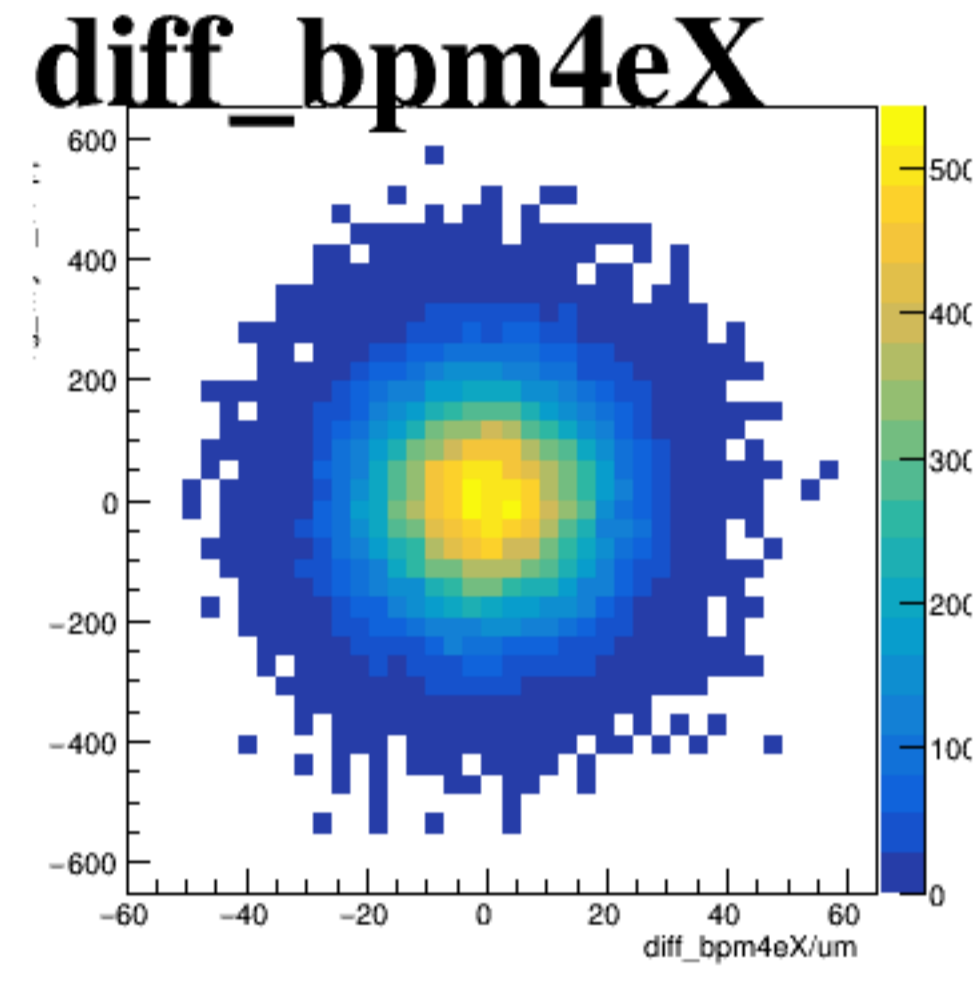
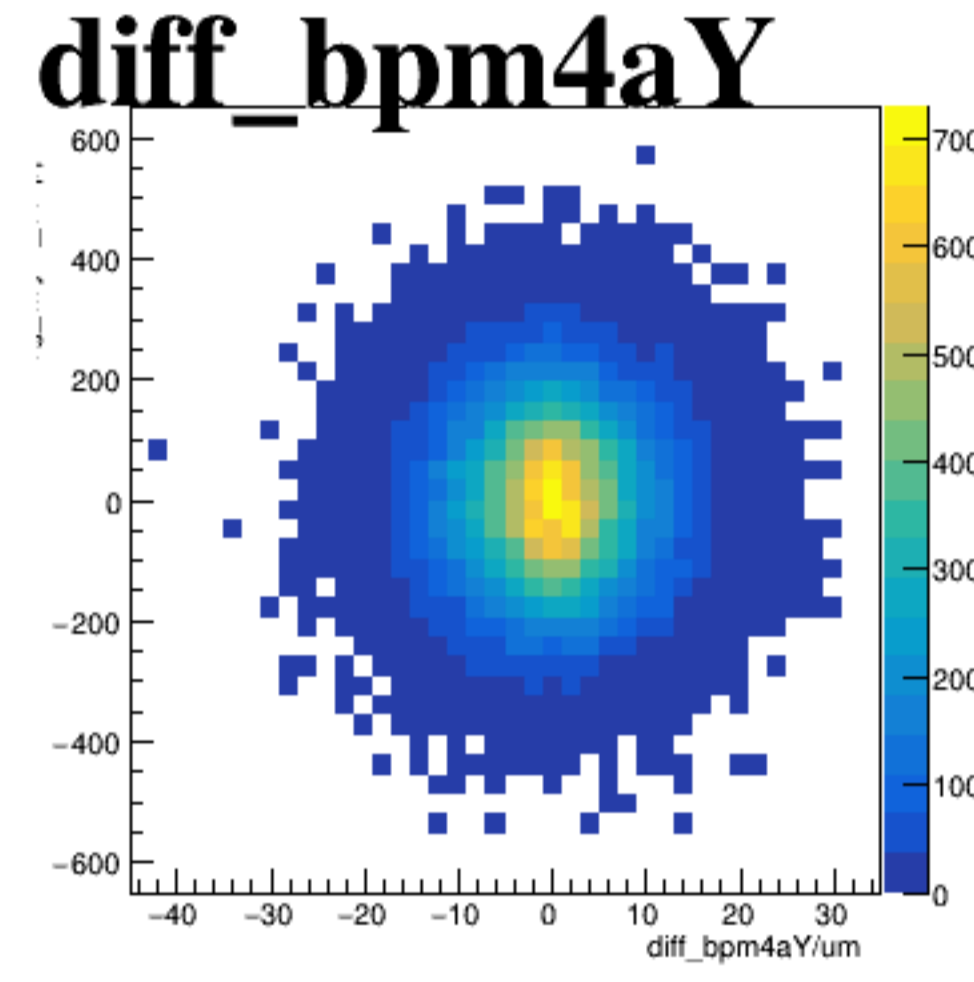
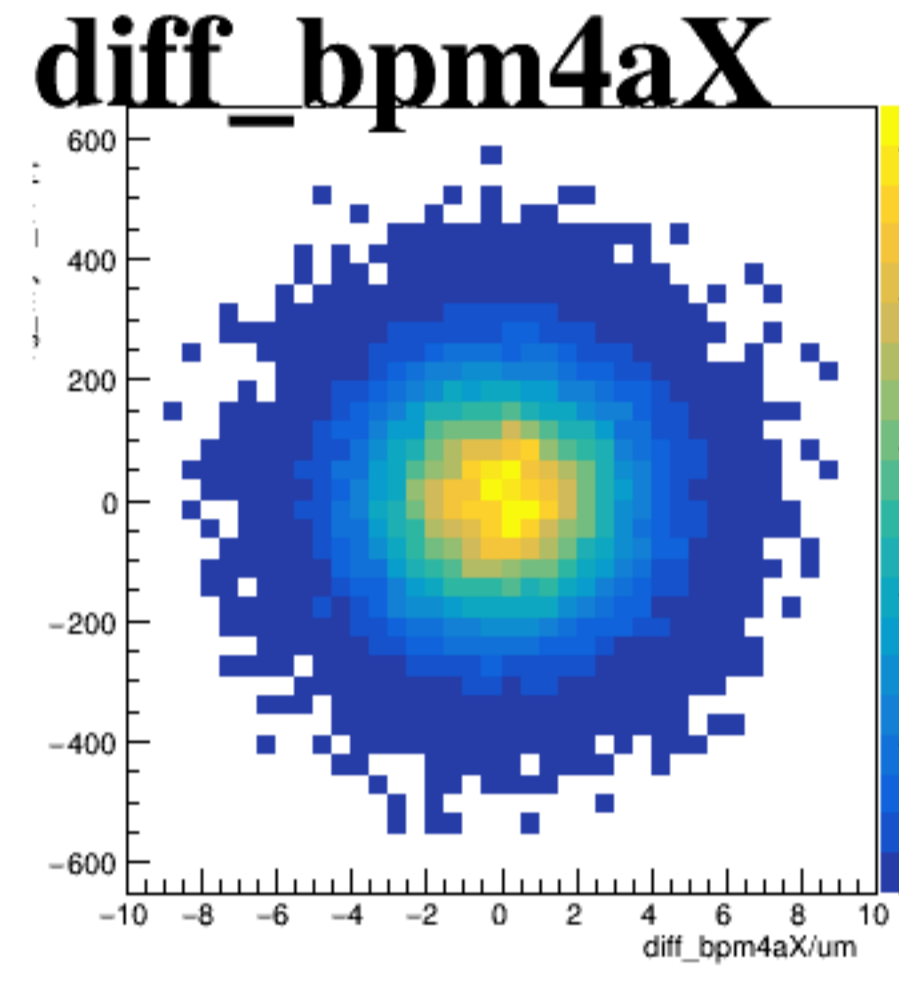




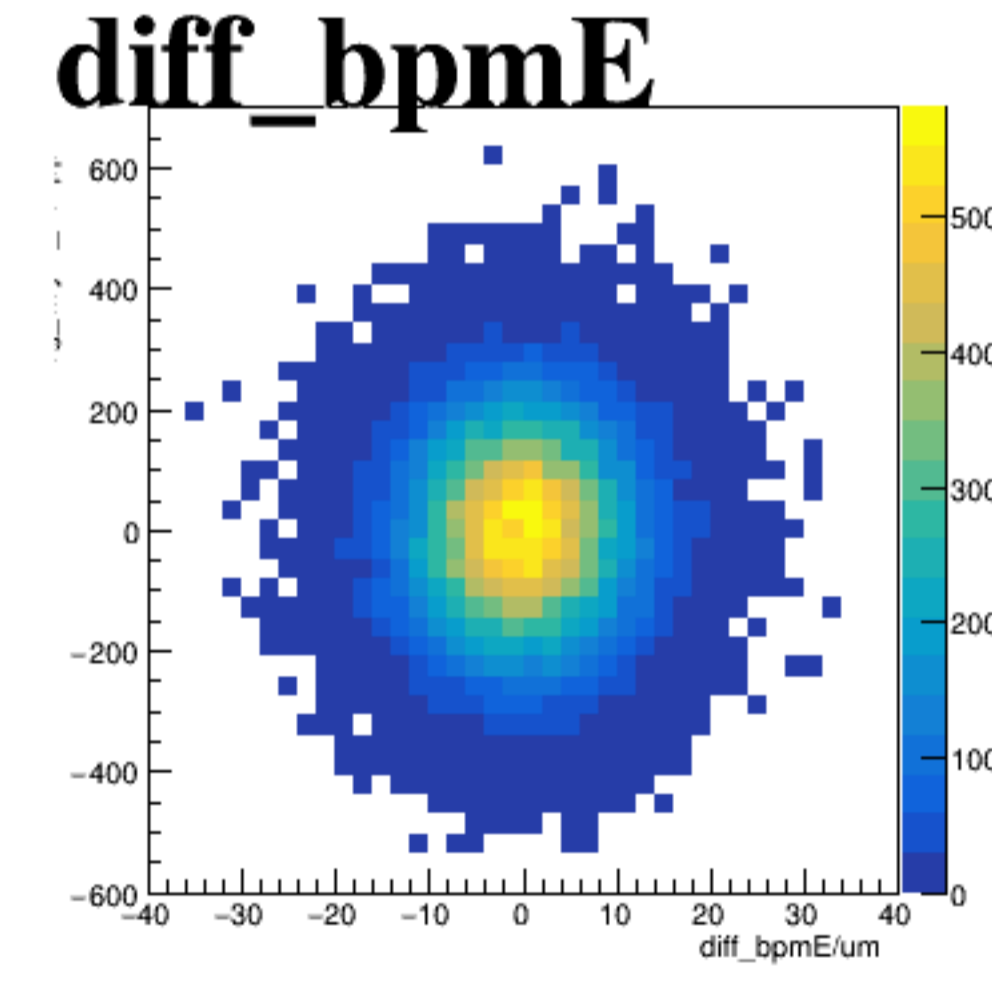
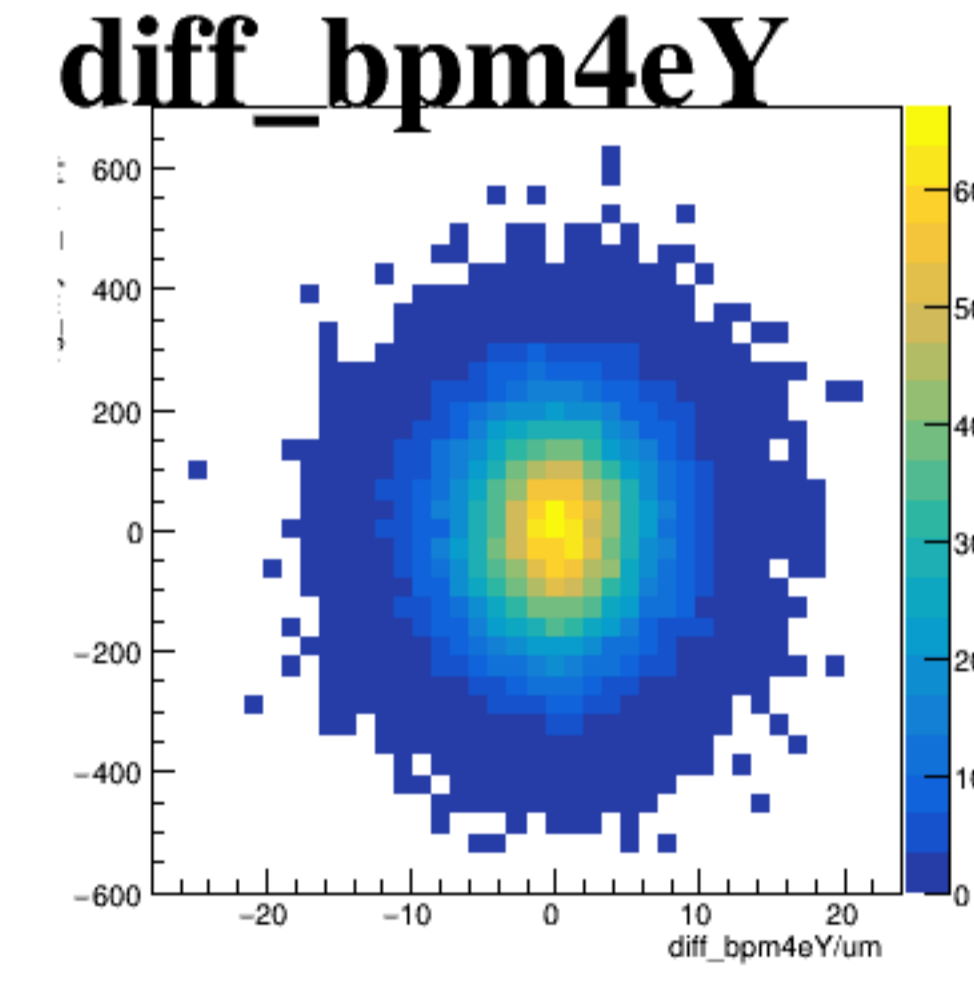
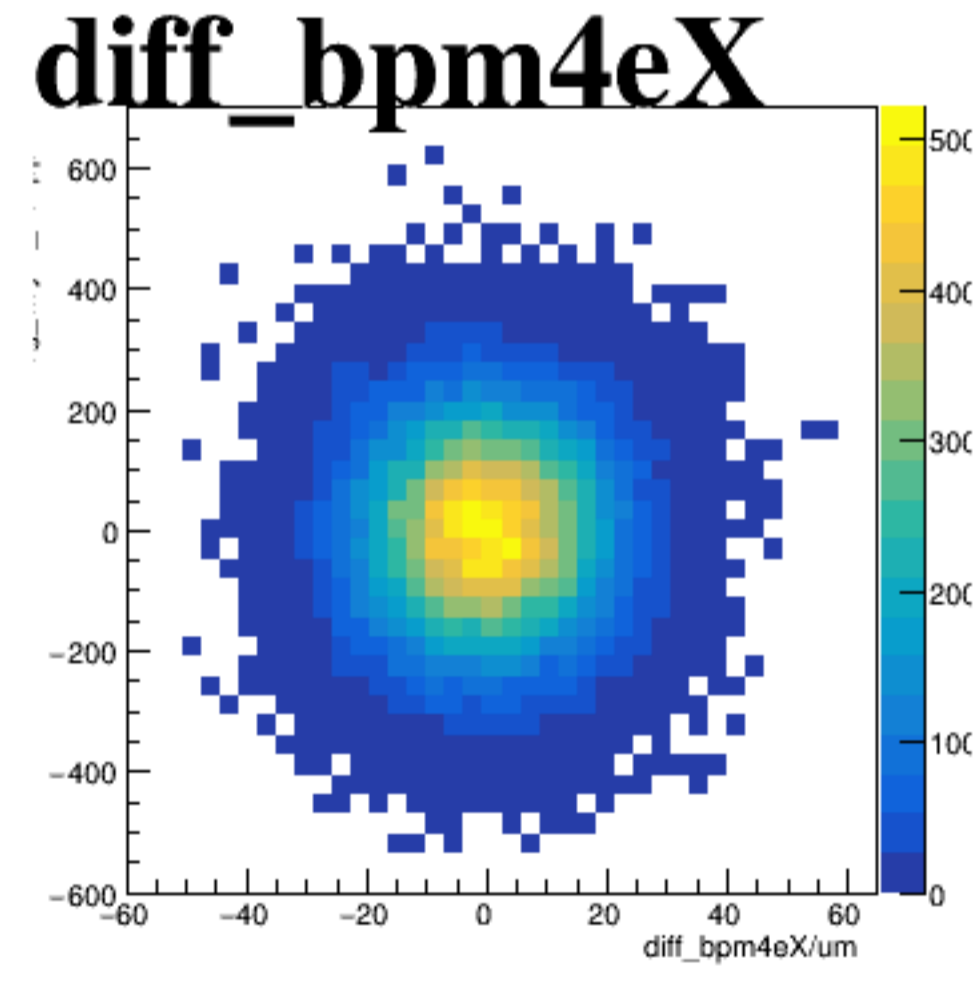
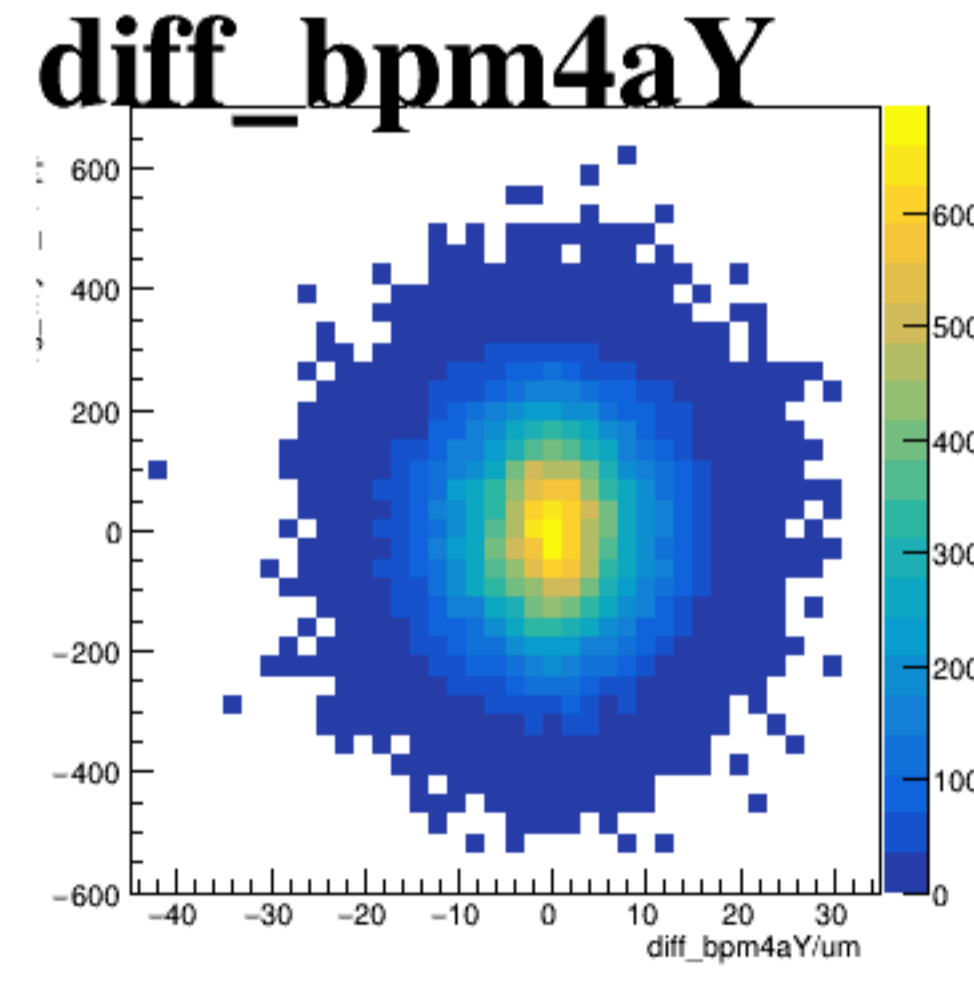
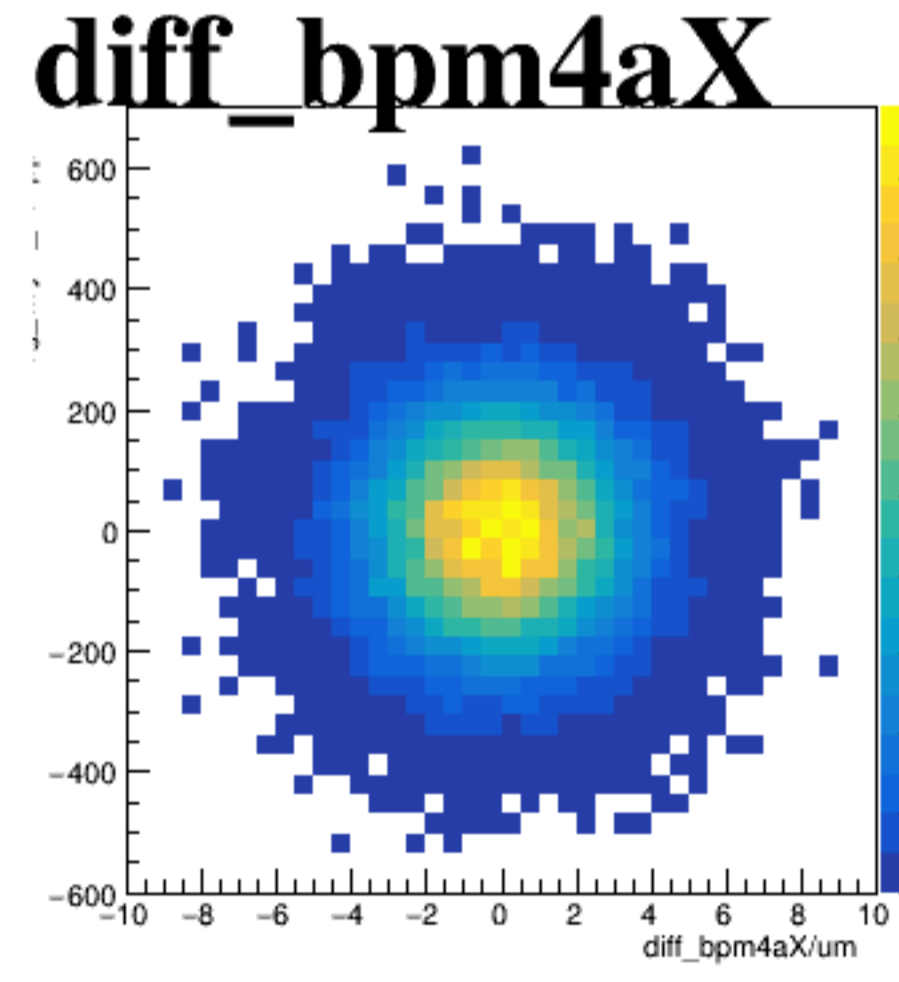
reg\_asym\_usl



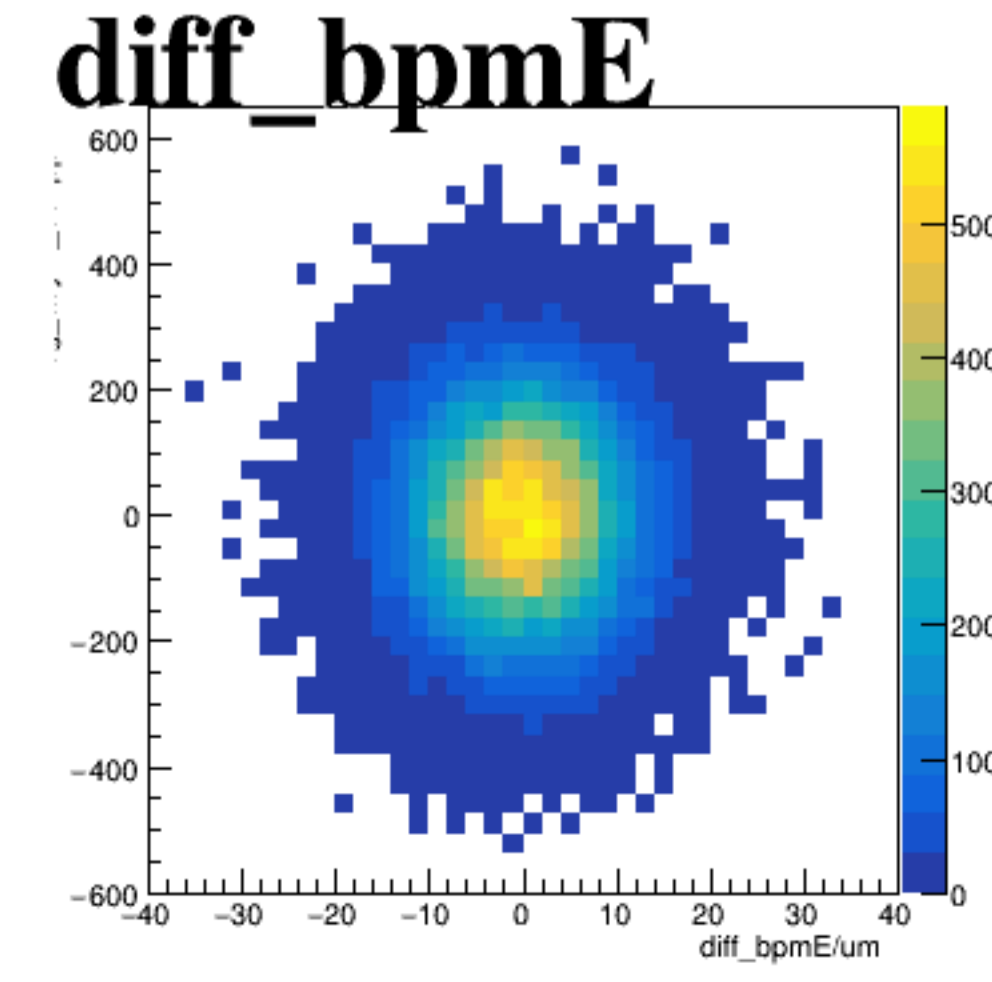
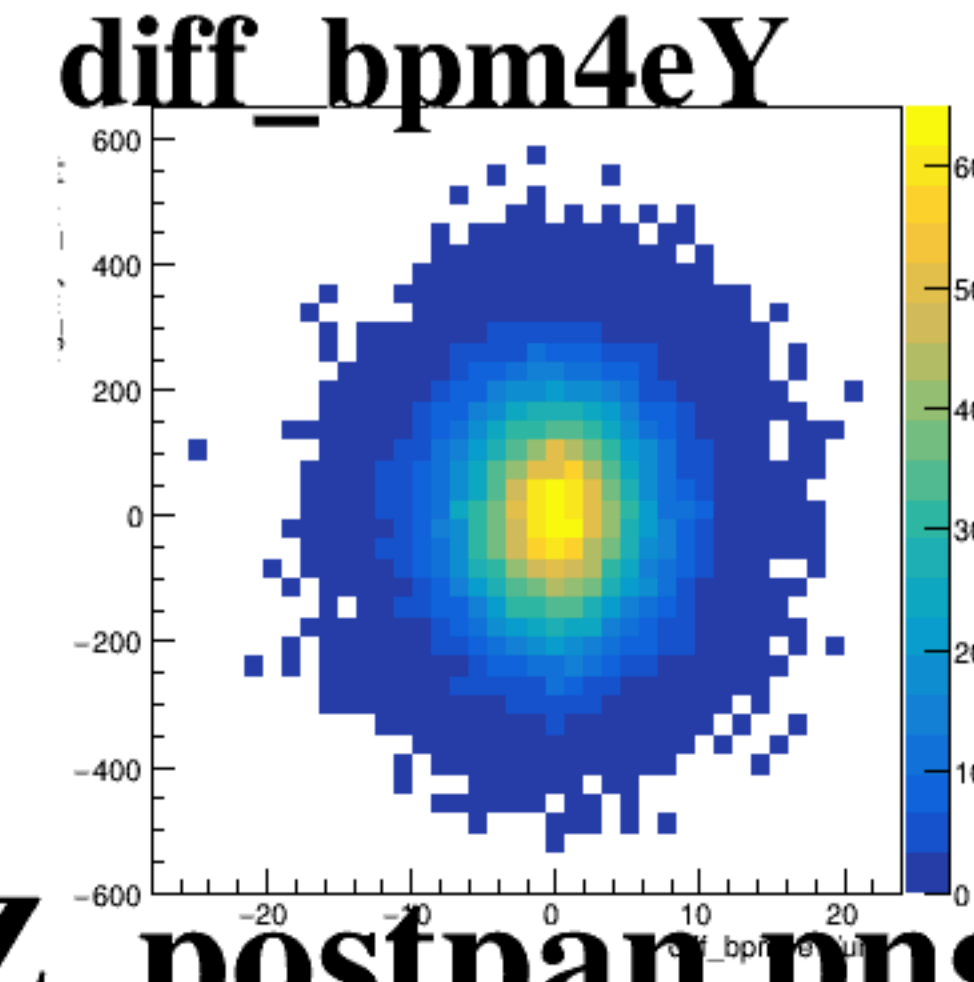
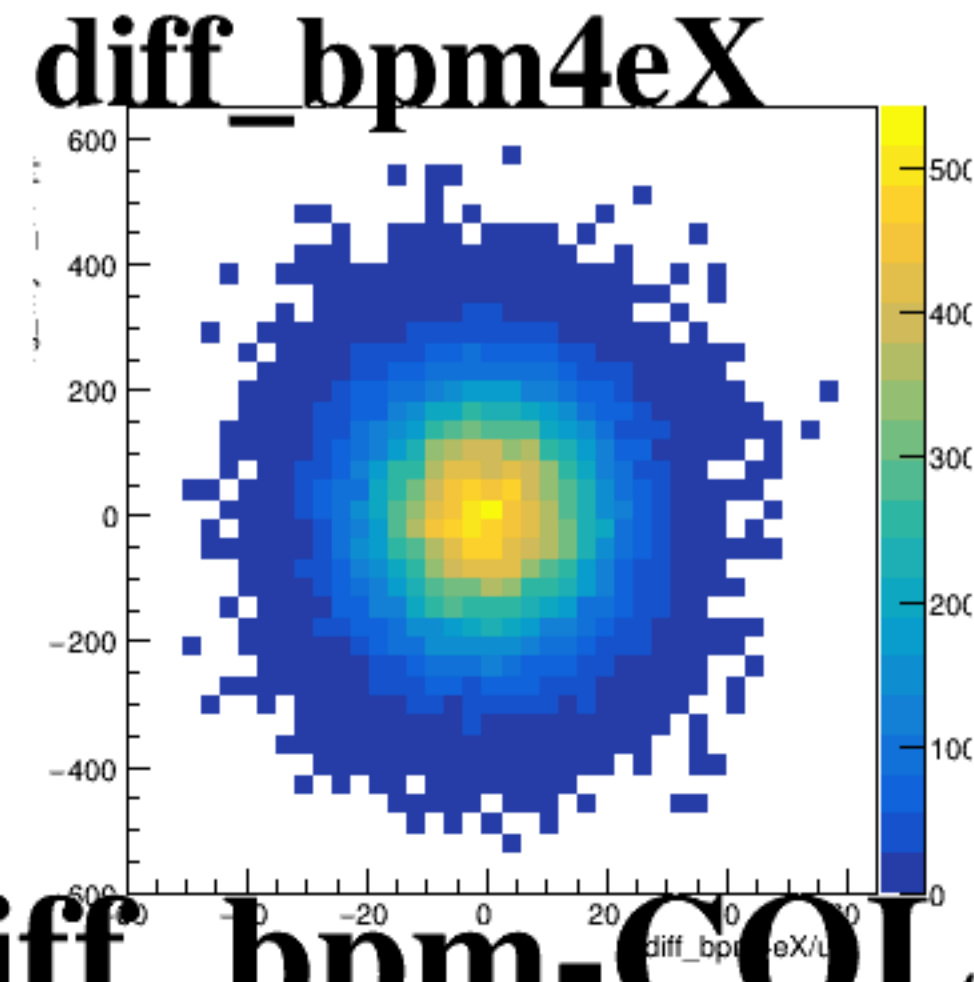
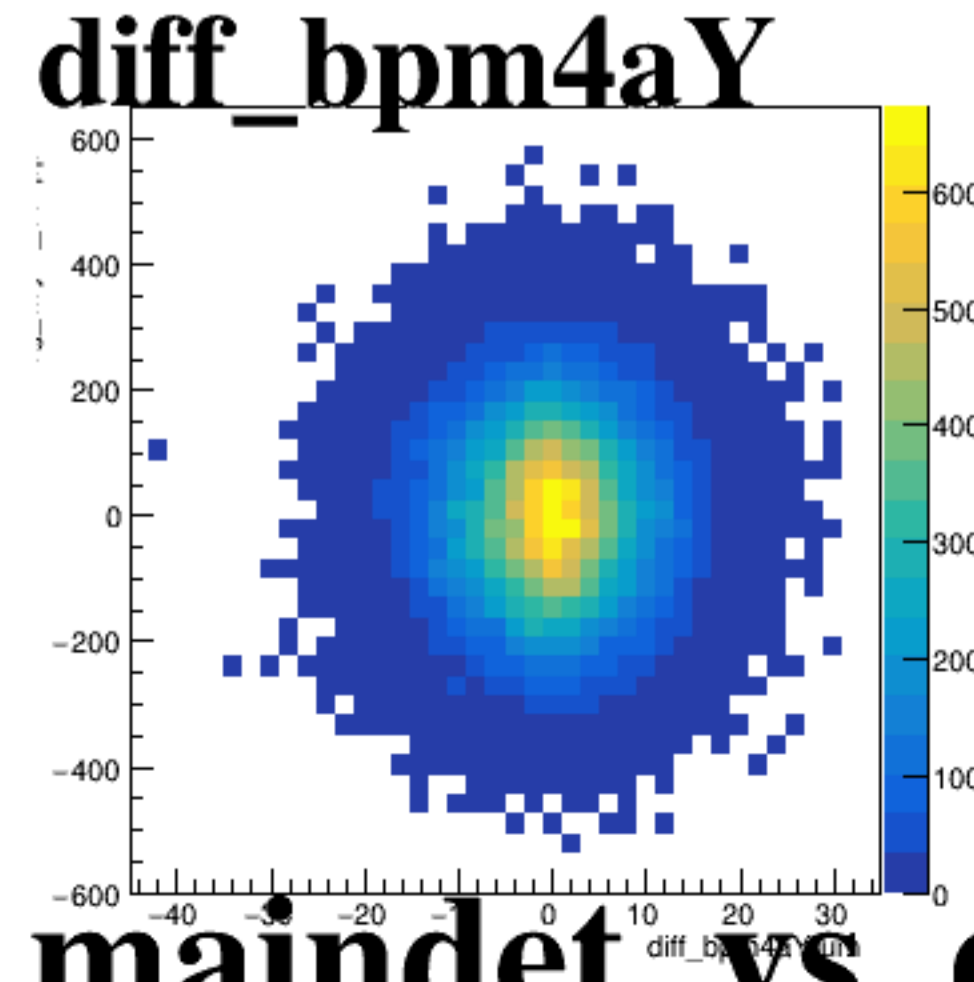
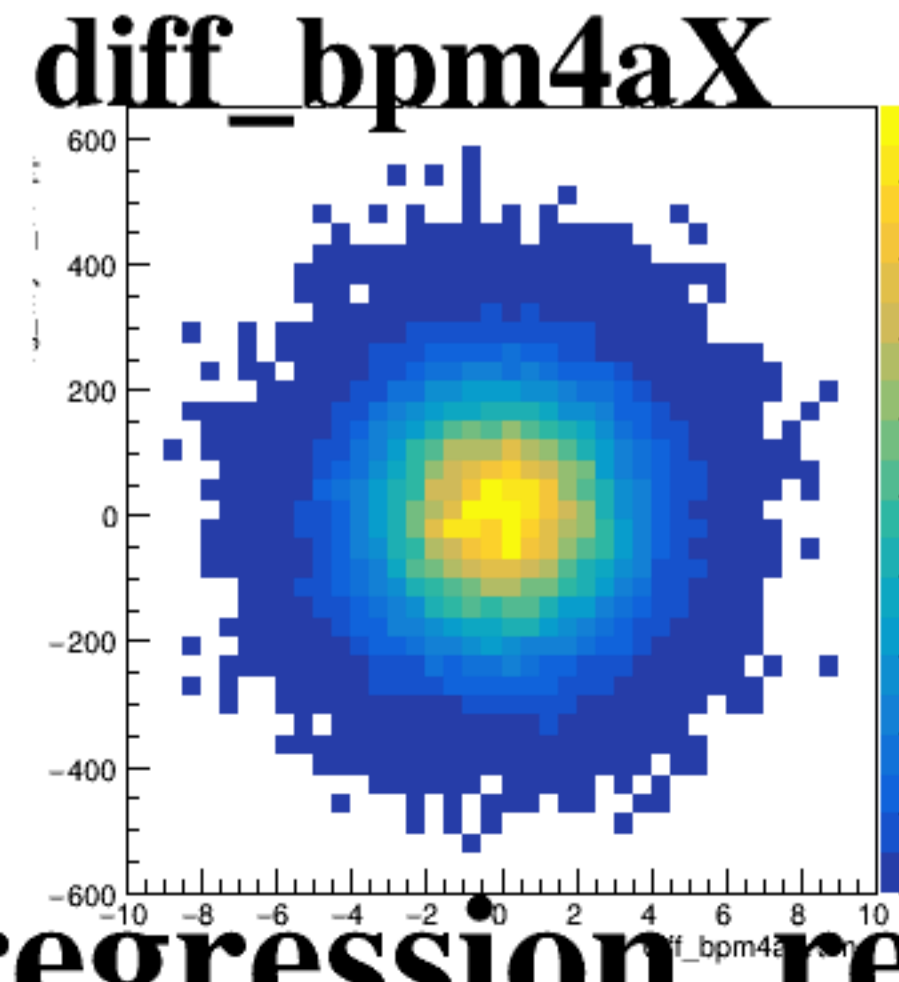
reg\_asym\_dsl



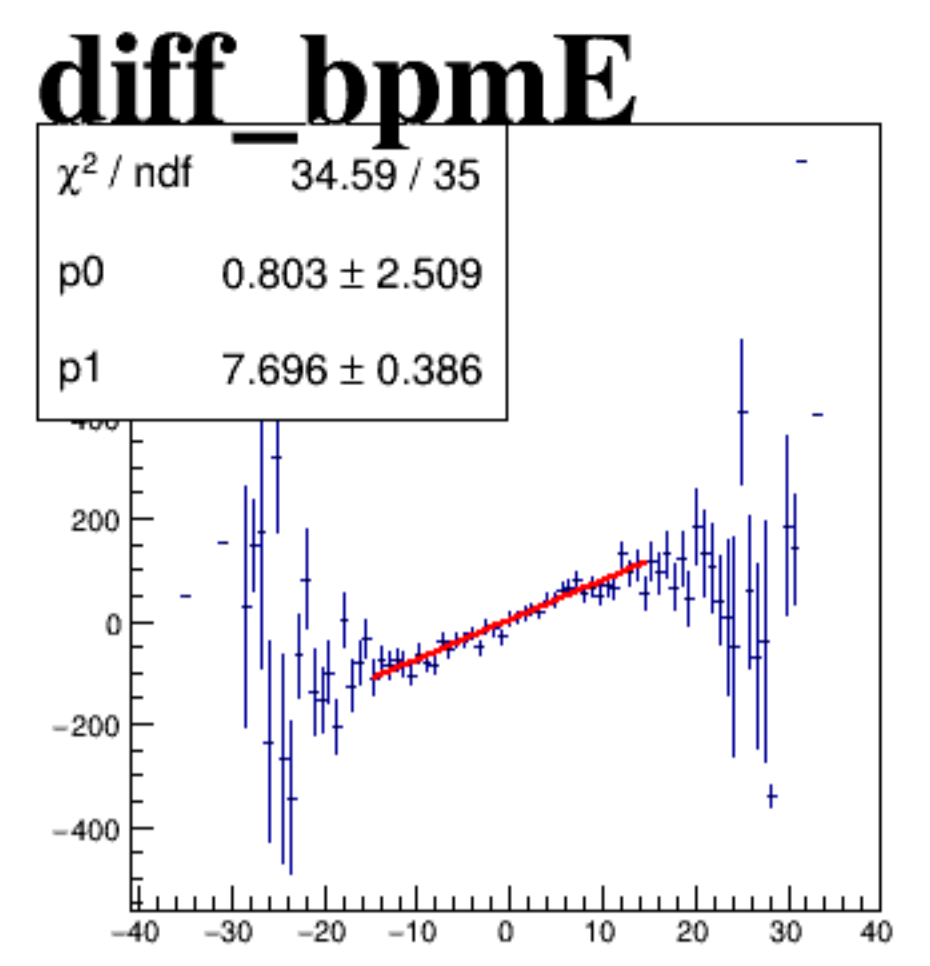
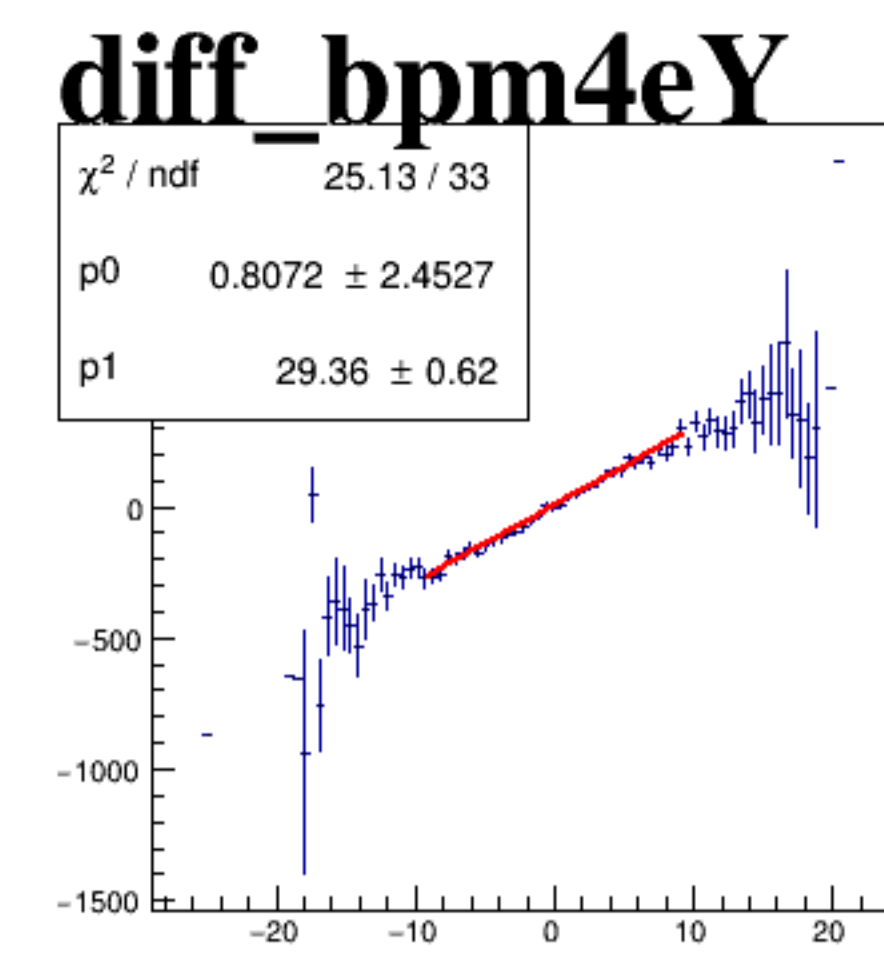
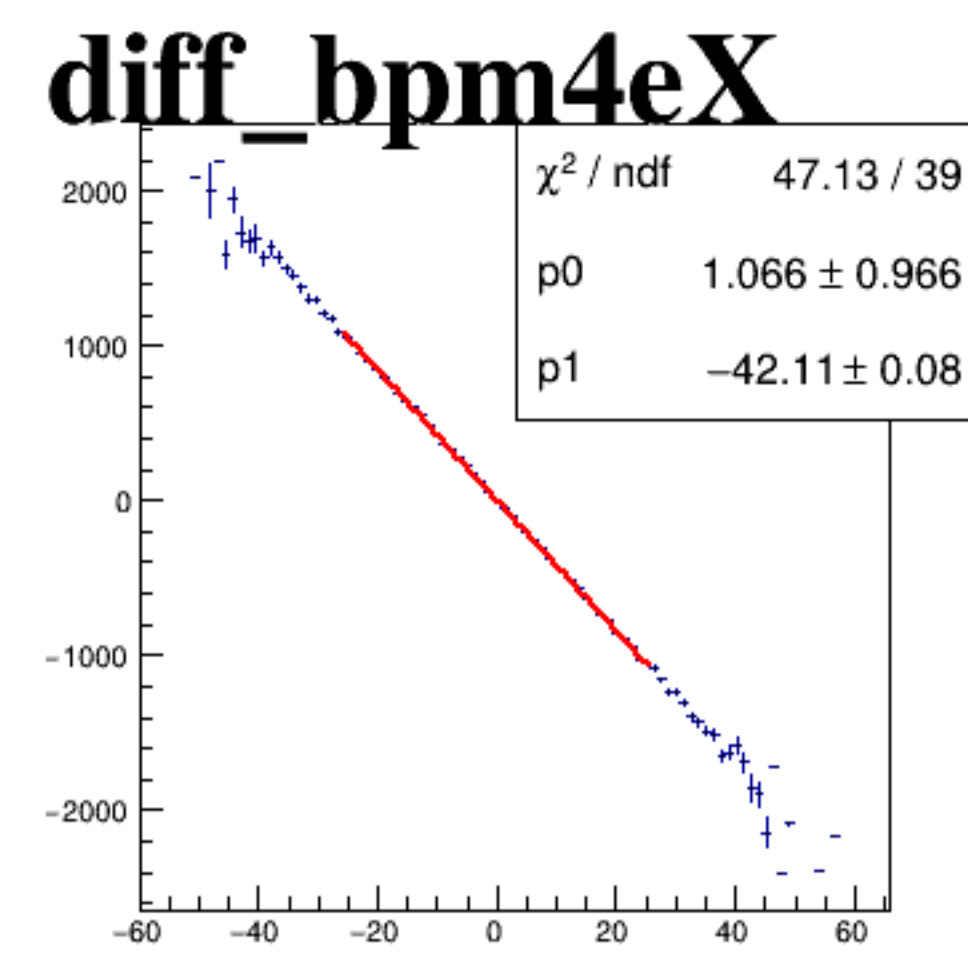
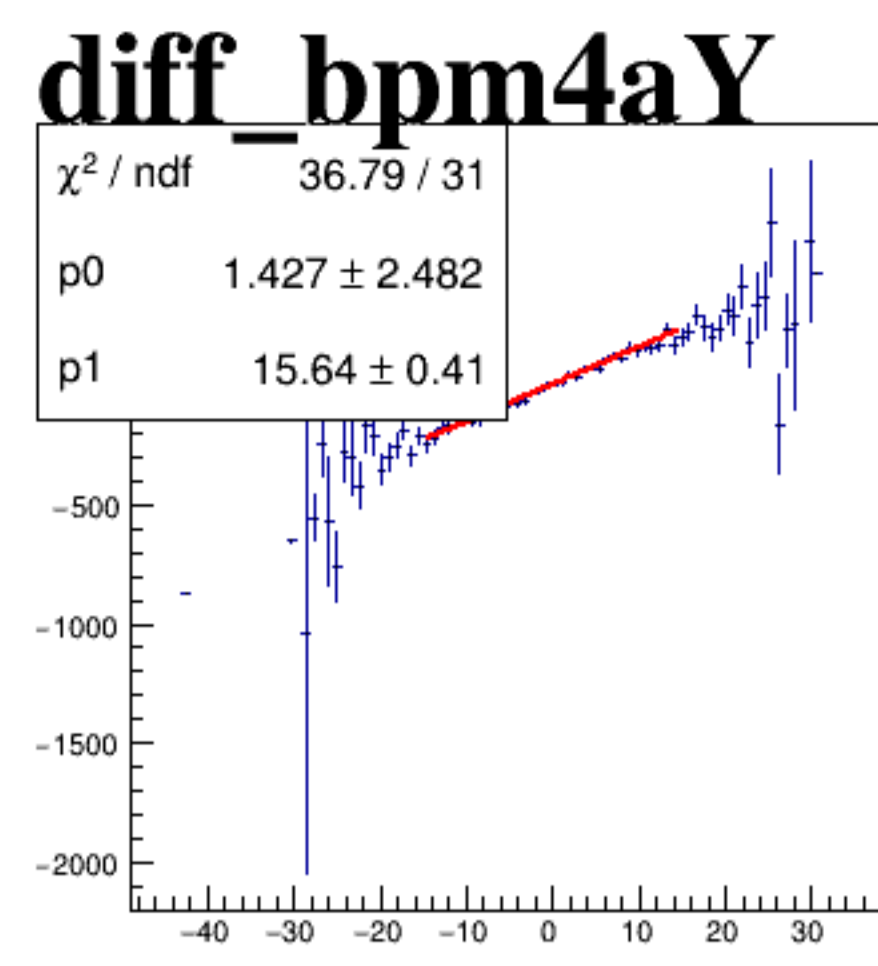
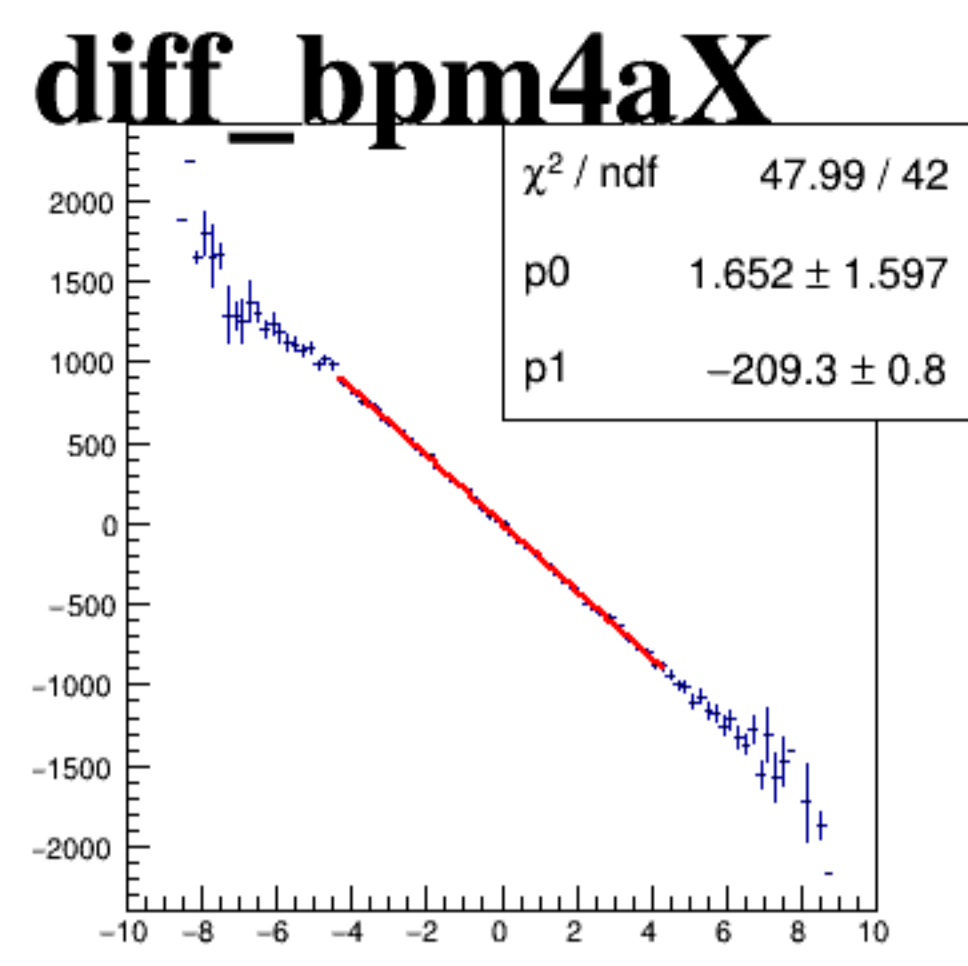
reg\_asym\_usr



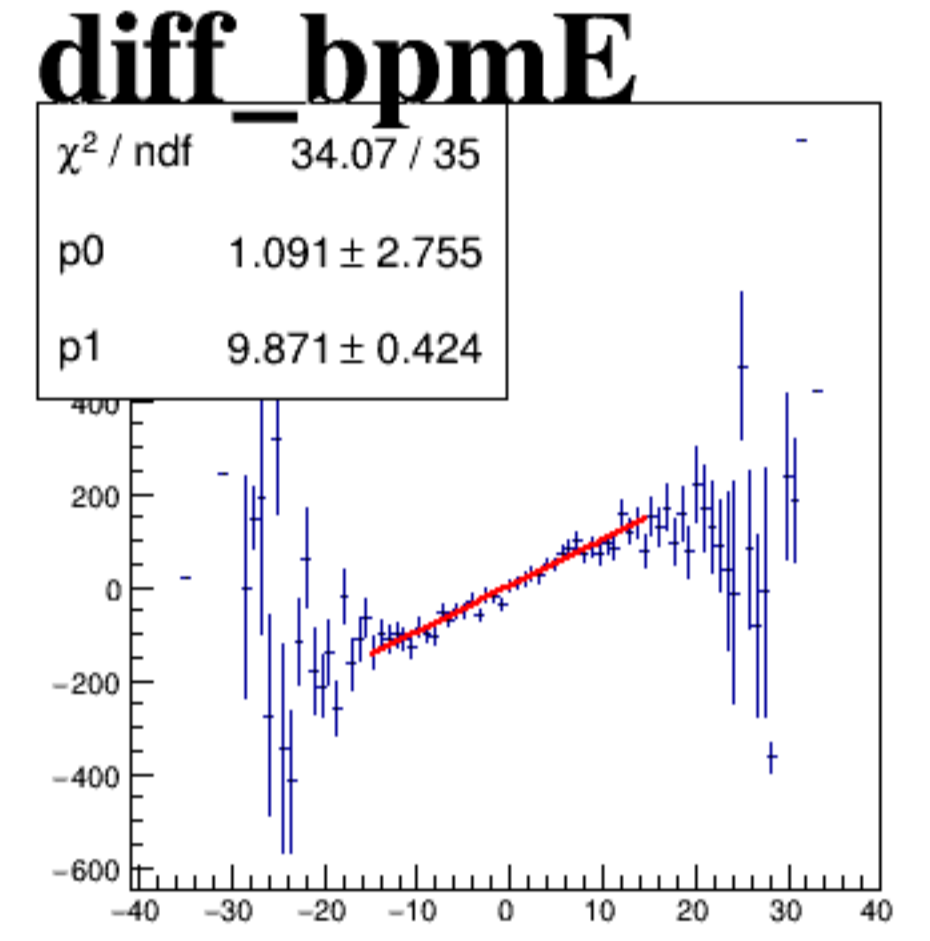
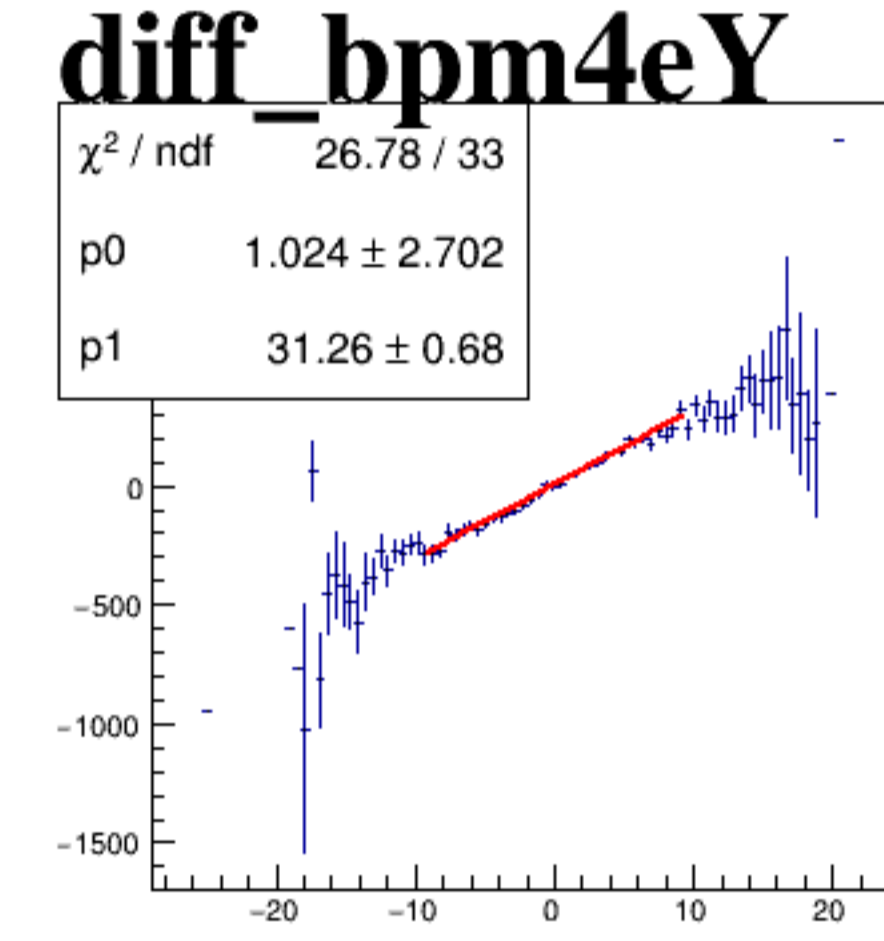
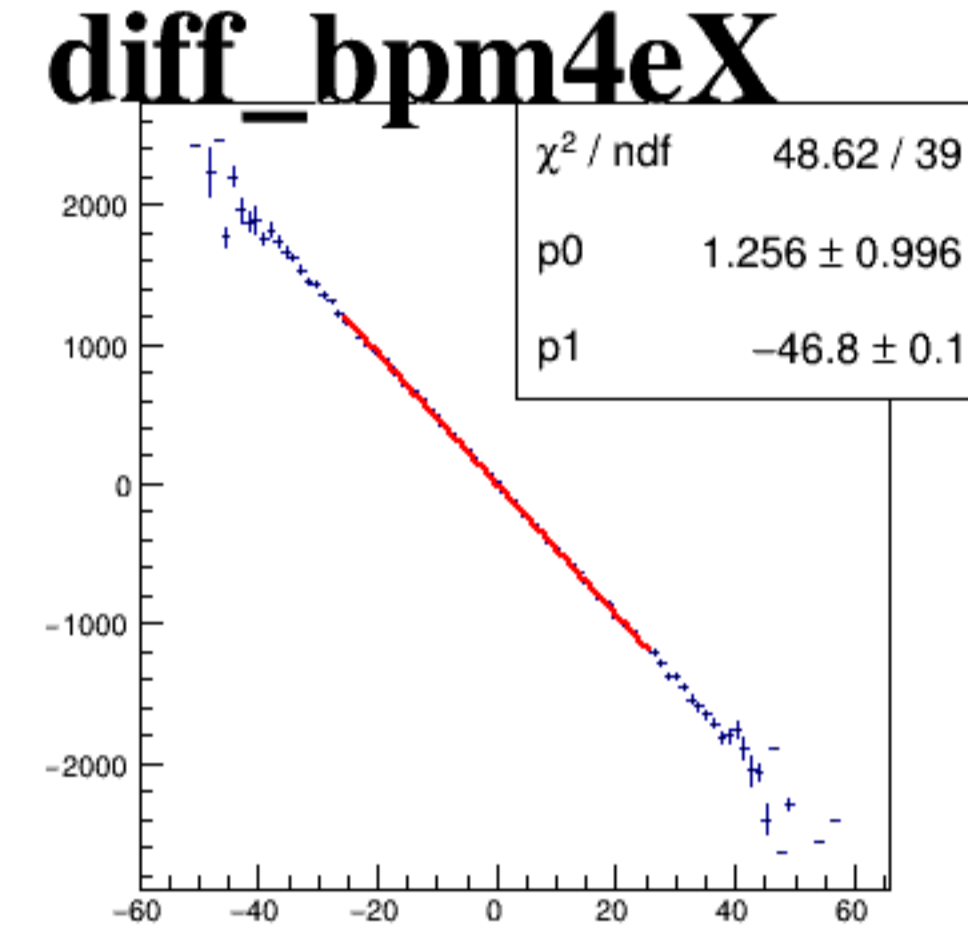
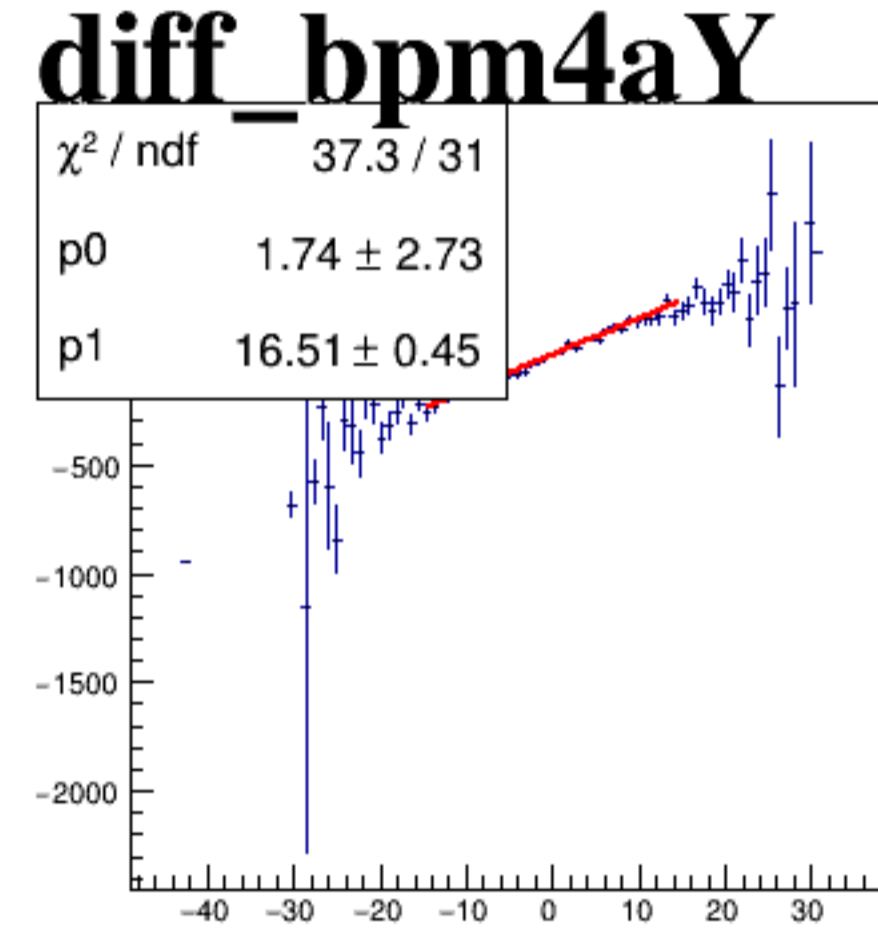
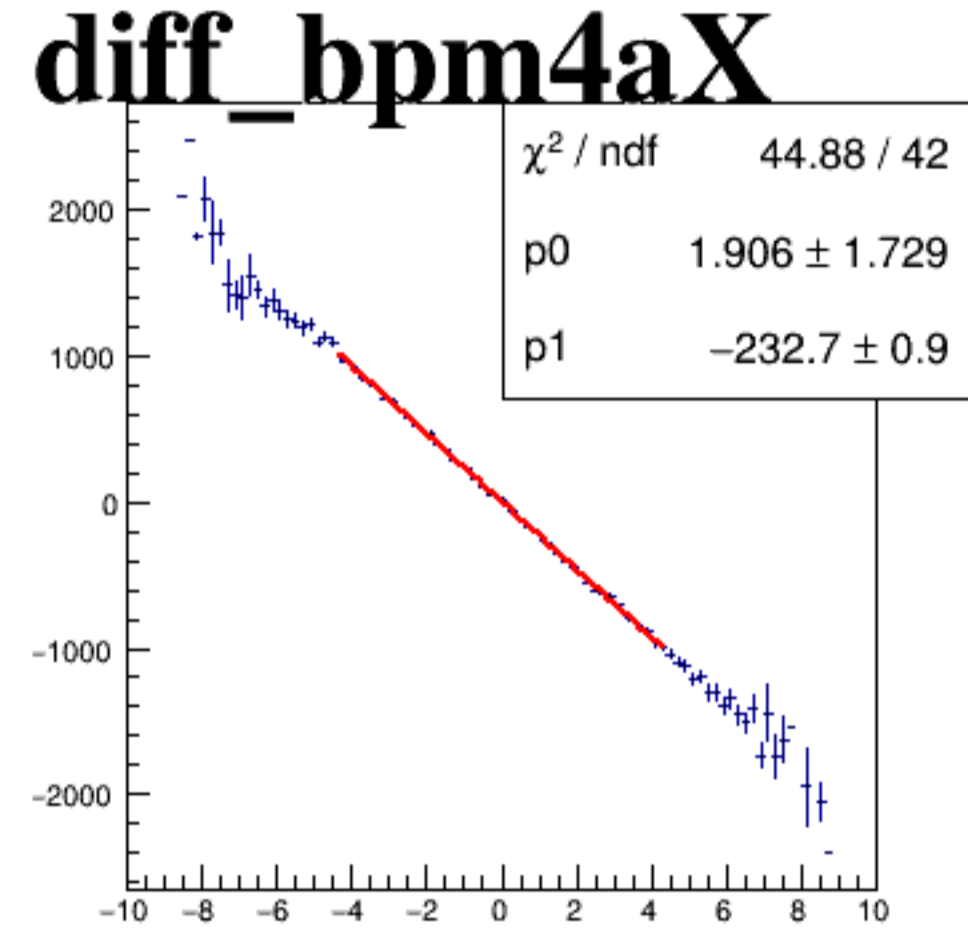
reg\_asym\_dsr



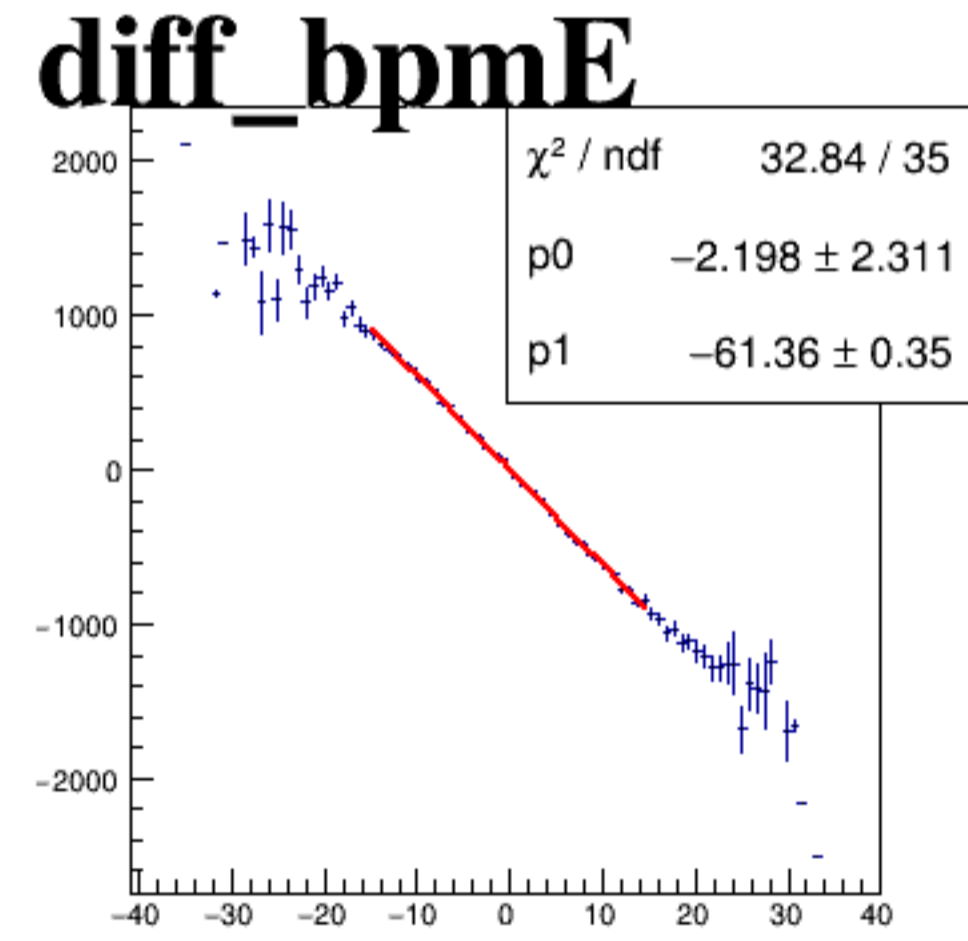
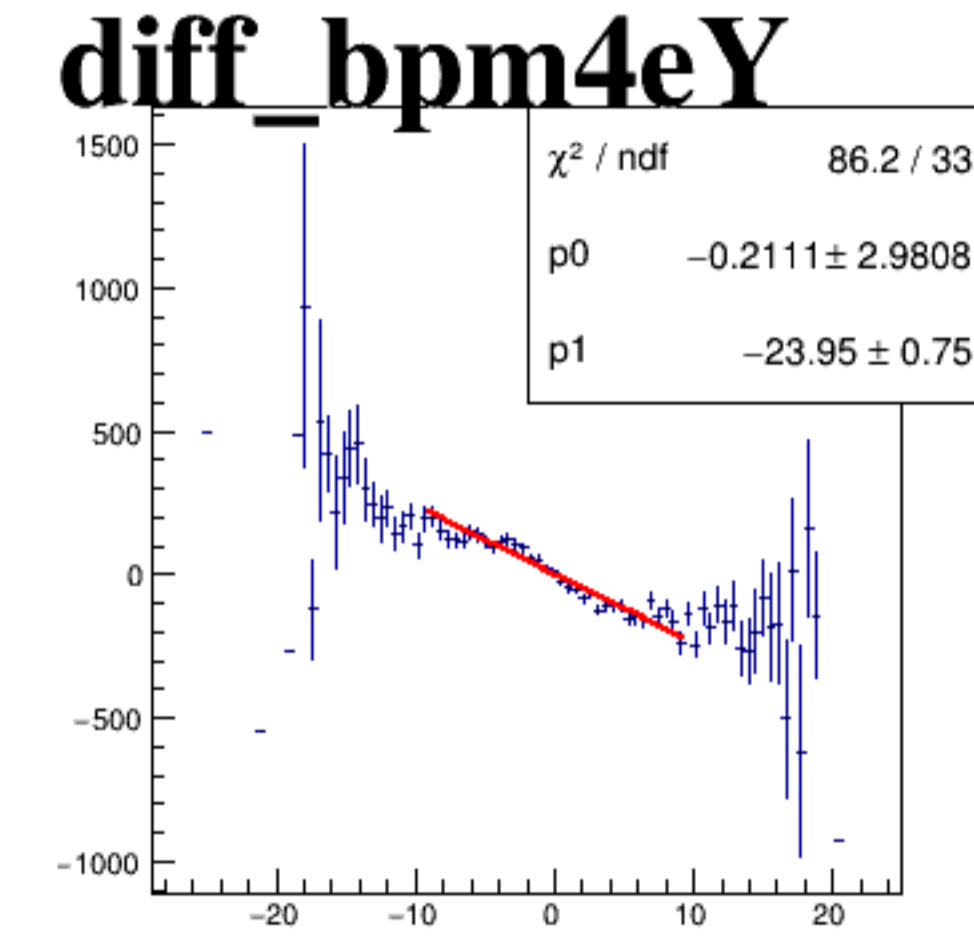
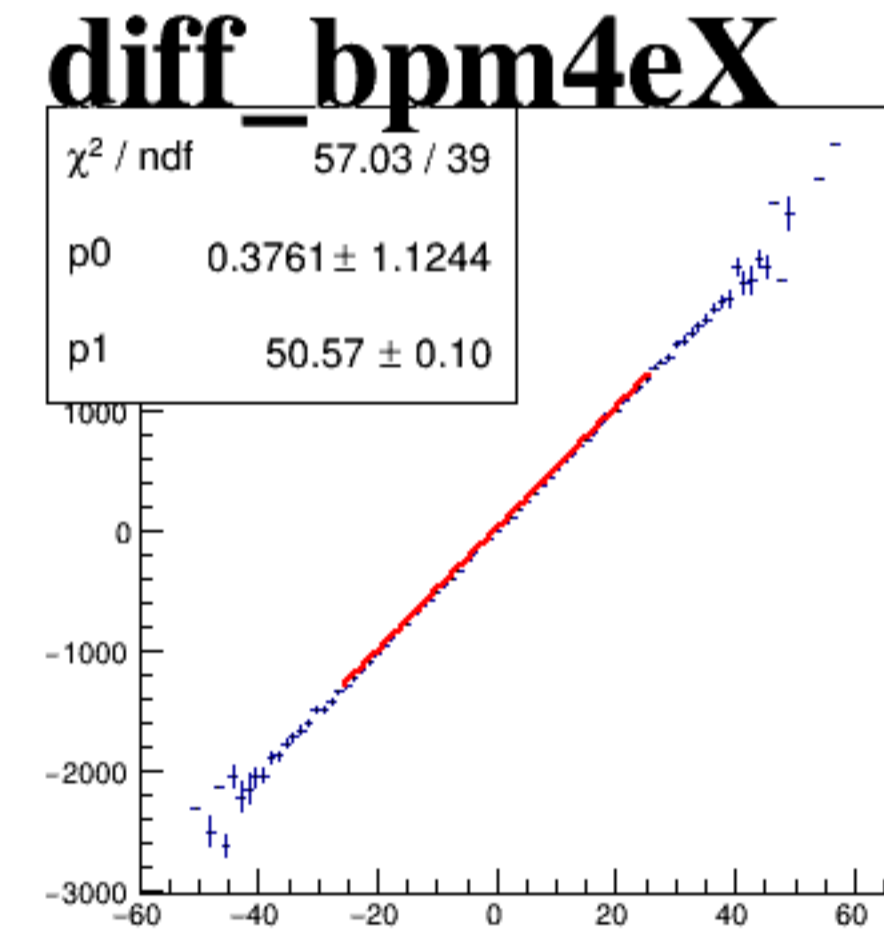
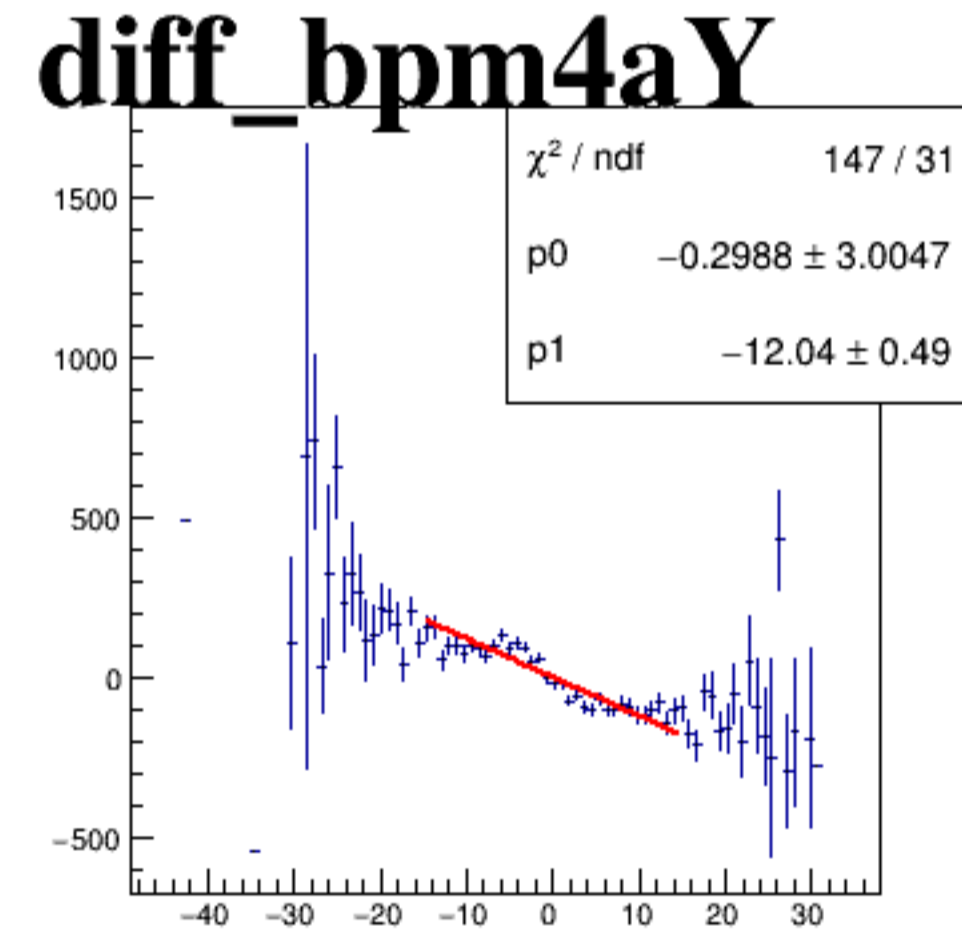
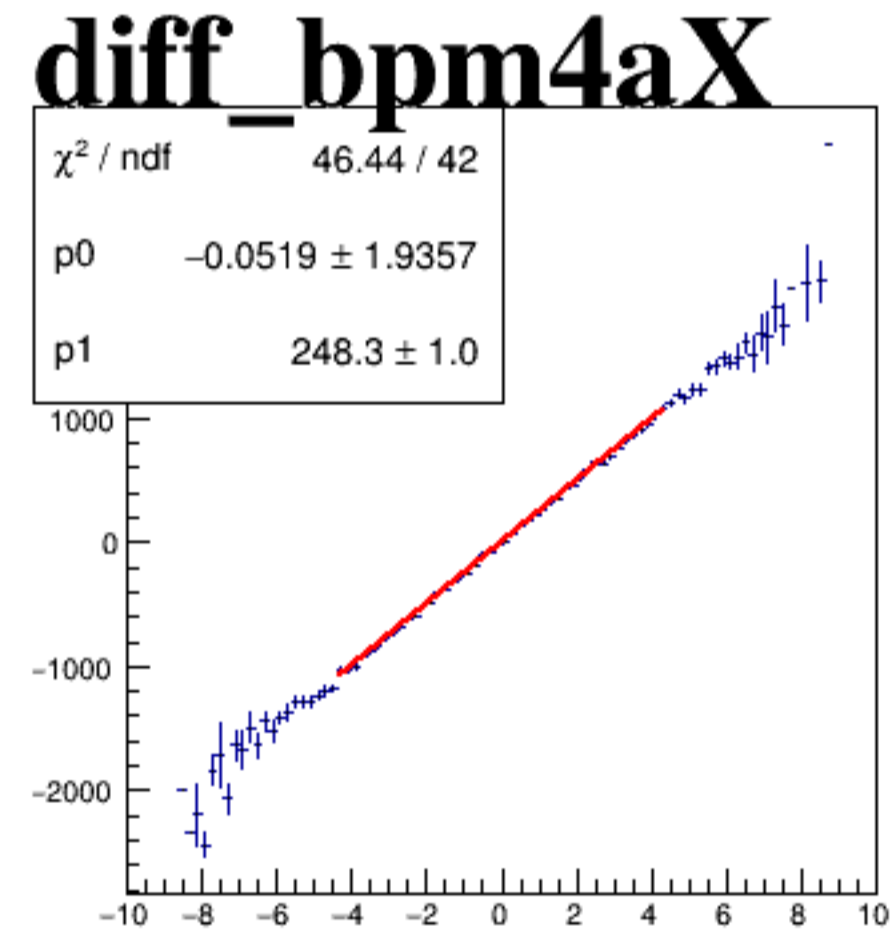
asym\_usl



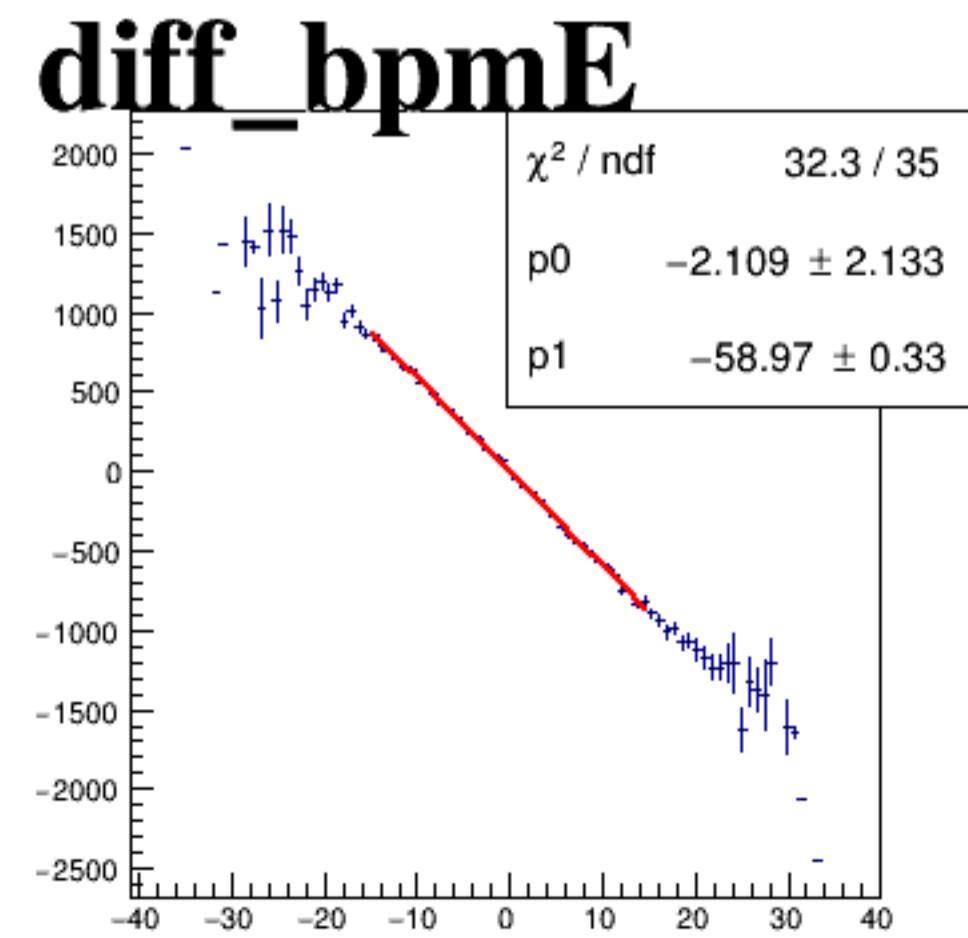
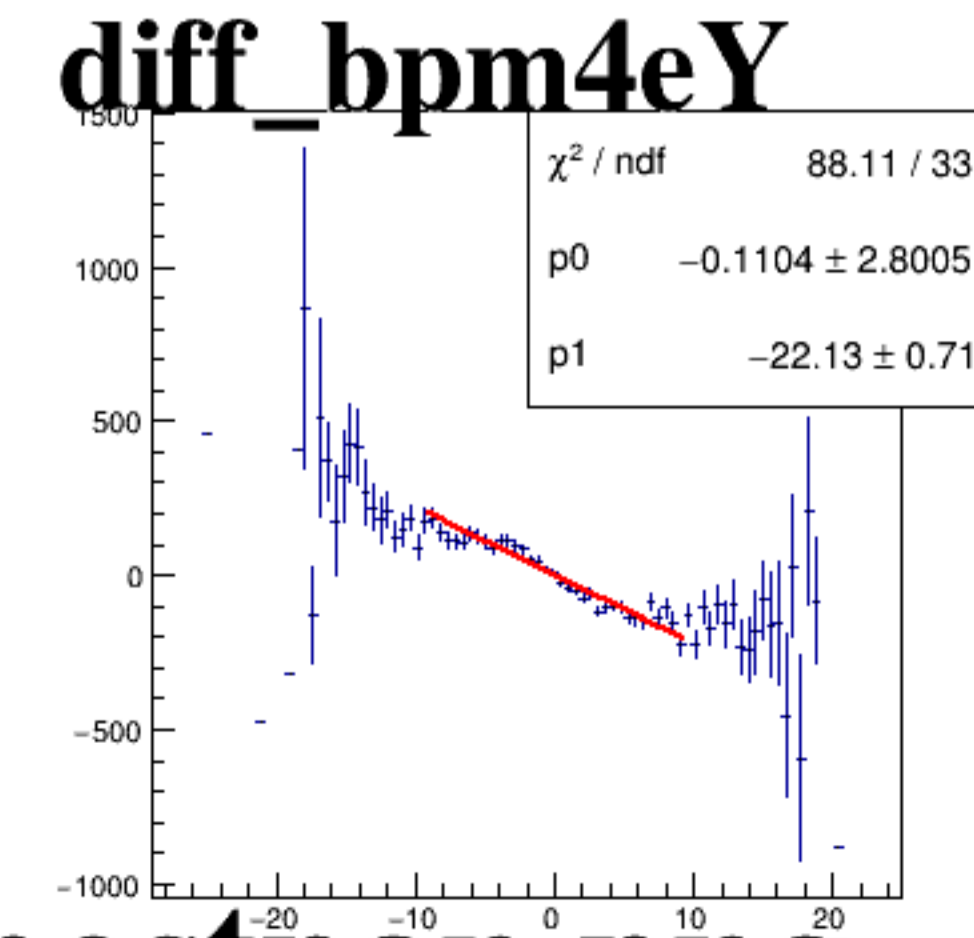
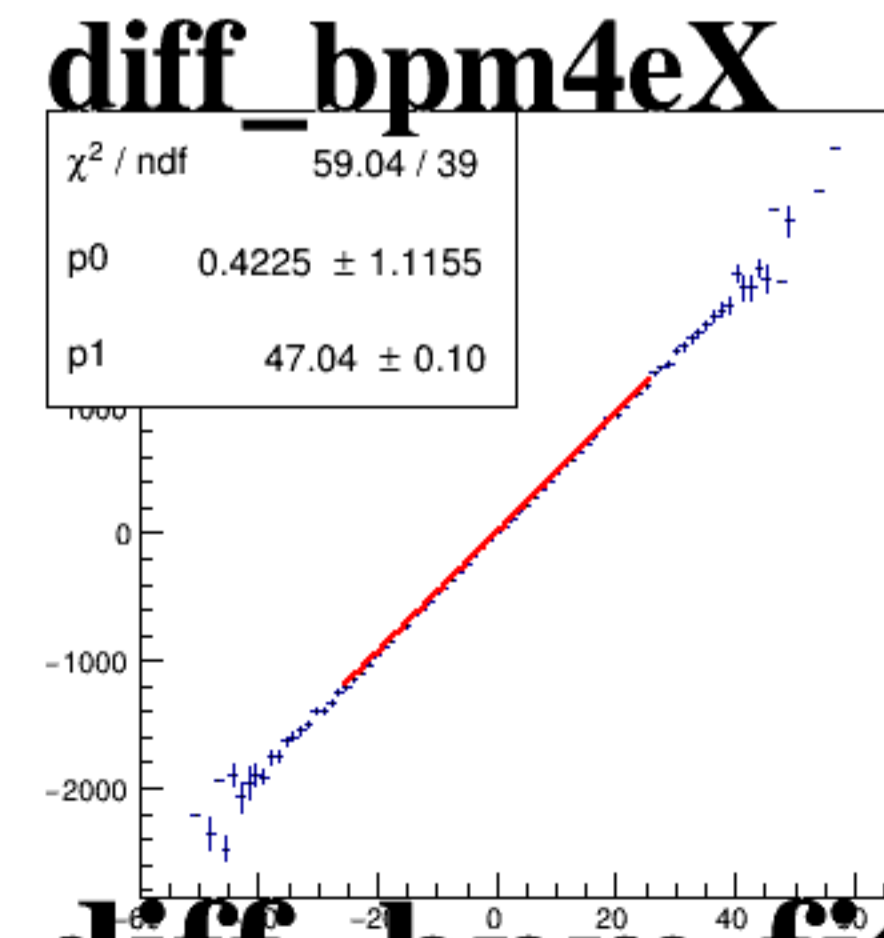
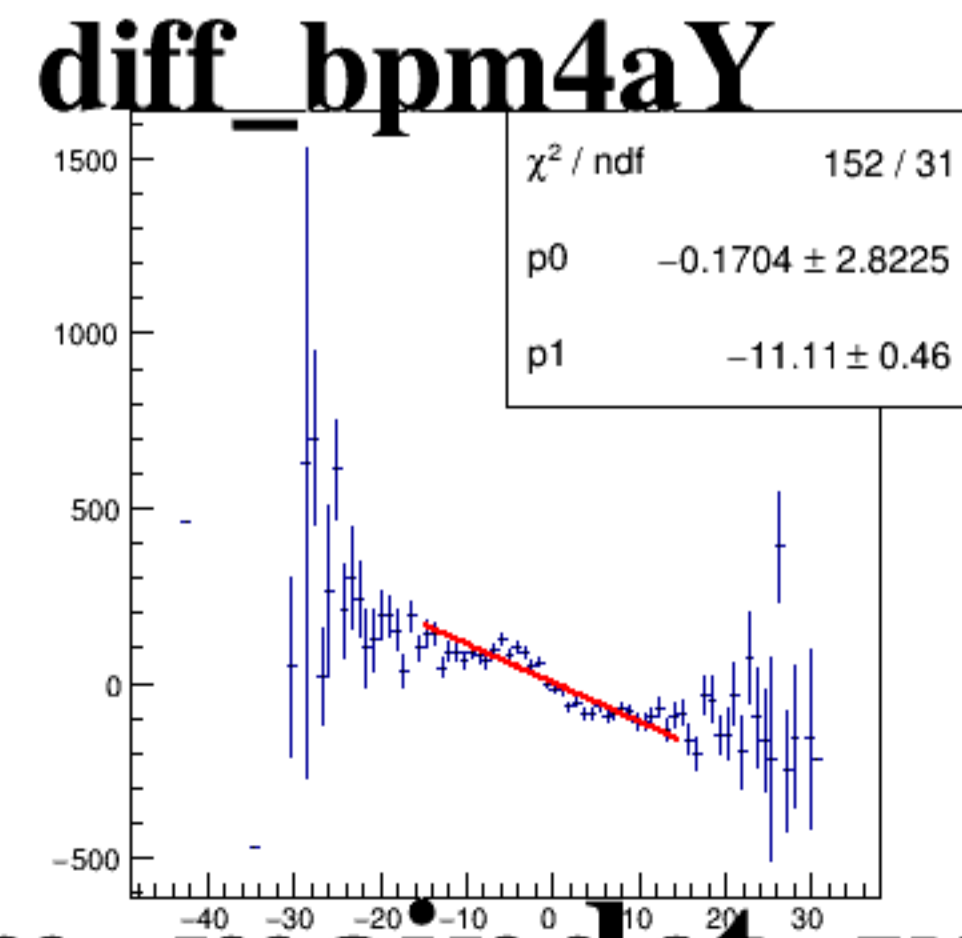
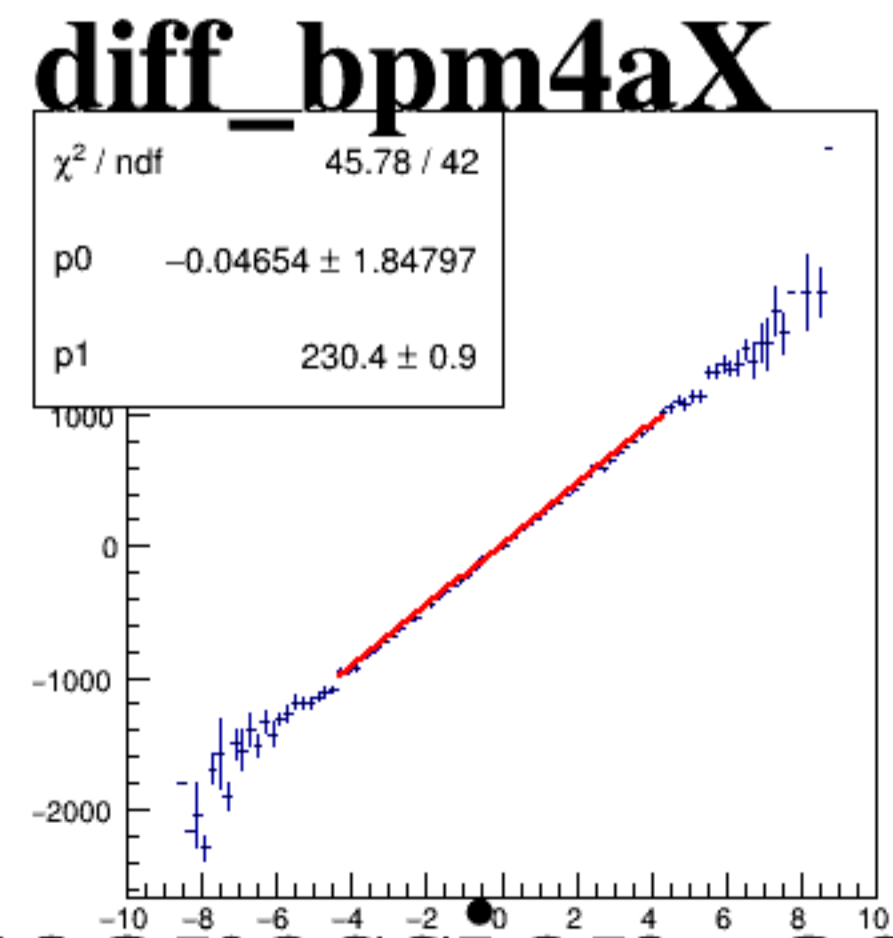
asym\_dsl



asym\_usr

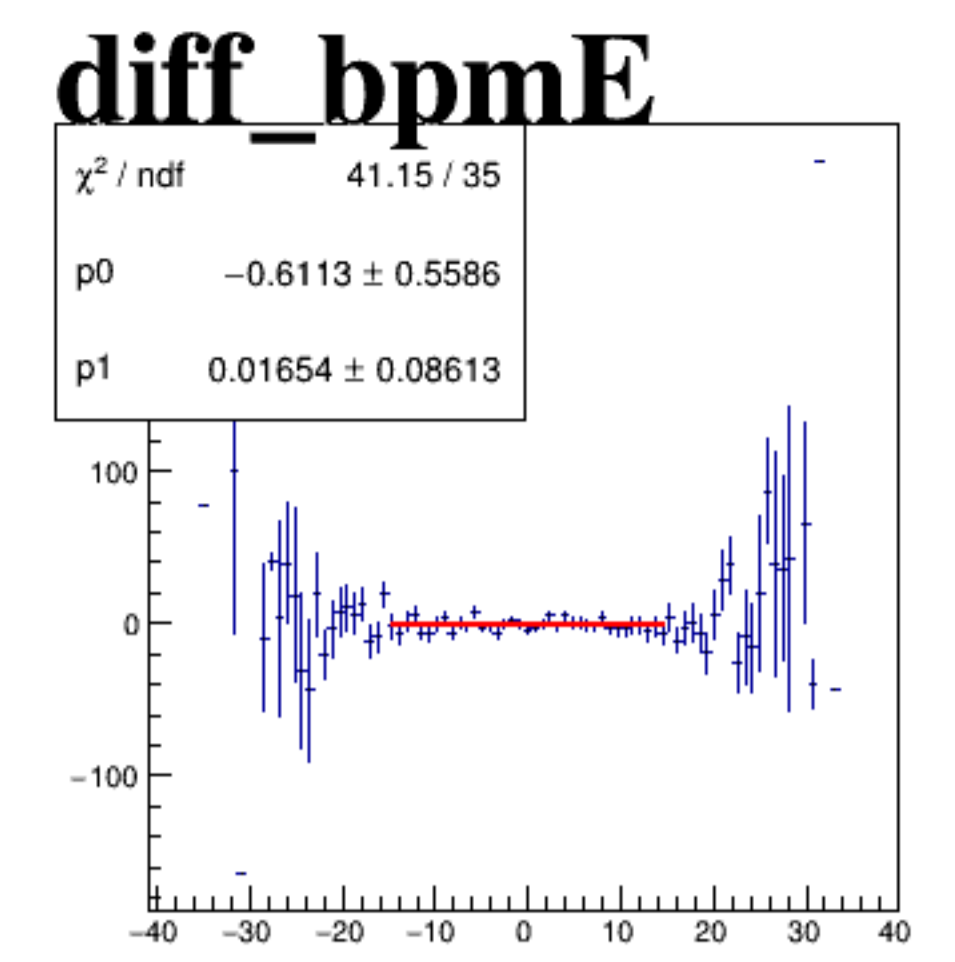
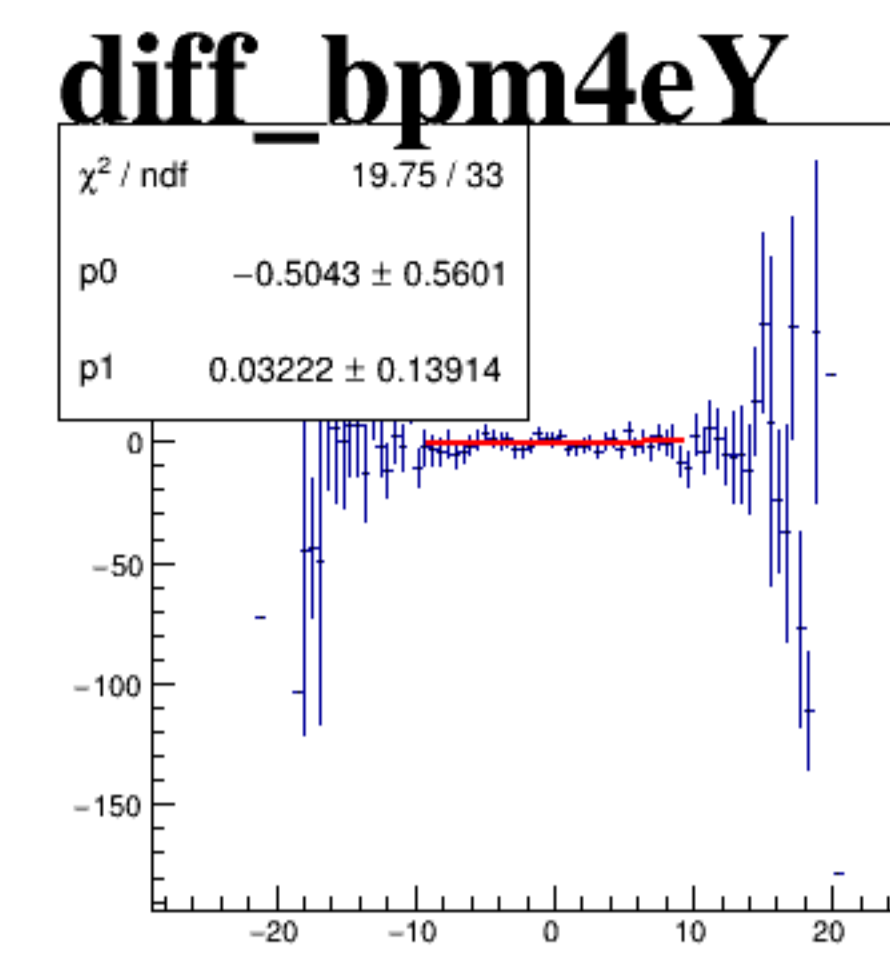
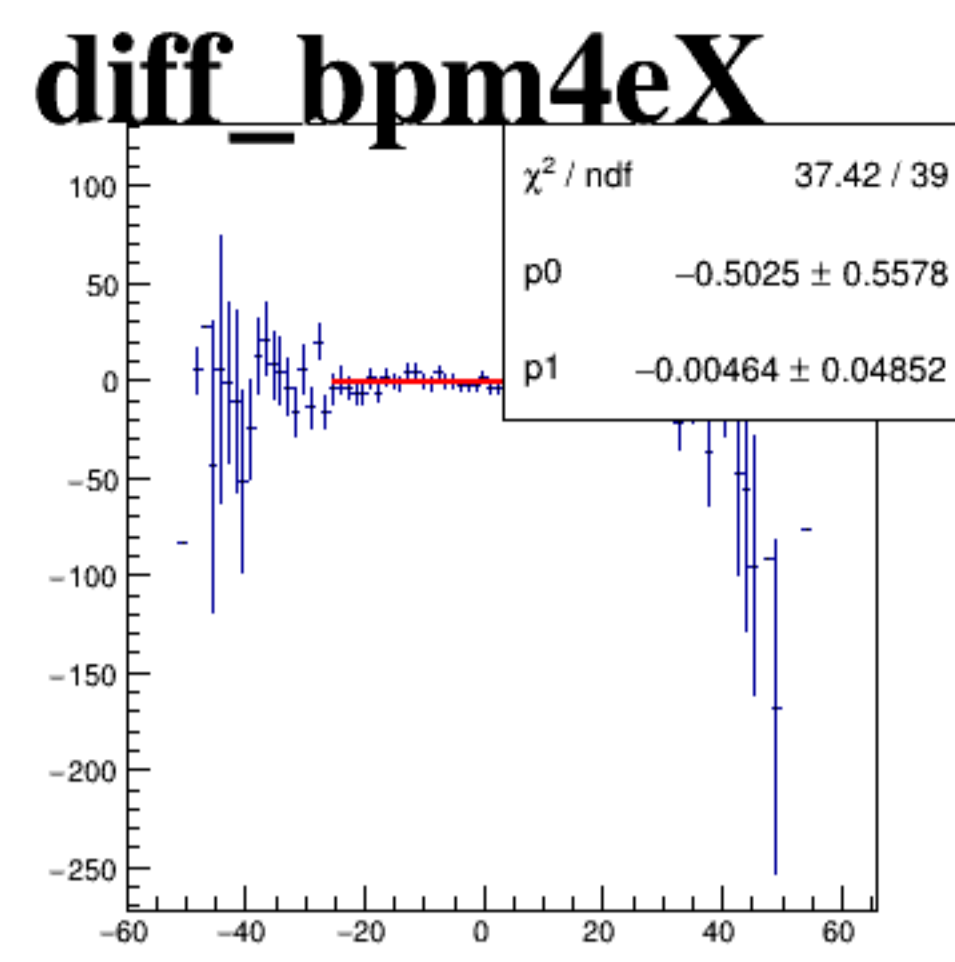
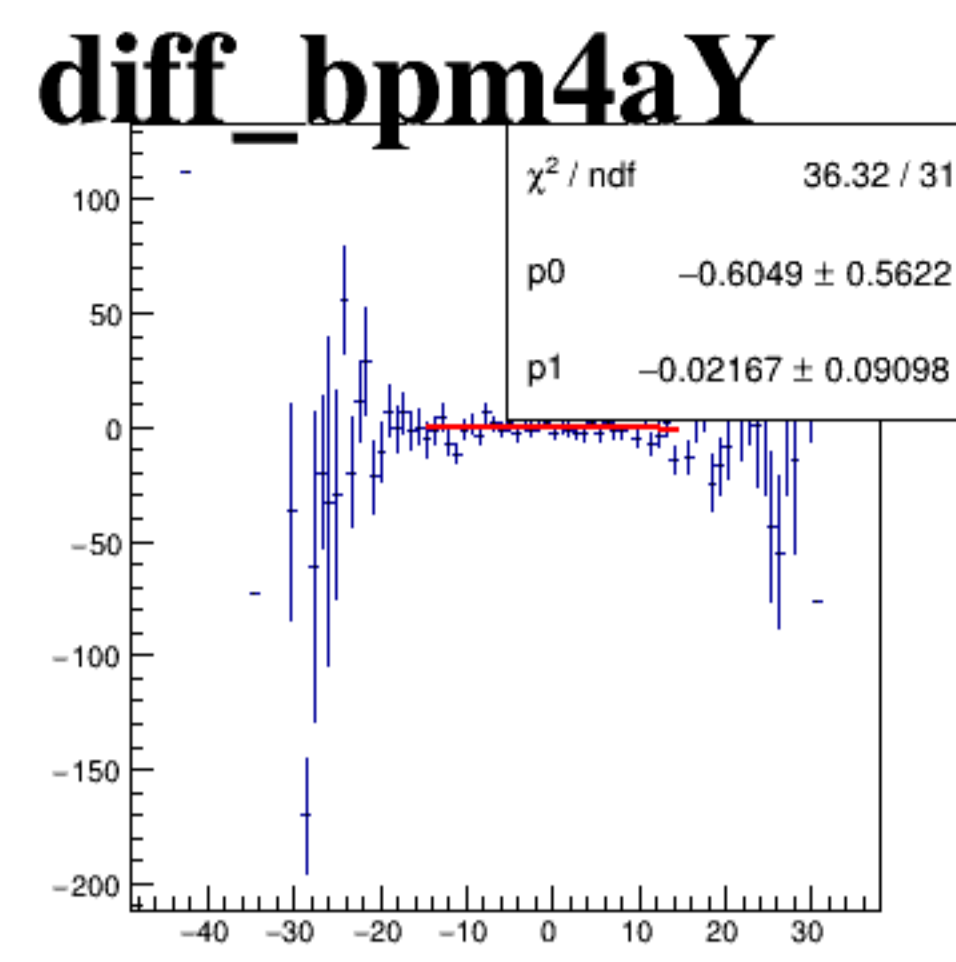
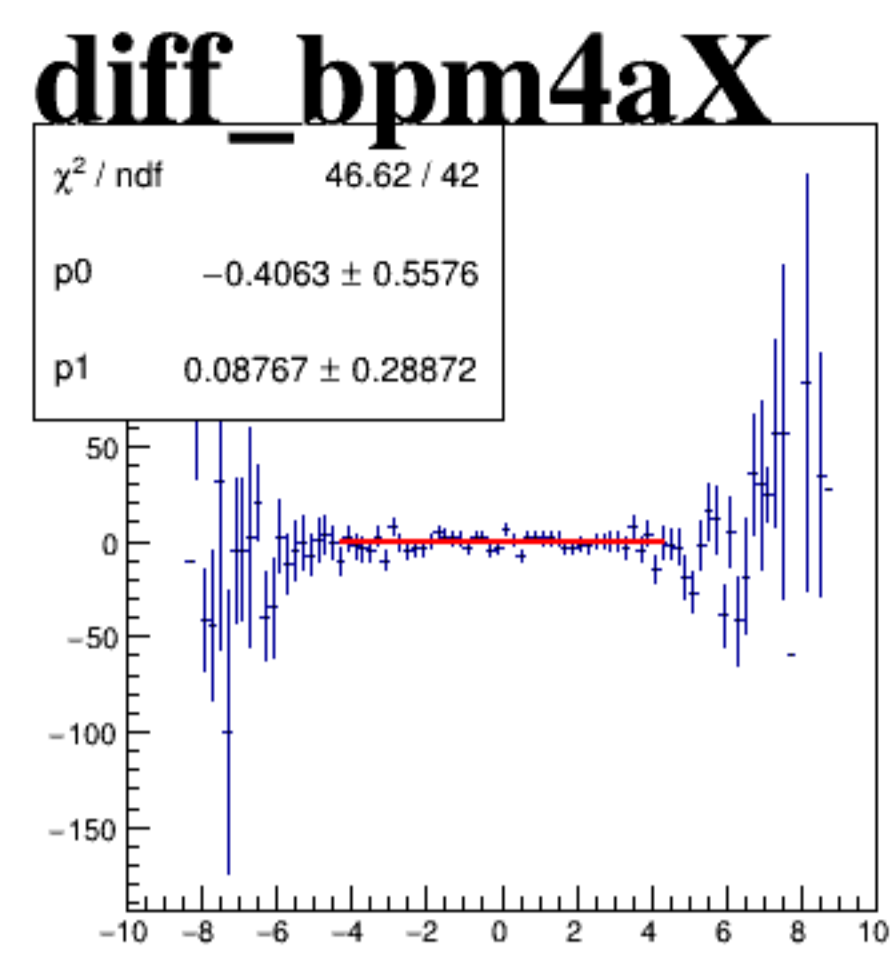


asym\_dsr

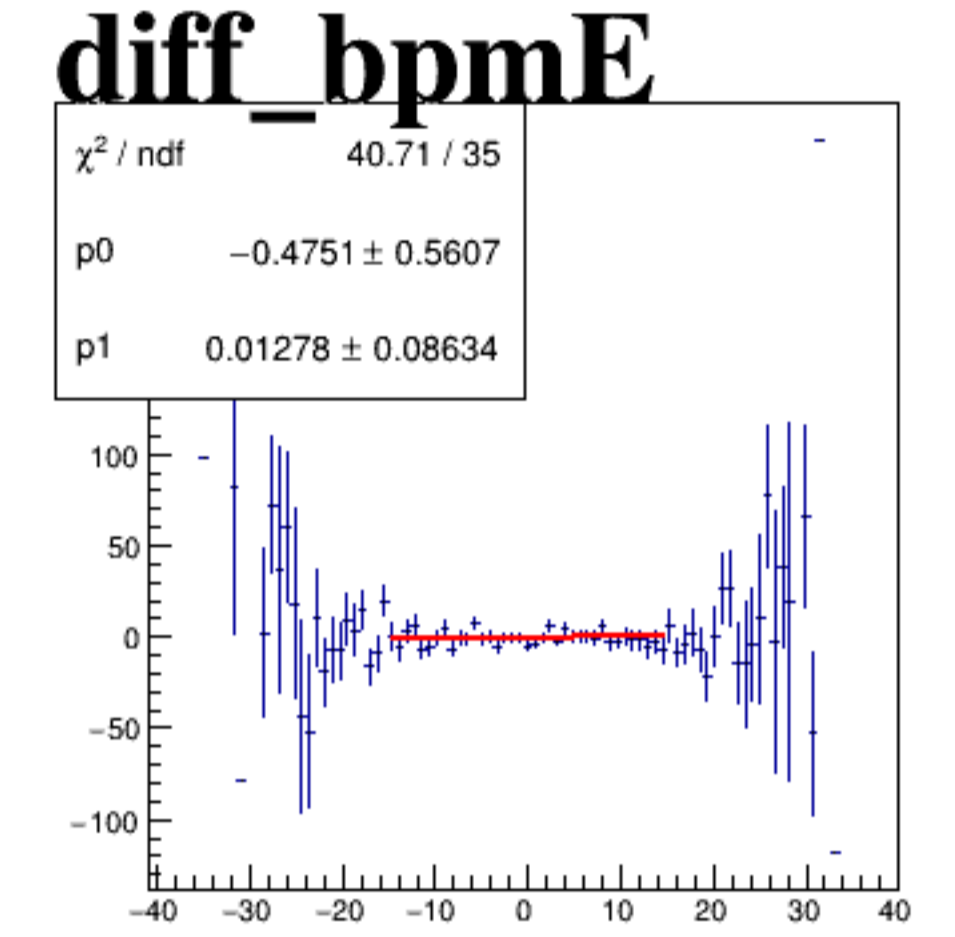
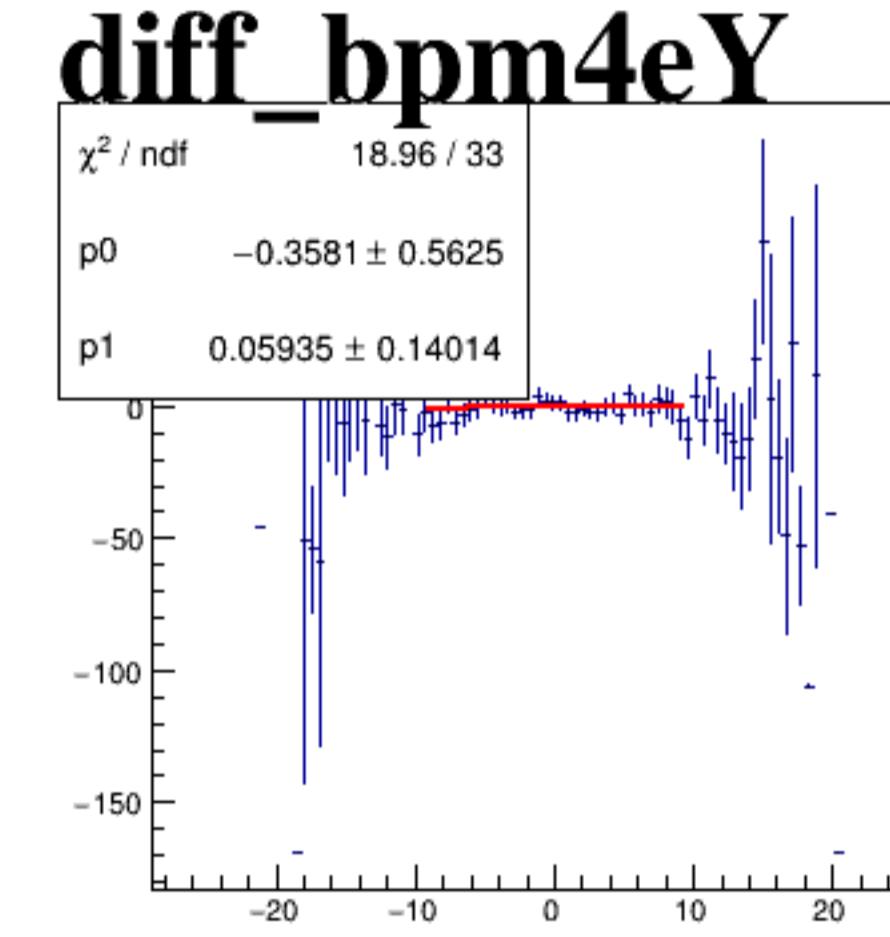
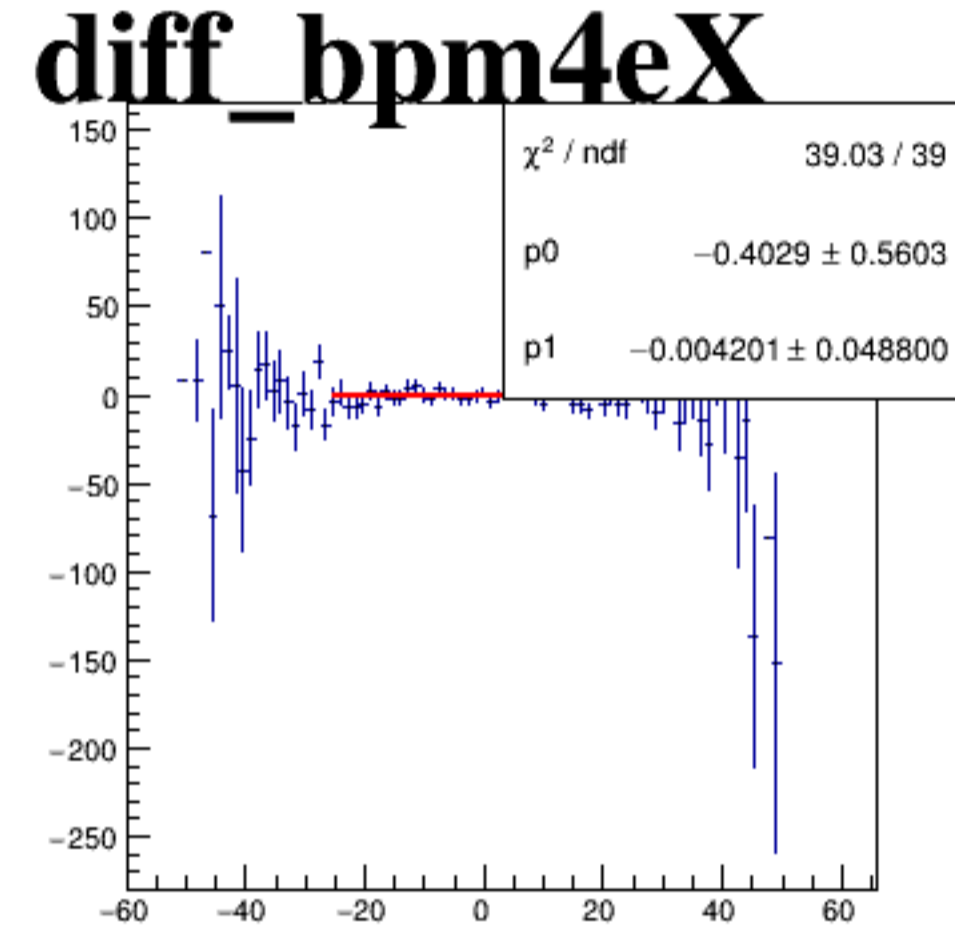
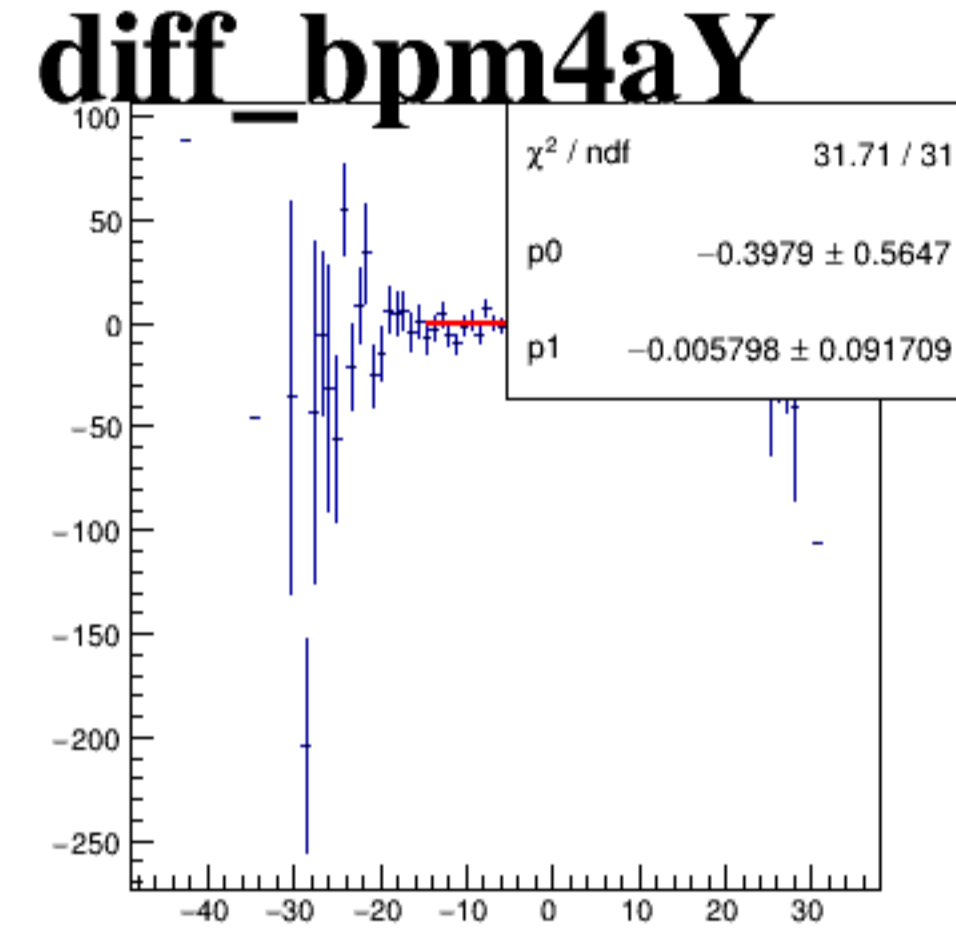
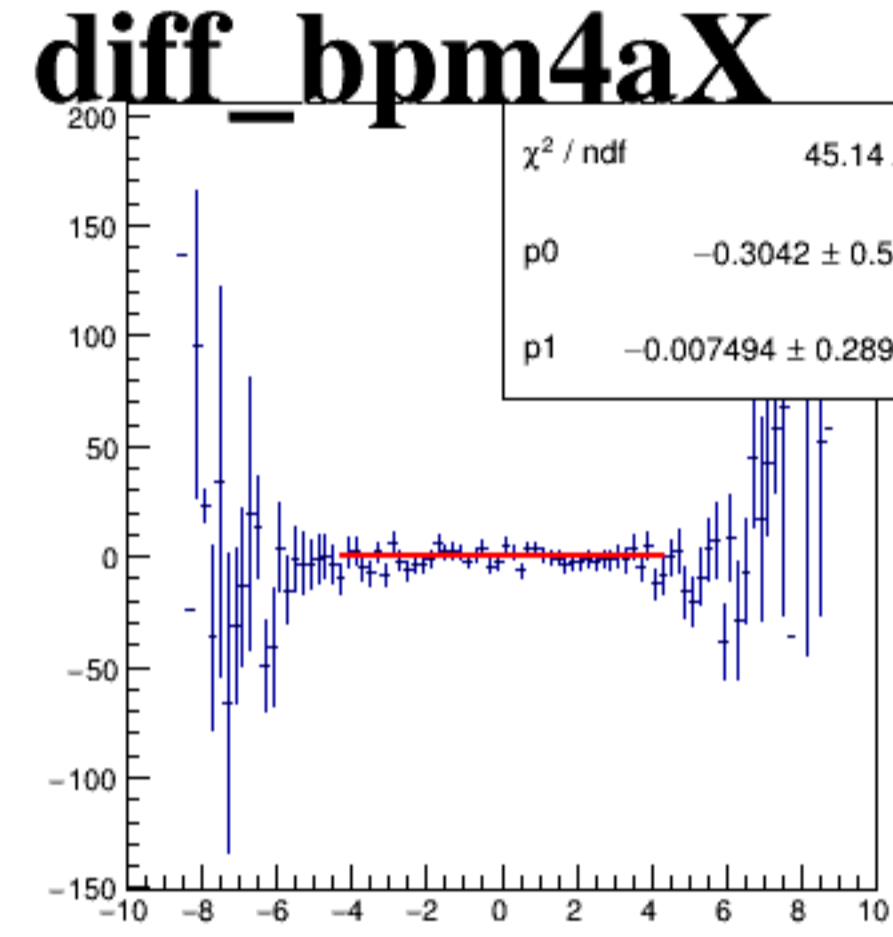


run3641\_000\_regression\_asym\_maindet\_vs\_diff\_bpm-fit\_postpan.png

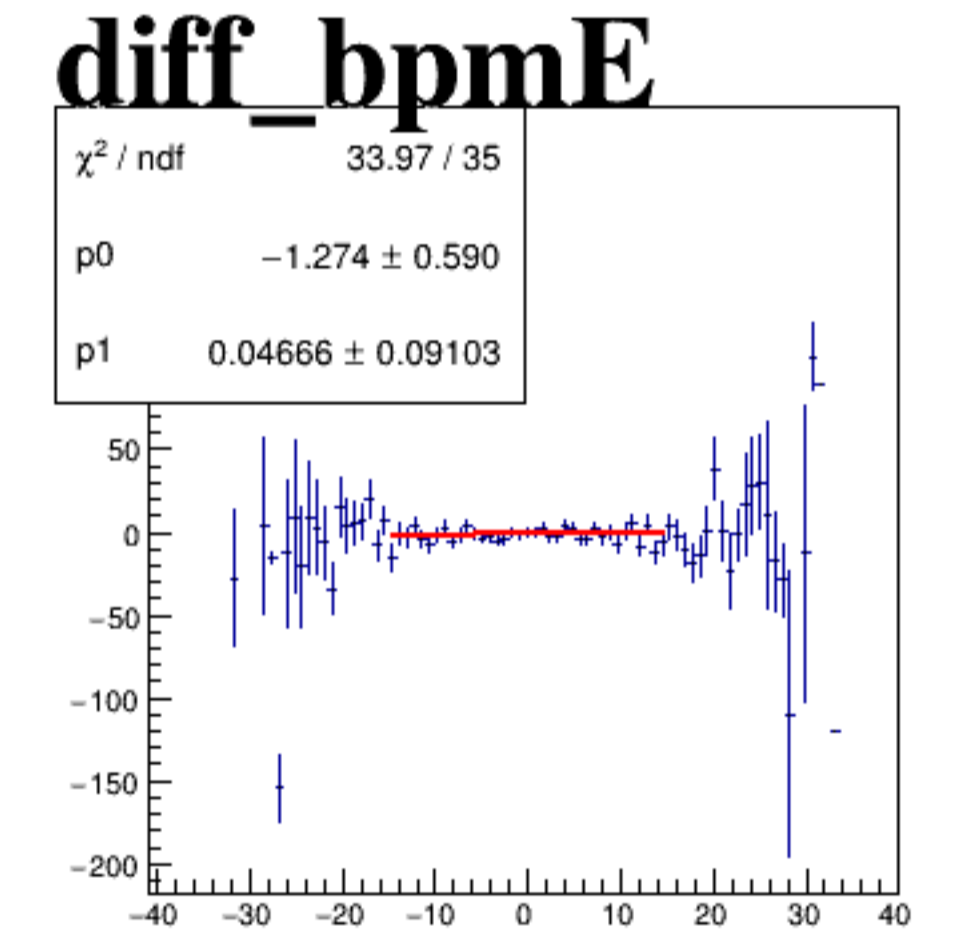
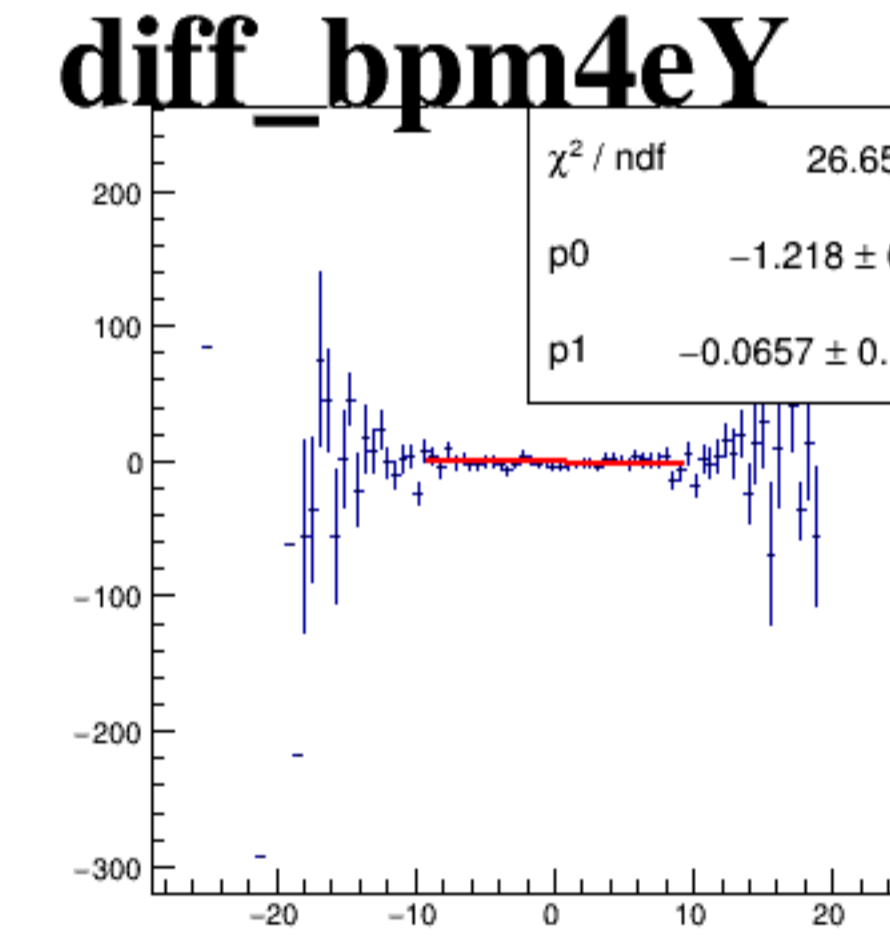
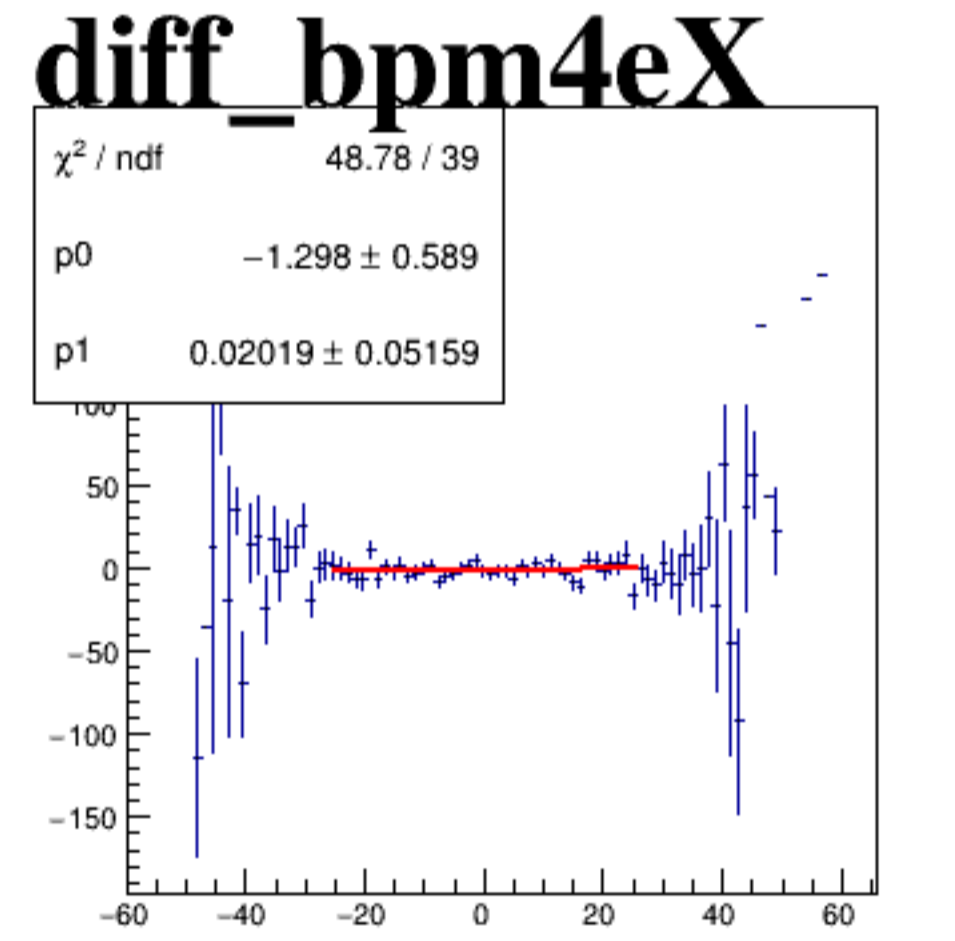
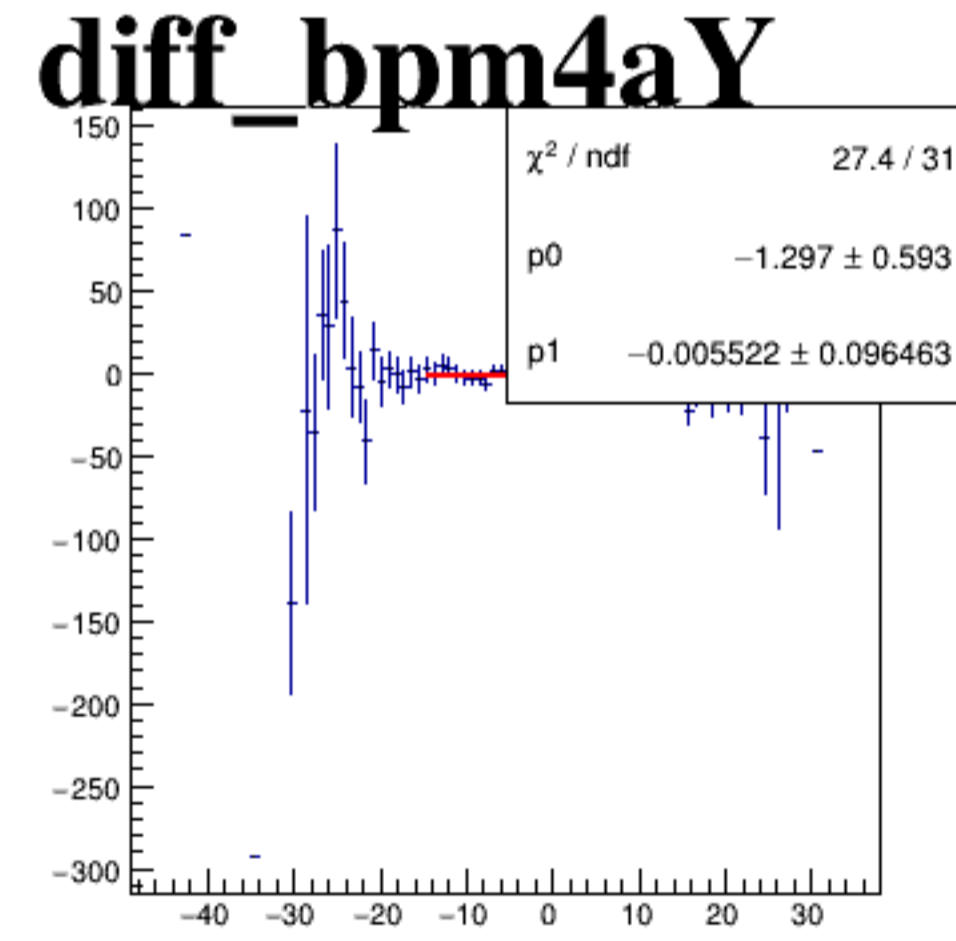
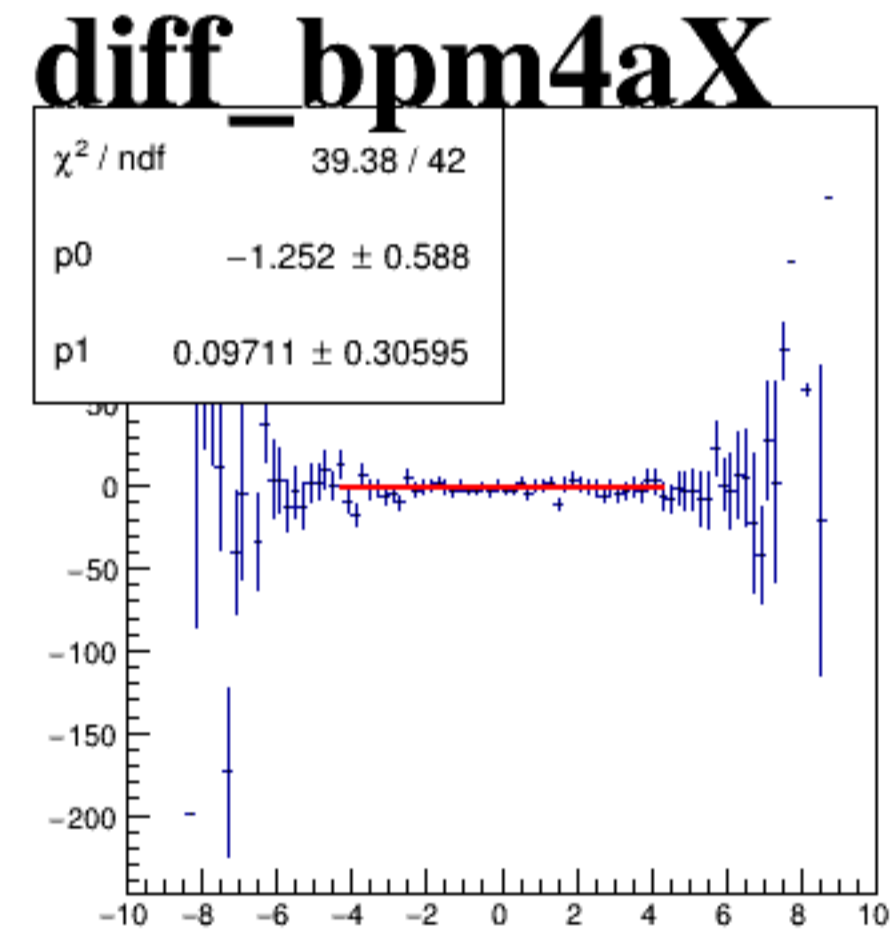
reg\_asym\_usl



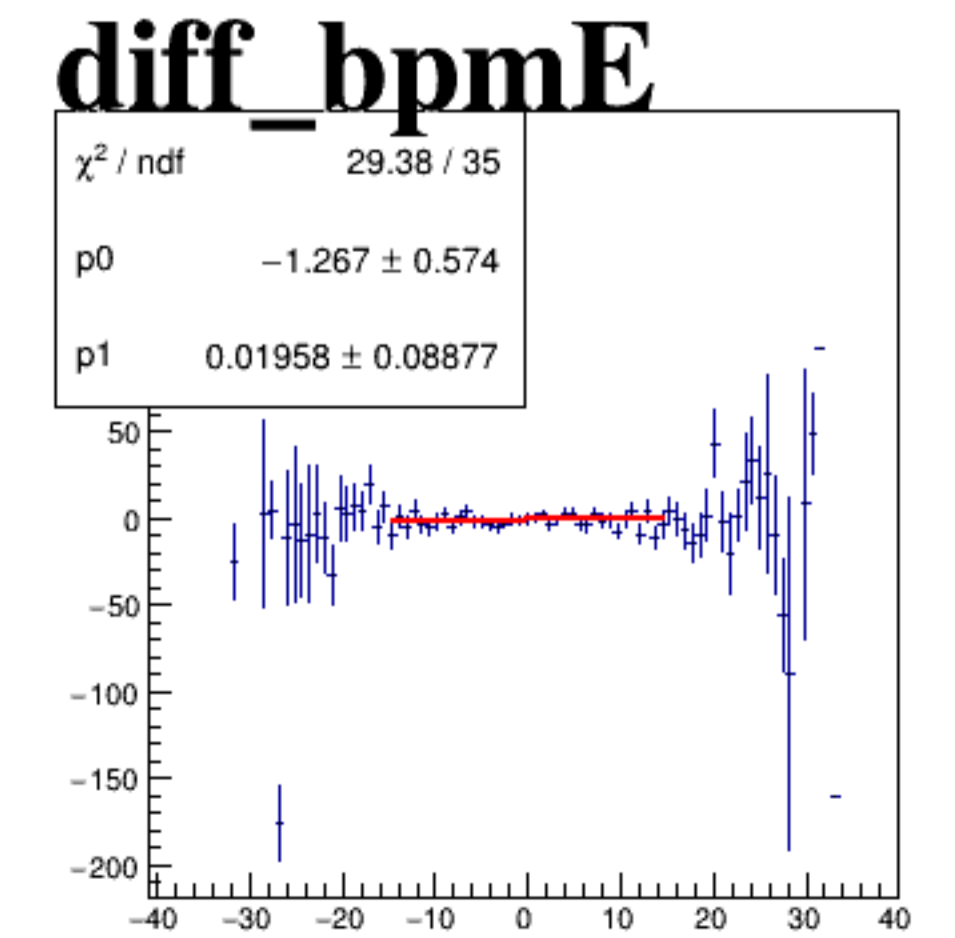
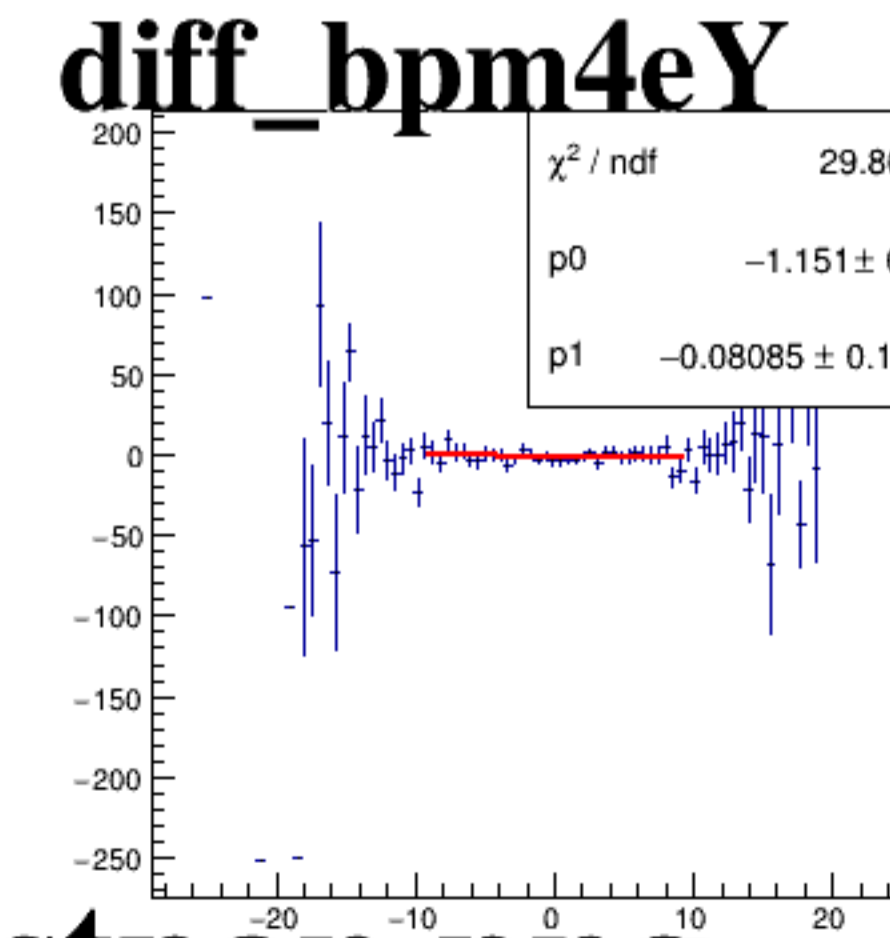
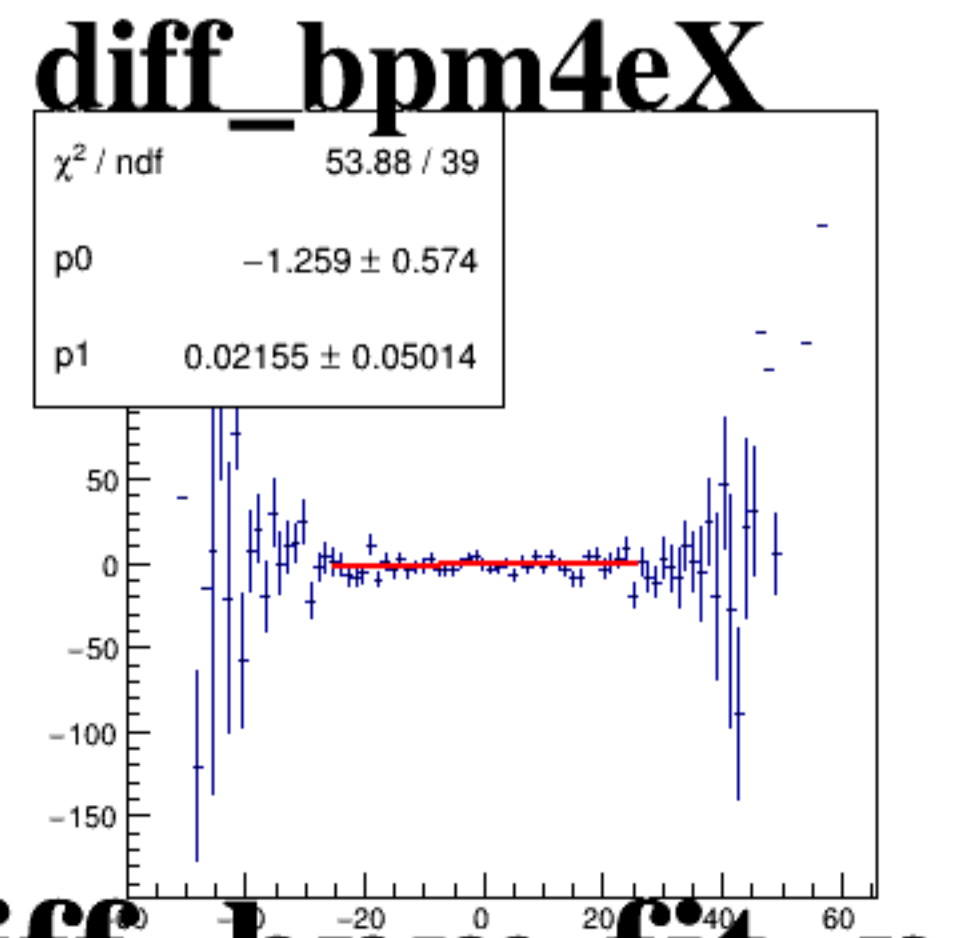
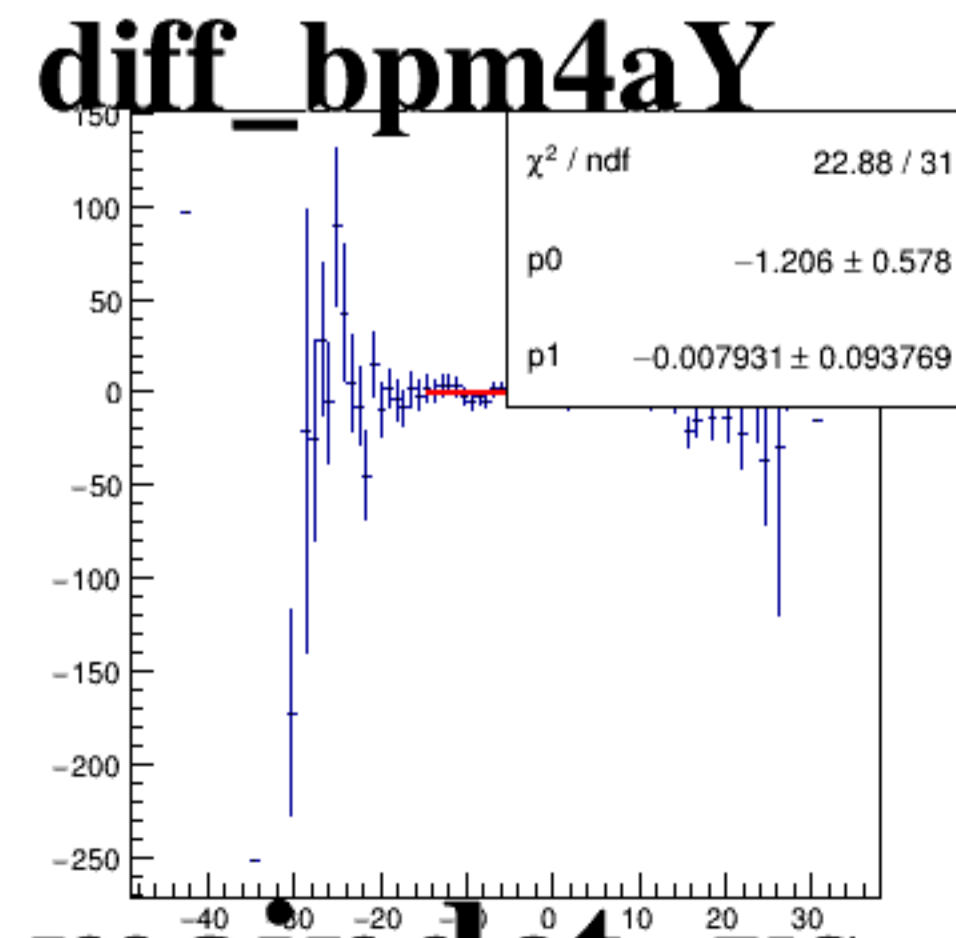
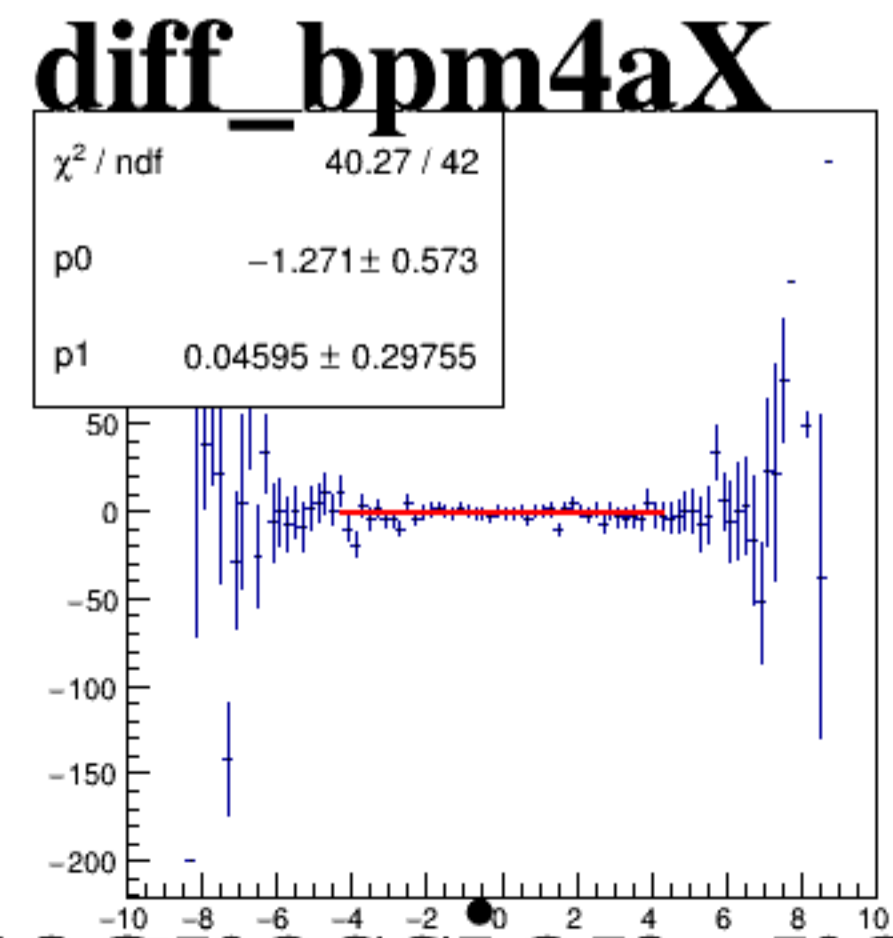
reg\_asym\_dsl



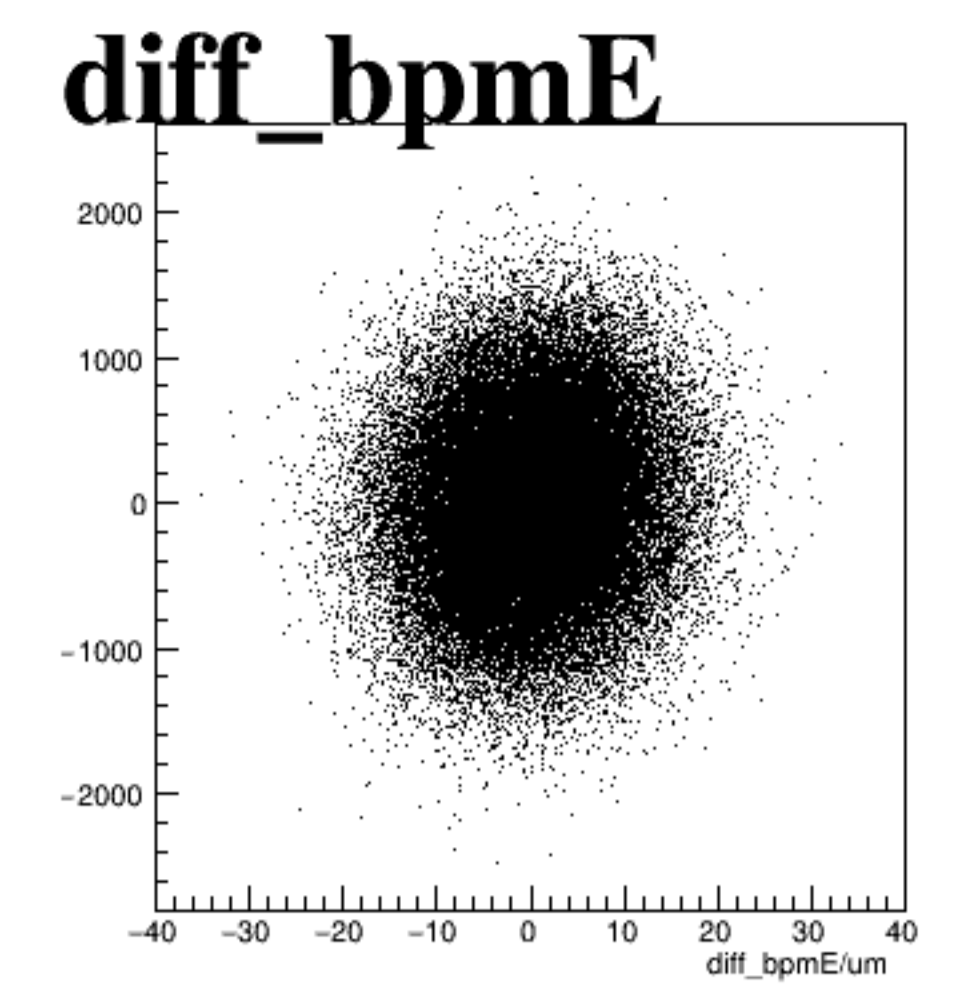
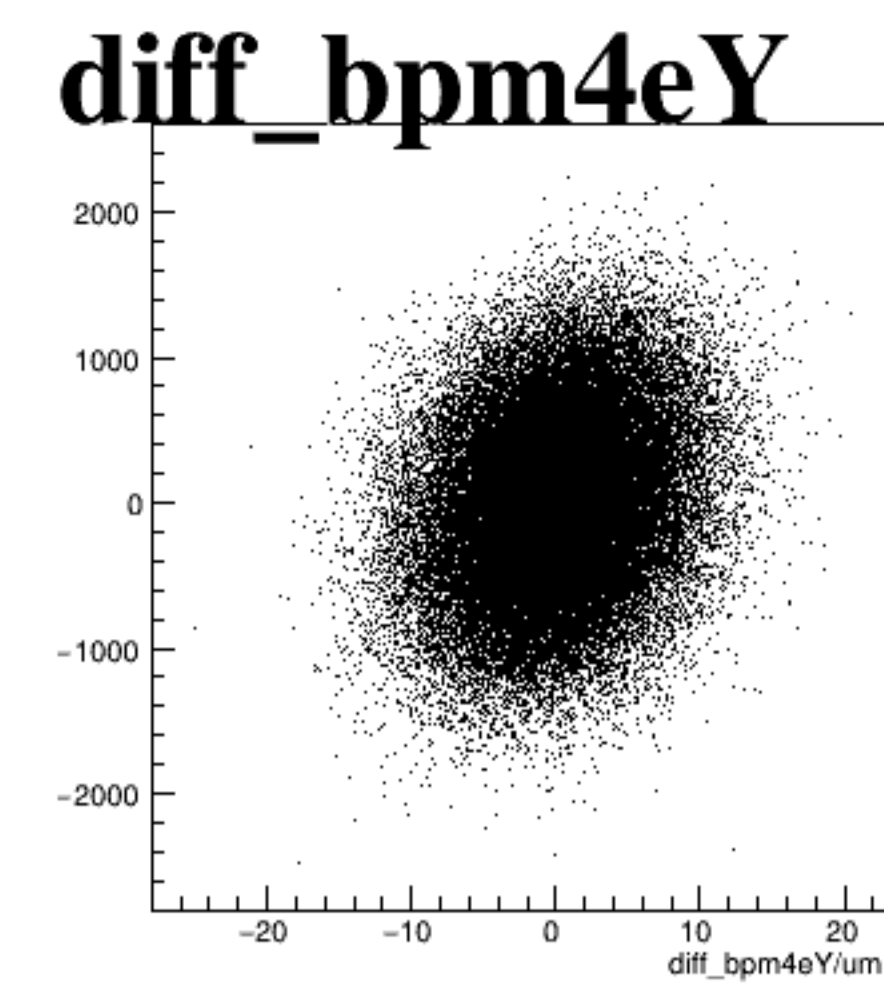
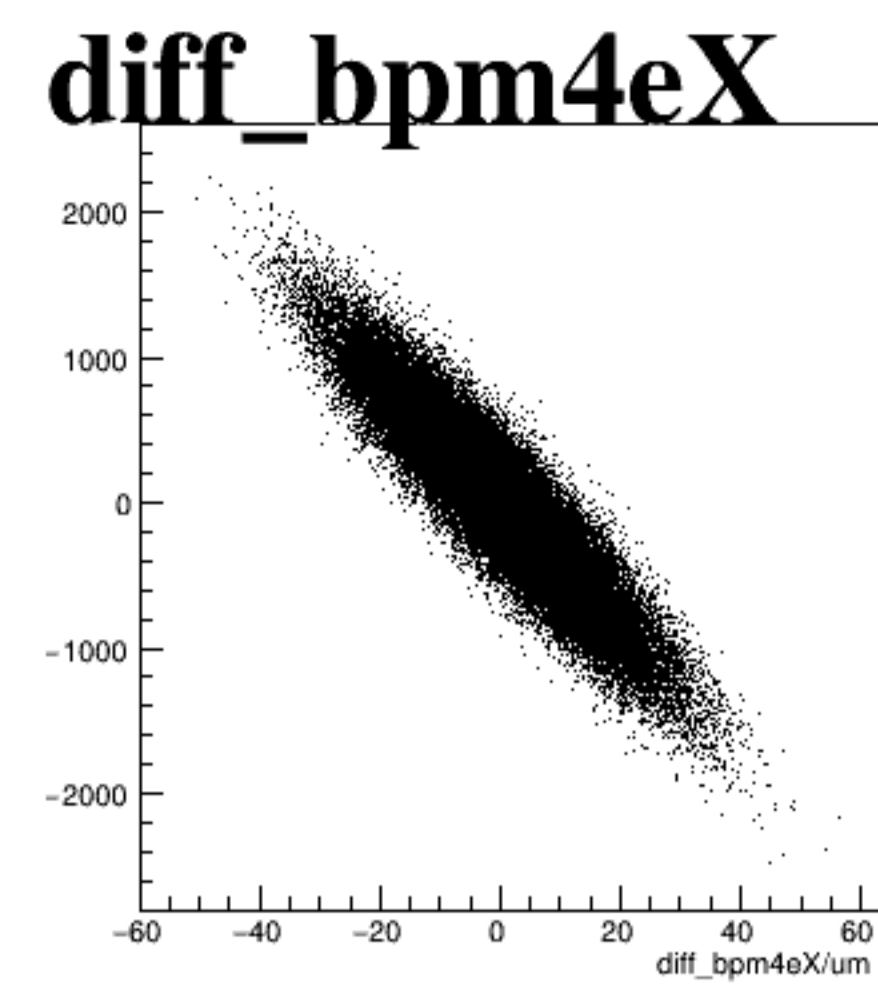
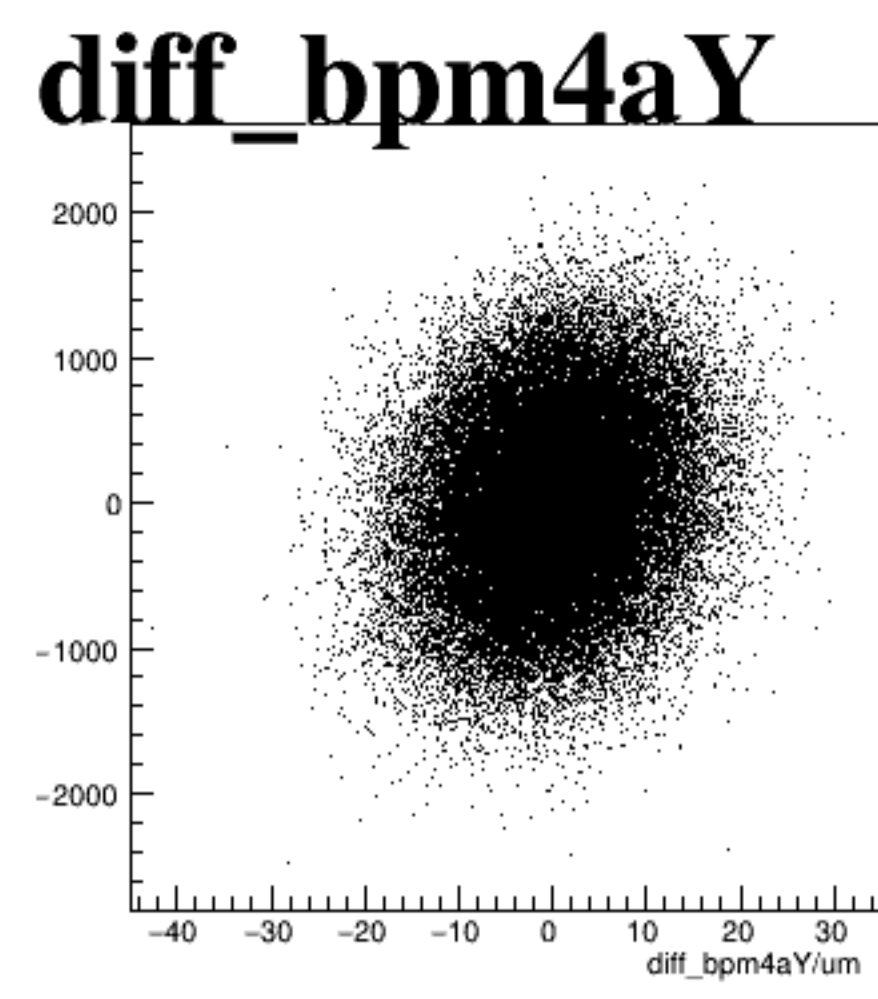
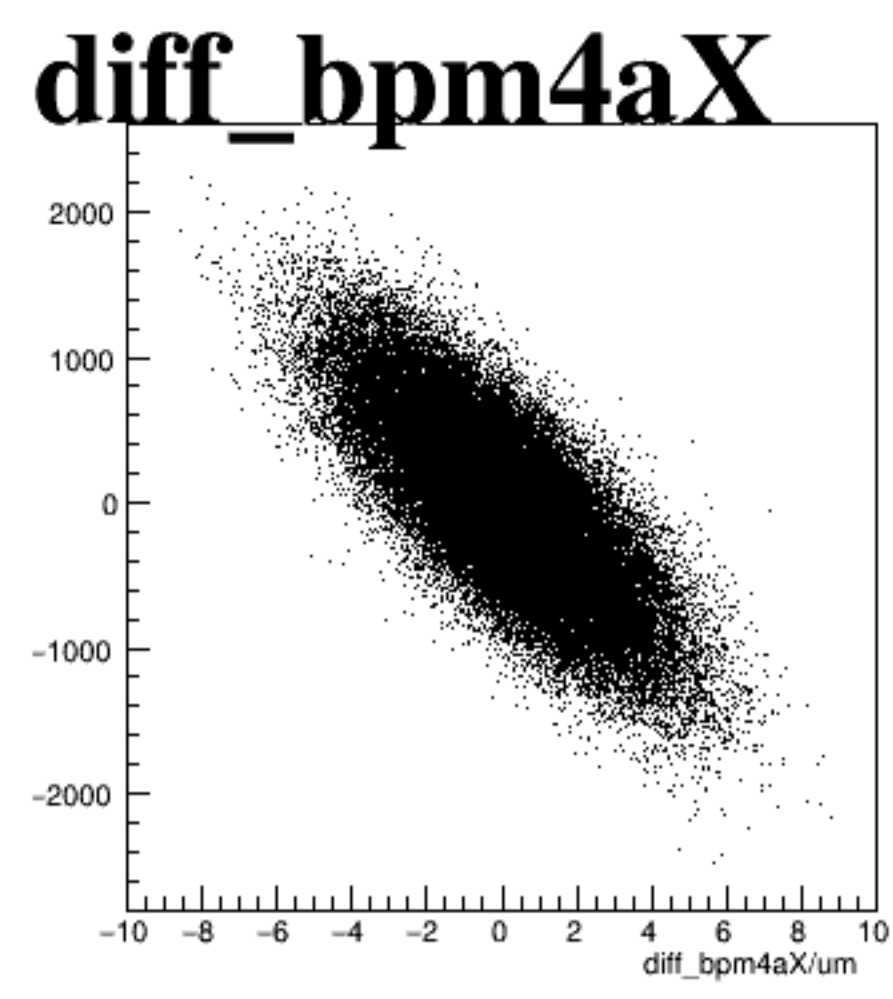
reg\_asym\_usr



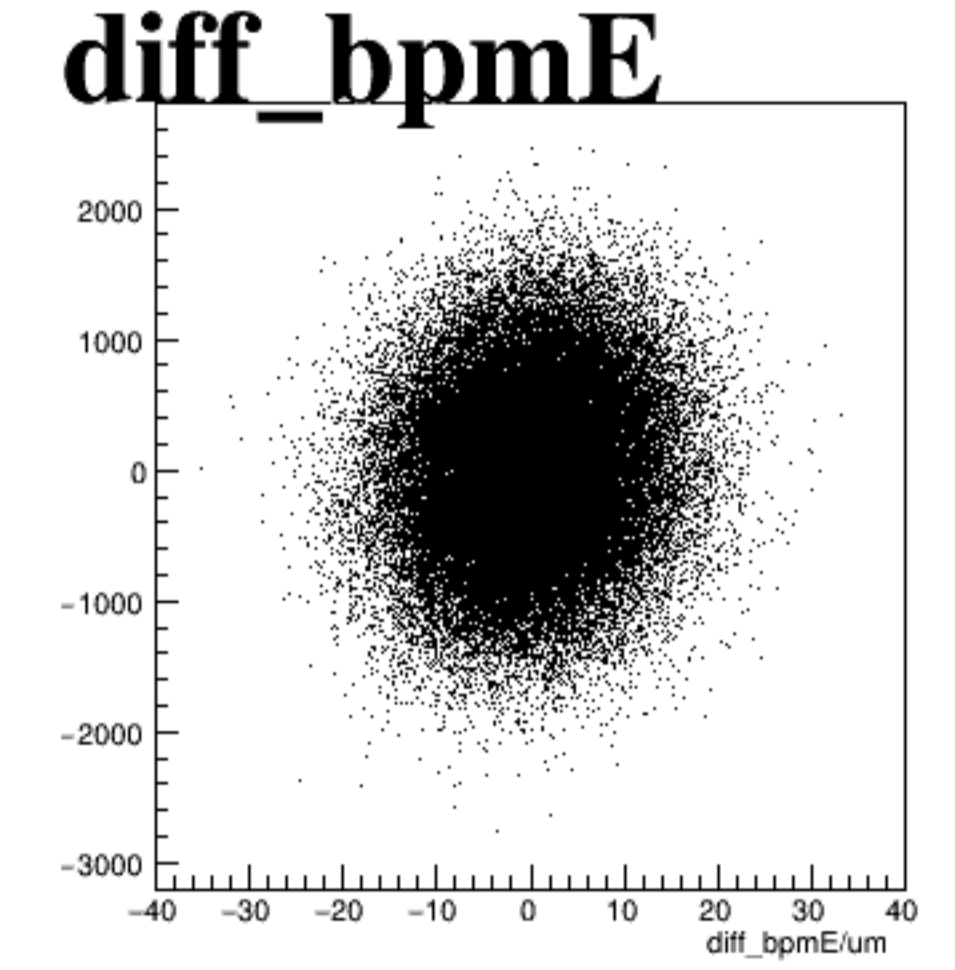
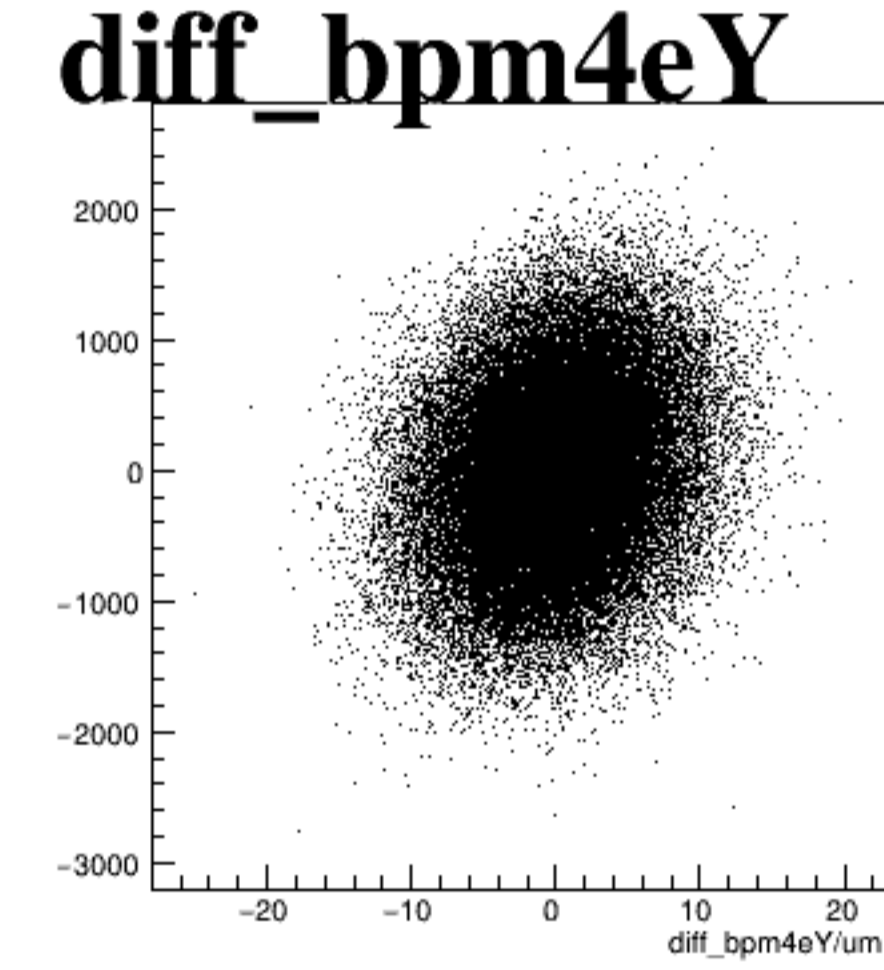
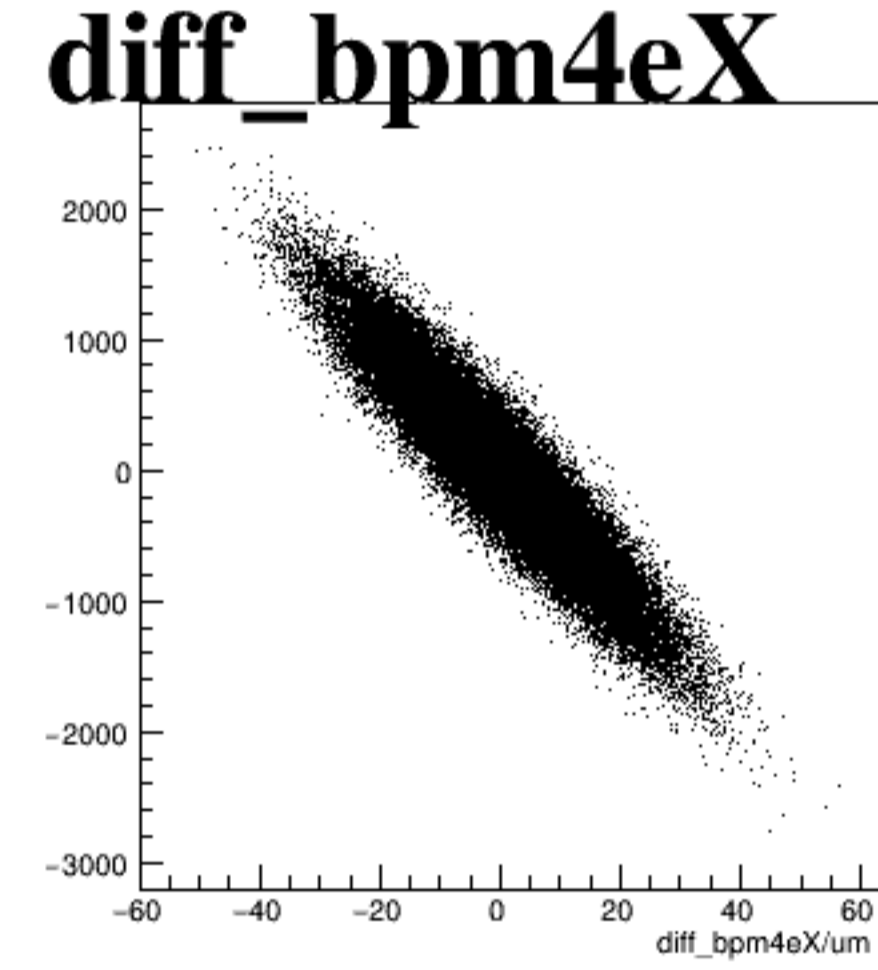
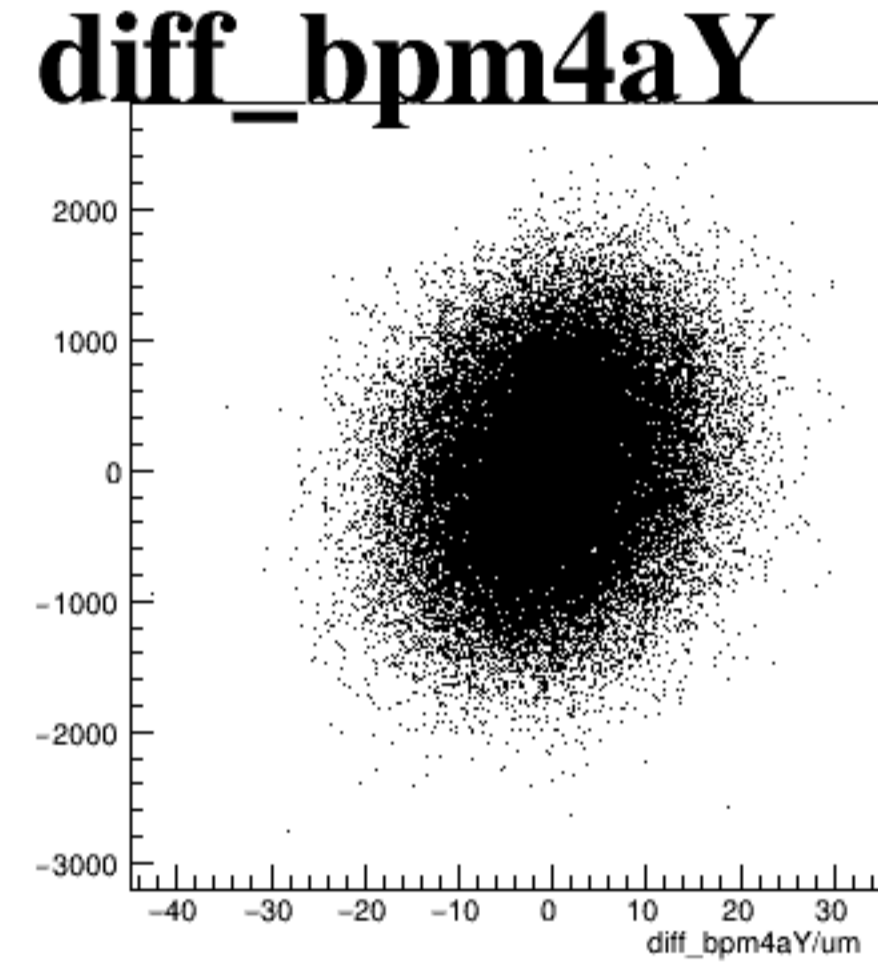
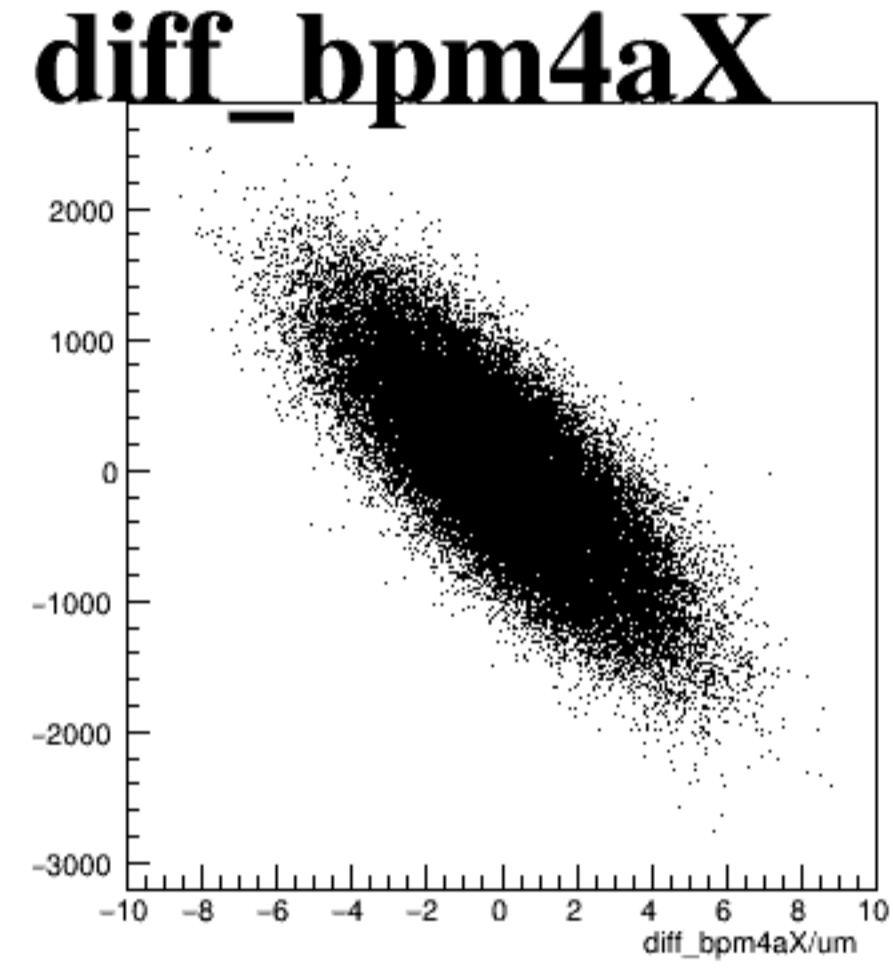
reg\_asym\_dsr



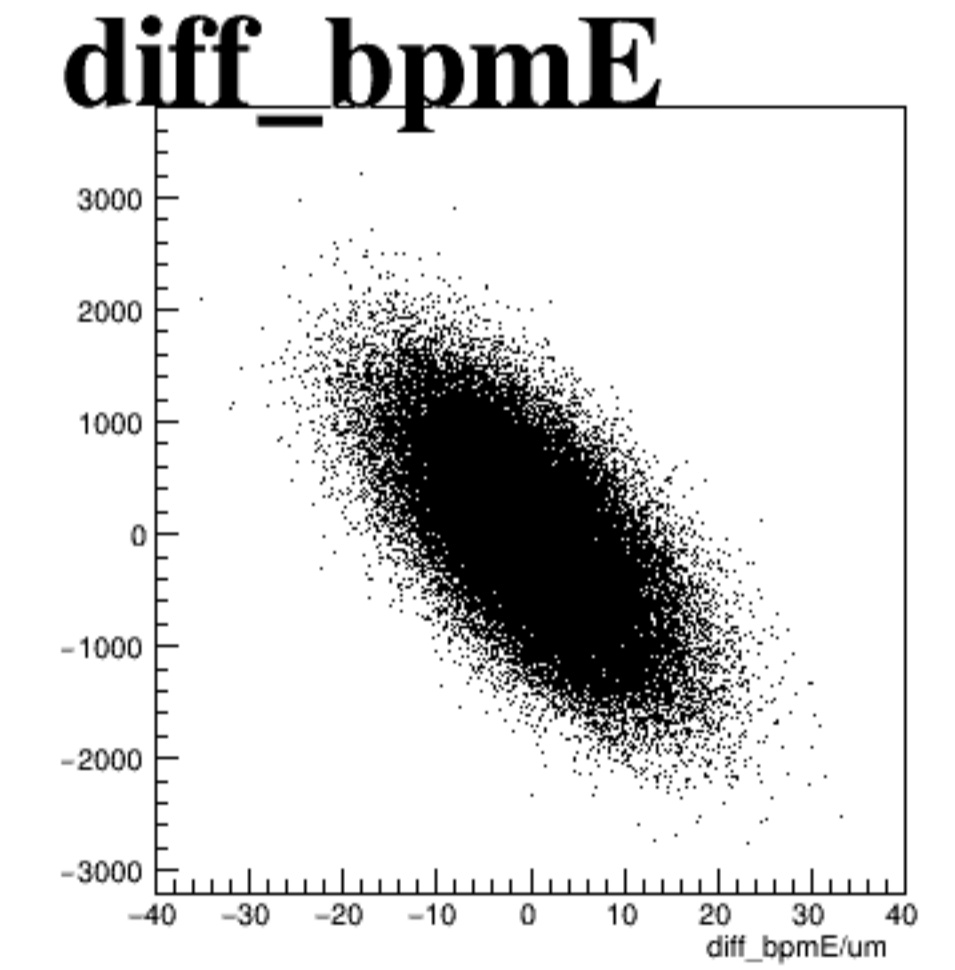
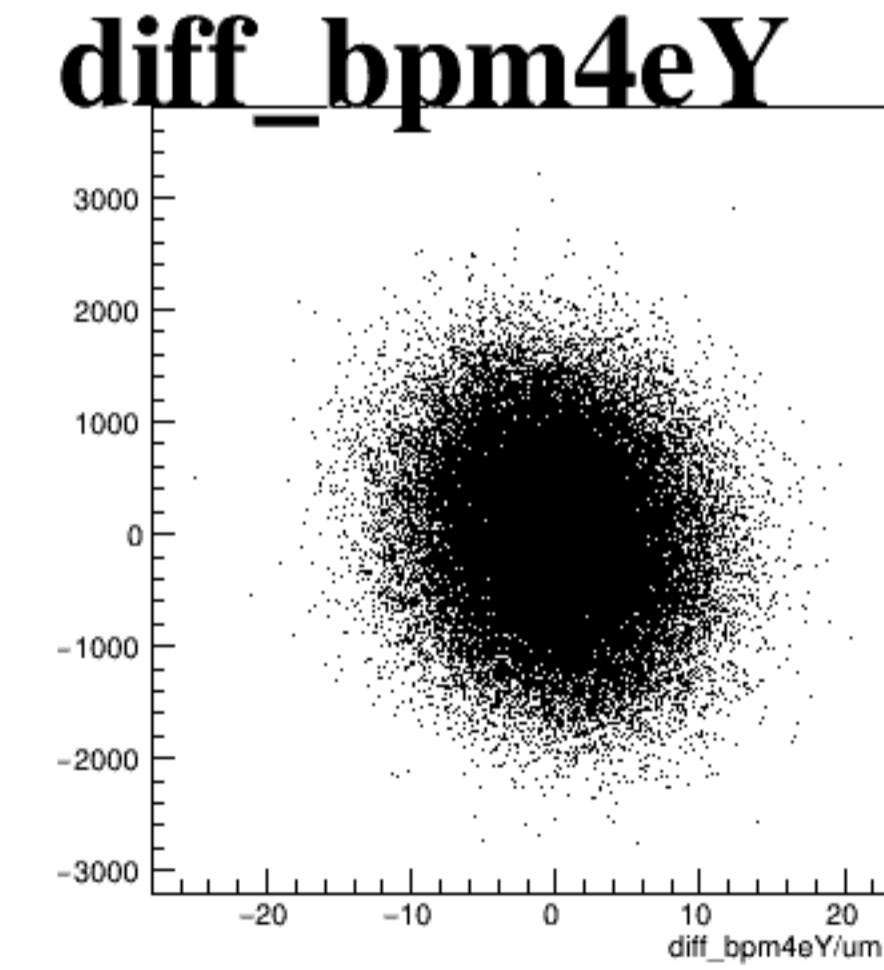
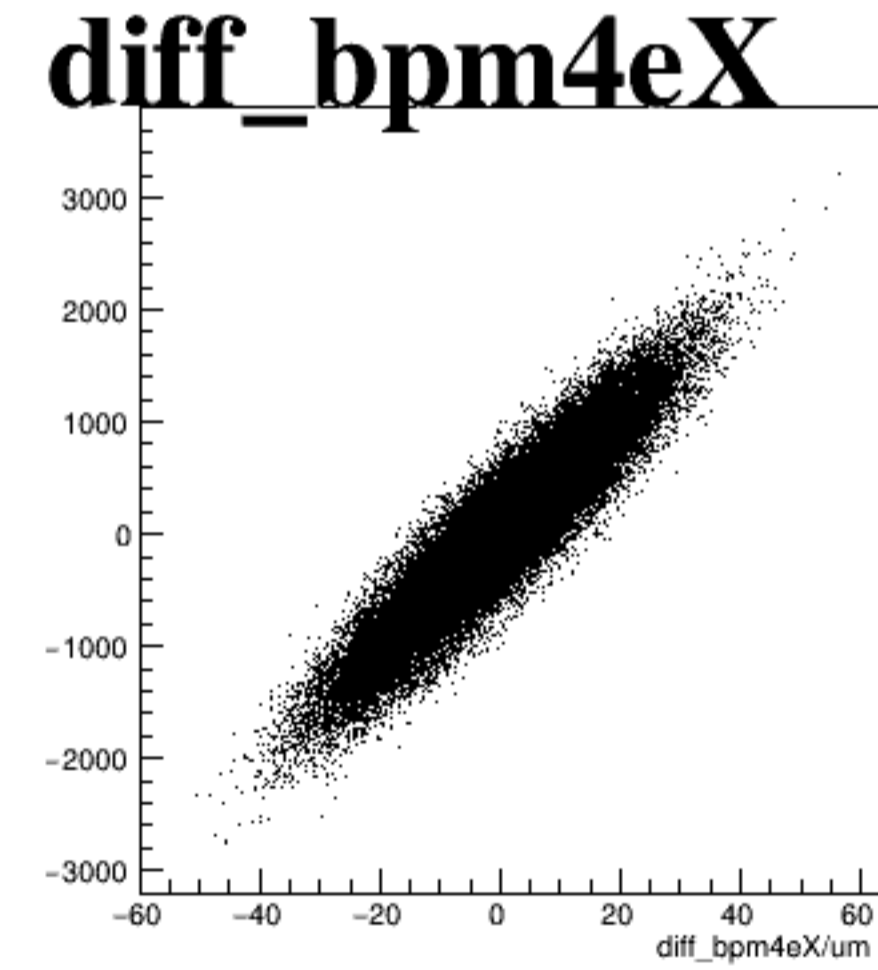
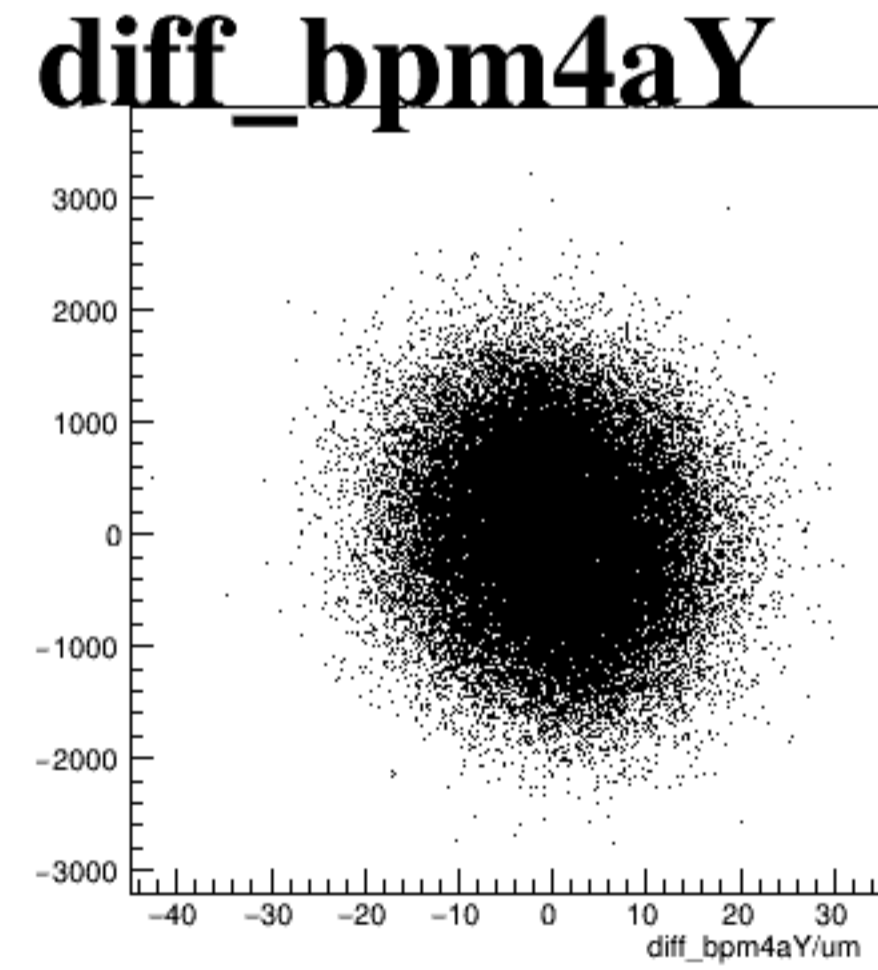
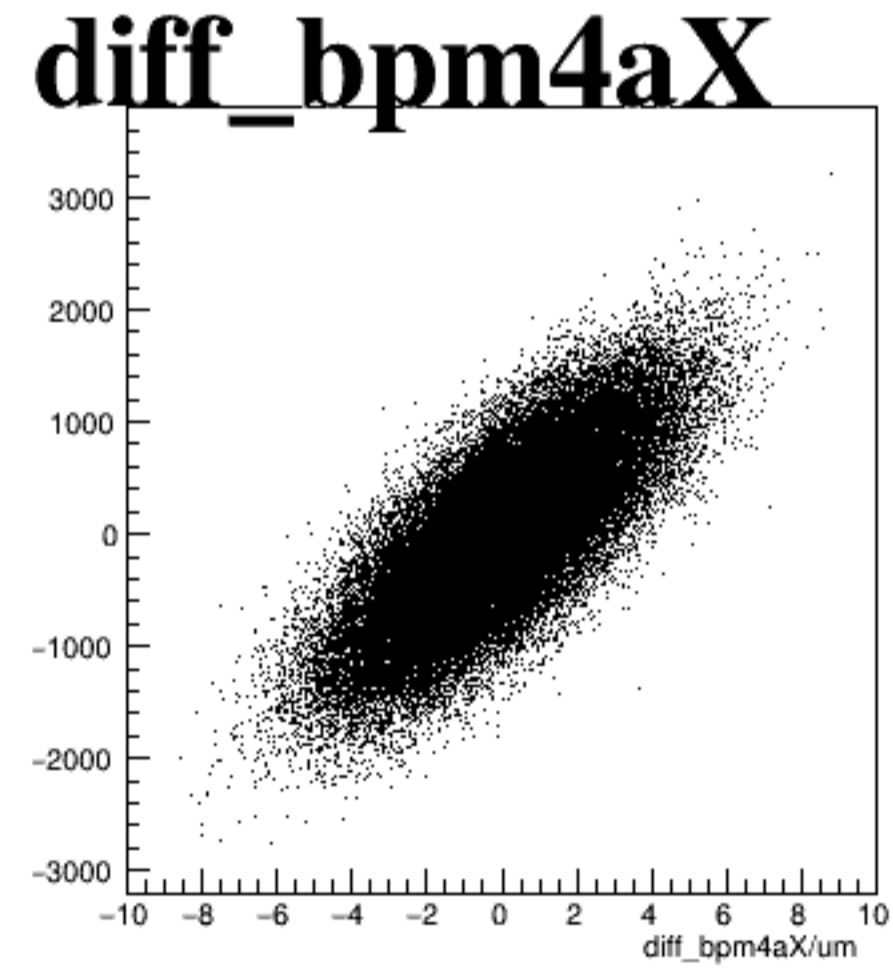
asym\_usl



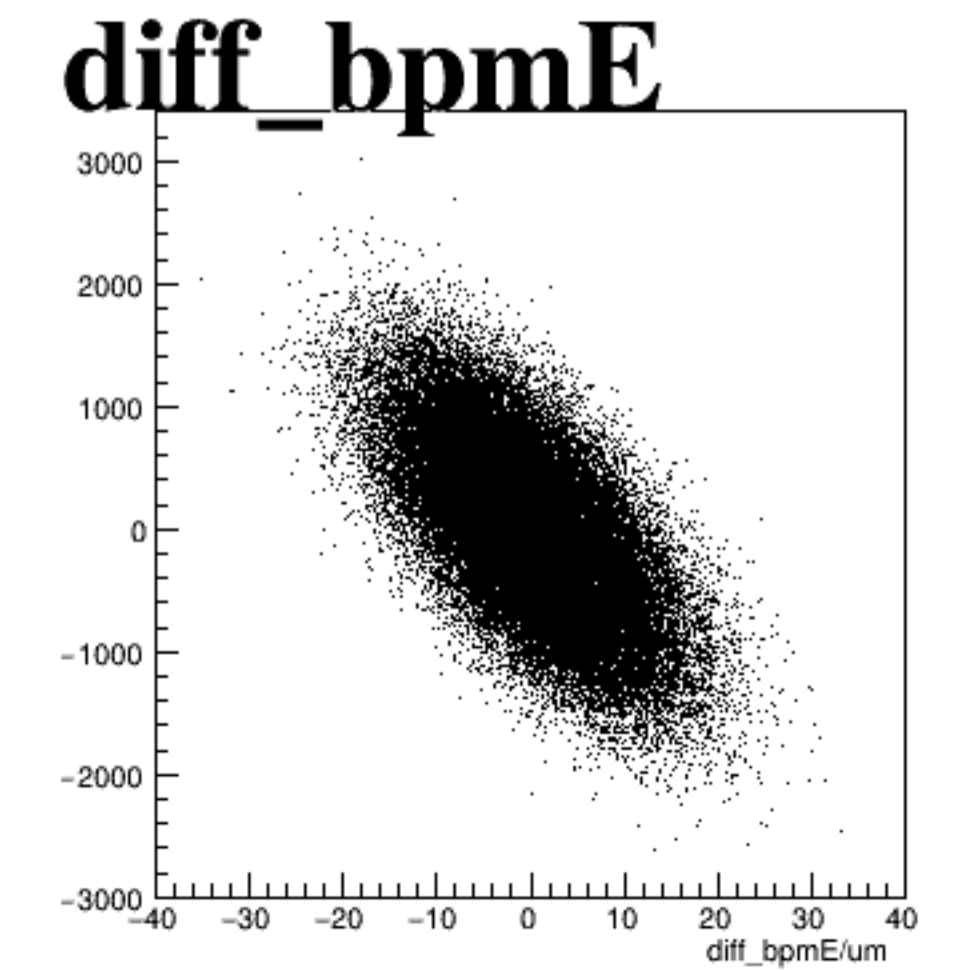
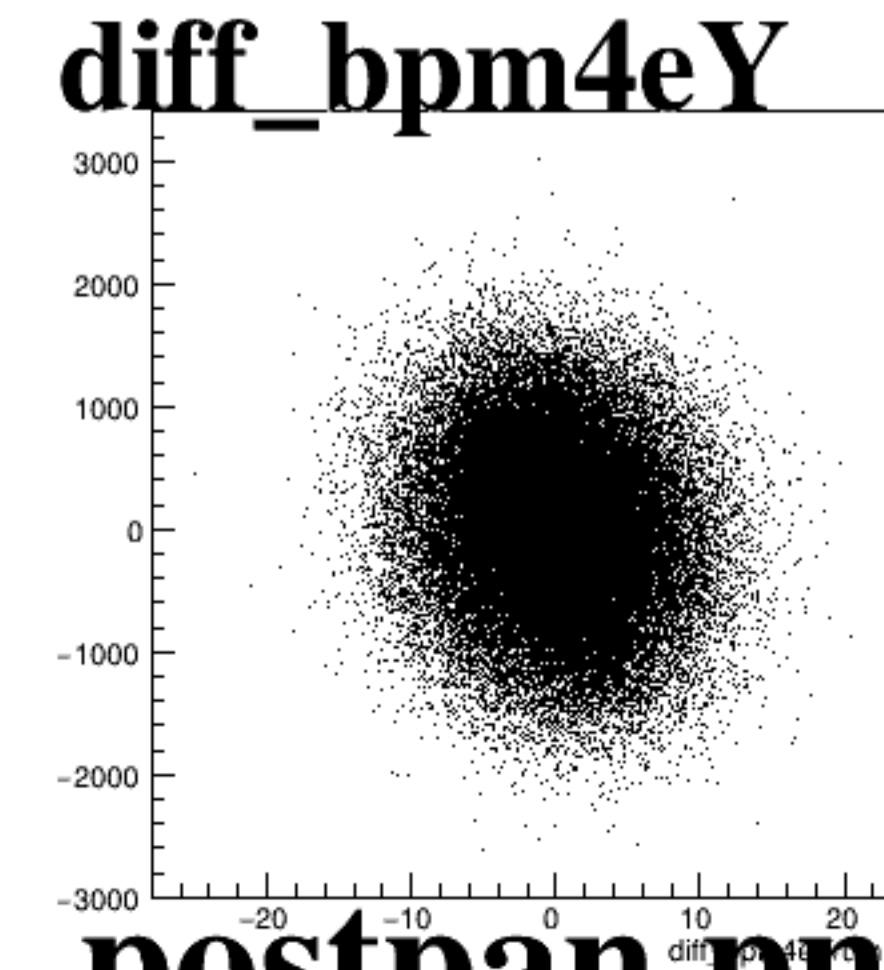
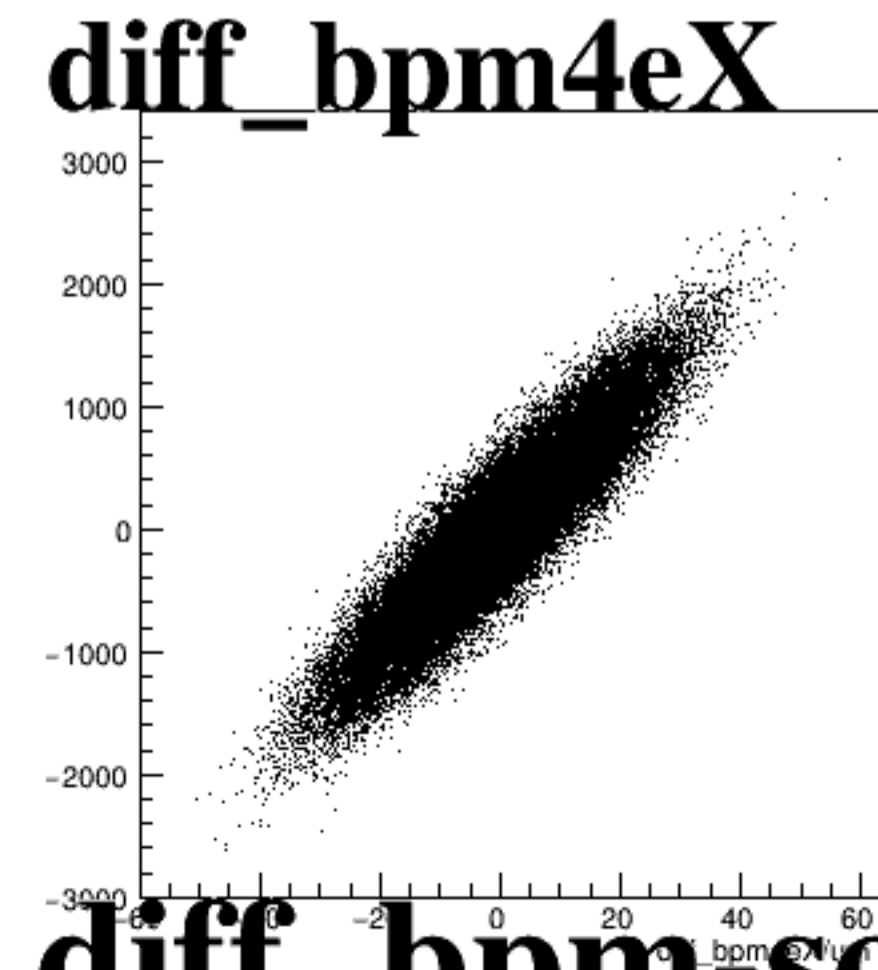
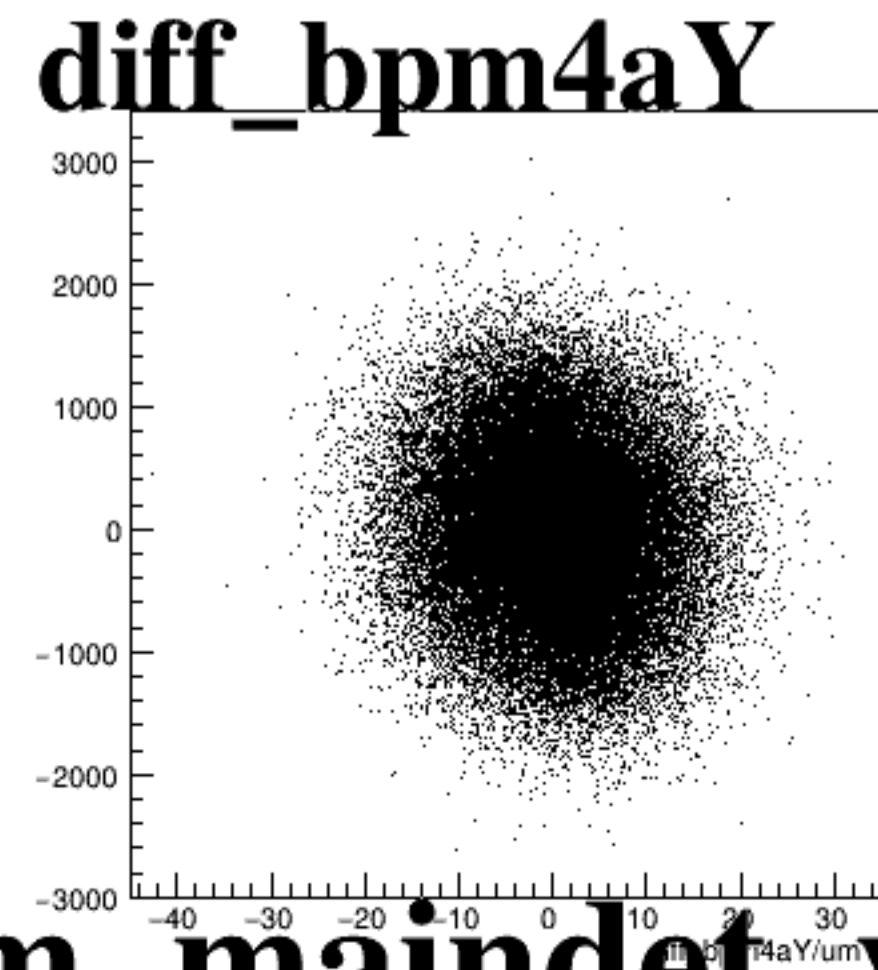
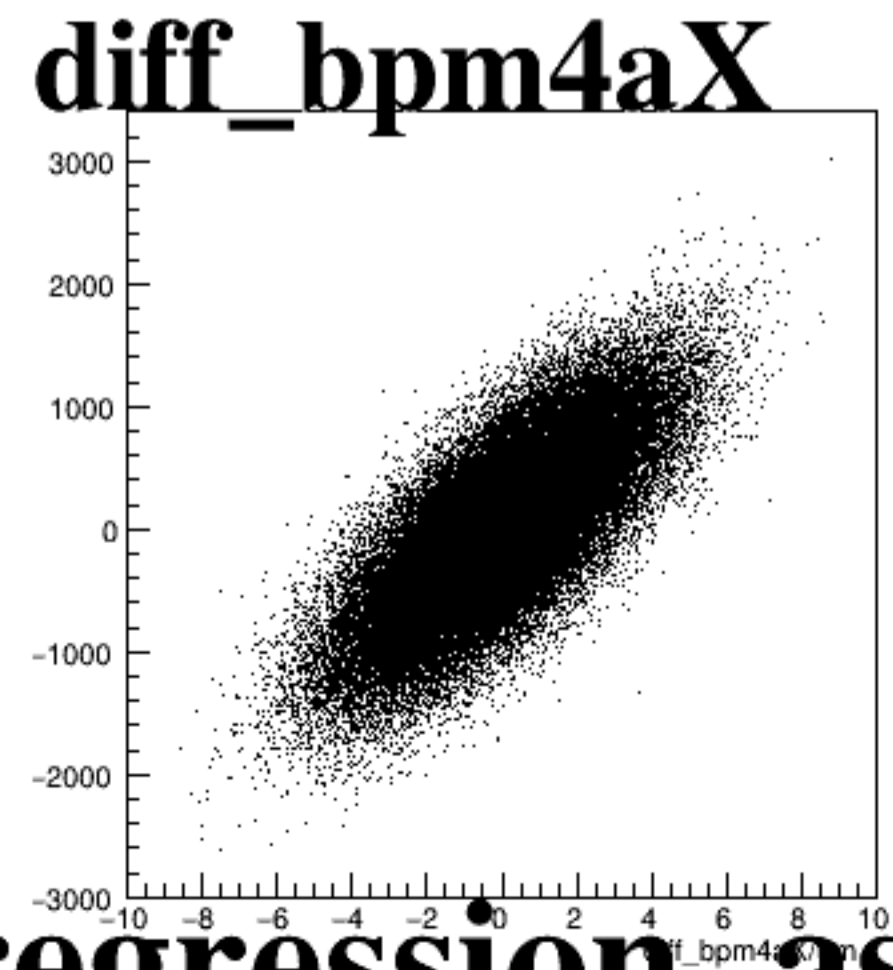
asym\_dsl



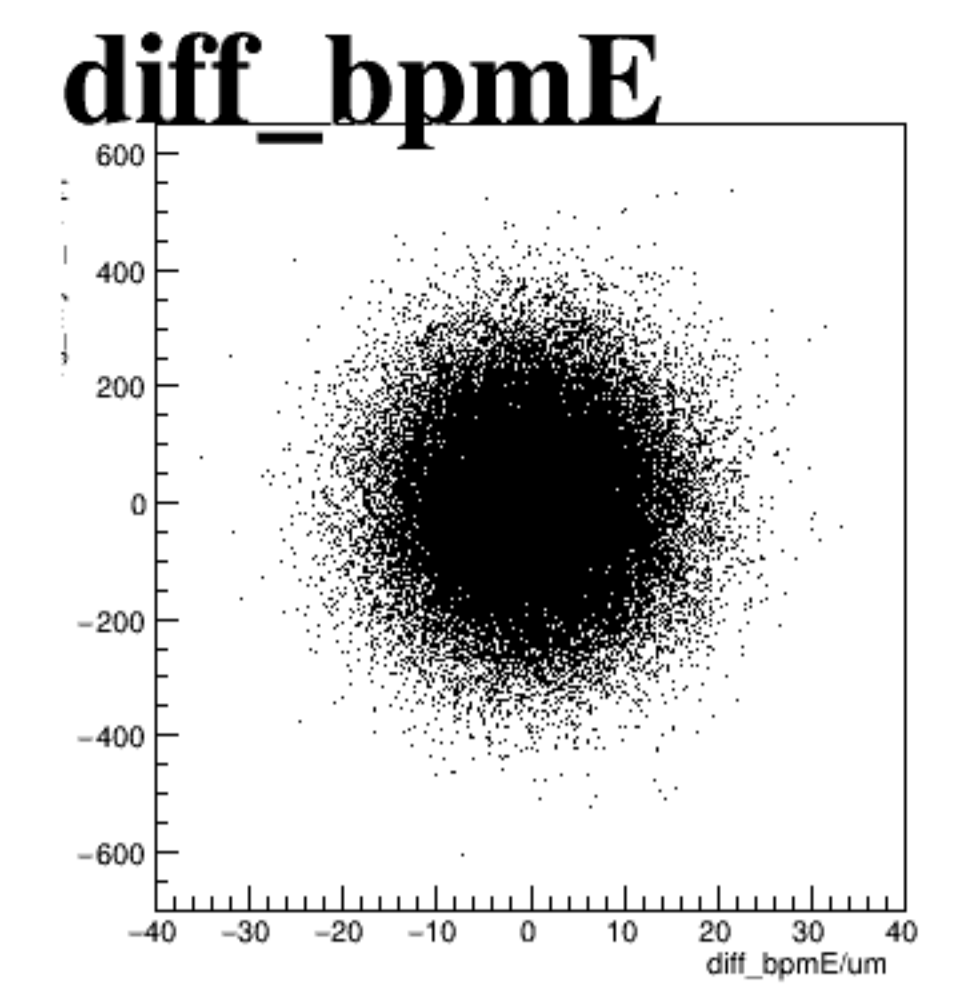
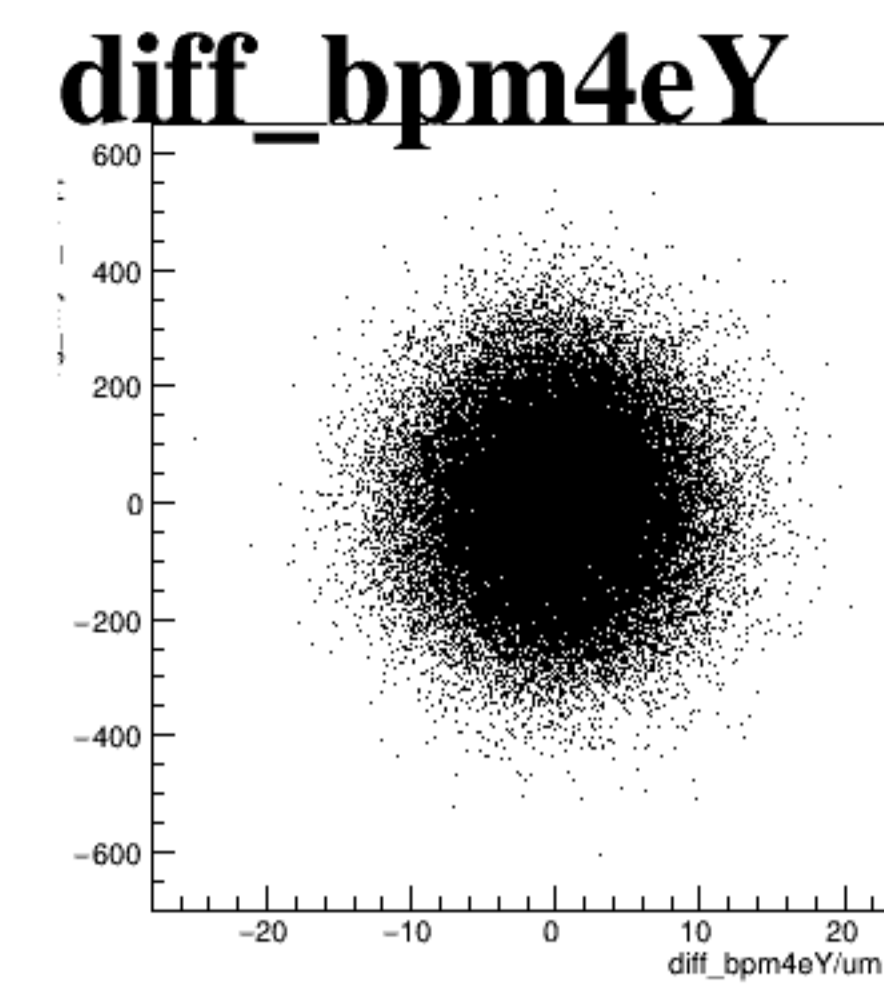
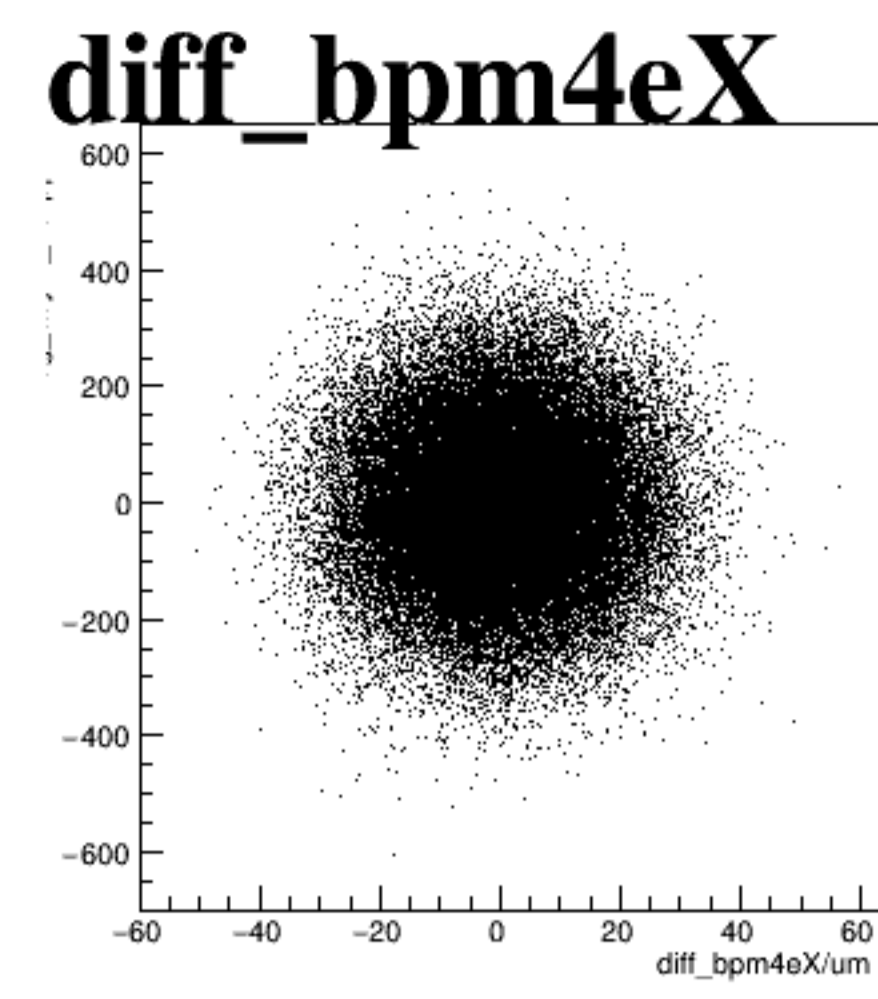
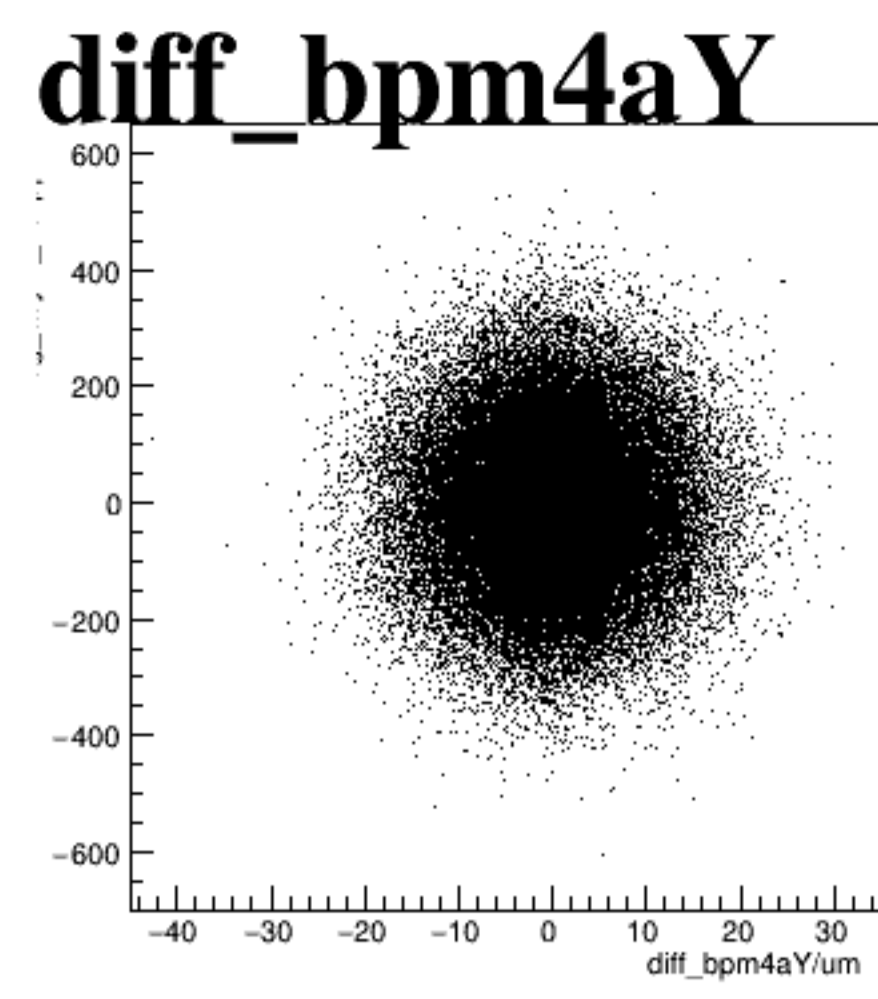
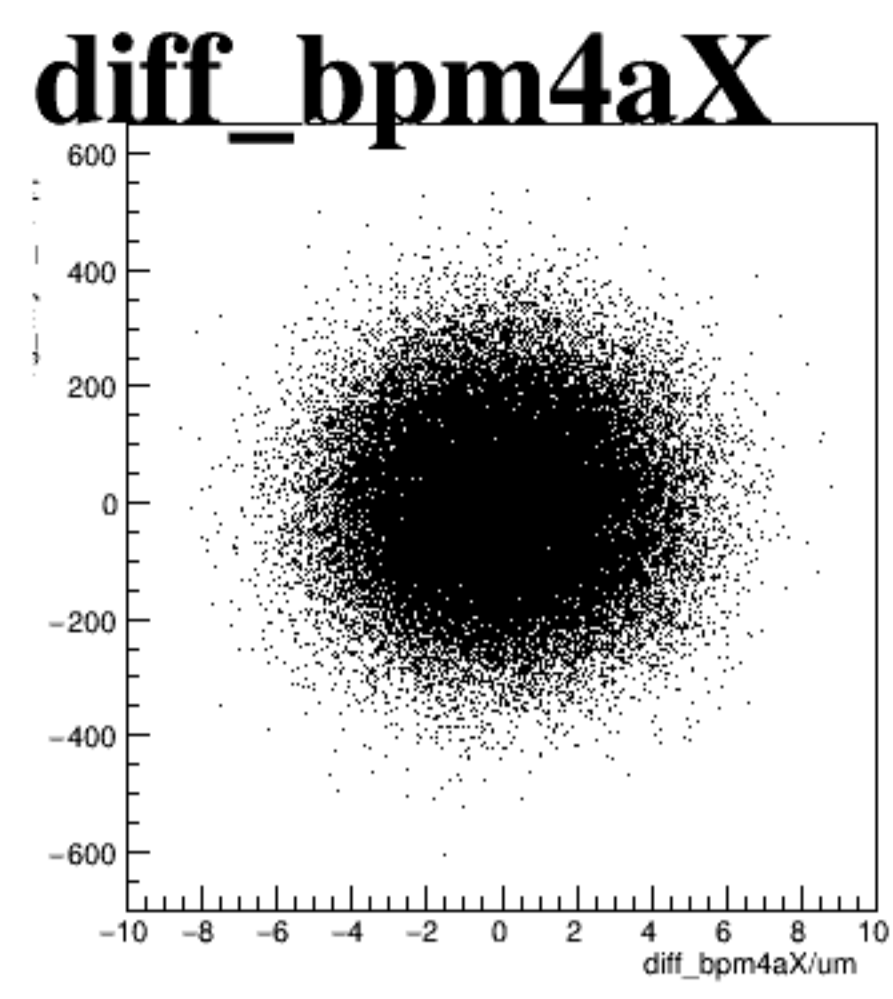
asym\_usr



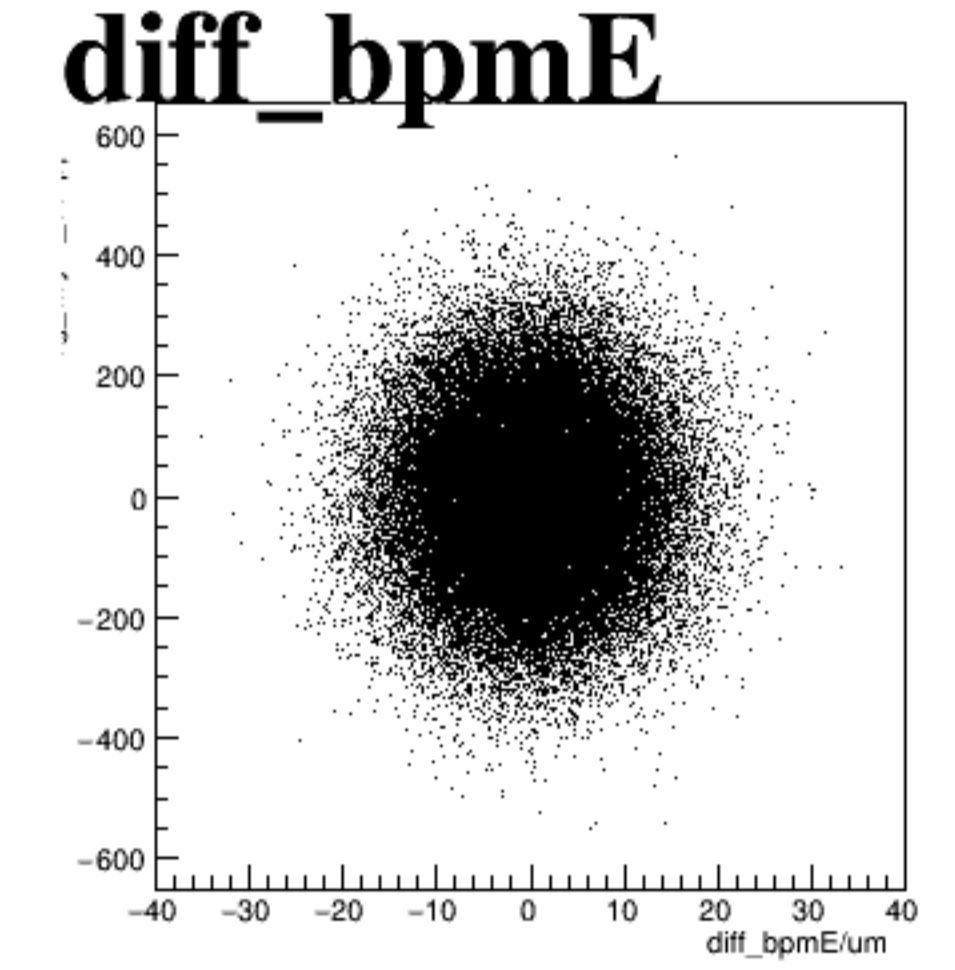
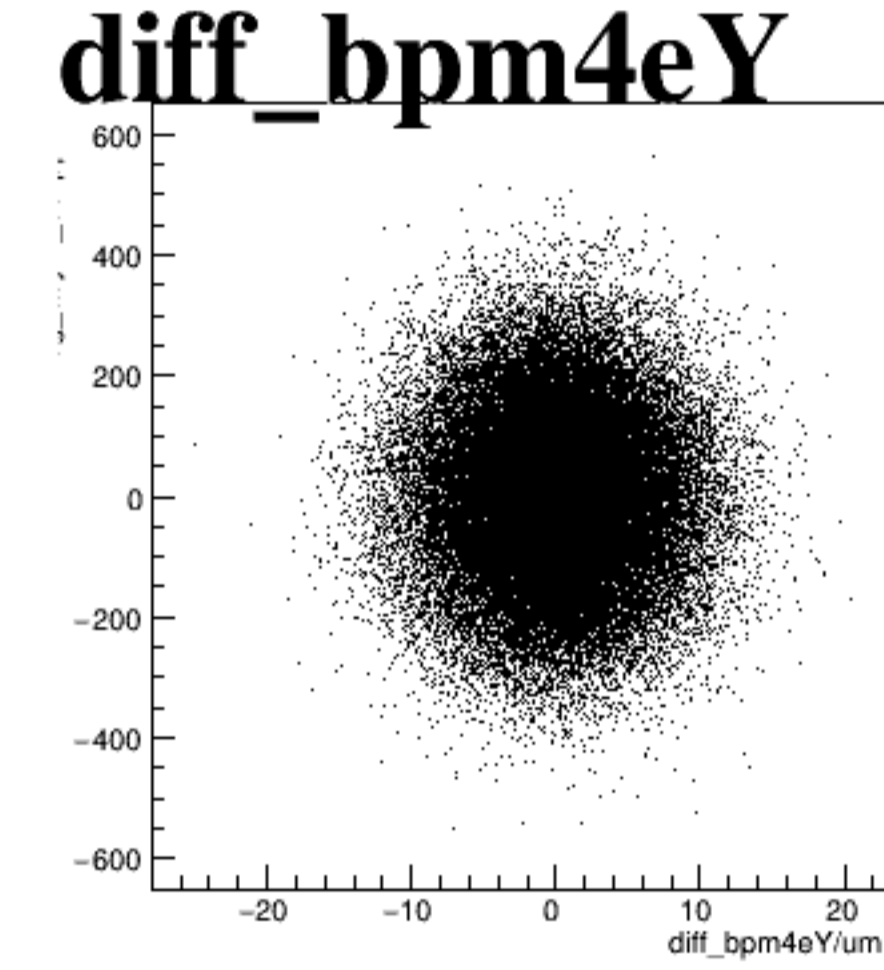
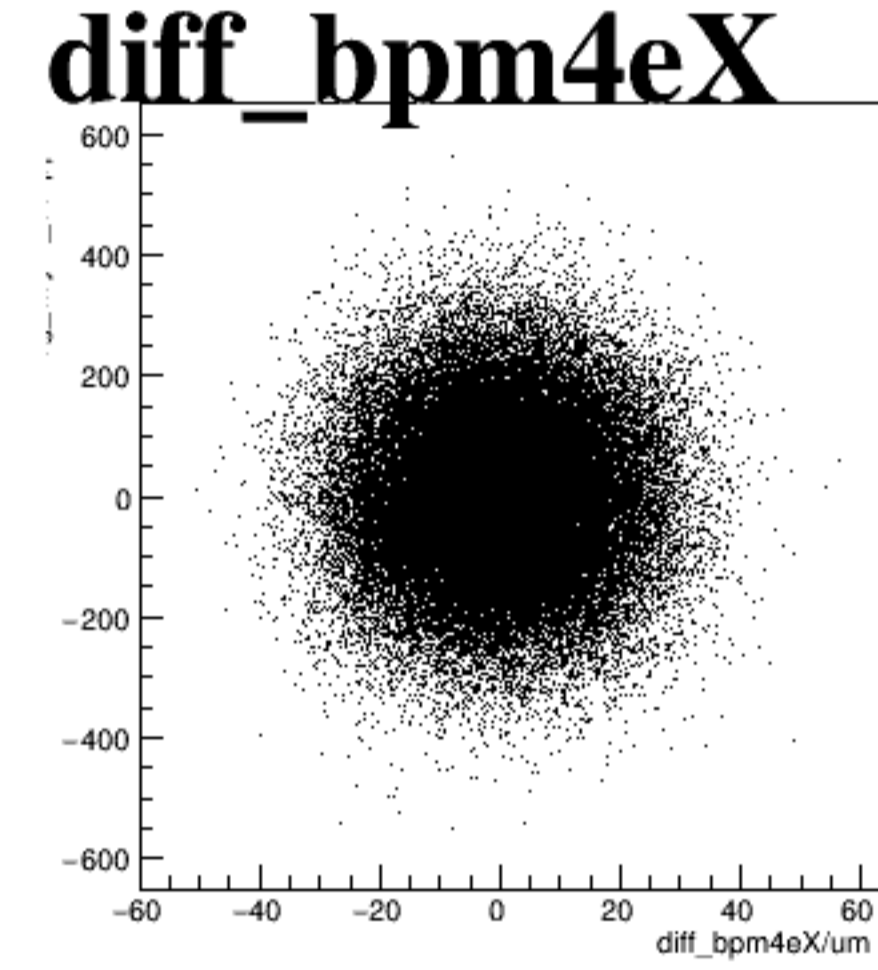
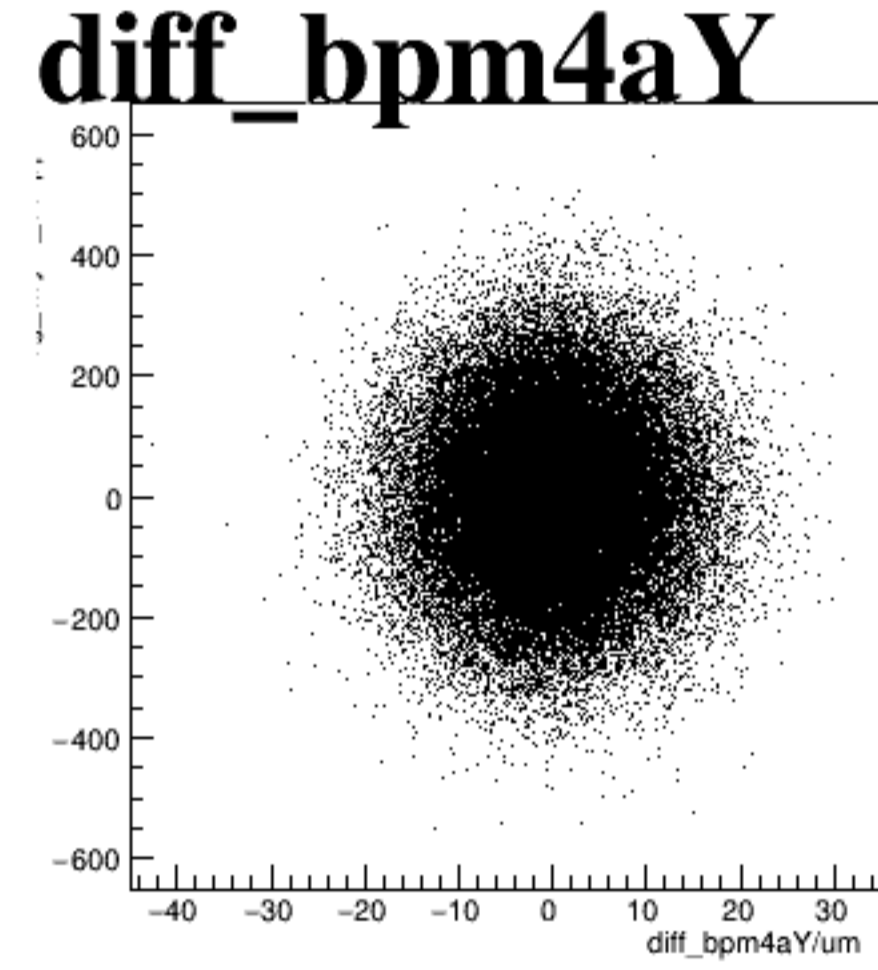
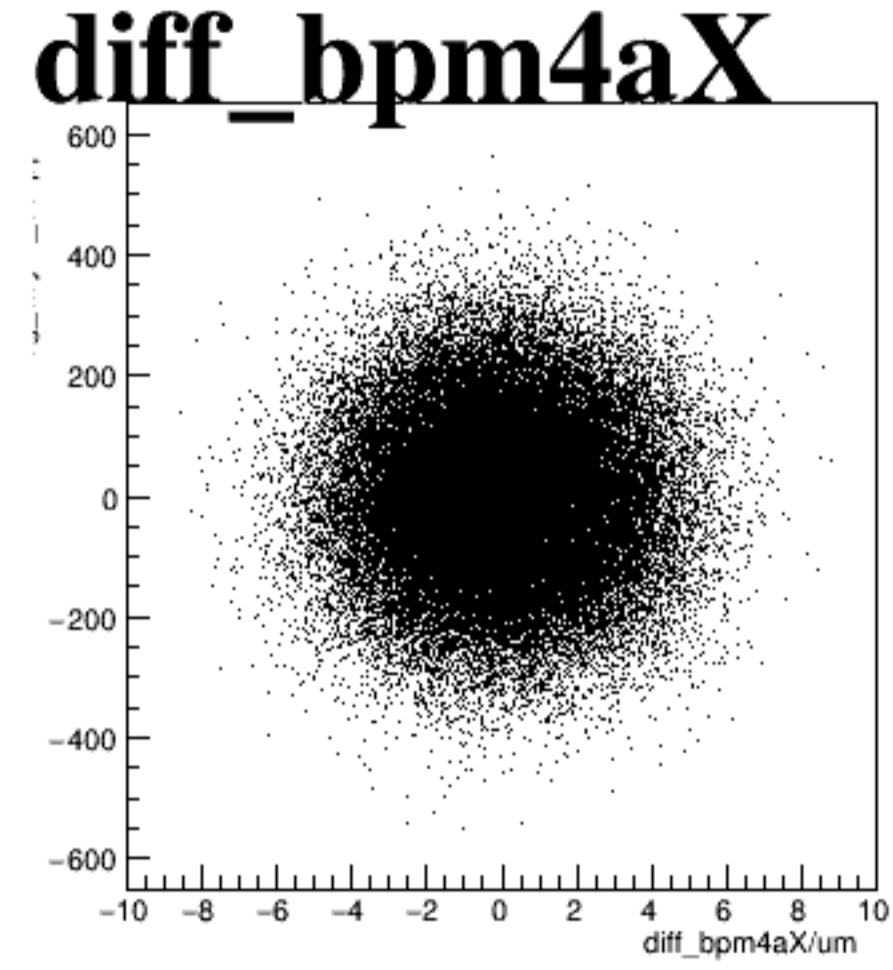
asym\_dsr



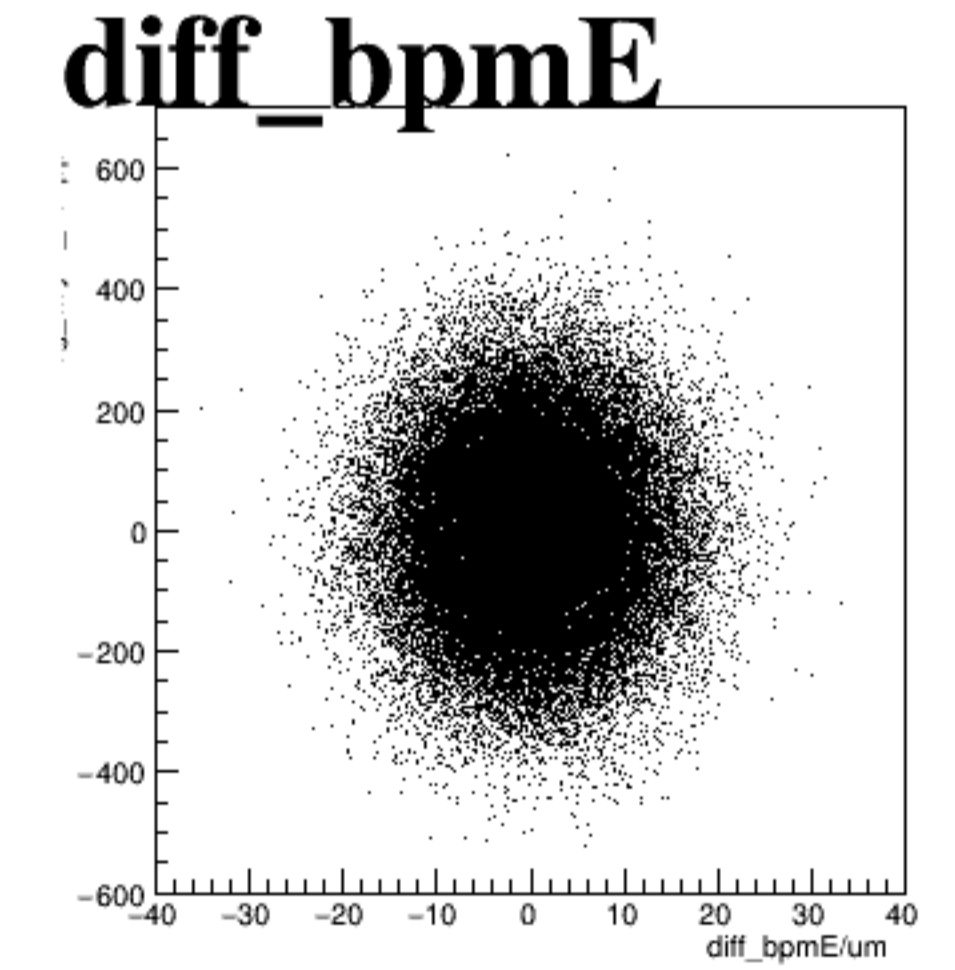
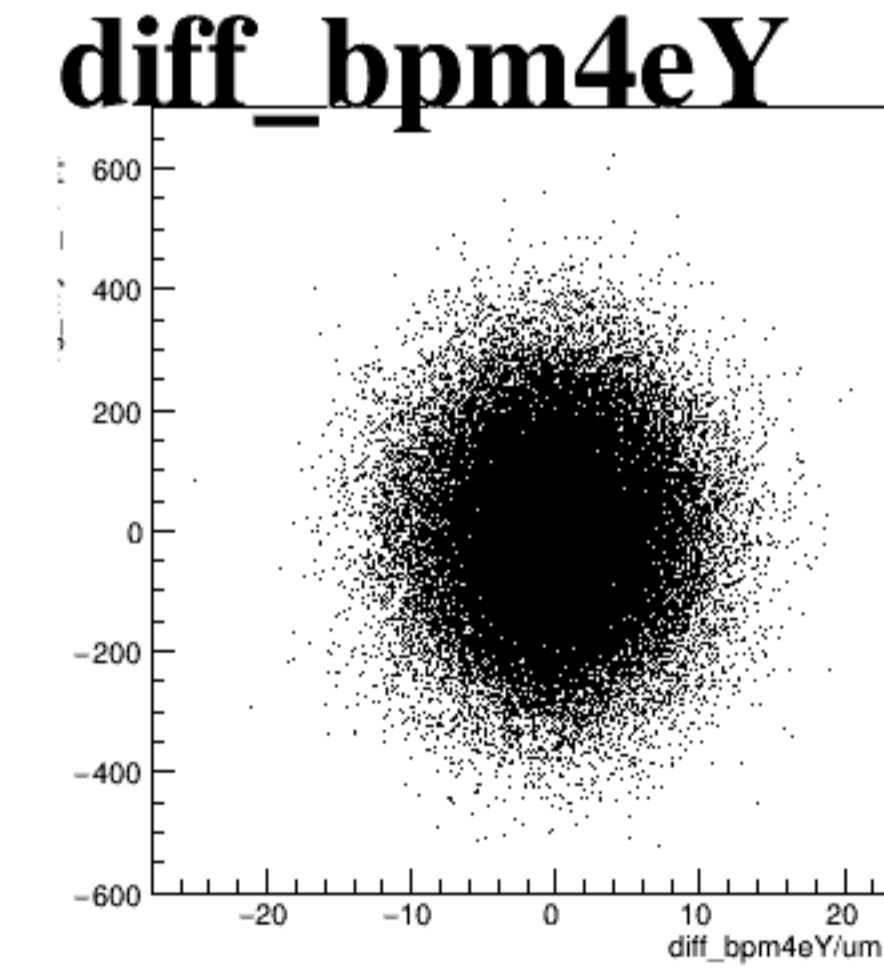
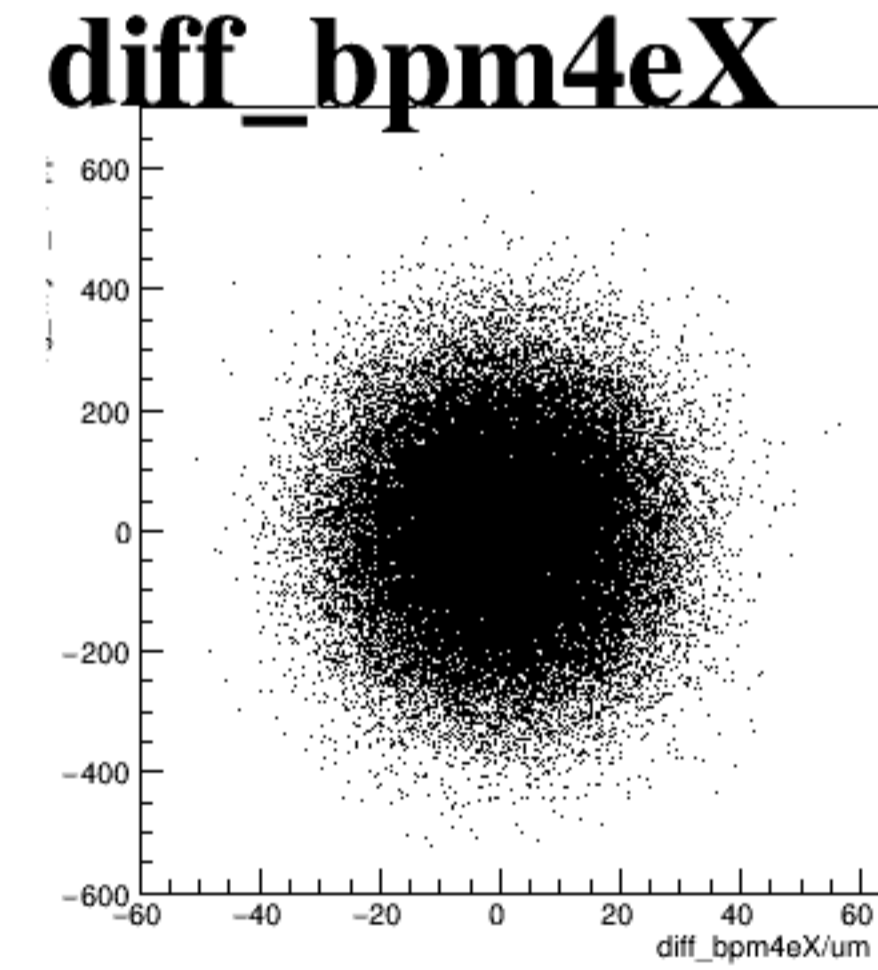
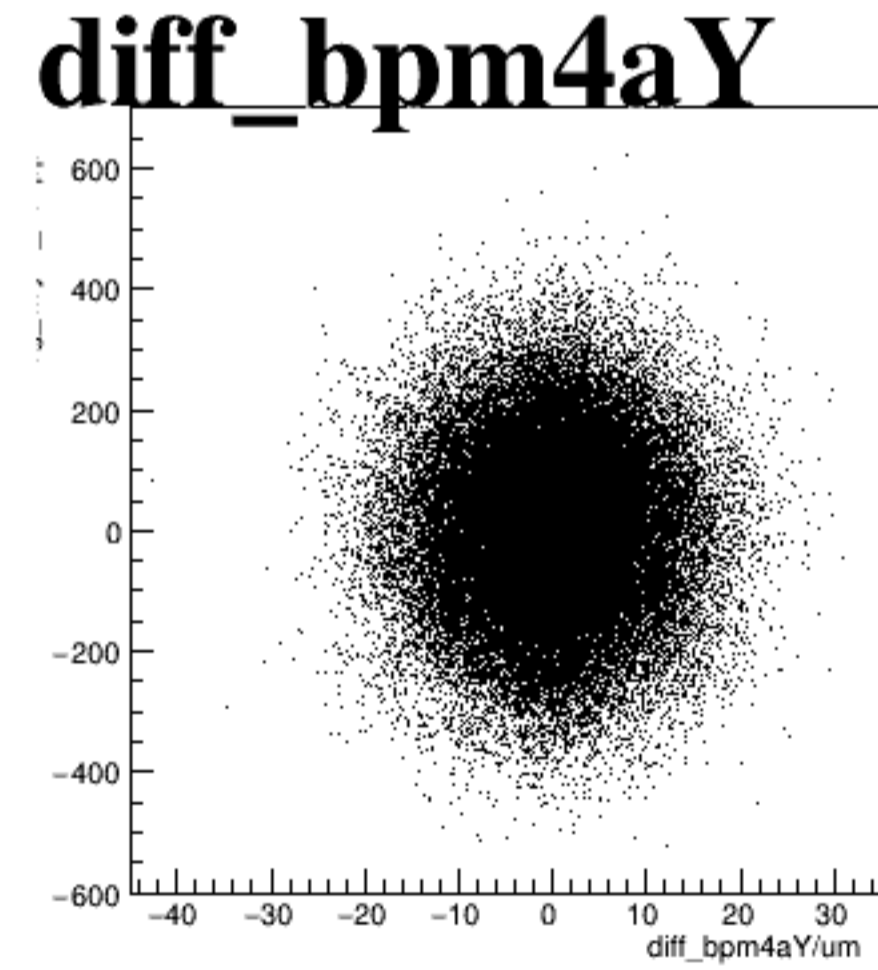
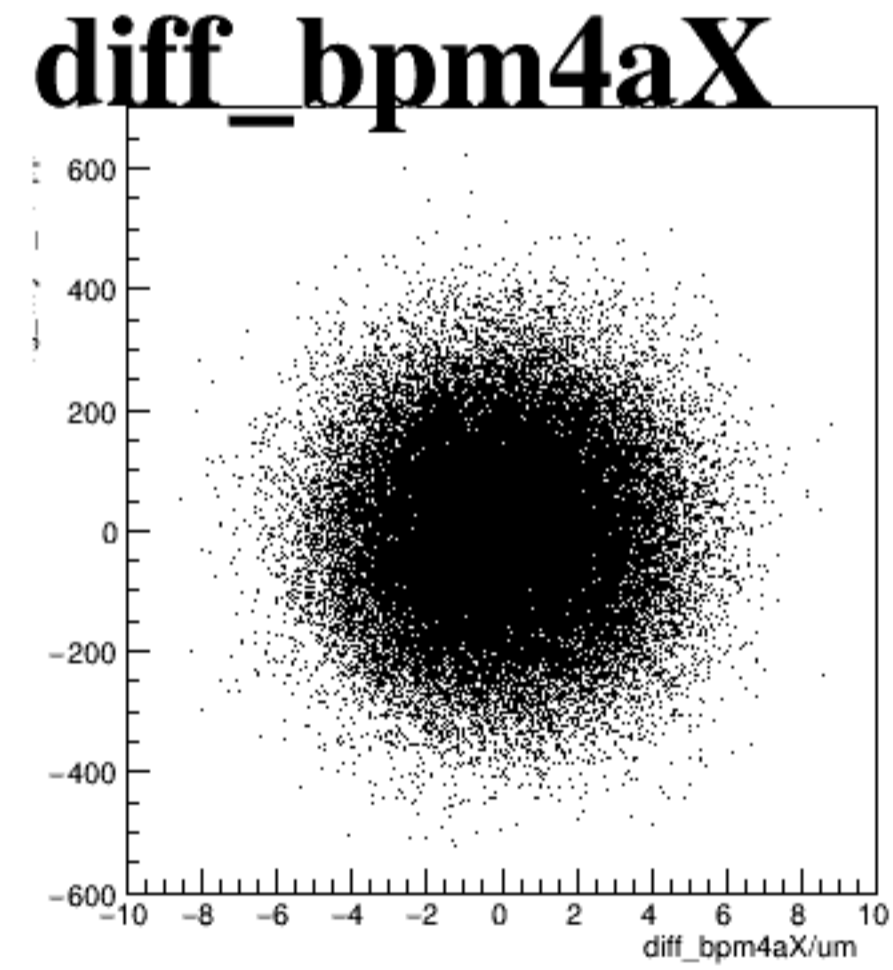
**reg\_asym\_usl**



**reg\_asym\_dsl**



**reg\_asym\_usr**



**reg\_asym\_dsr**

