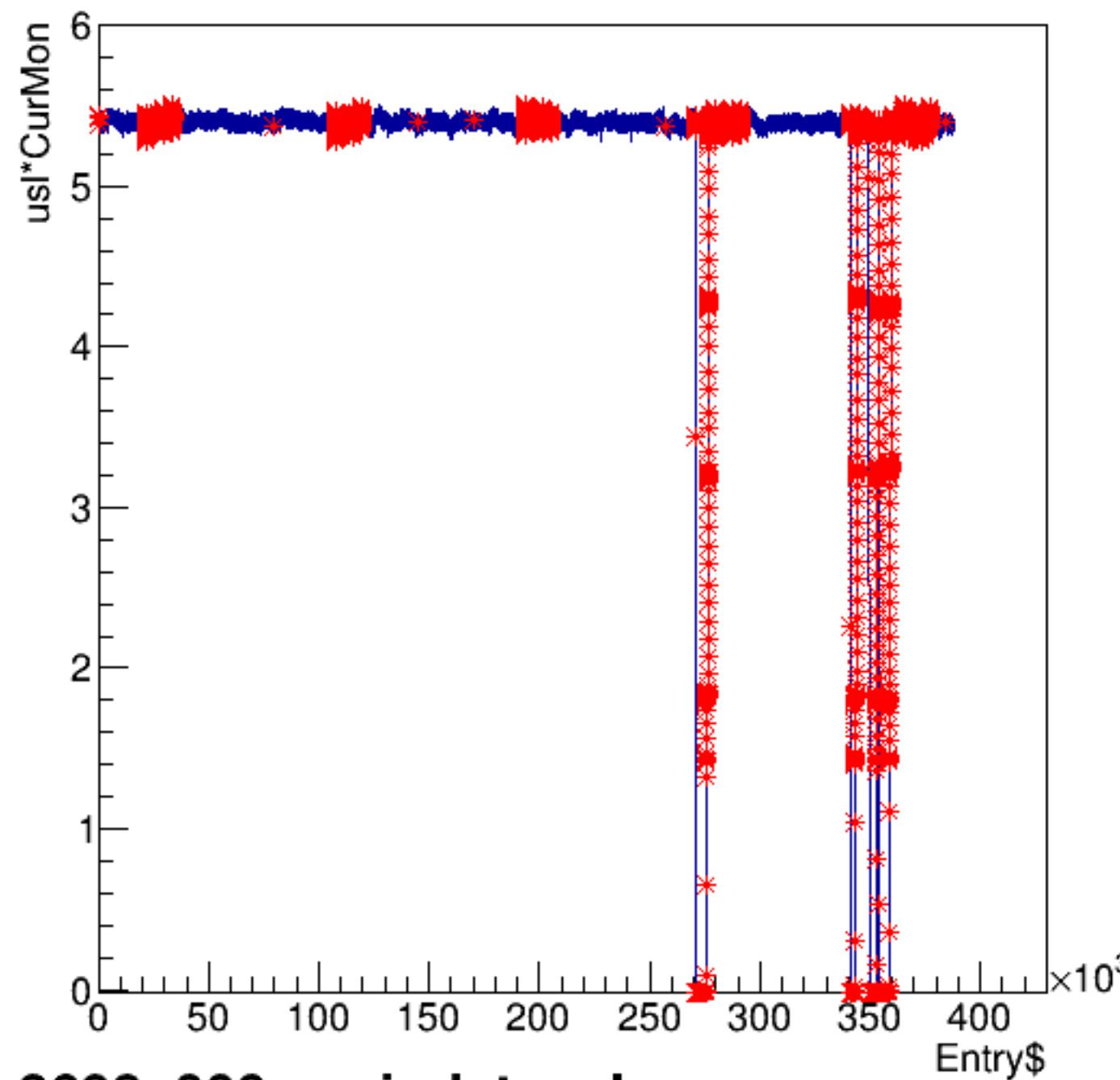
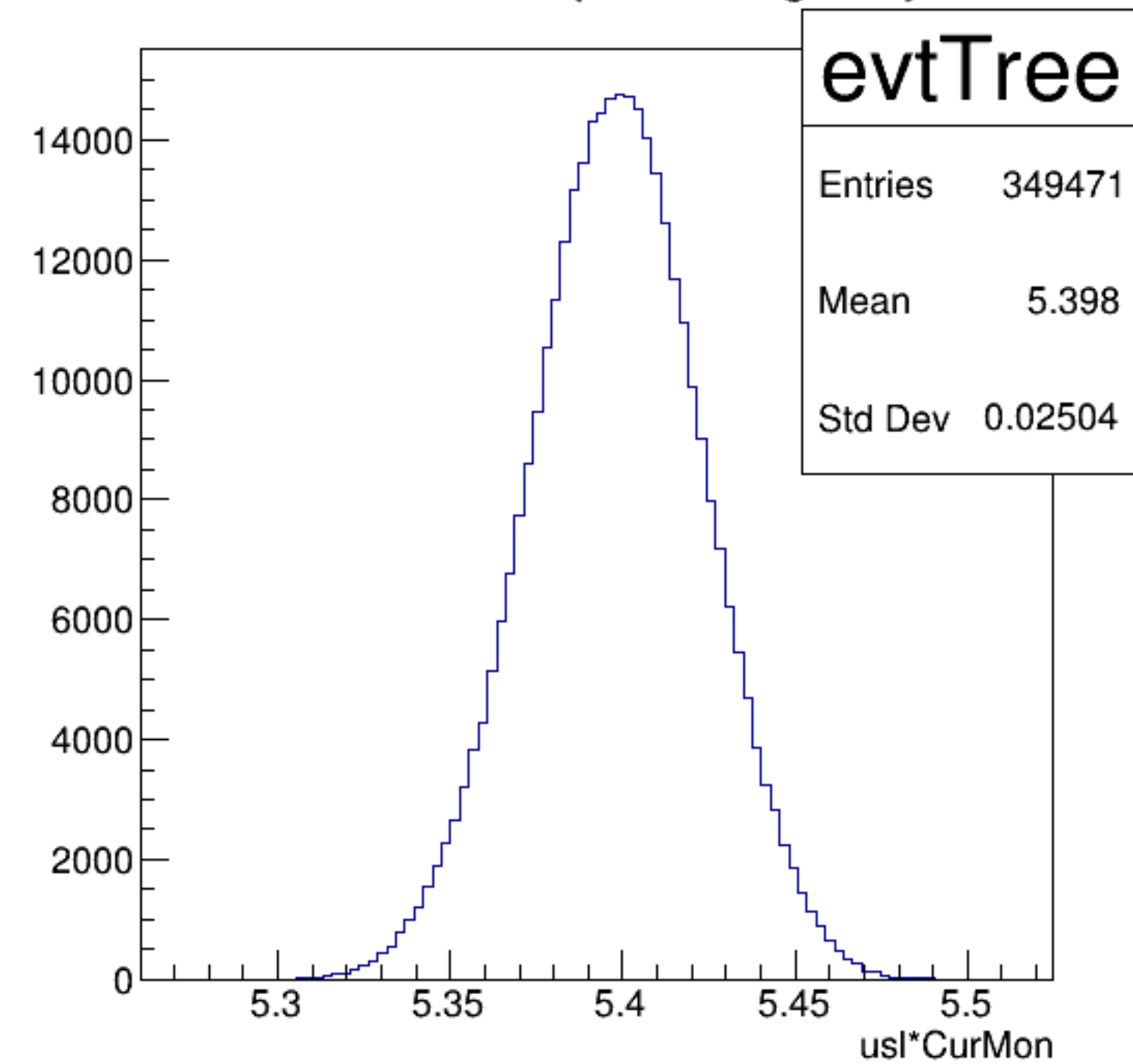


usl\*CurMon:Entry\$



run3698\_000\_maindet\_usl.png

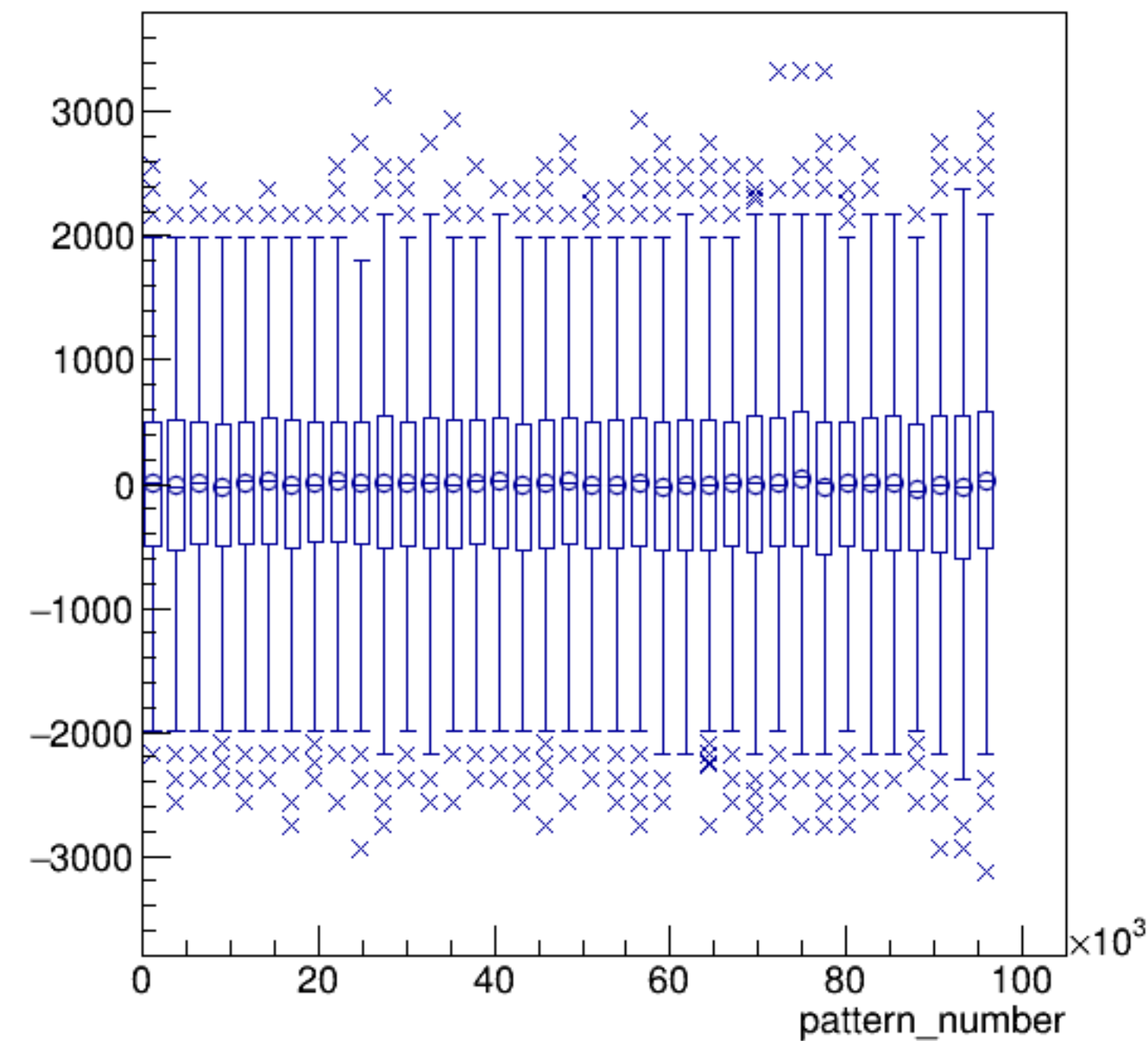
usl\*CurMon {ErrorFlag==0}



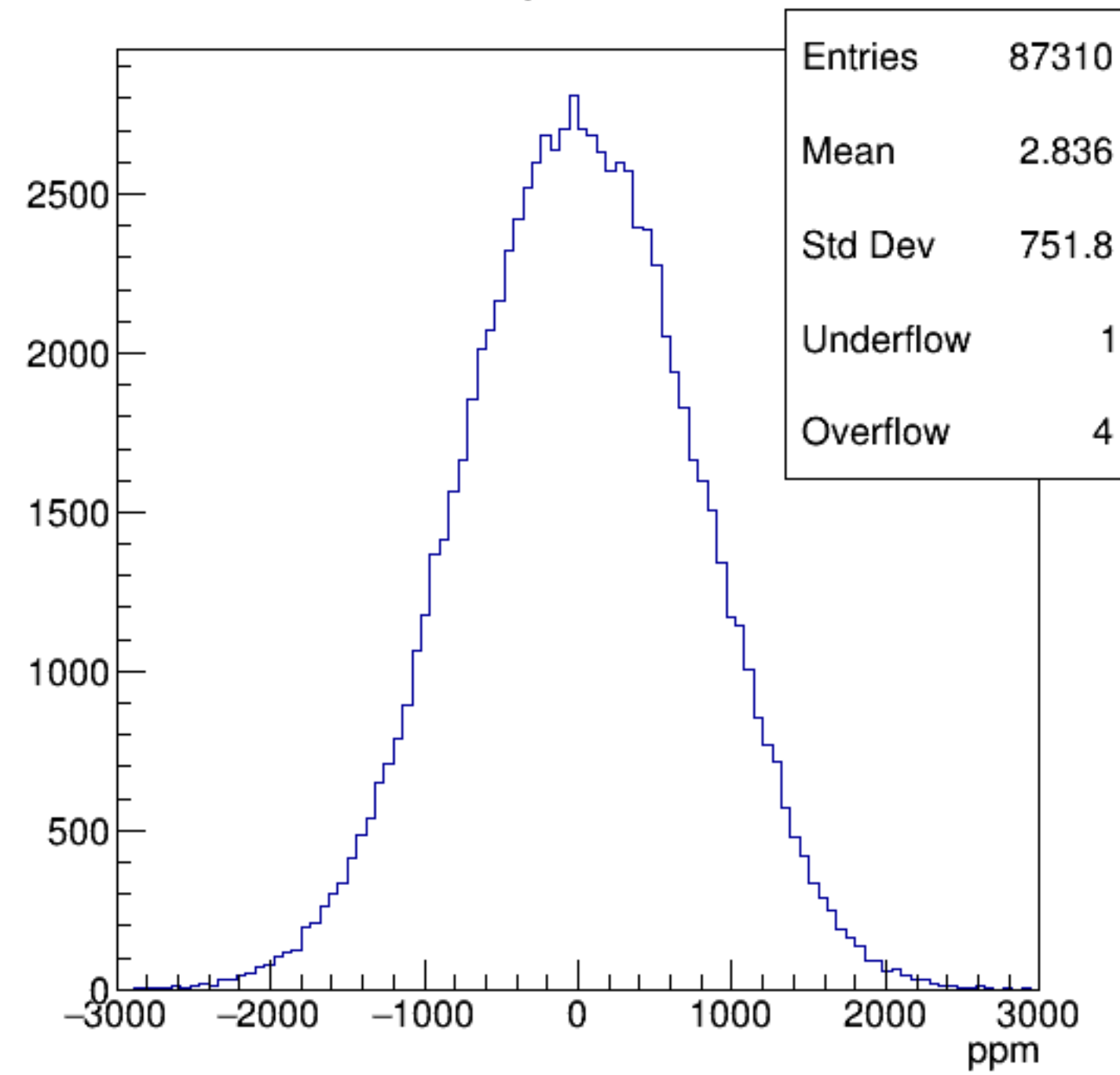
evtTree

Entries 349471  
Mean 5.398  
Std Dev 0.02504

asym\_usl/ppm:pattern\_number {ErrorFlag==0}

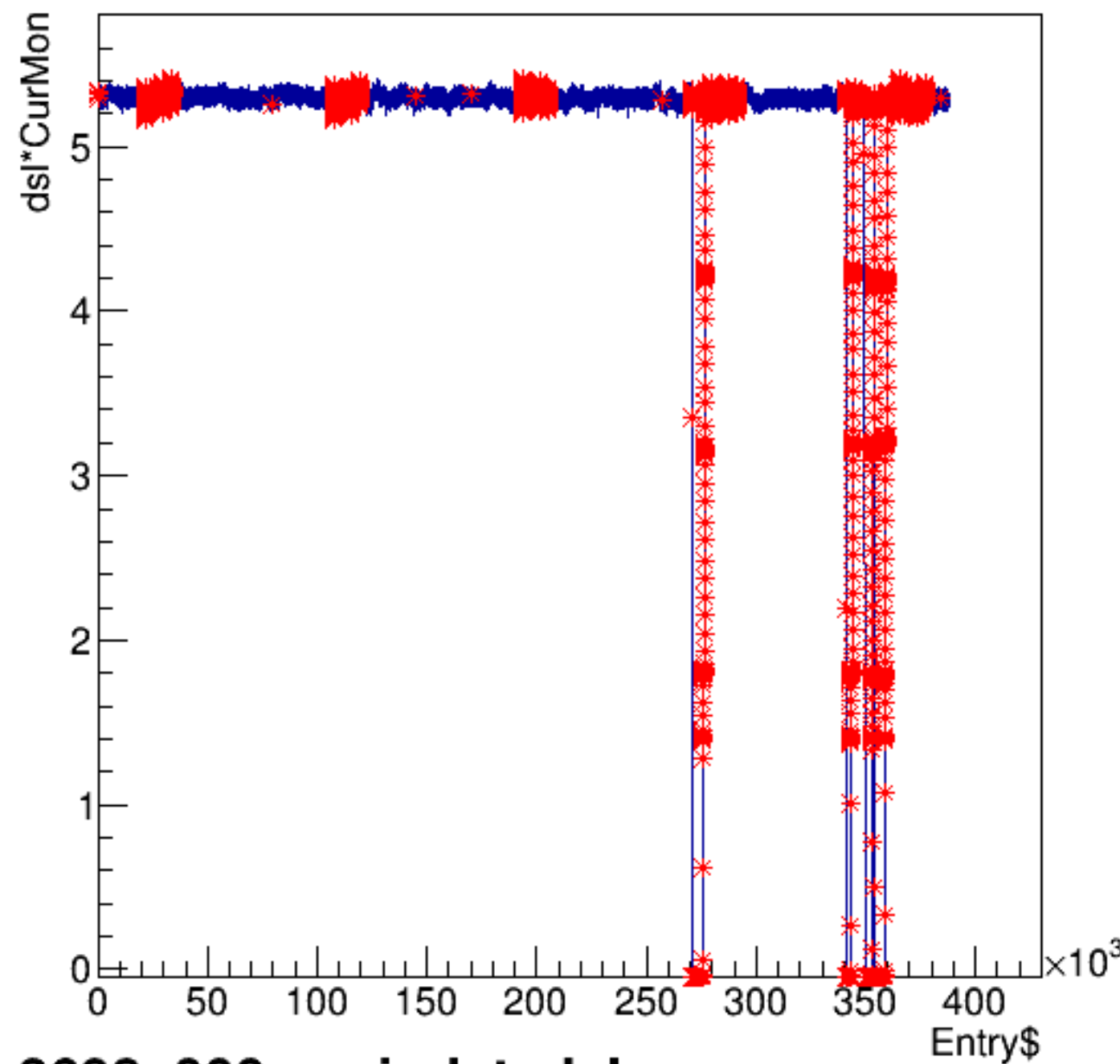


asym\_usl



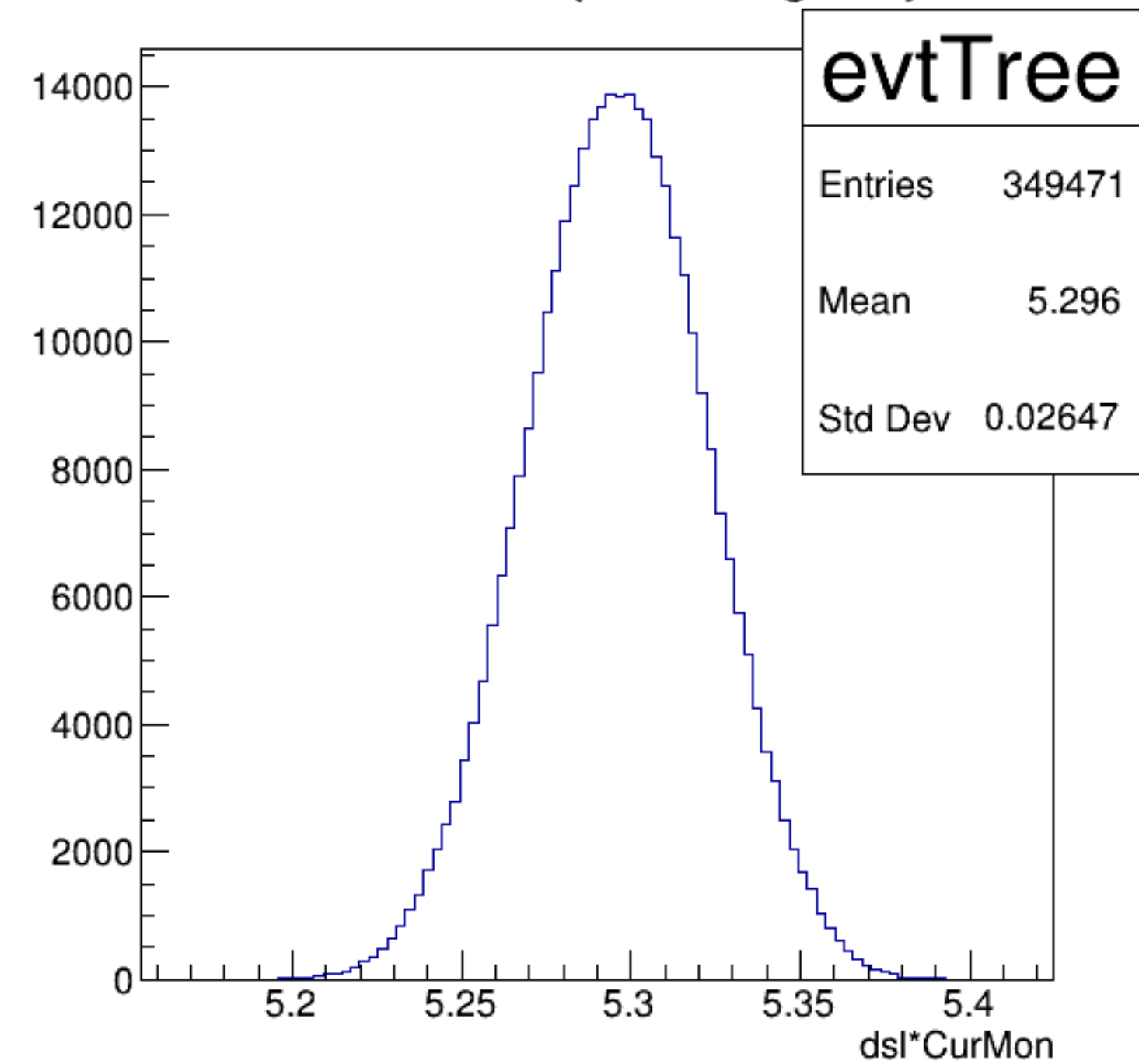
Entries 87310  
Mean 2.836  
Std Dev 751.8  
Underflow 1  
Overflow 4

dsl\*CurMon:Entry\$

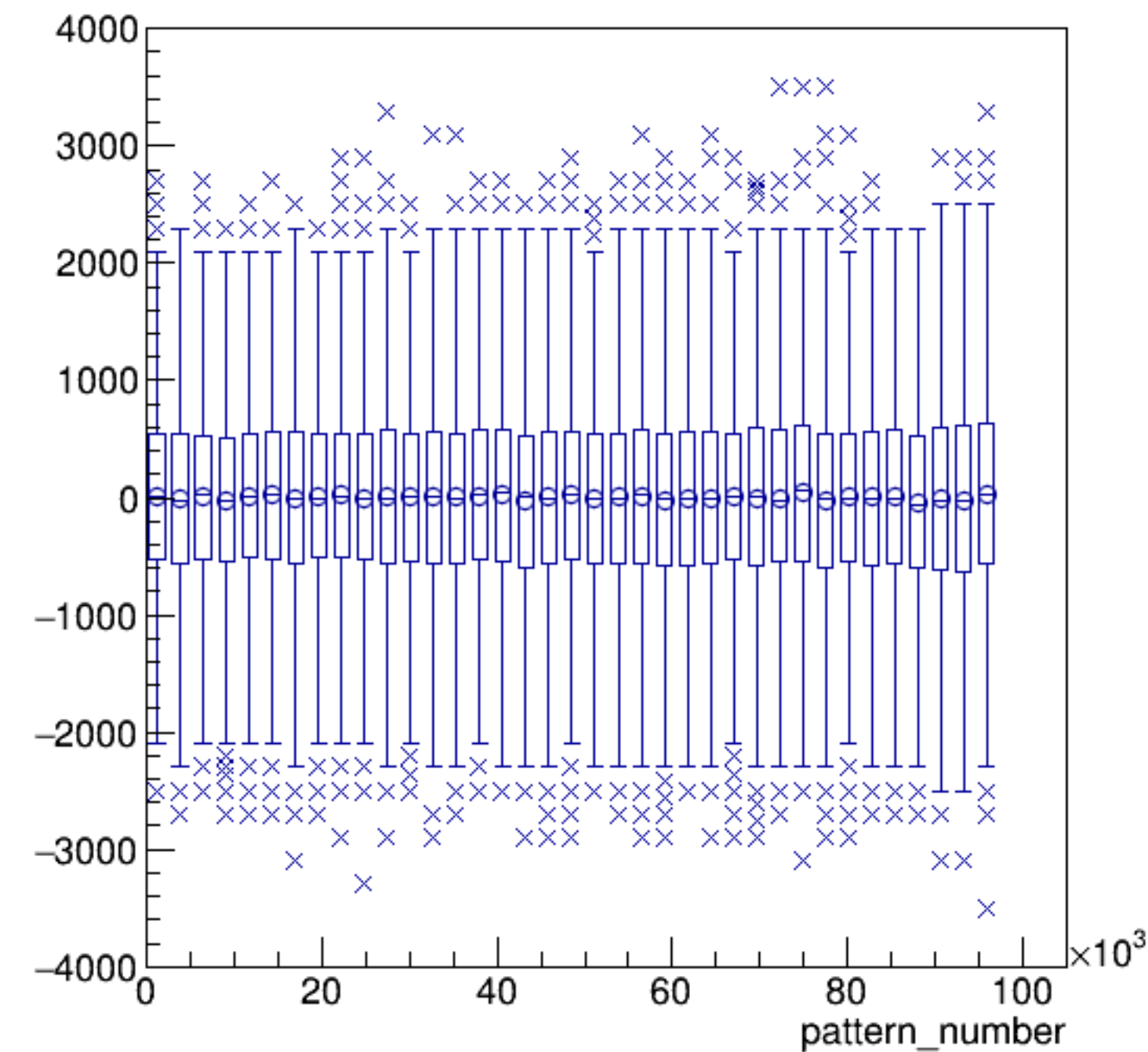


run3698\_000\_maindet\_dsl.png

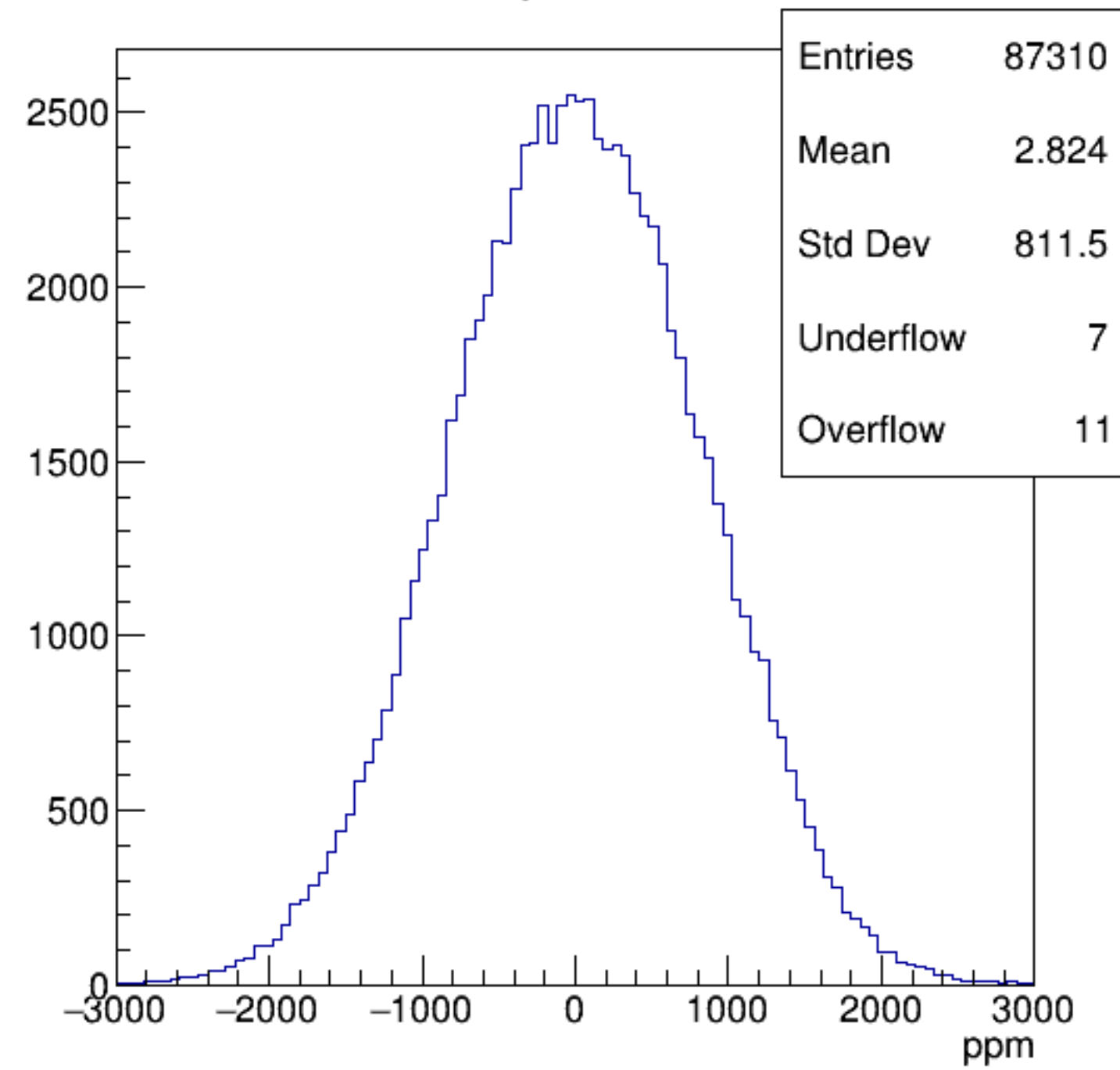
dsl\*CurMon {ErrorFlag==0}



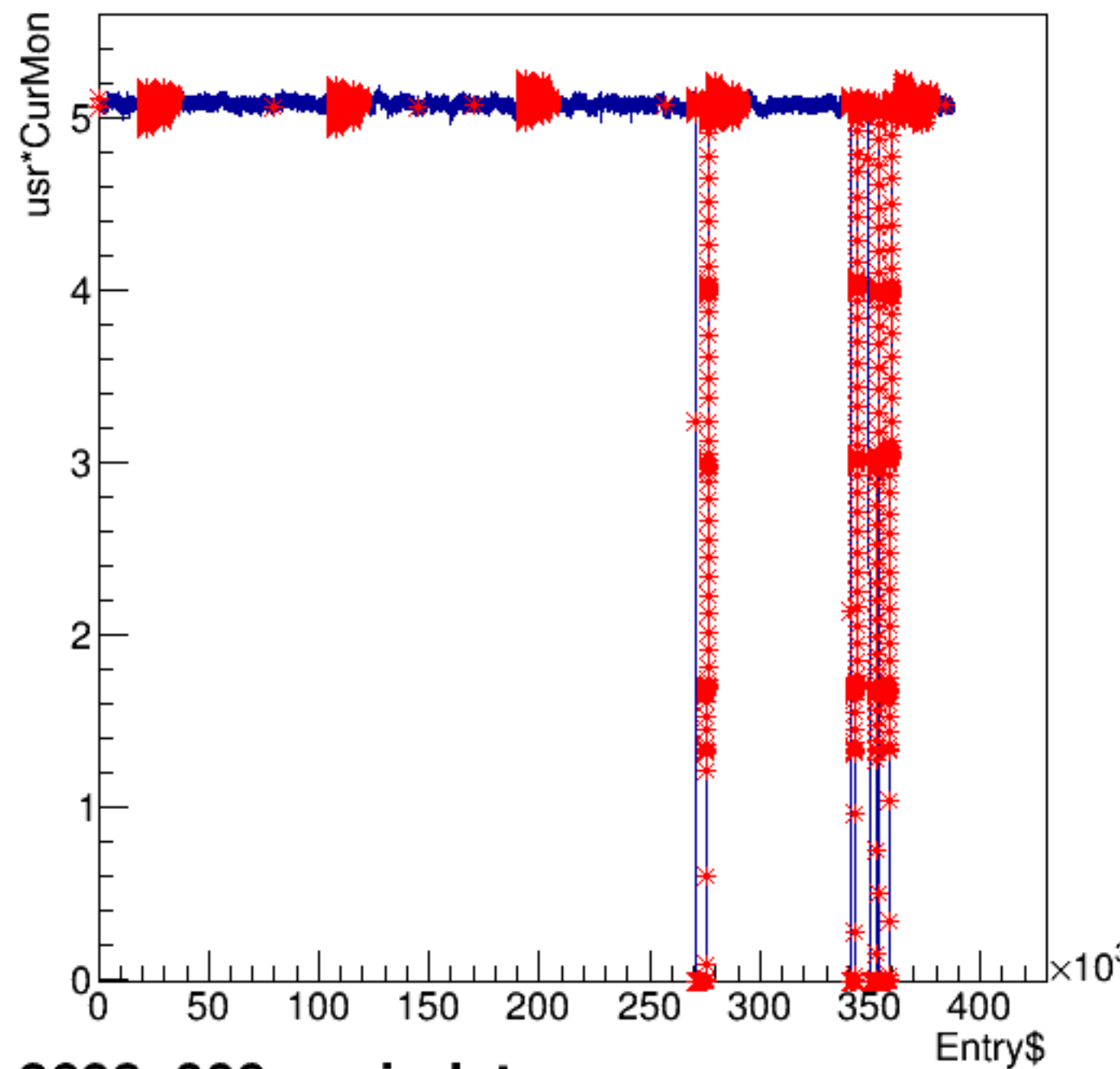
asym\_dsl/ppm:pattern\_number {ErrorFlag==0}



asym\_dsl

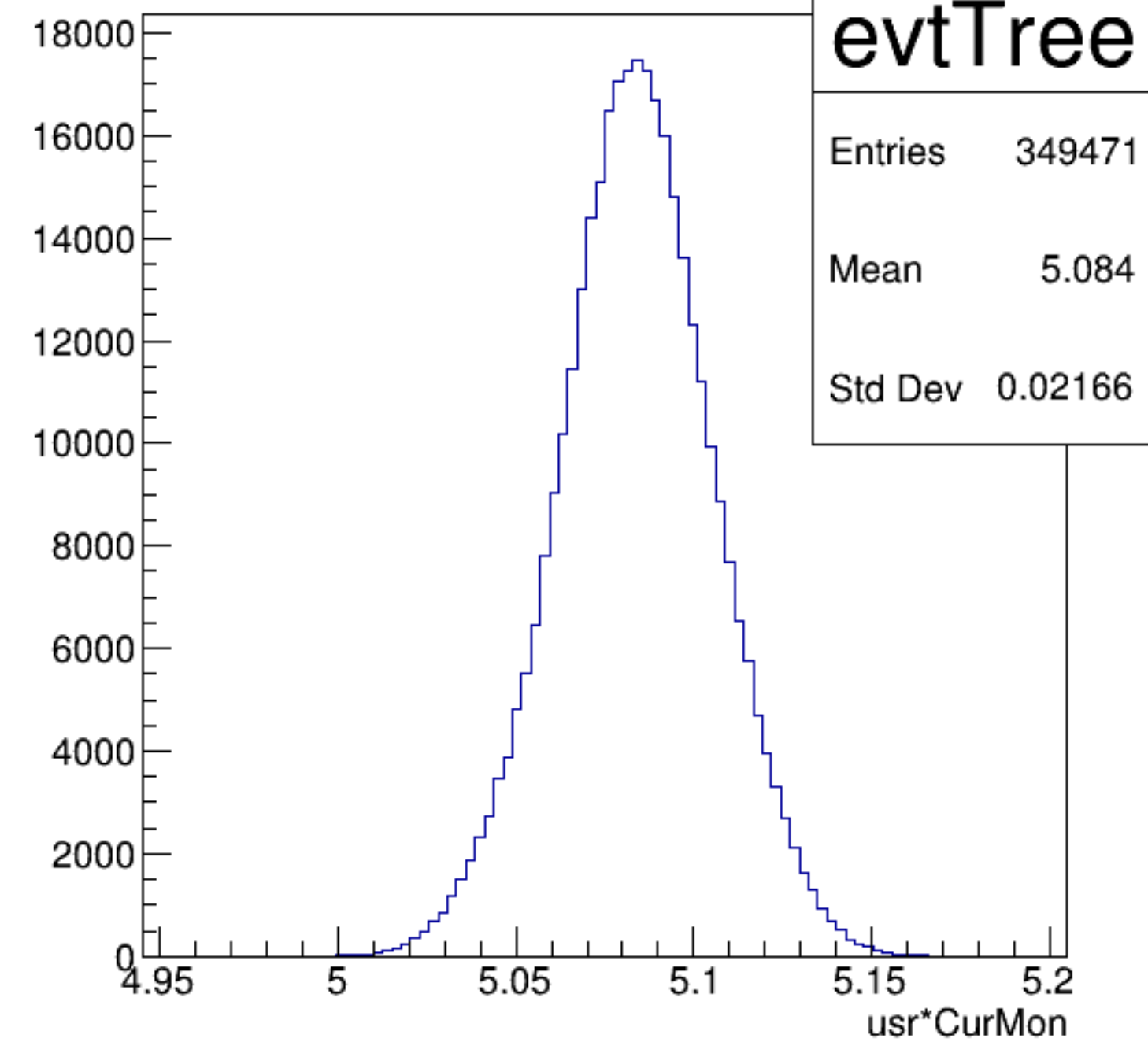


usr\*CurMon:Entry\$



run3698\_000\_maindet\_usr.png

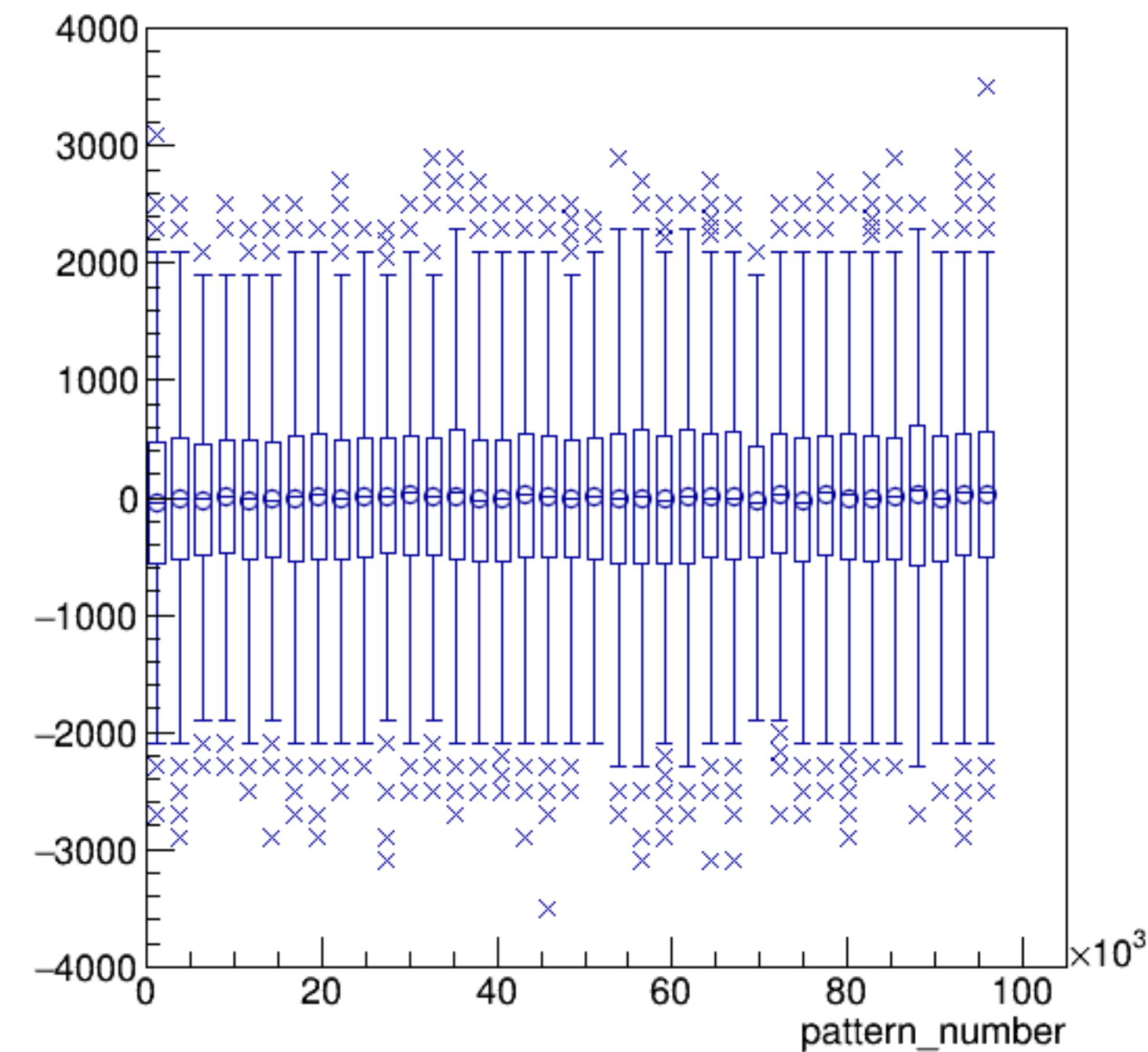
usr\*CurMon {ErrorFlag==0}



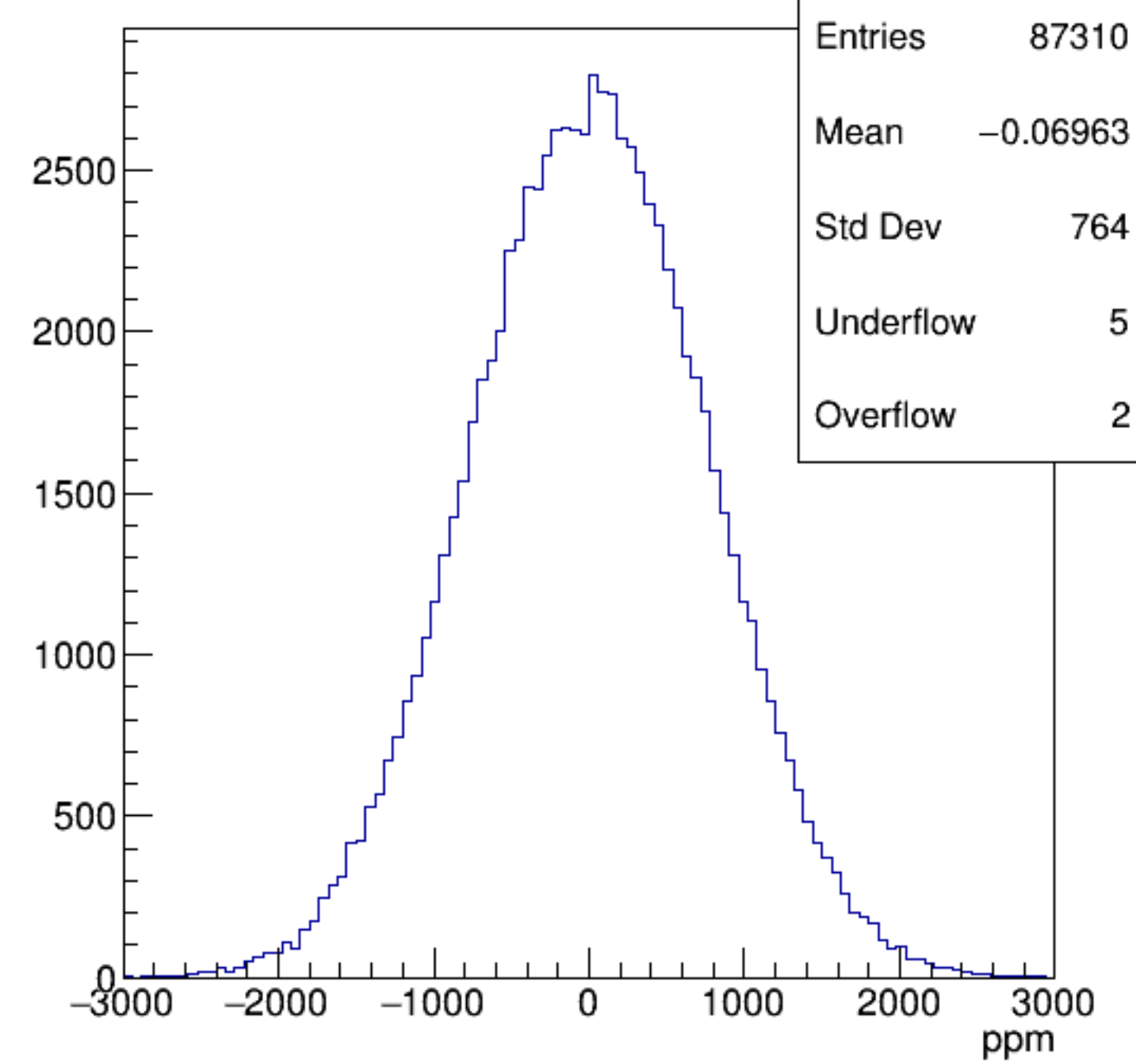
evtTree

|         |         |
|---------|---------|
| Entries | 349471  |
| Mean    | 5.084   |
| Std Dev | 0.02166 |

asym\_usr/ppm:pattern\_number {ErrorFlag==0}

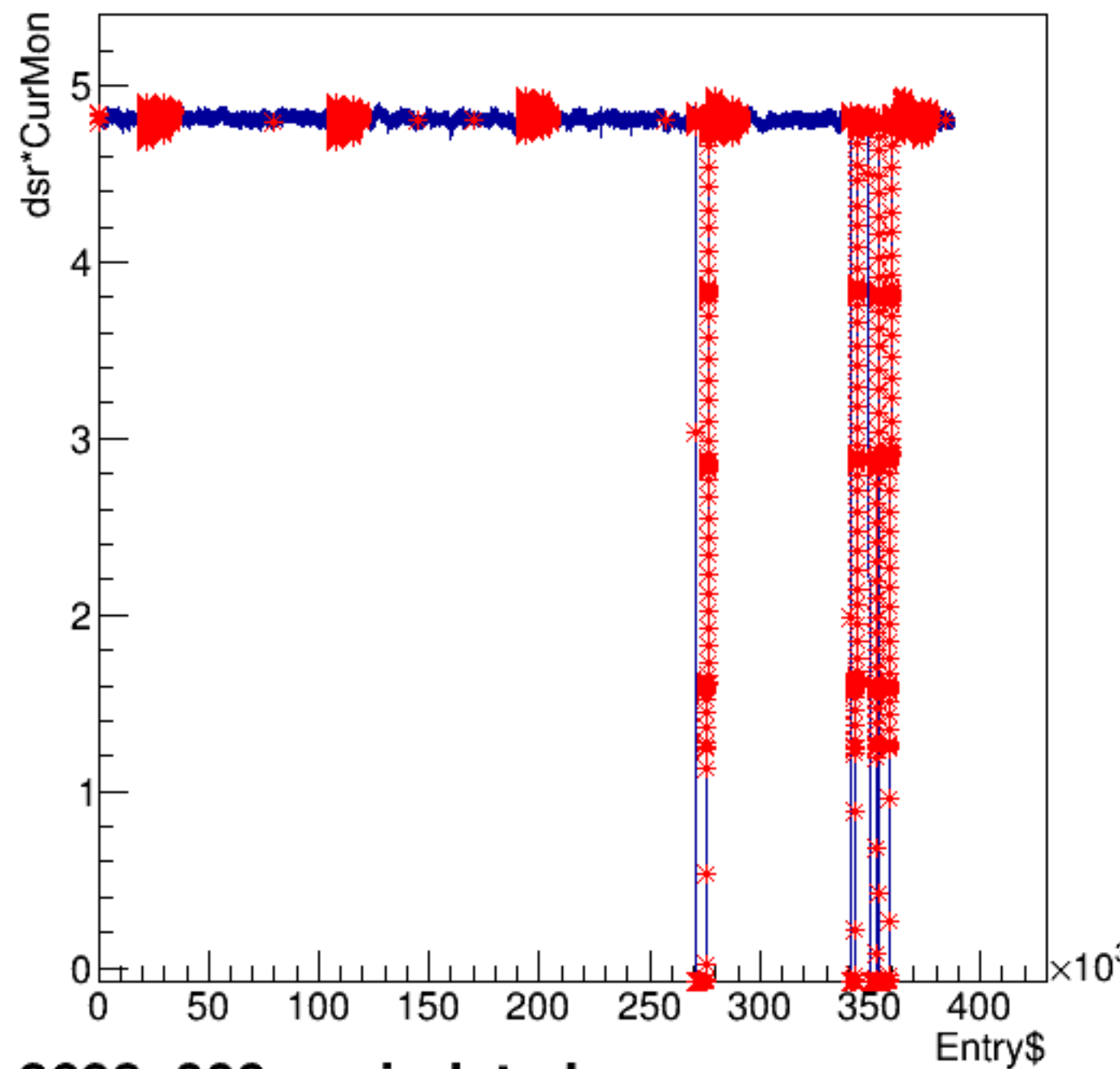


asym\_usr



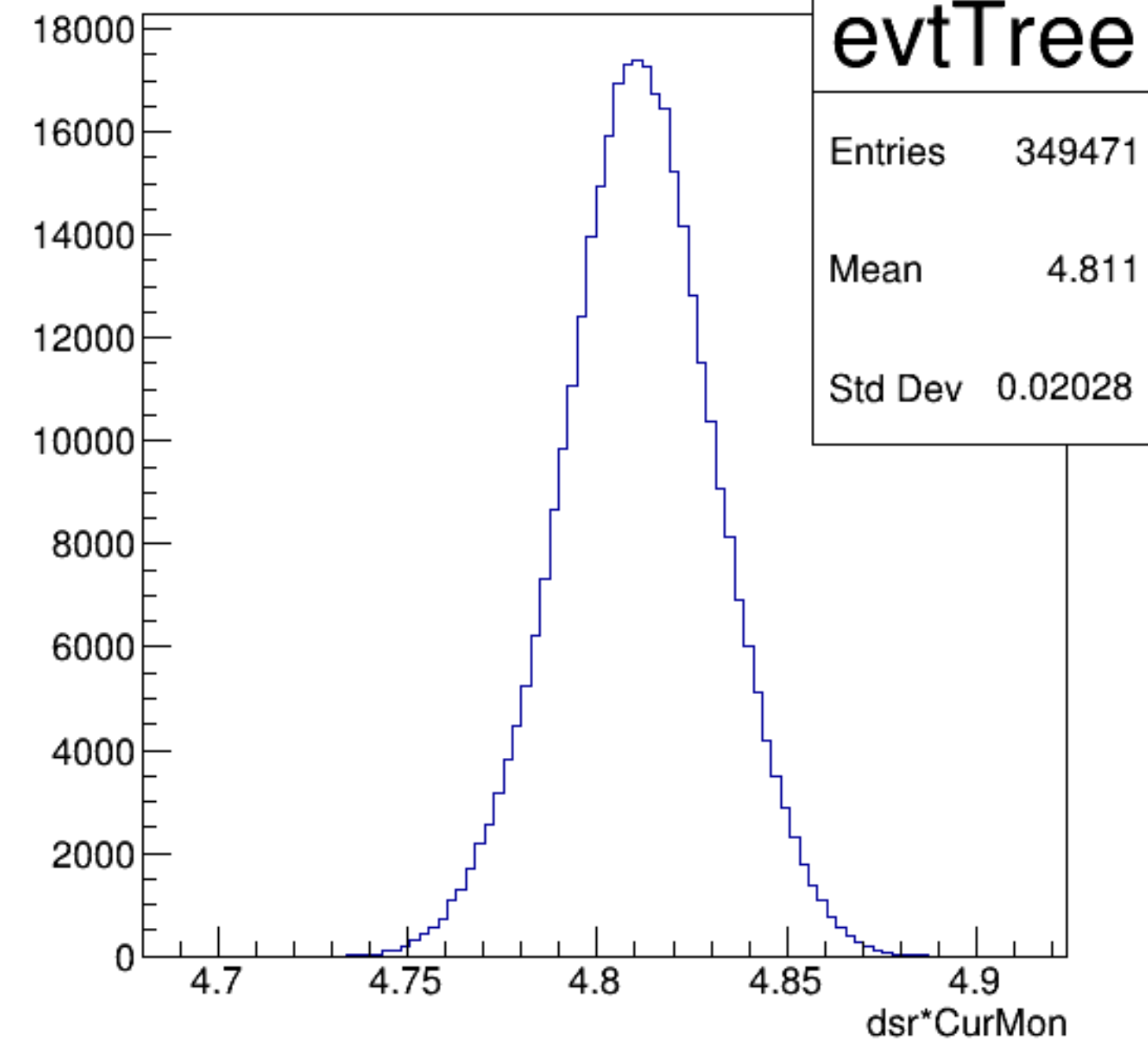
|           |          |
|-----------|----------|
| Entries   | 87310    |
| Mean      | -0.06963 |
| Std Dev   | 764      |
| Underflow | 5        |
| Overflow  | 2        |

dsr\*CurMon:Entry\$

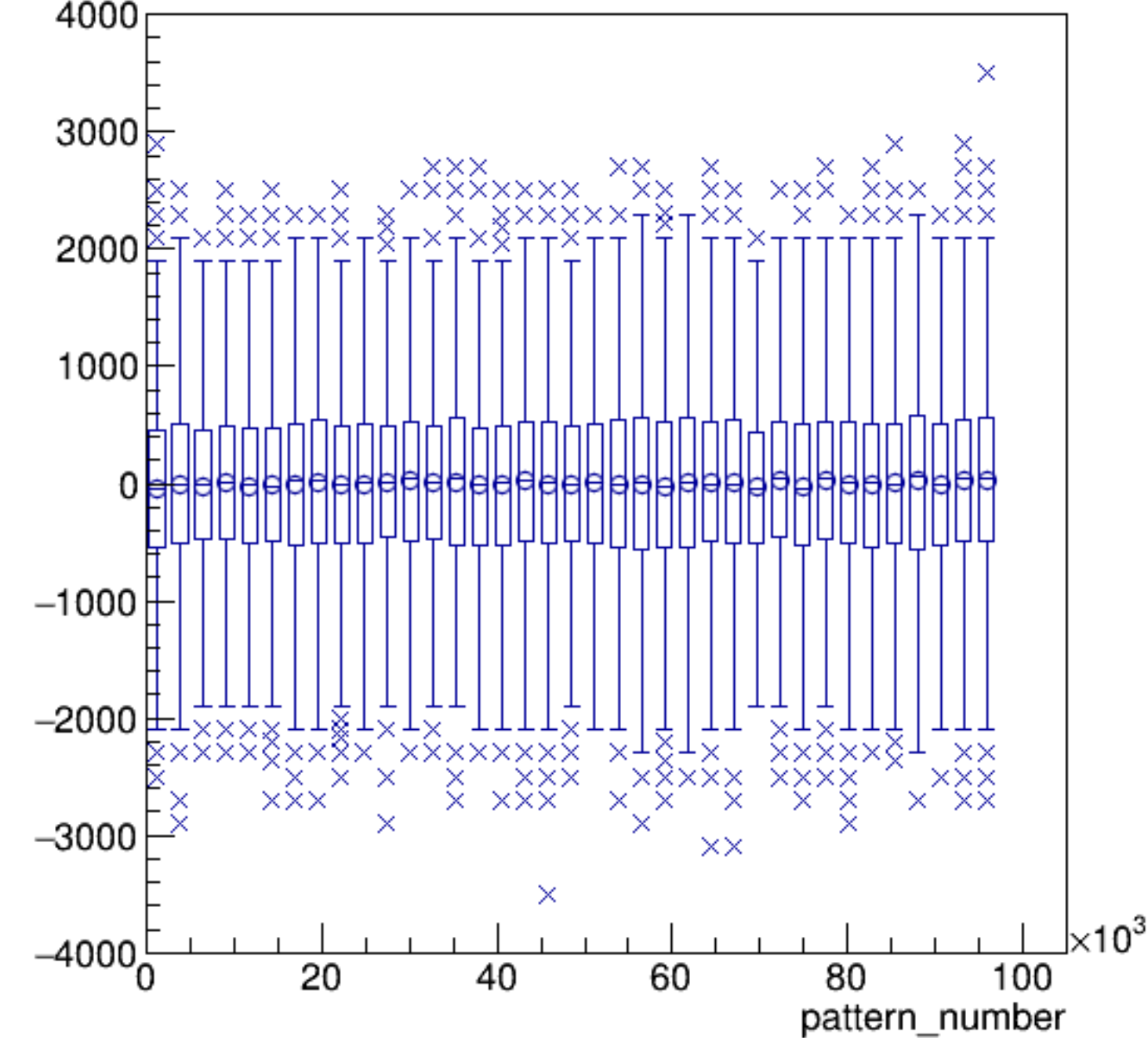


run3698\_000\_maindet\_dsr.png

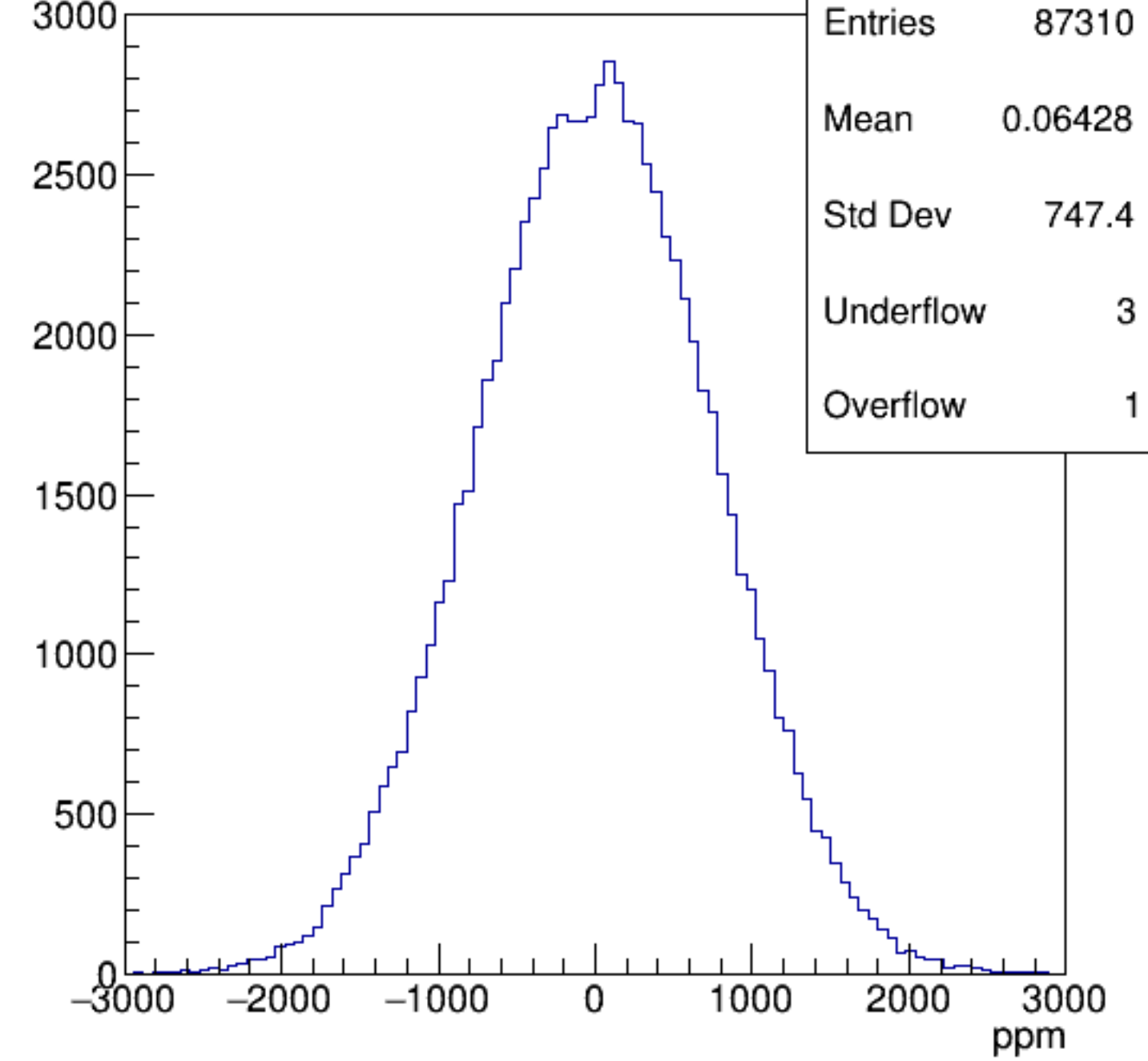
dsr\*CurMon {ErrorFlag==0}



asym\_dsr/ppm:pattern\_number {ErrorFlag==0}

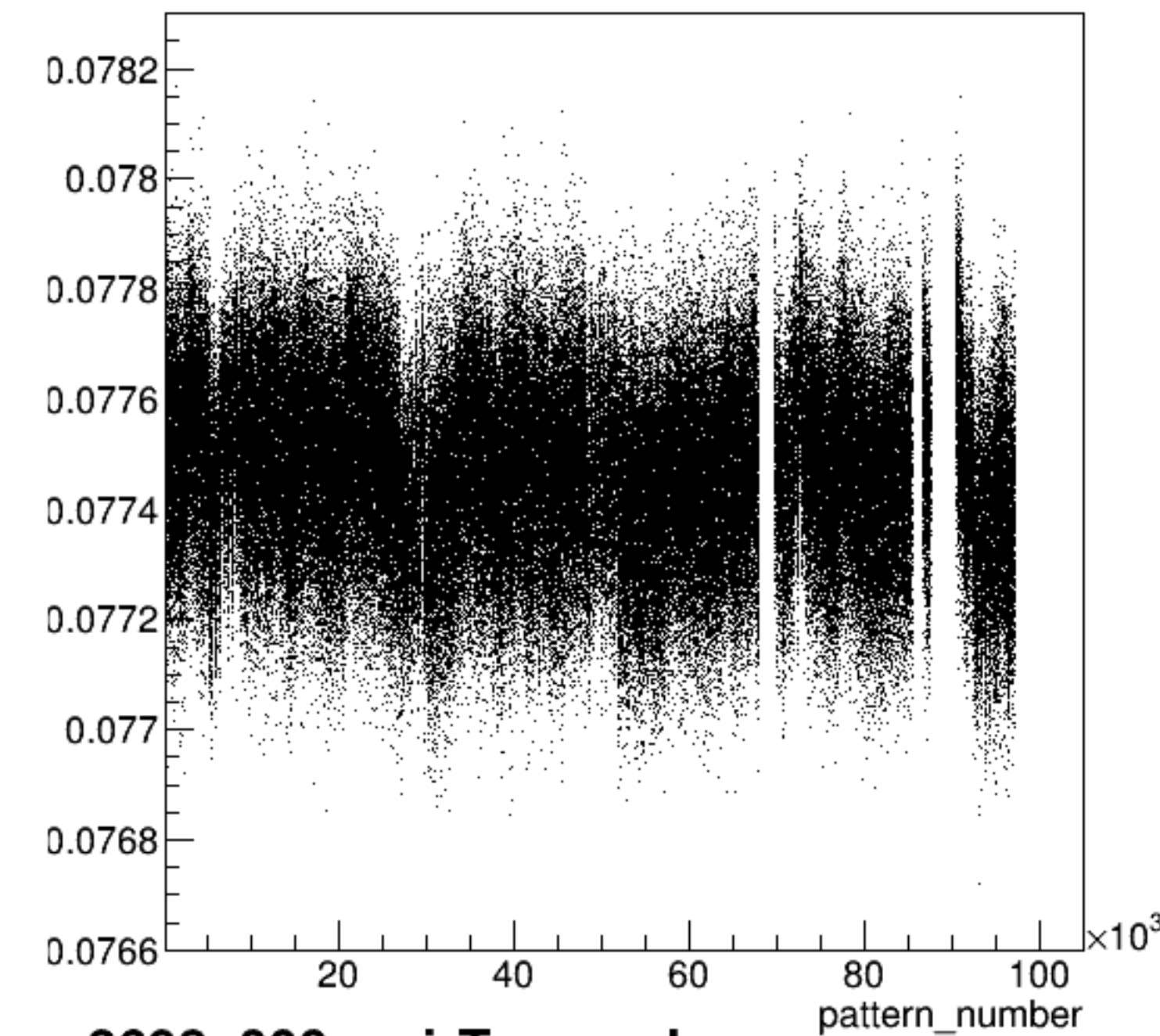


asym\_dsr



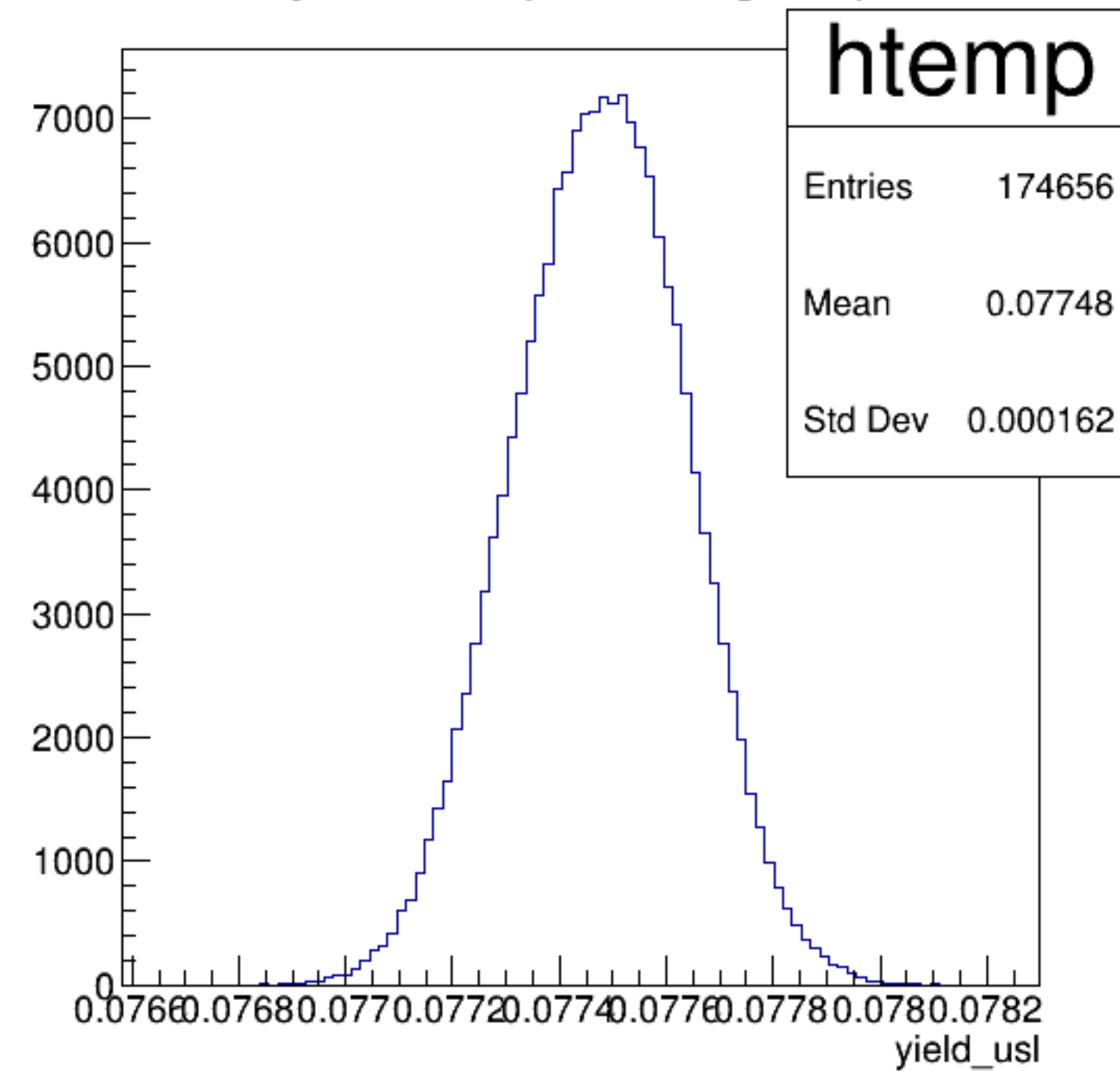


yield\_usl:pattern\_number {ErrorFlag==0}

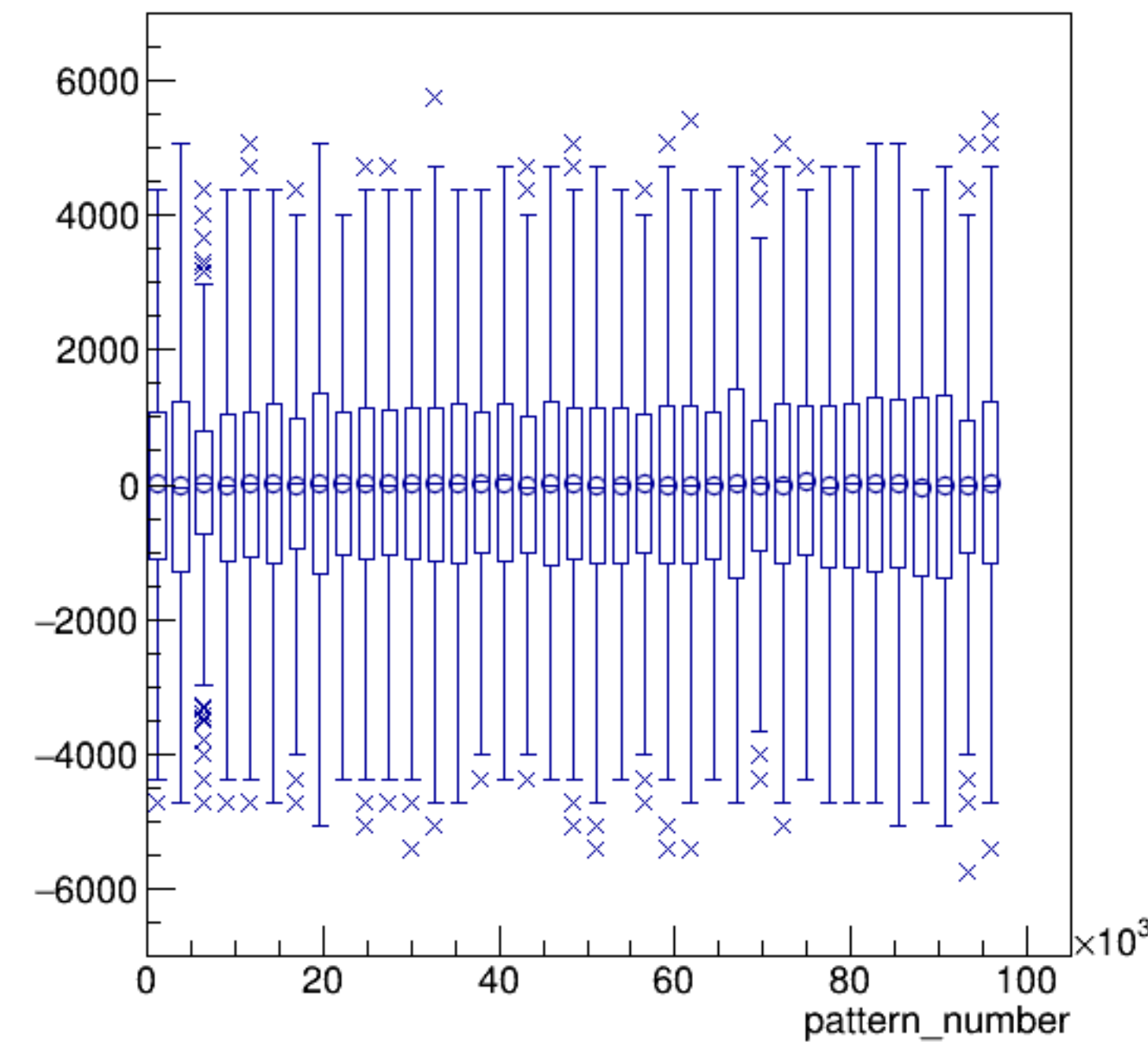


run3698\_000\_pairTree\_usl.png

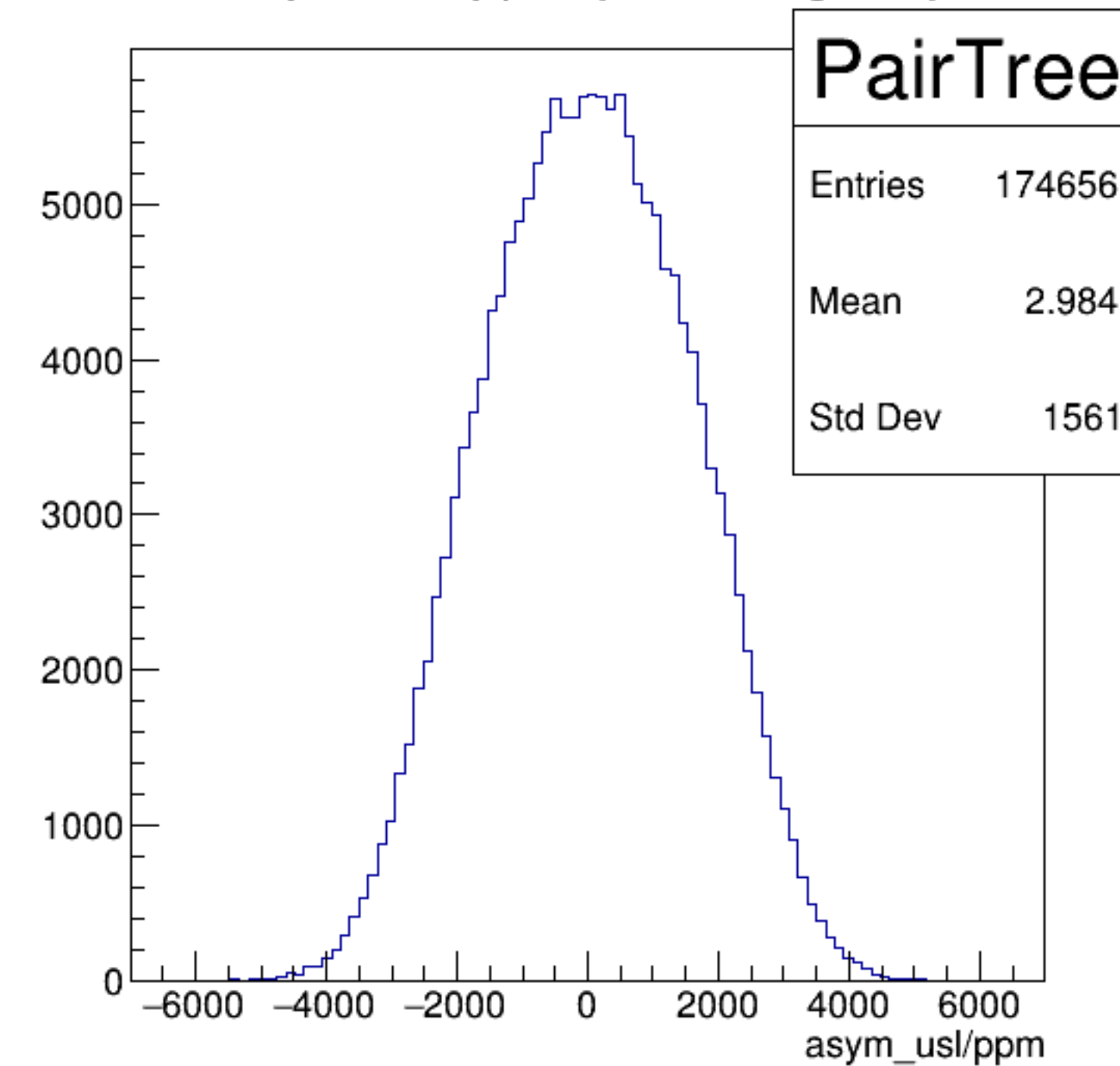
yield\_usl {ErrorFlag==0}



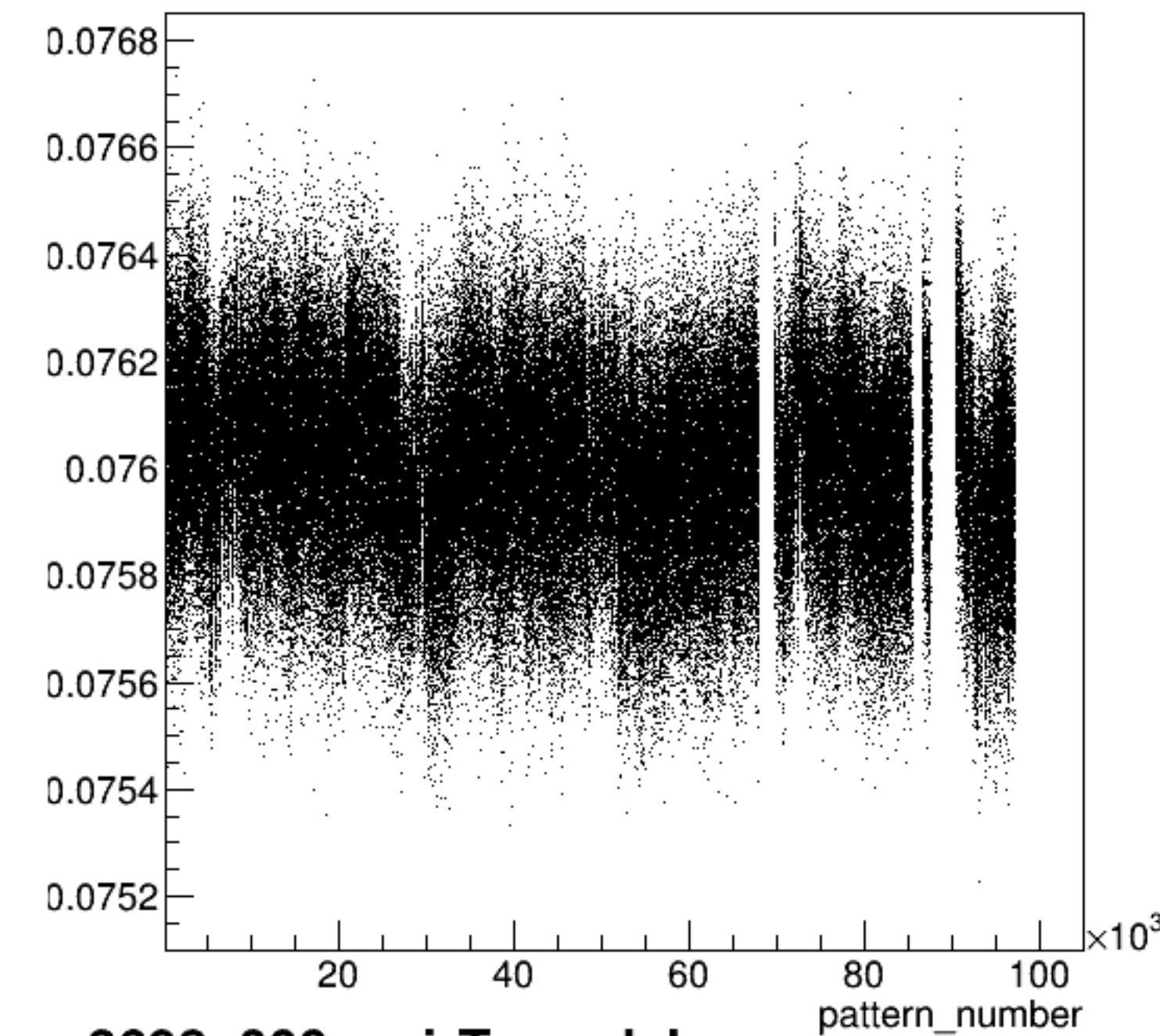
asym\_usl/ppm:pattern\_number {ErrorFlag==0}



asym\_usl/ppm {ErrorFlag==0}

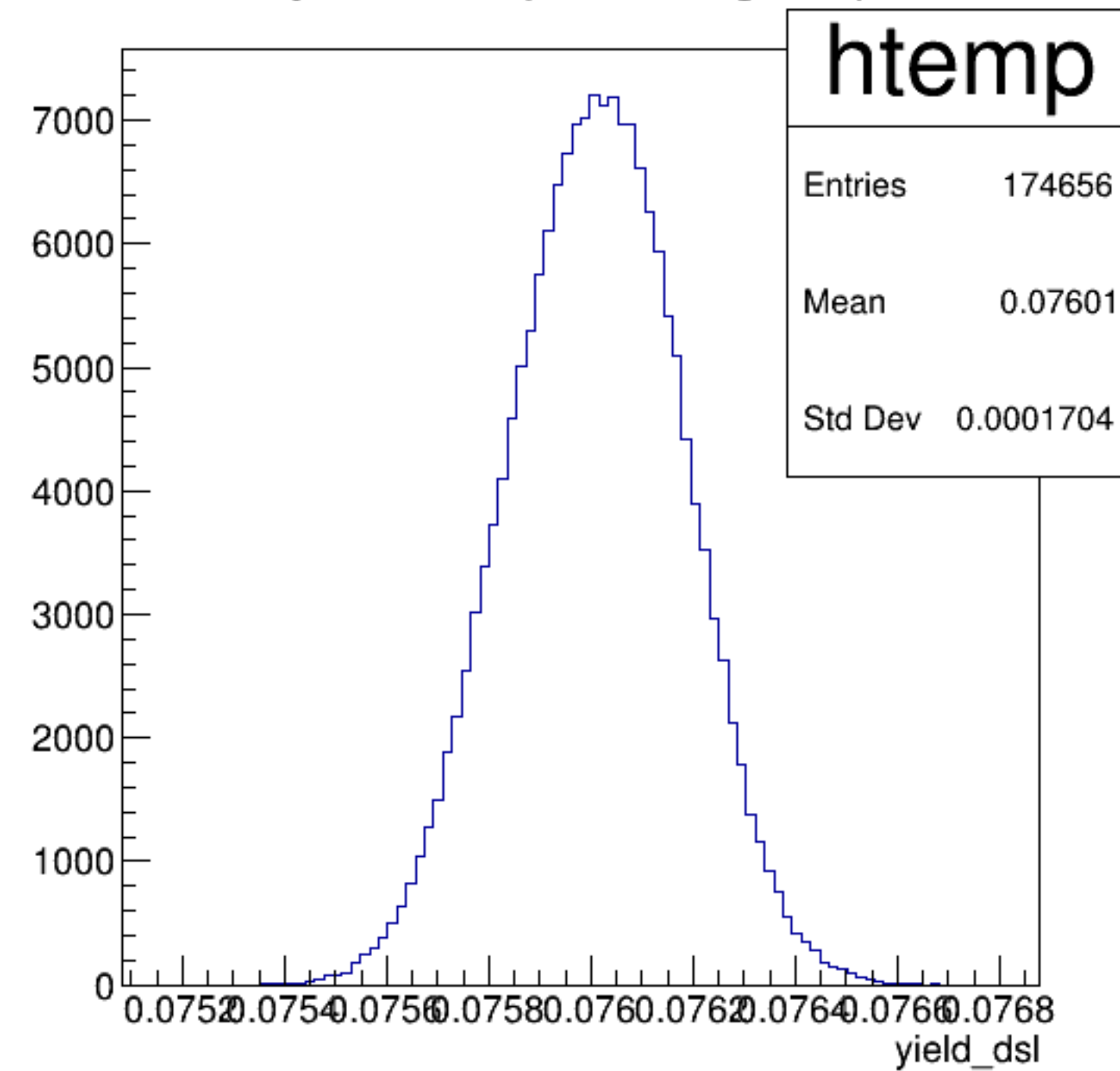


yield\_dsl:pattern\_number {ErrorFlag==0}

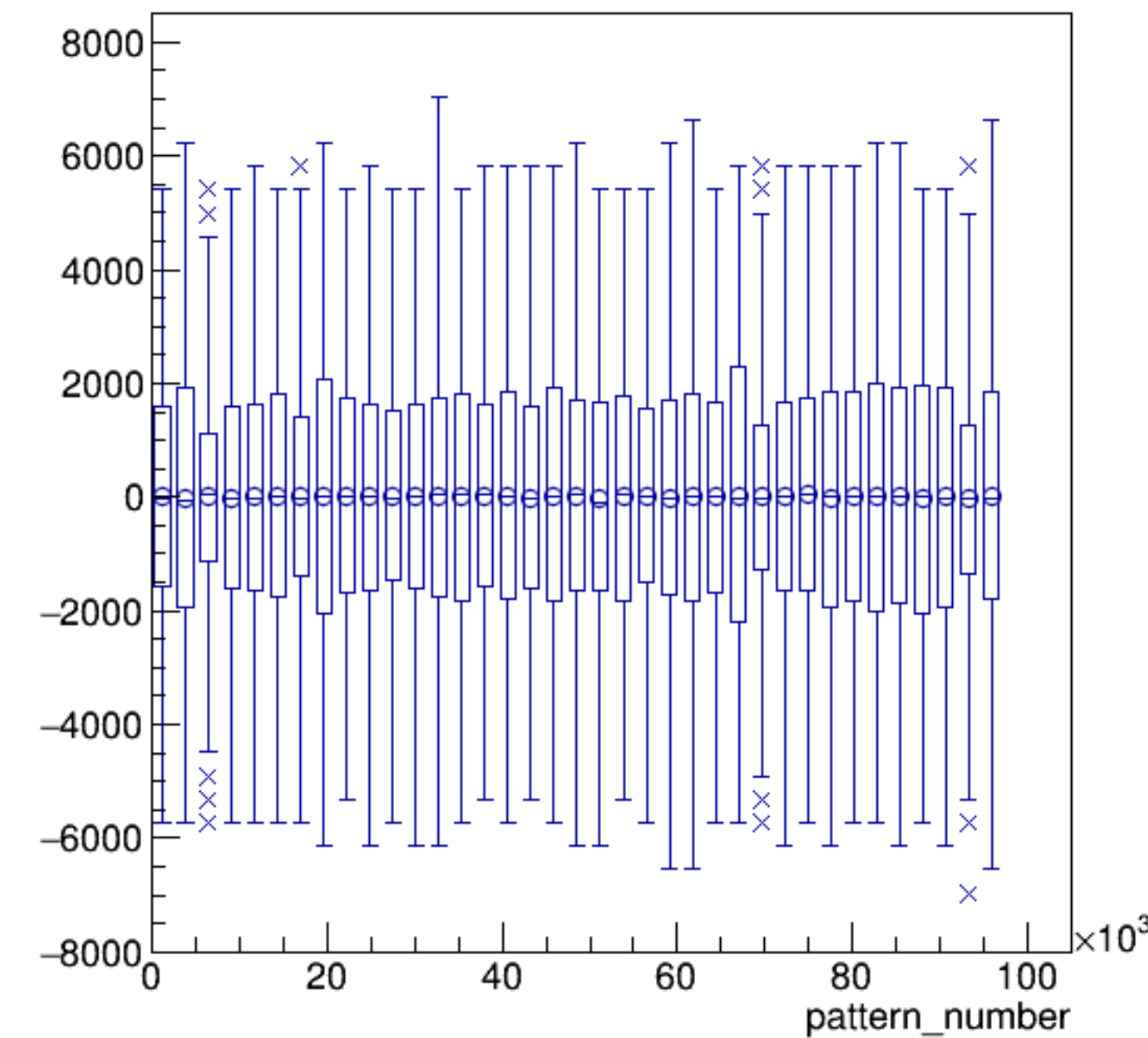


run3698\_000\_pairTree\_dsl.png

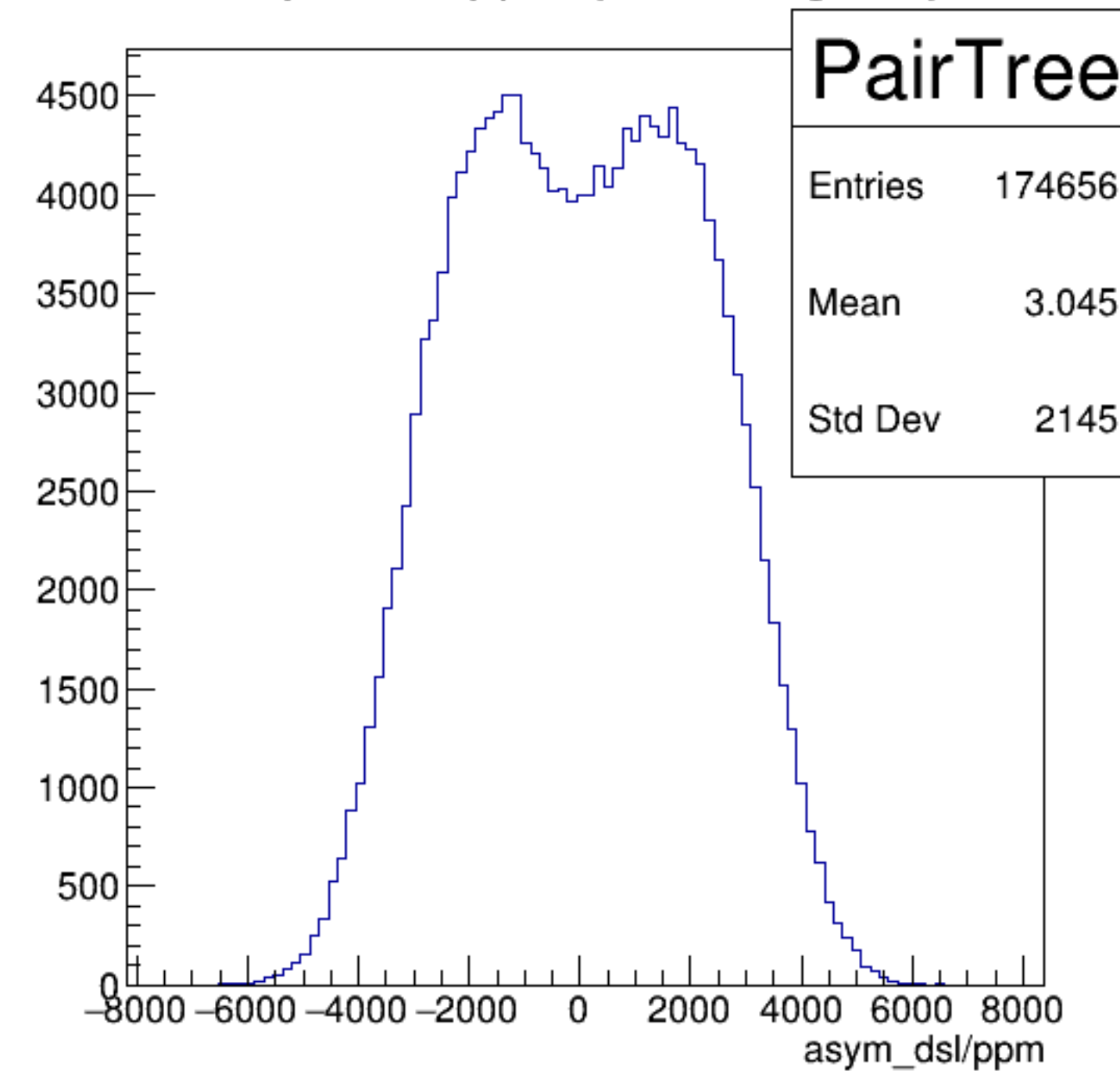
yield\_dsl {ErrorFlag==0}



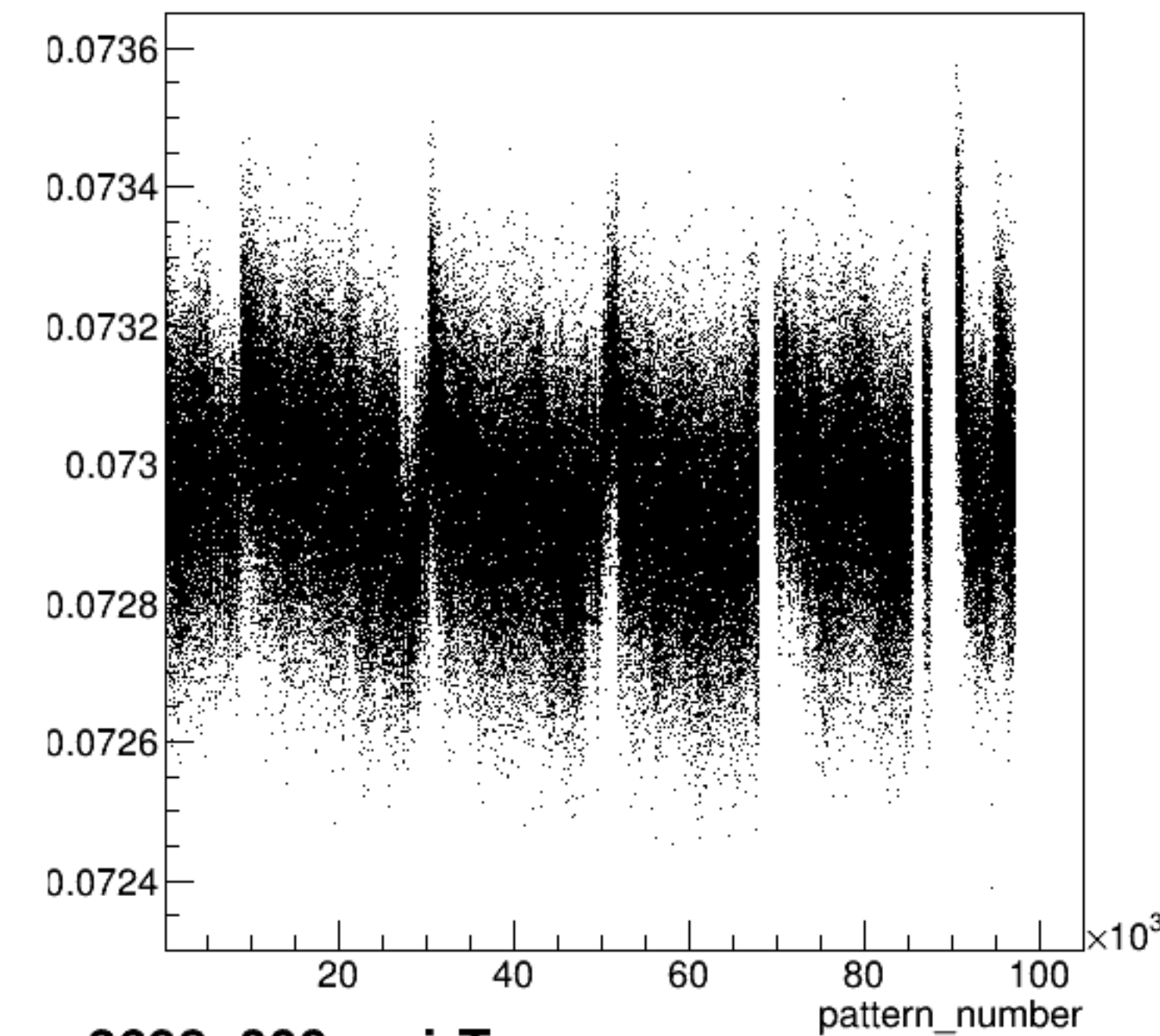
asym\_dsl/ppm:pattern\_number {ErrorFlag==0}



asym\_dsl/ppm {ErrorFlag==0}

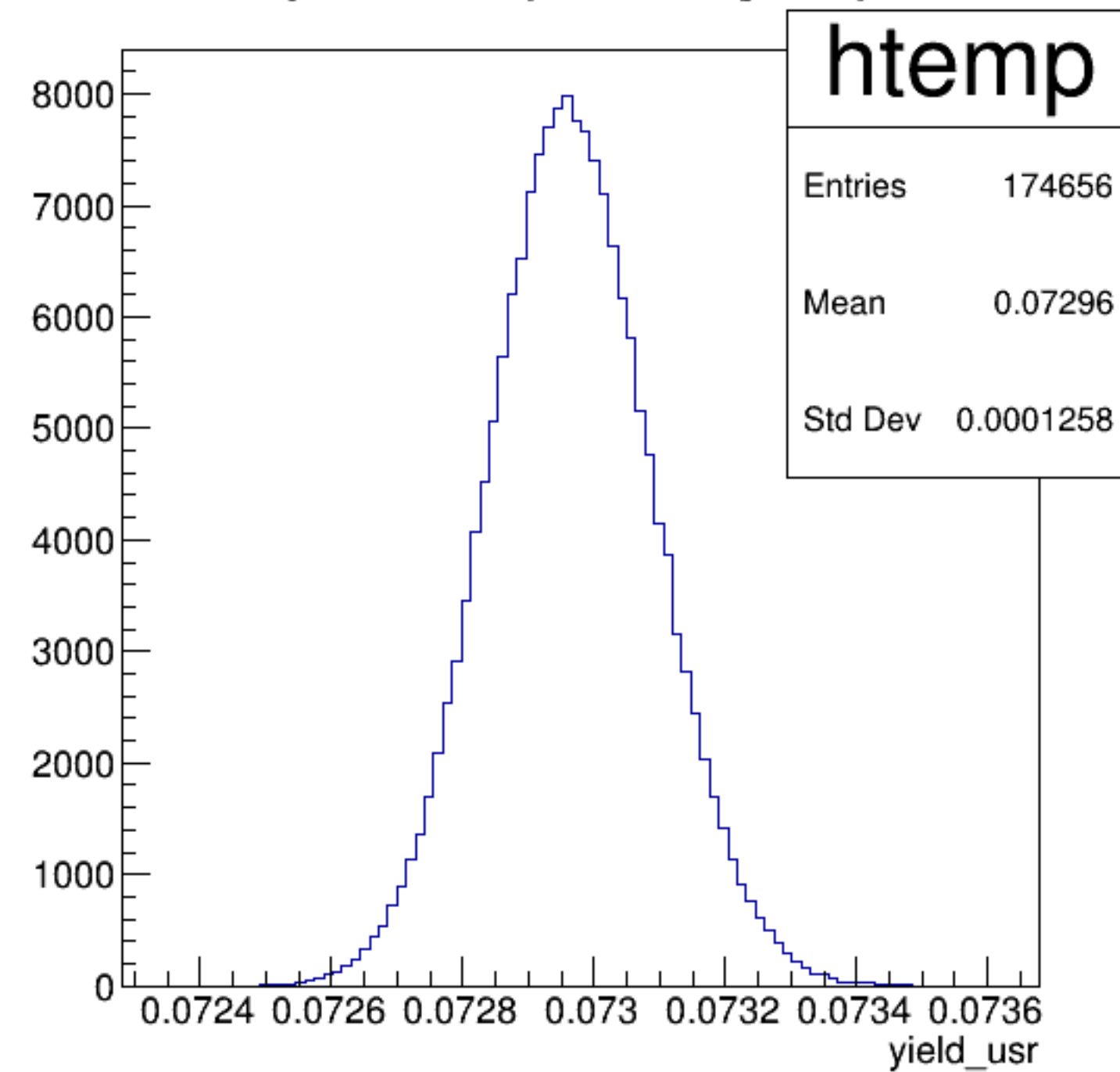


yield\_usr:pattern\_number {ErrorFlag==0}

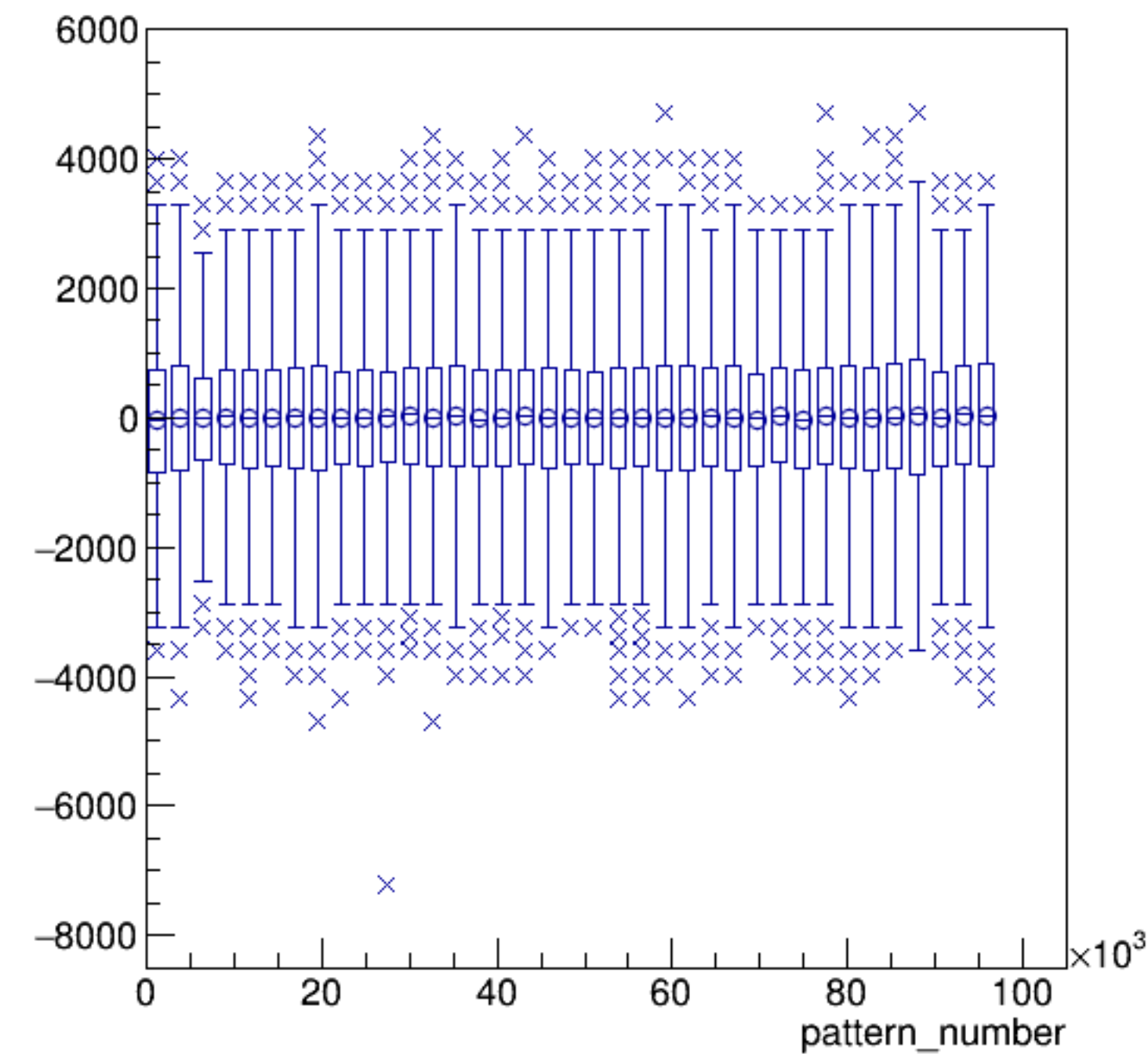


run3698\_000\_pairTree\_usr.png

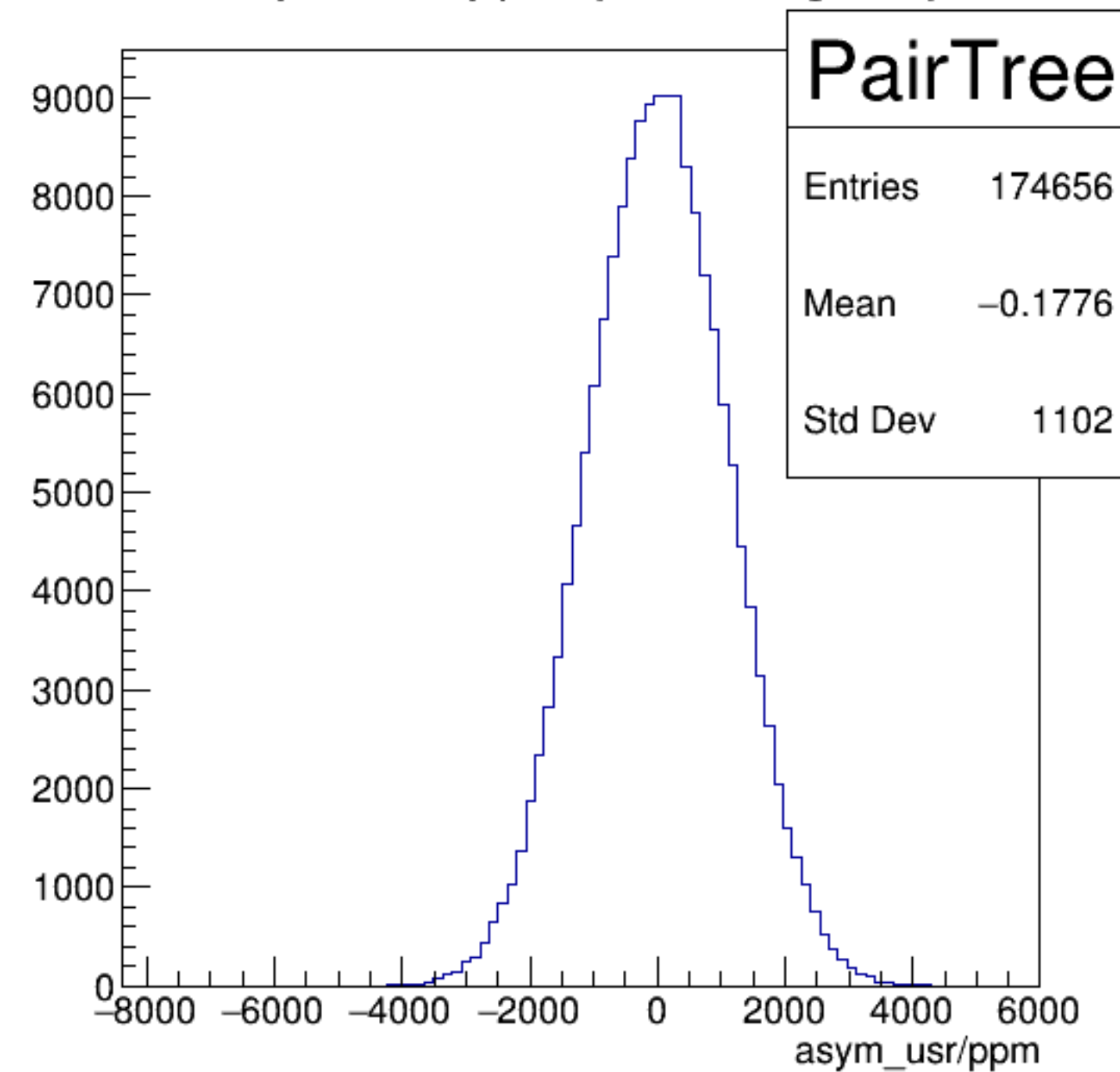
yield\_usr {ErrorFlag==0}



asym\_usr/ppm:pattern\_number {ErrorFlag==0}

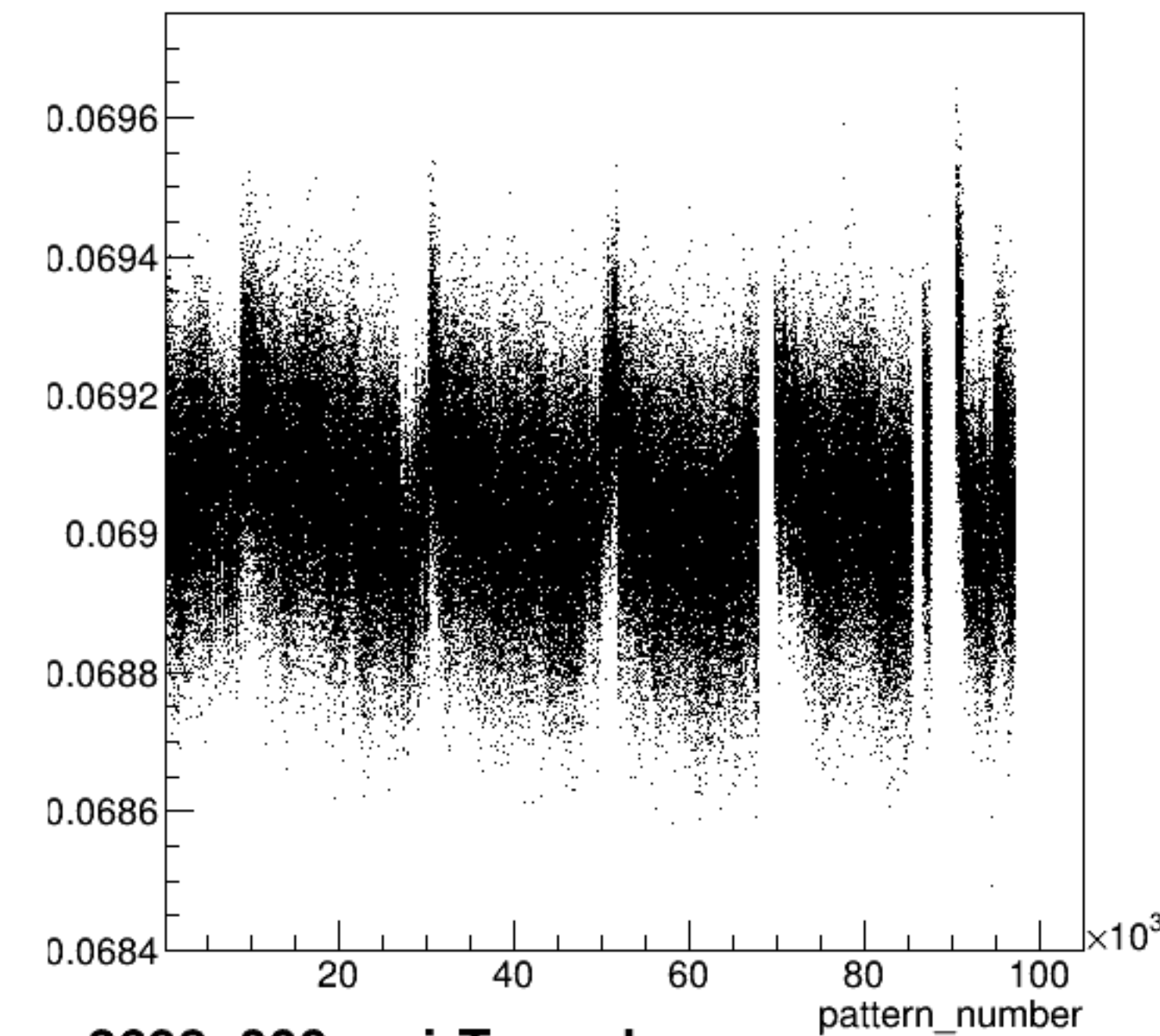


asym\_usr/ppm {ErrorFlag==0}



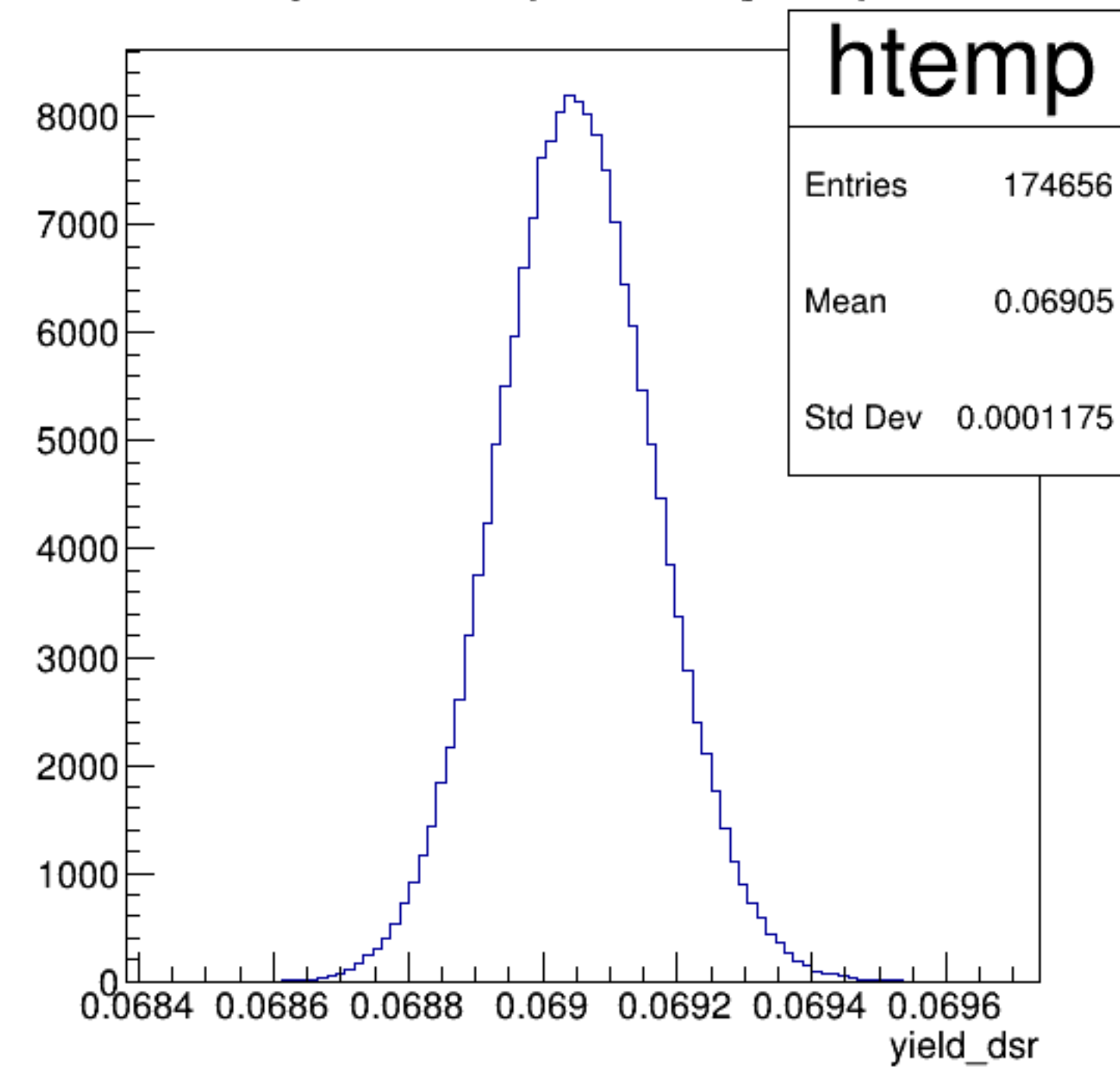


yield\_dsr:pattern\_number {ErrorFlag==0}

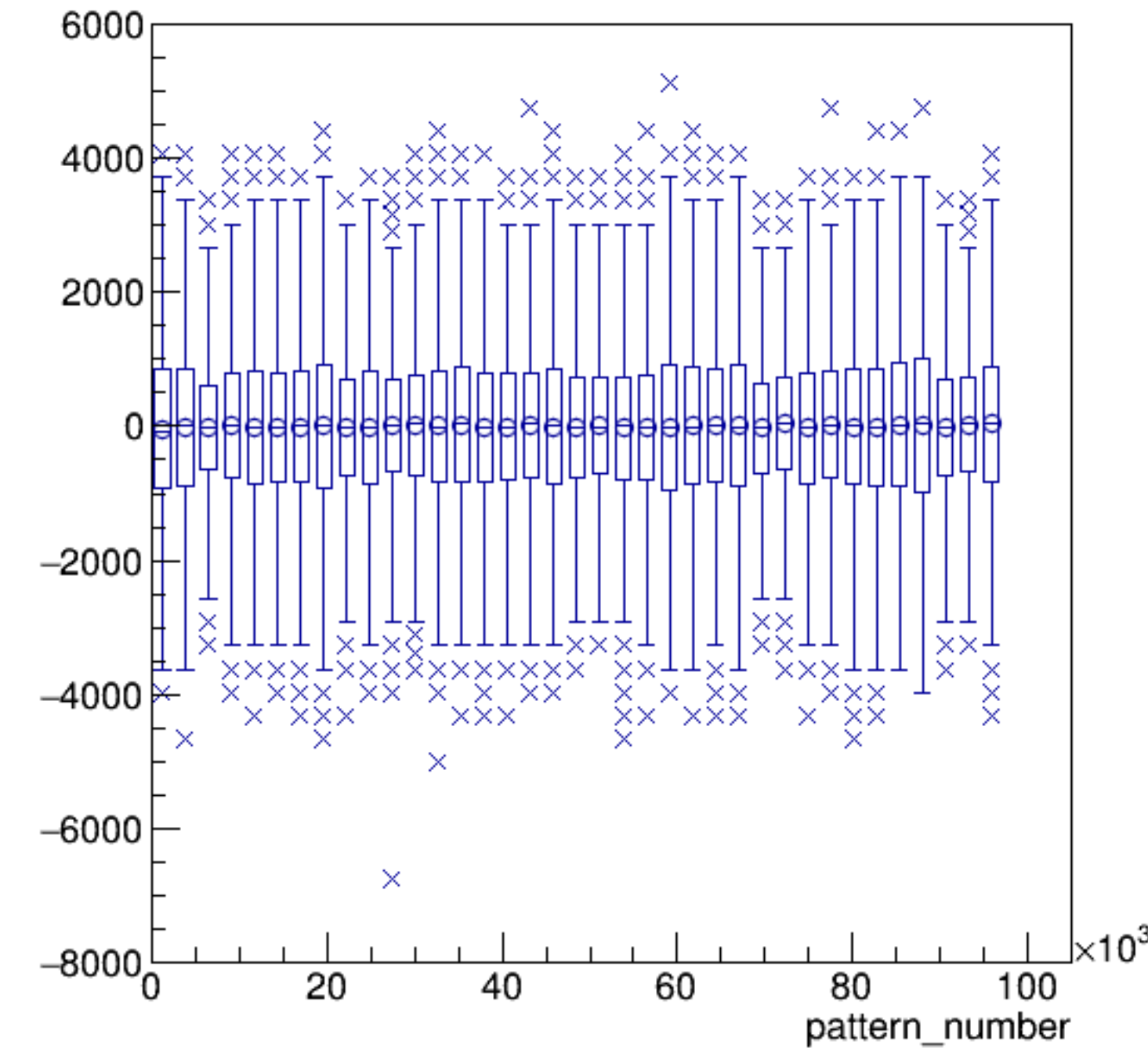


run3698\_000\_pairTree\_dsr.png

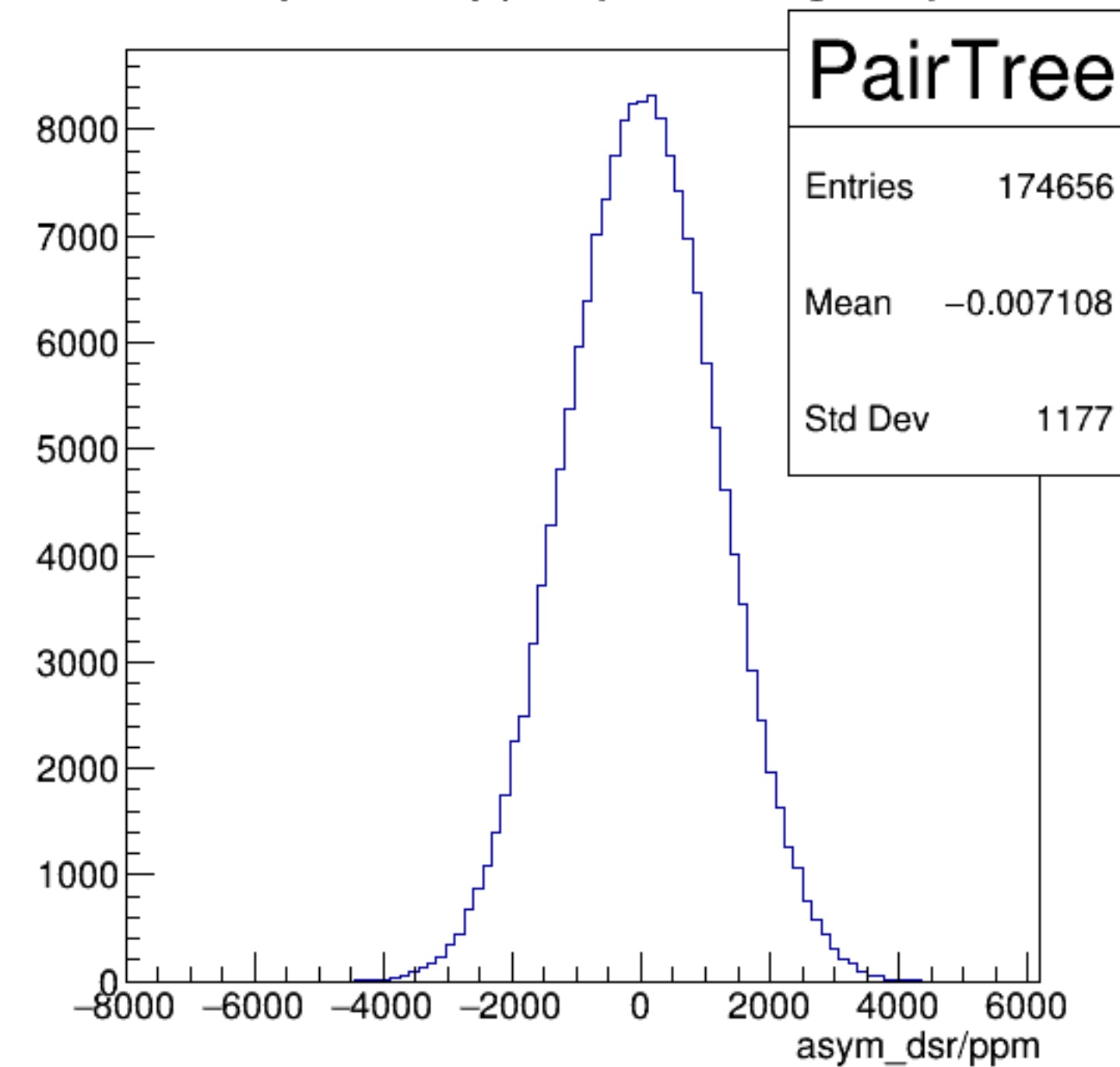
yield\_dsr {ErrorFlag==0}



asym\_dsr/ppm:pattern\_number {ErrorFlag==0}



asym\_dsr/ppm {ErrorFlag==0}

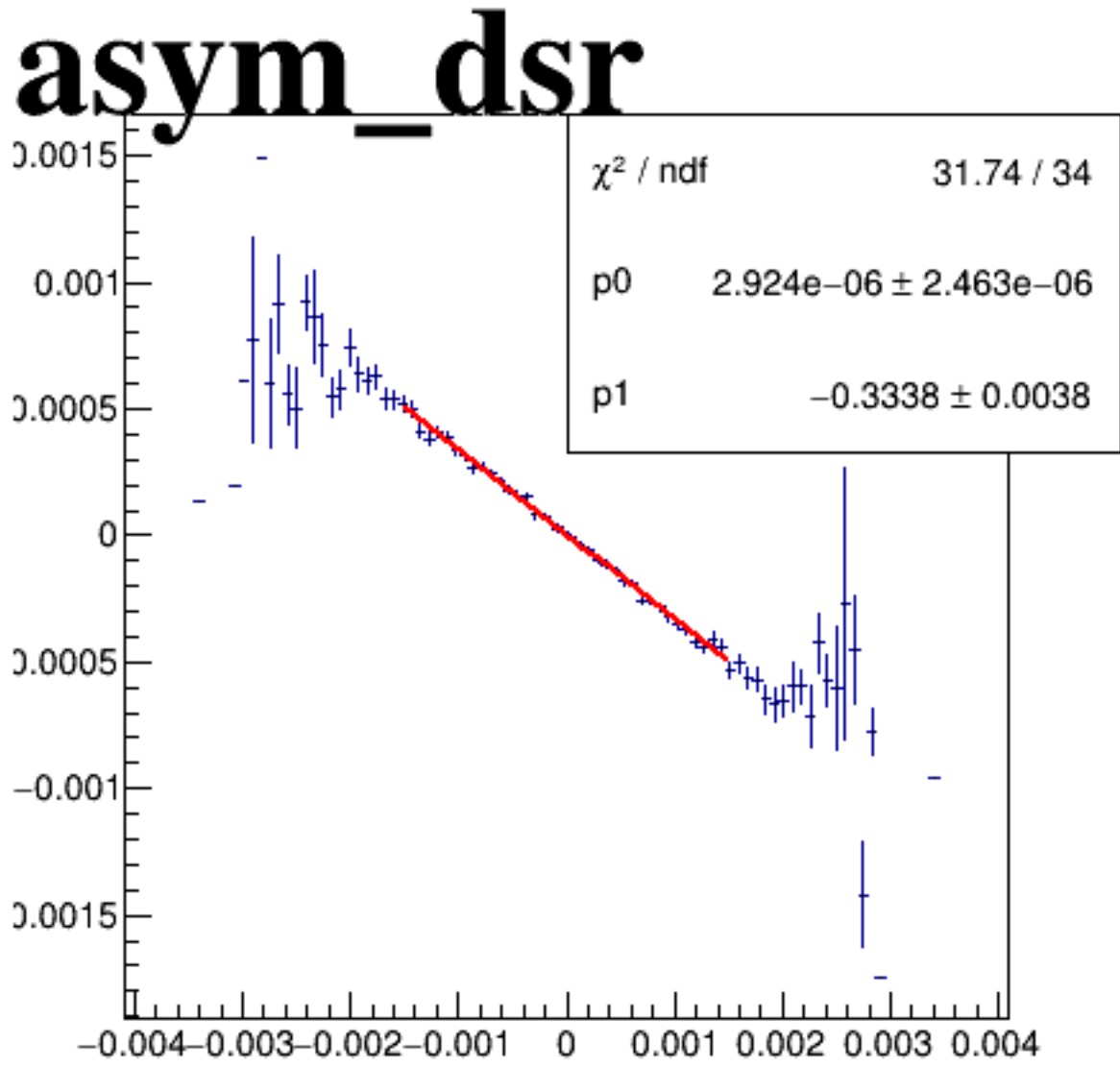
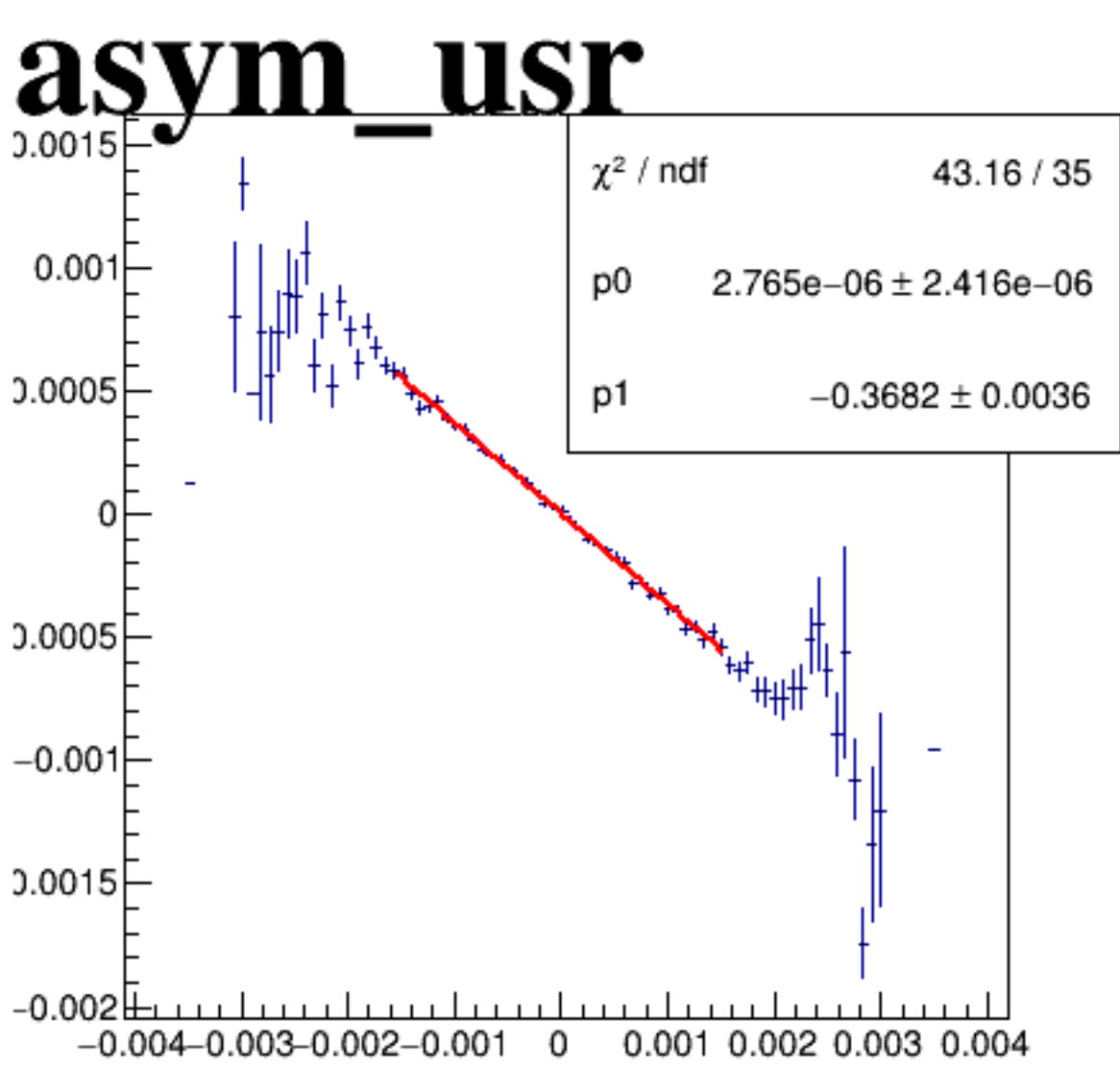
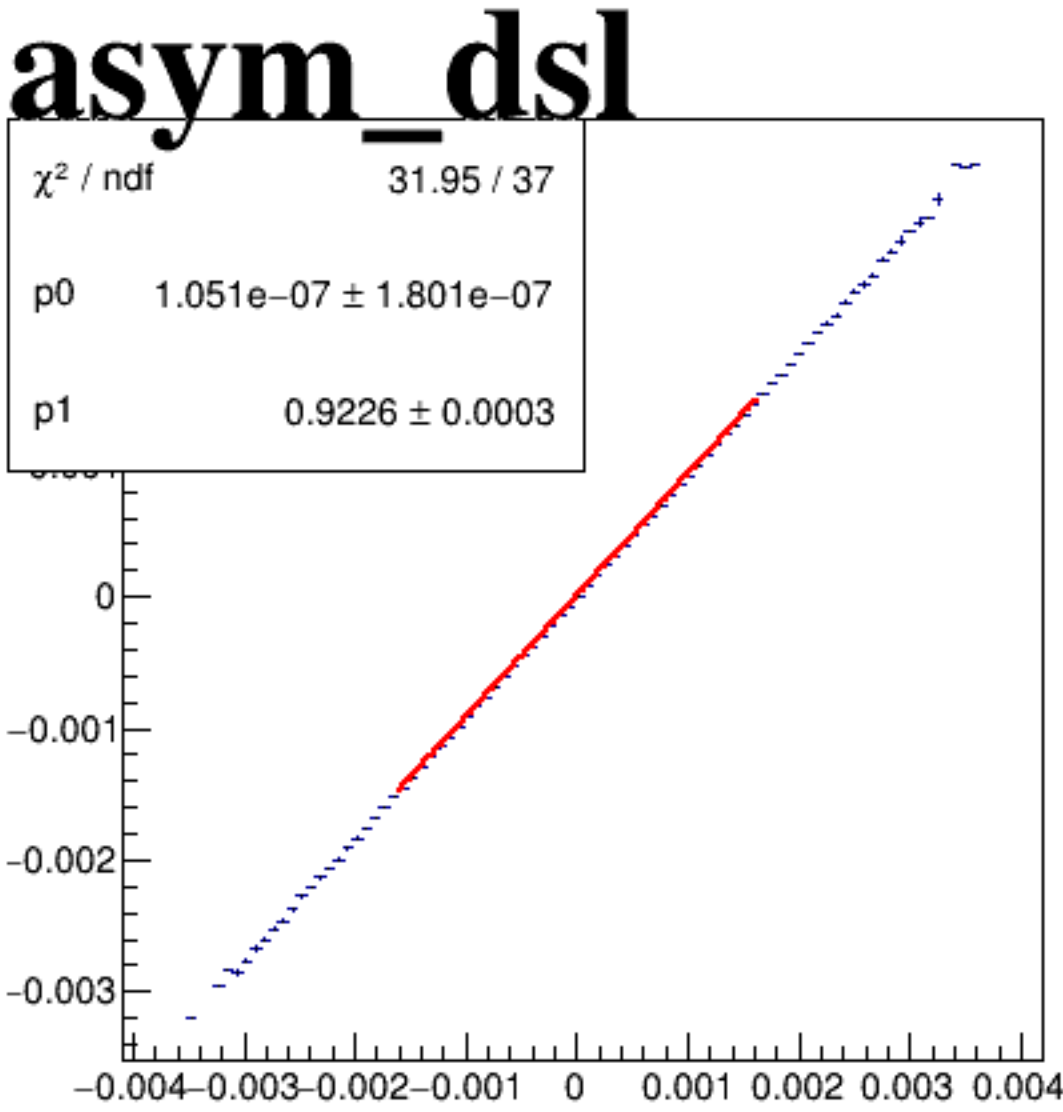
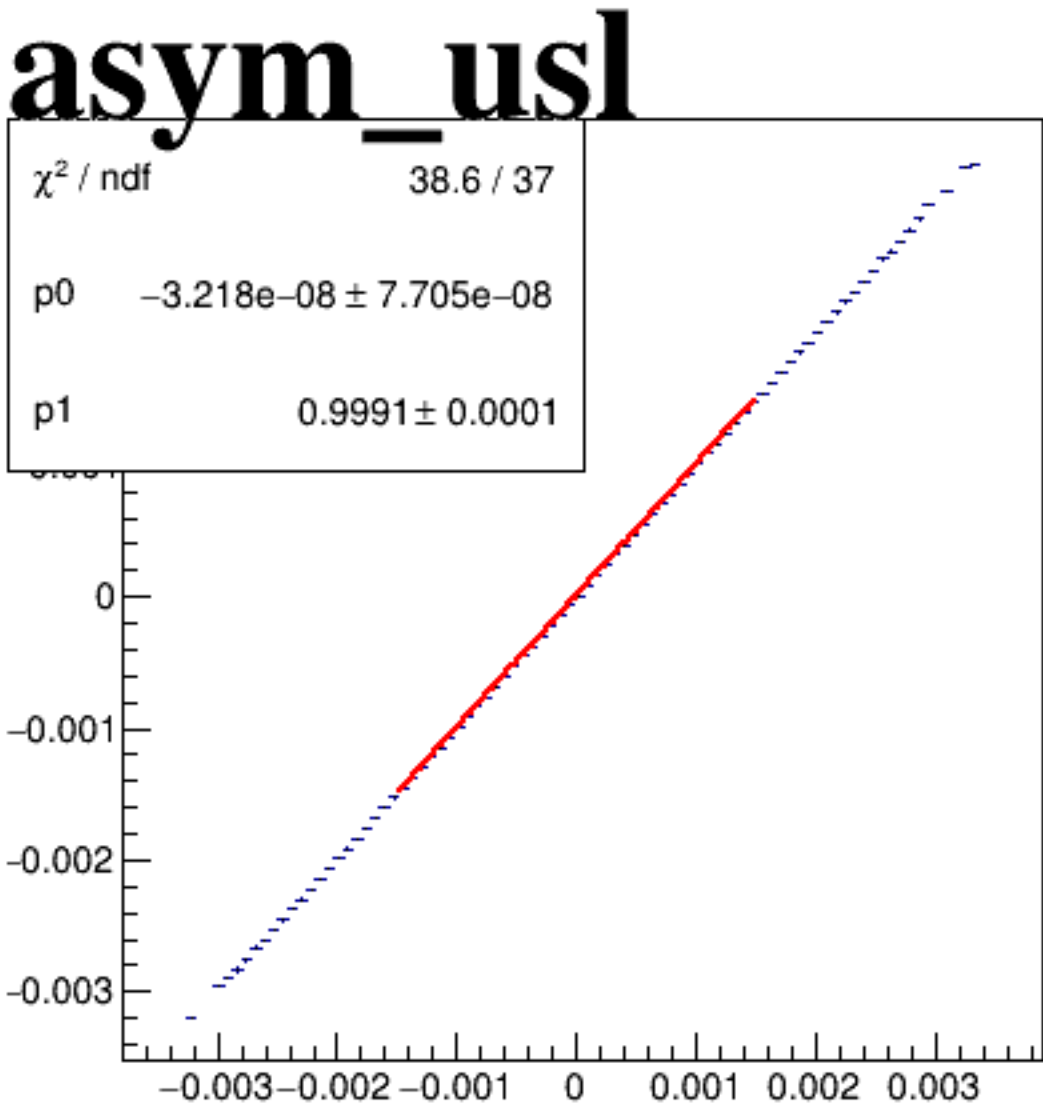




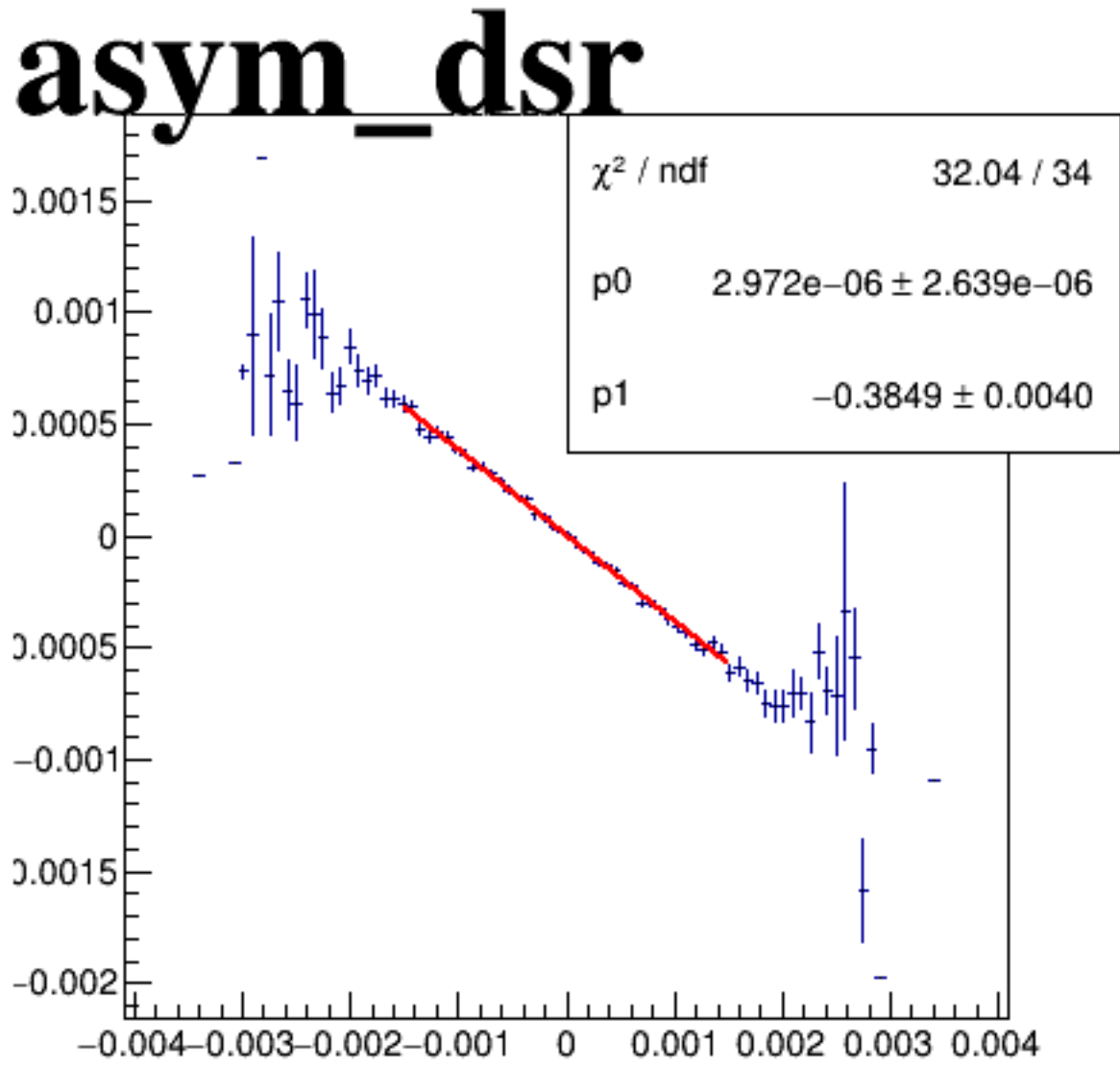
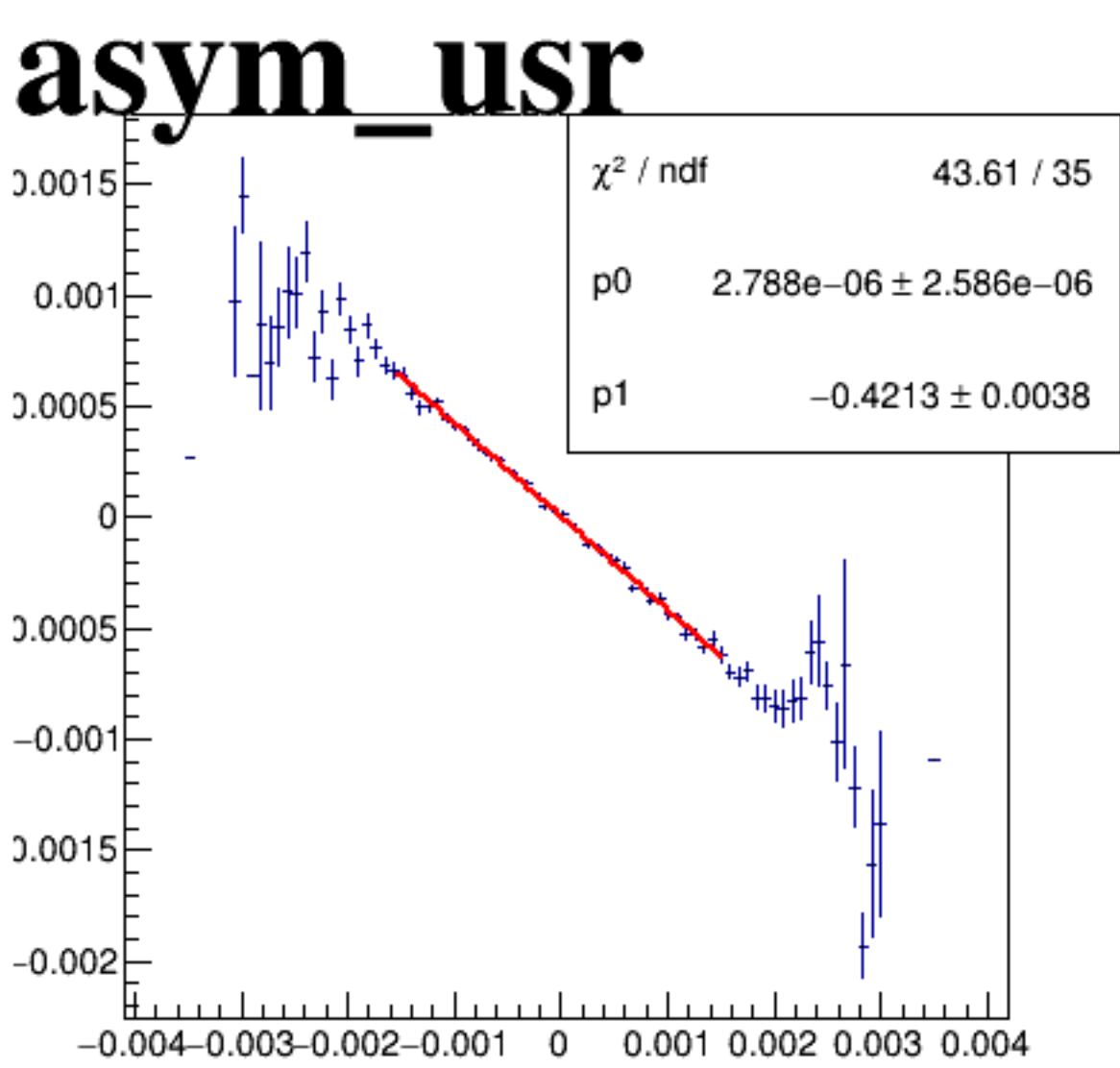
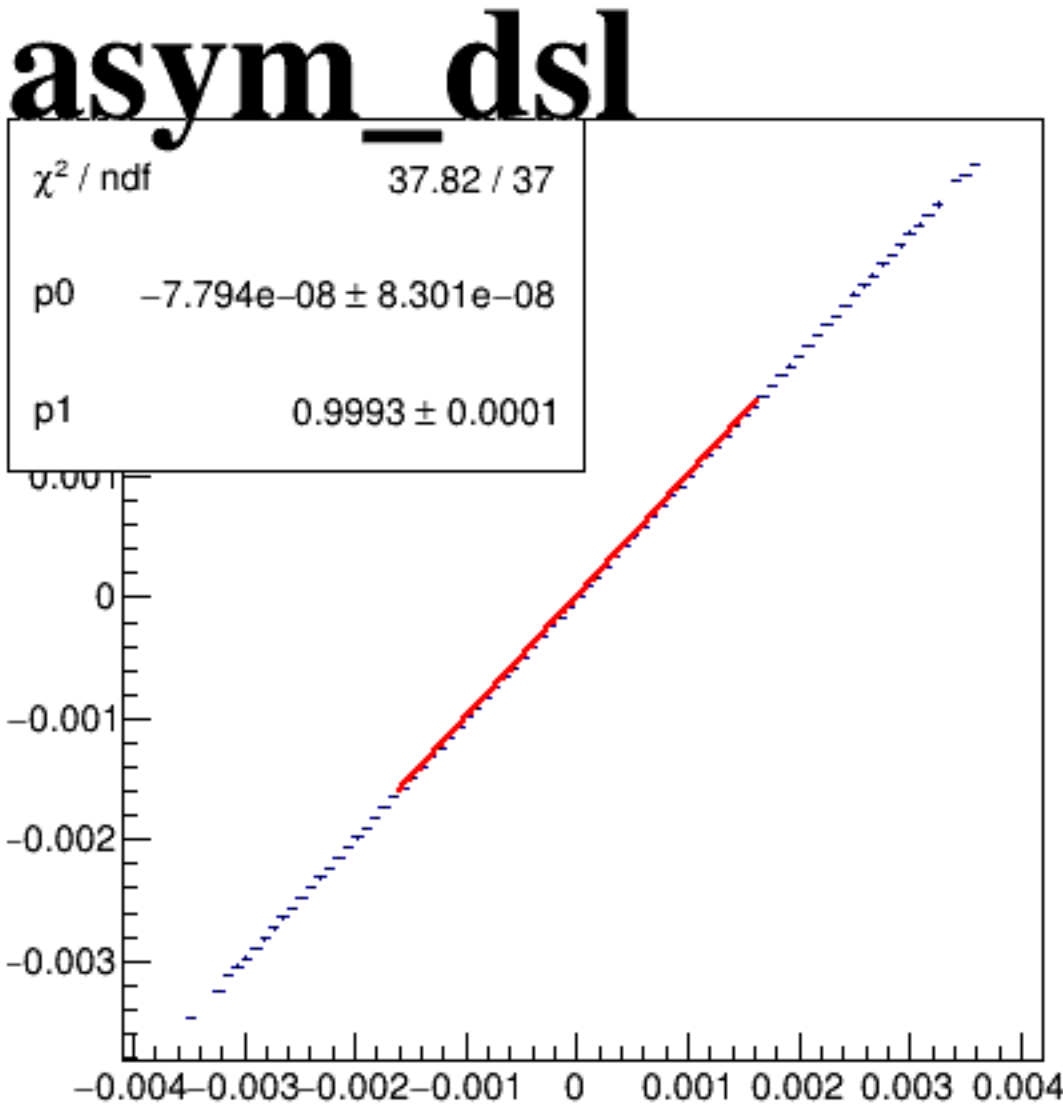
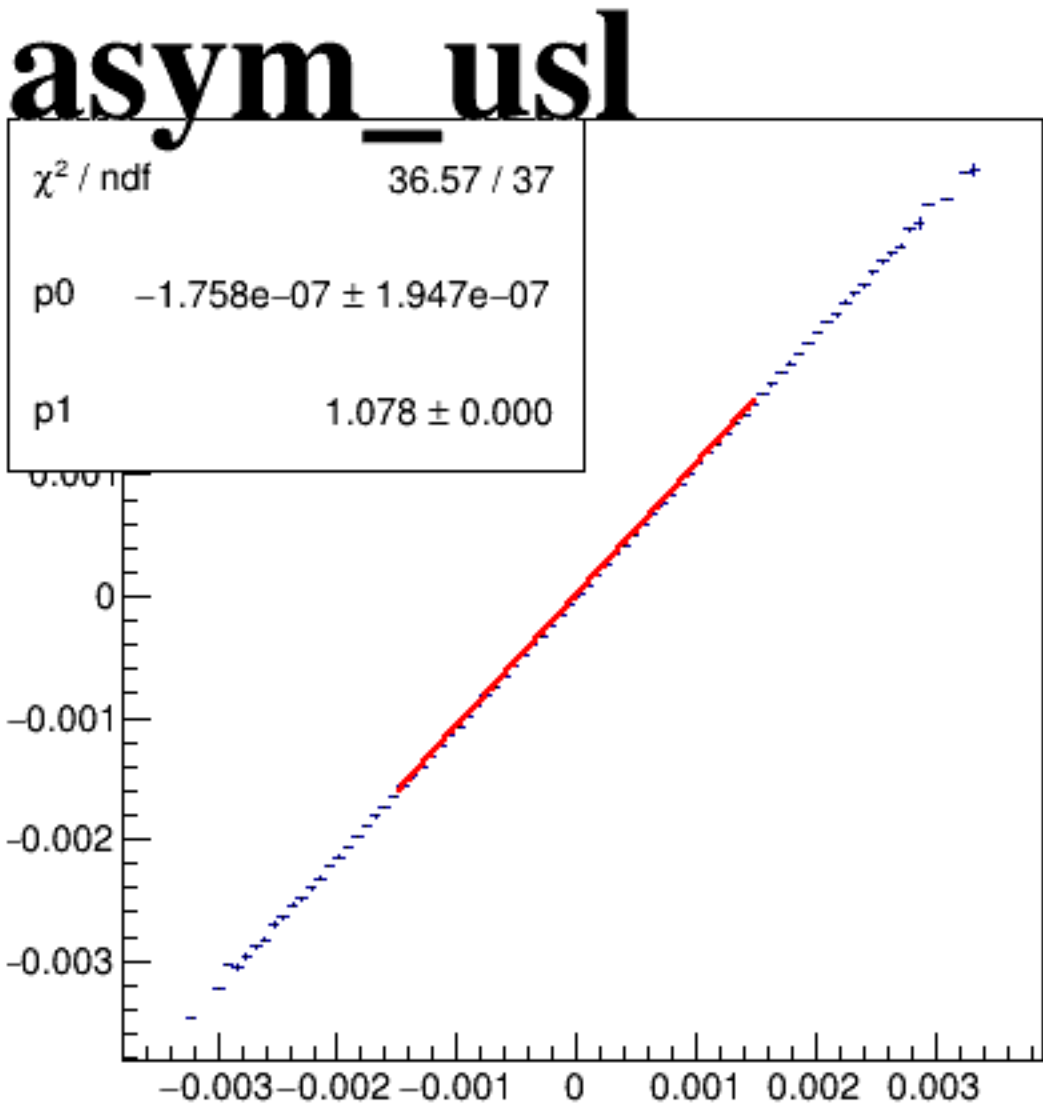




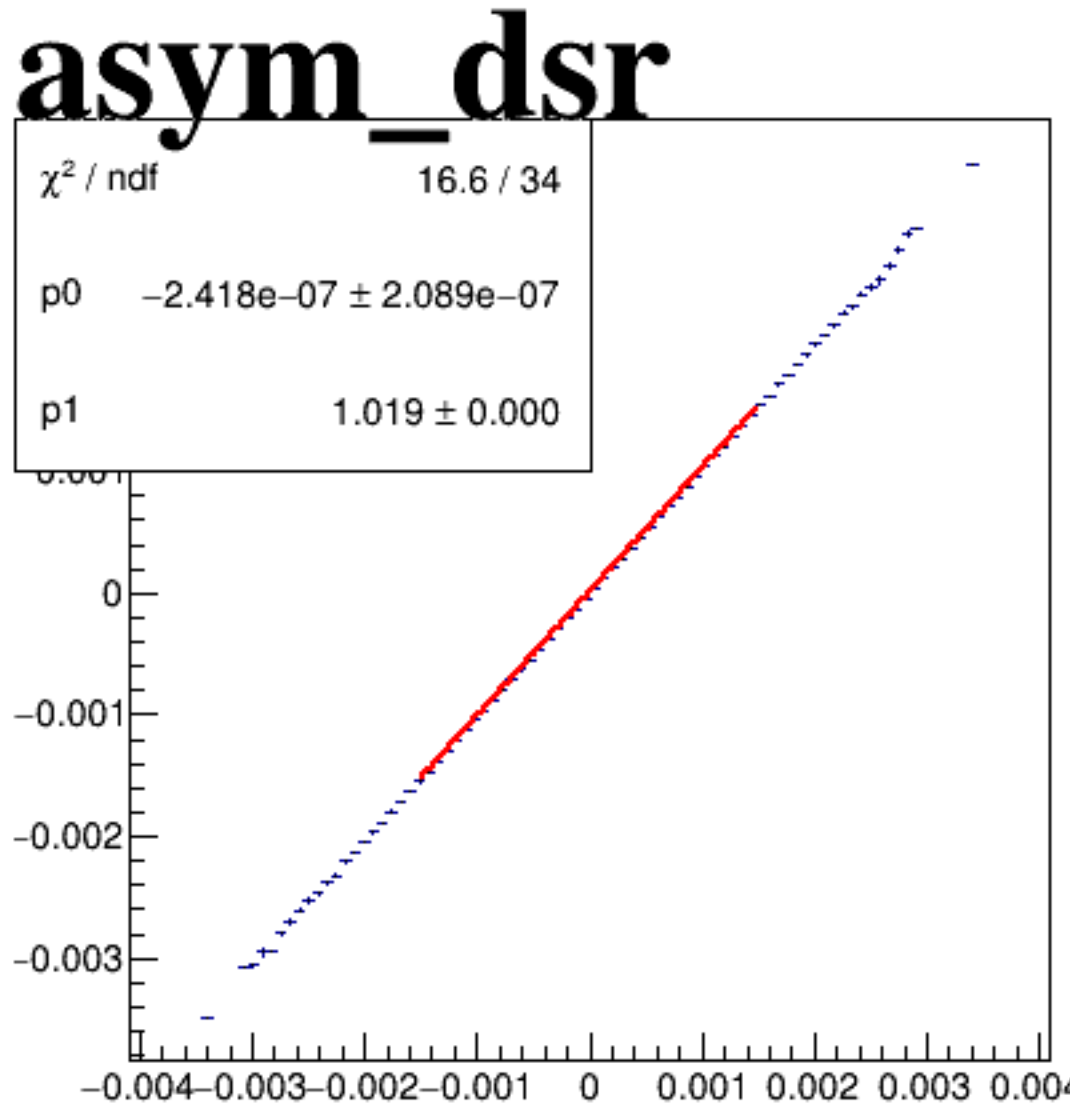
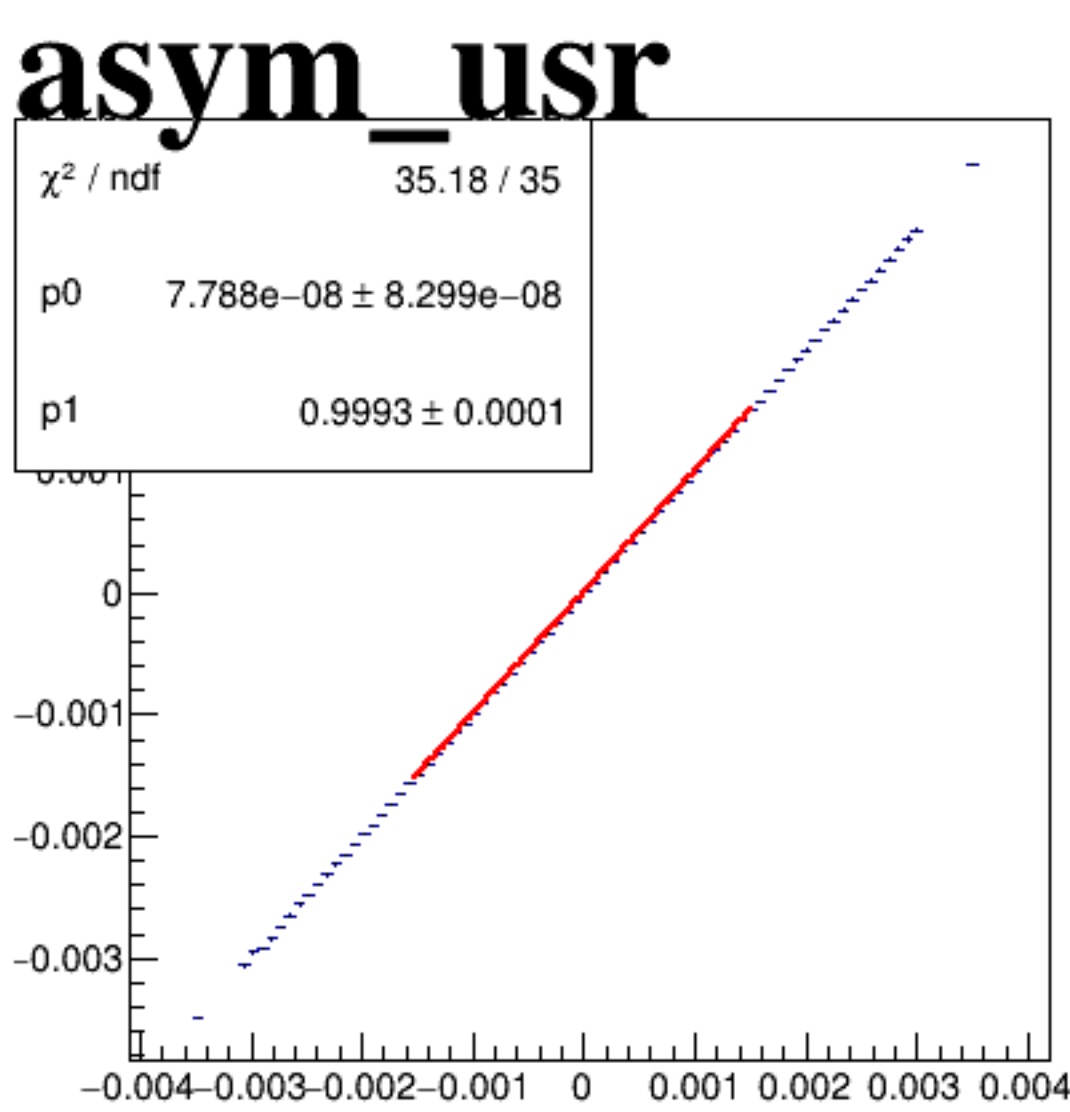
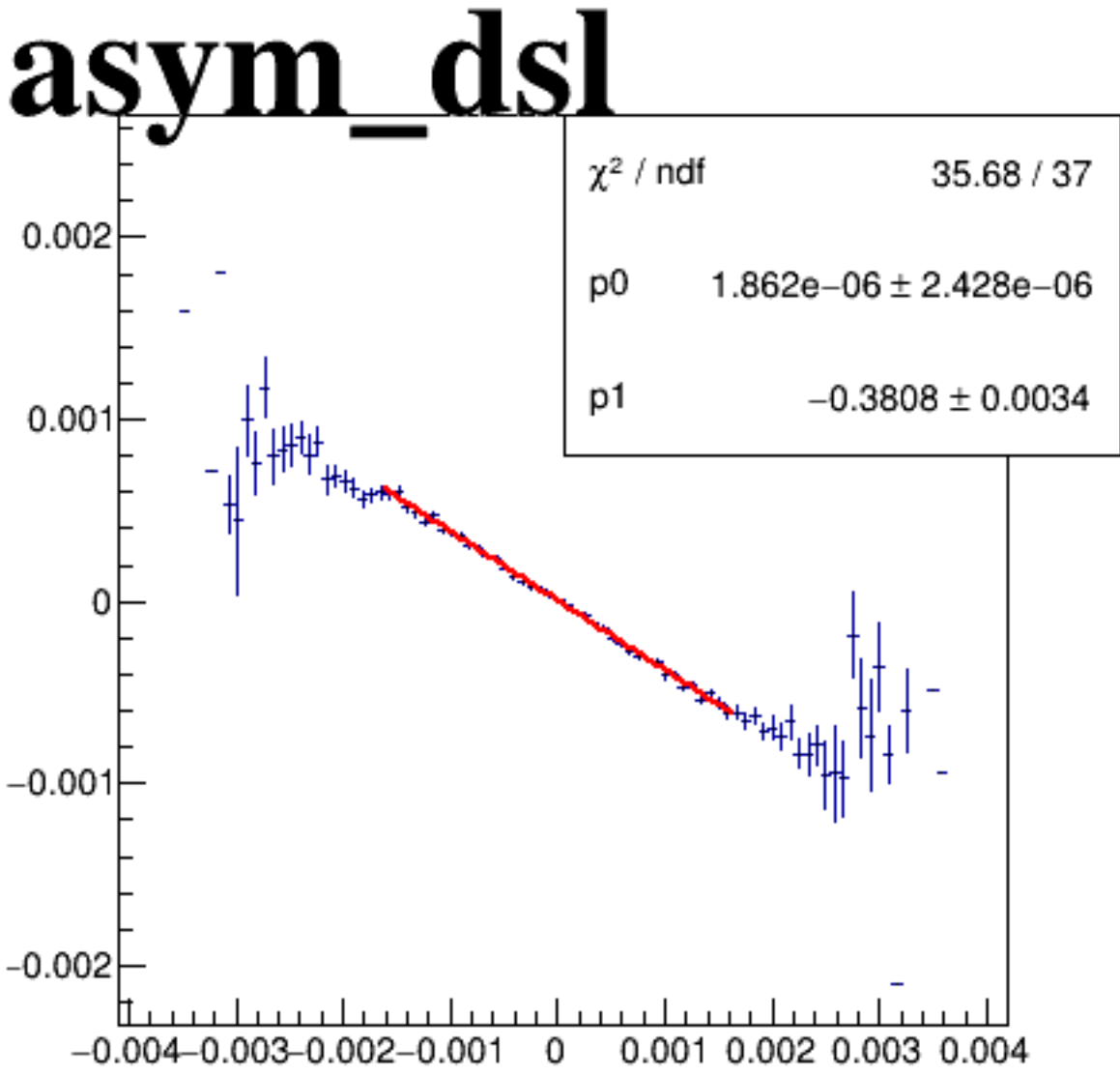
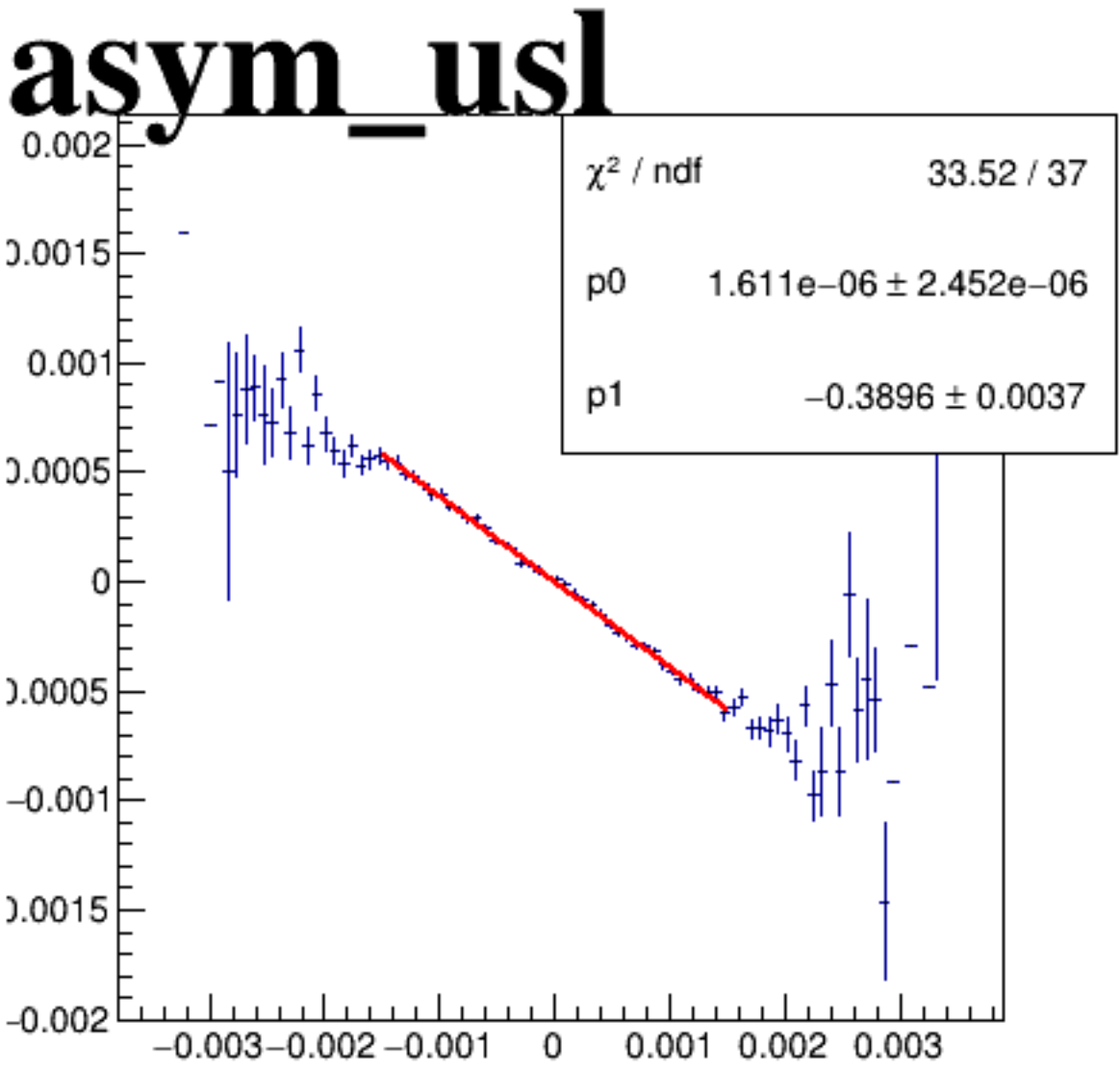
asym\_usl



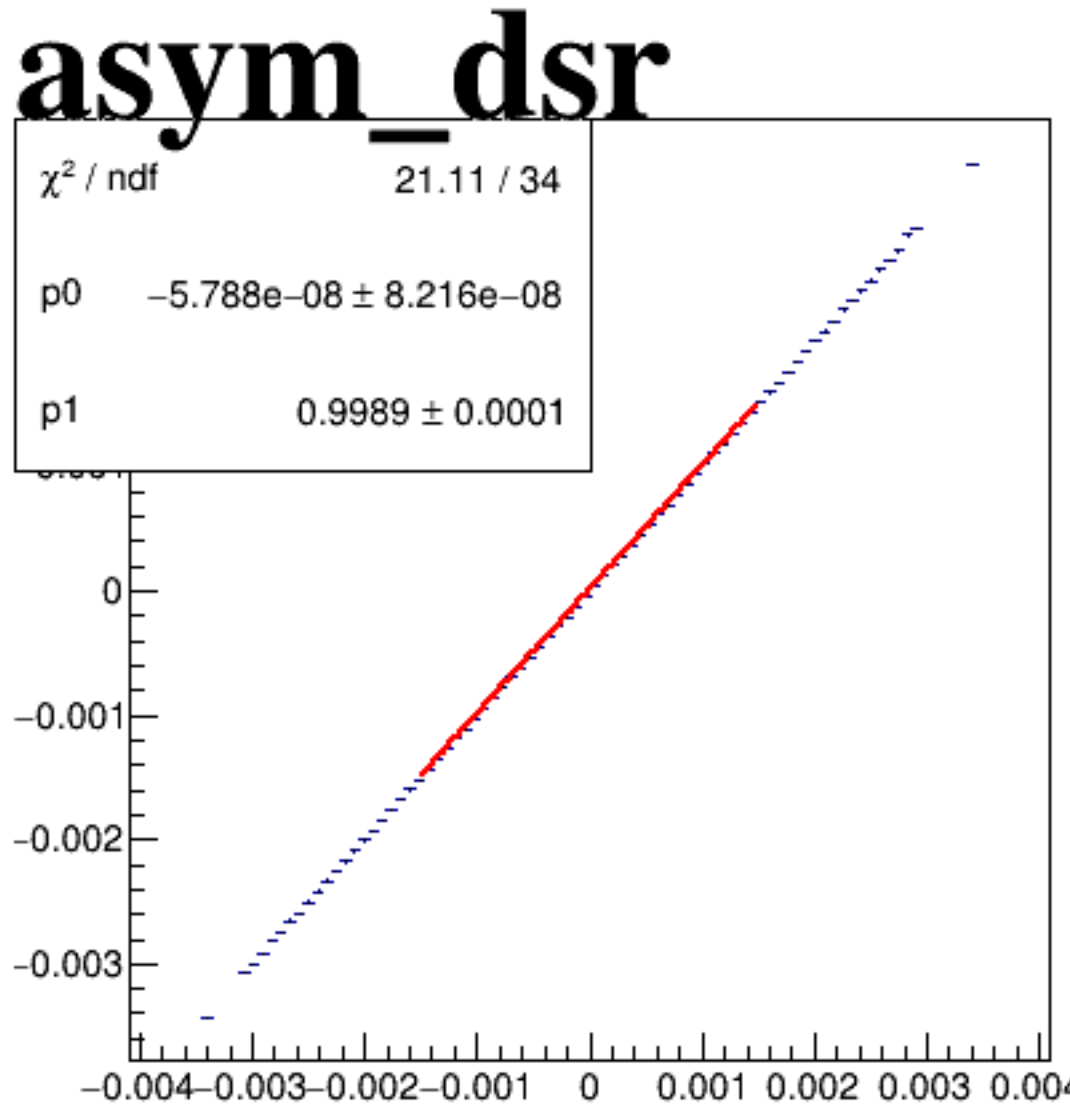
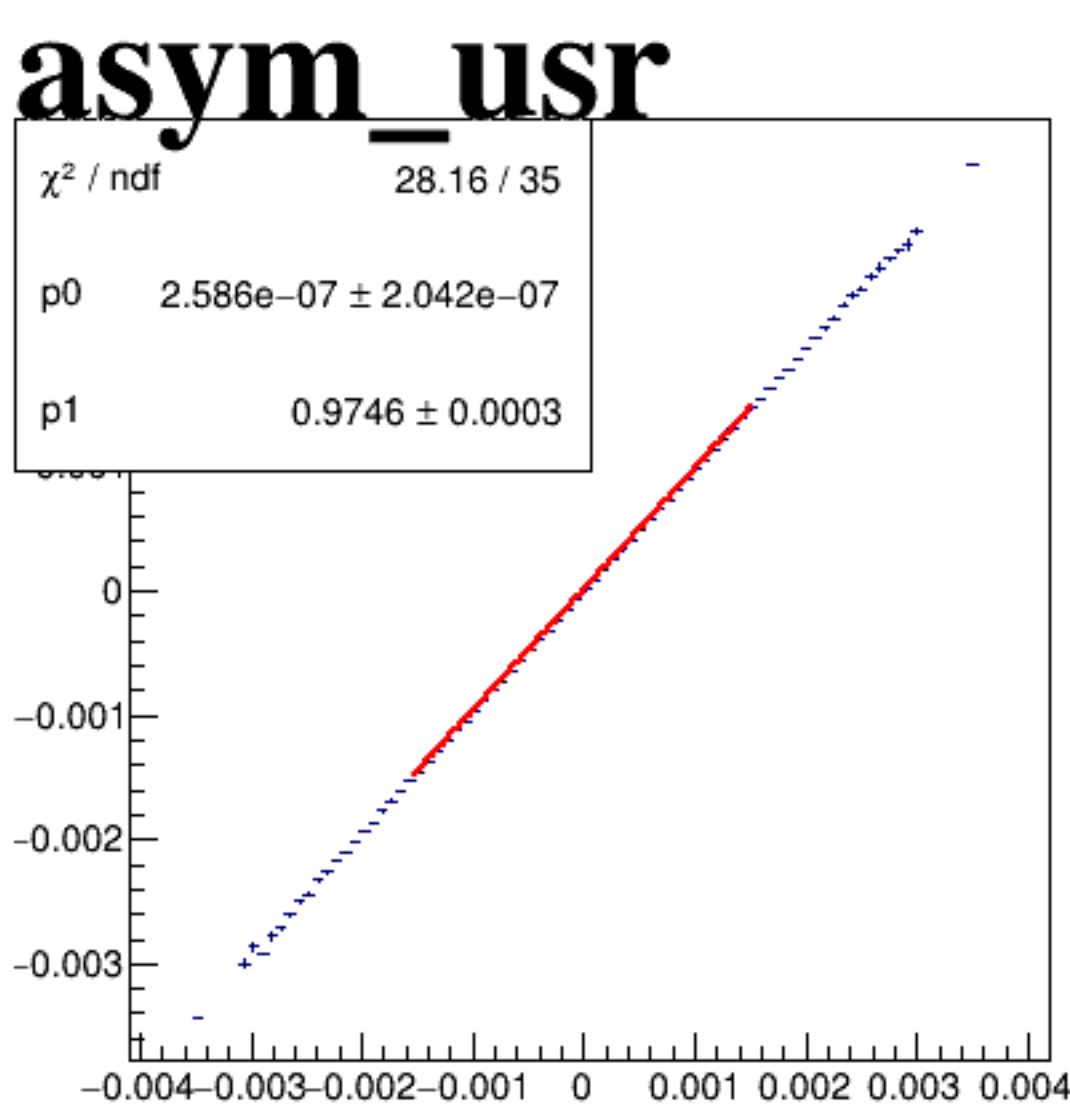
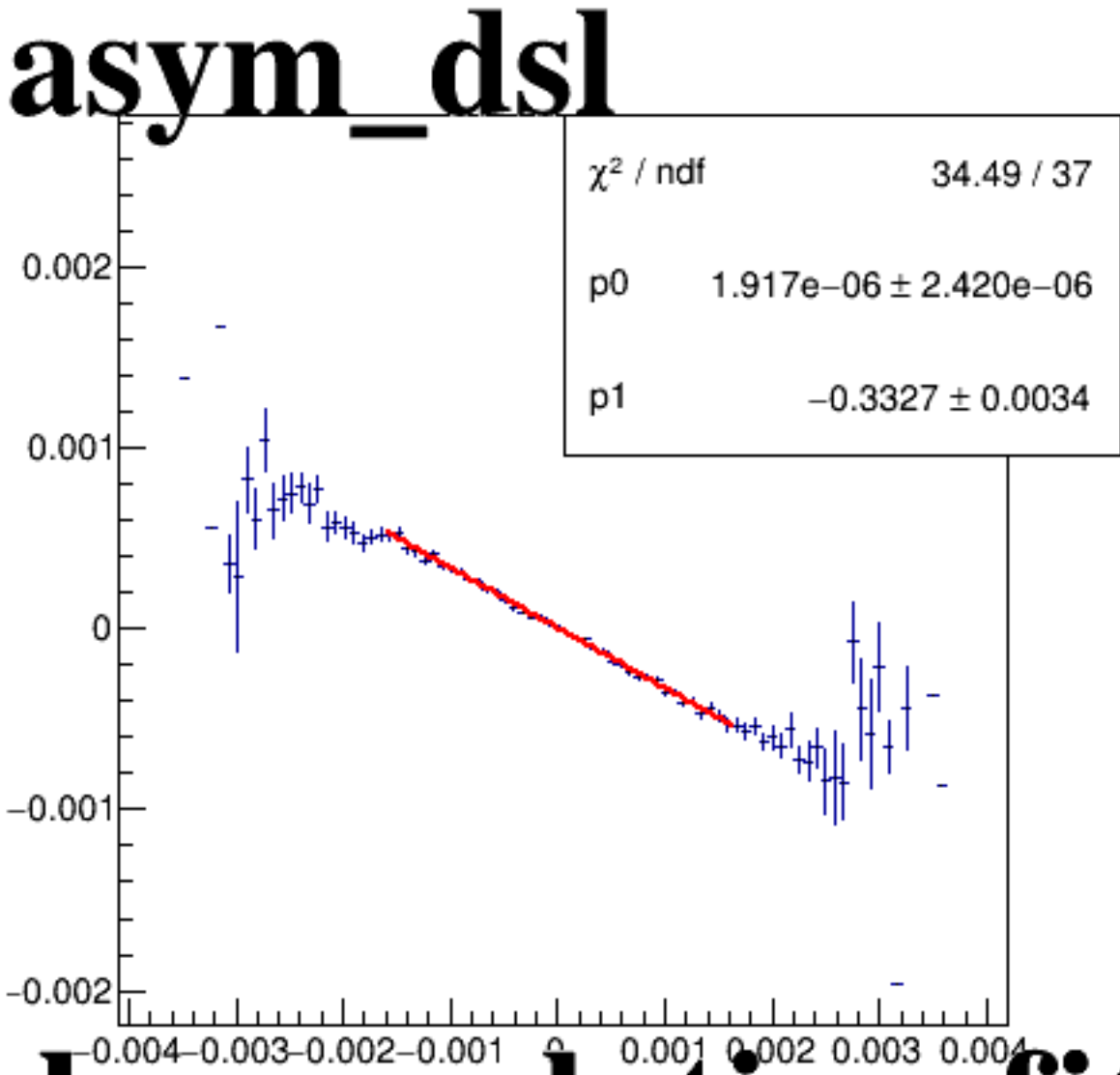
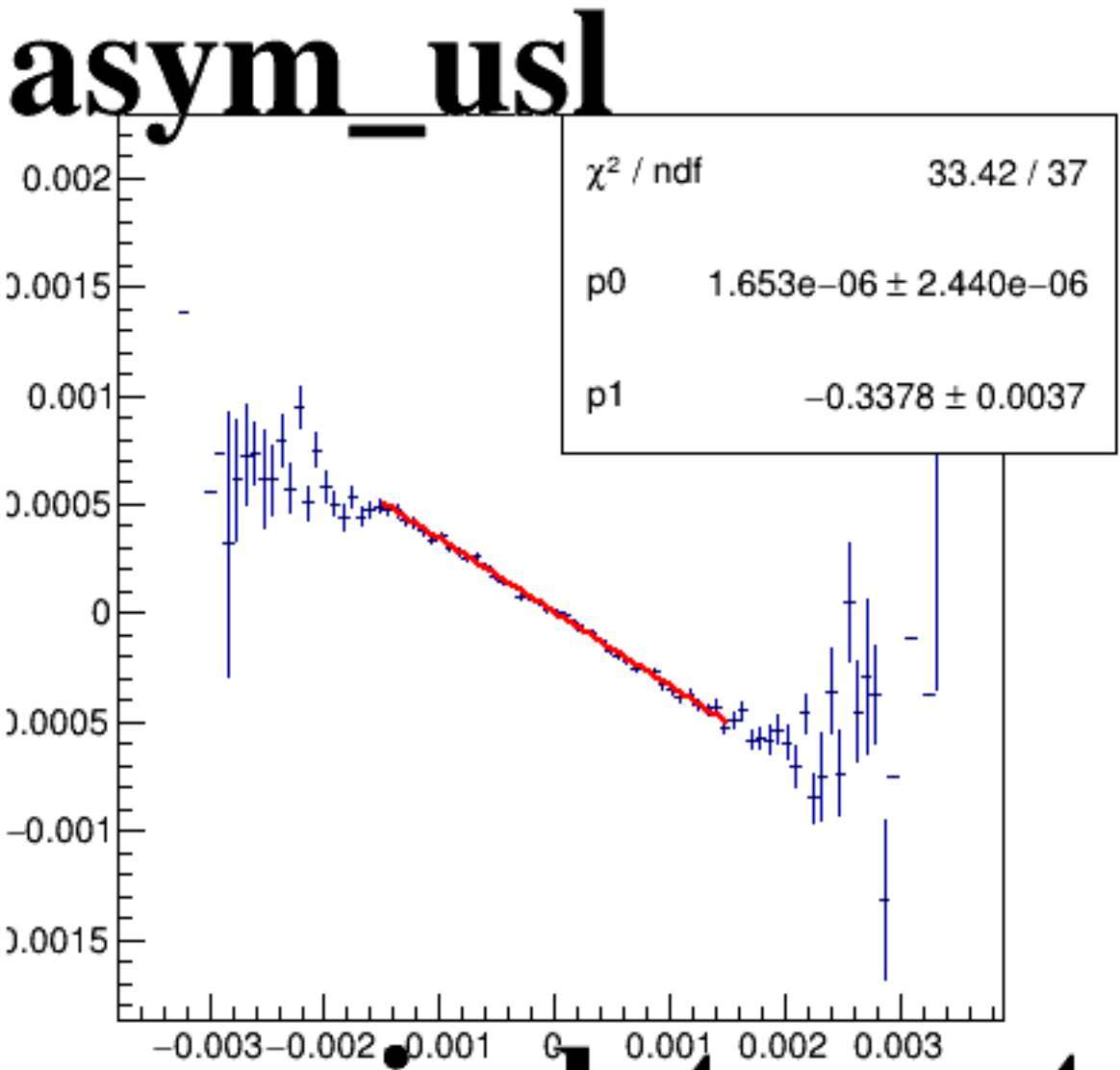
asym\_dsl



asym\_usr

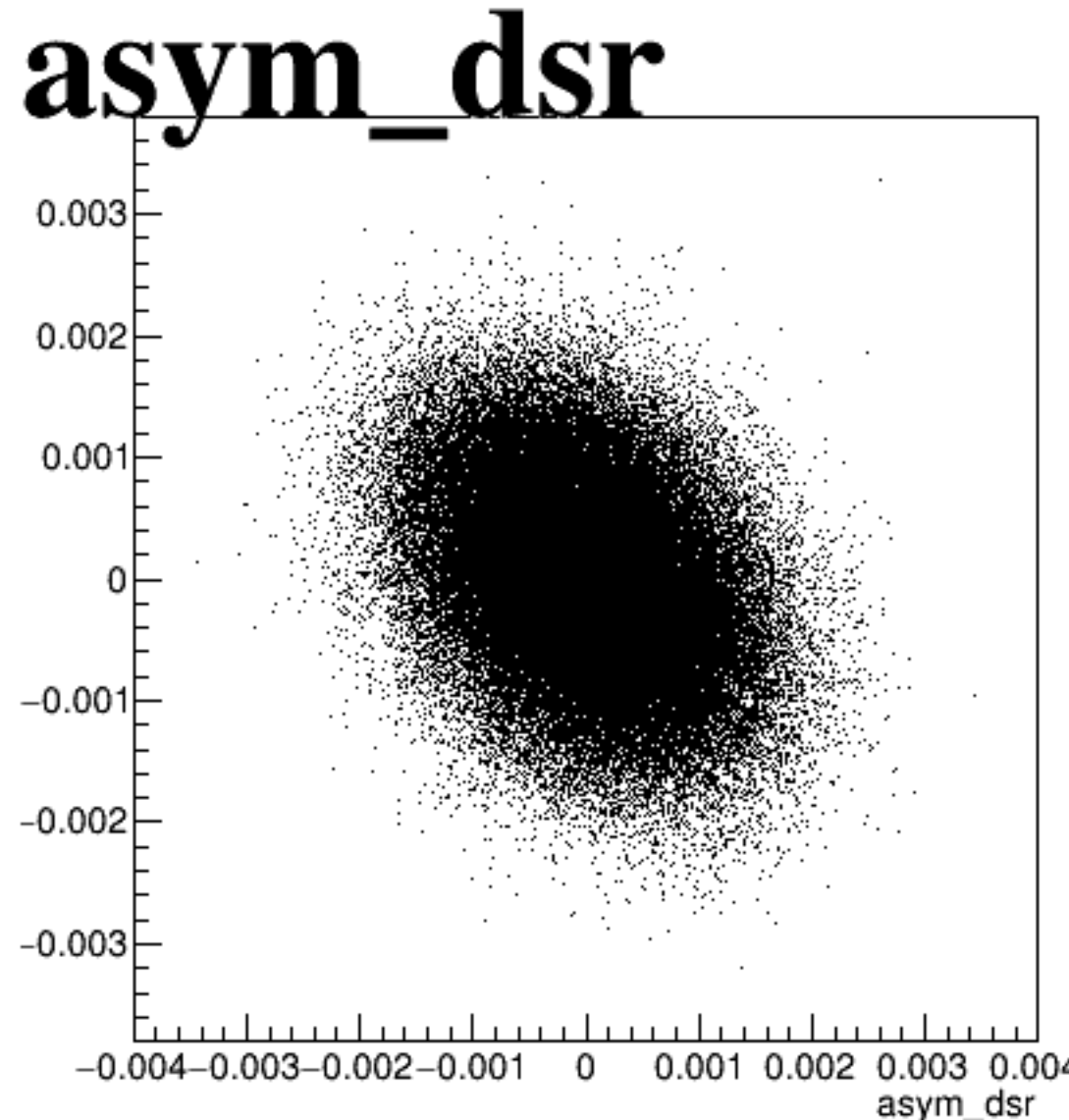
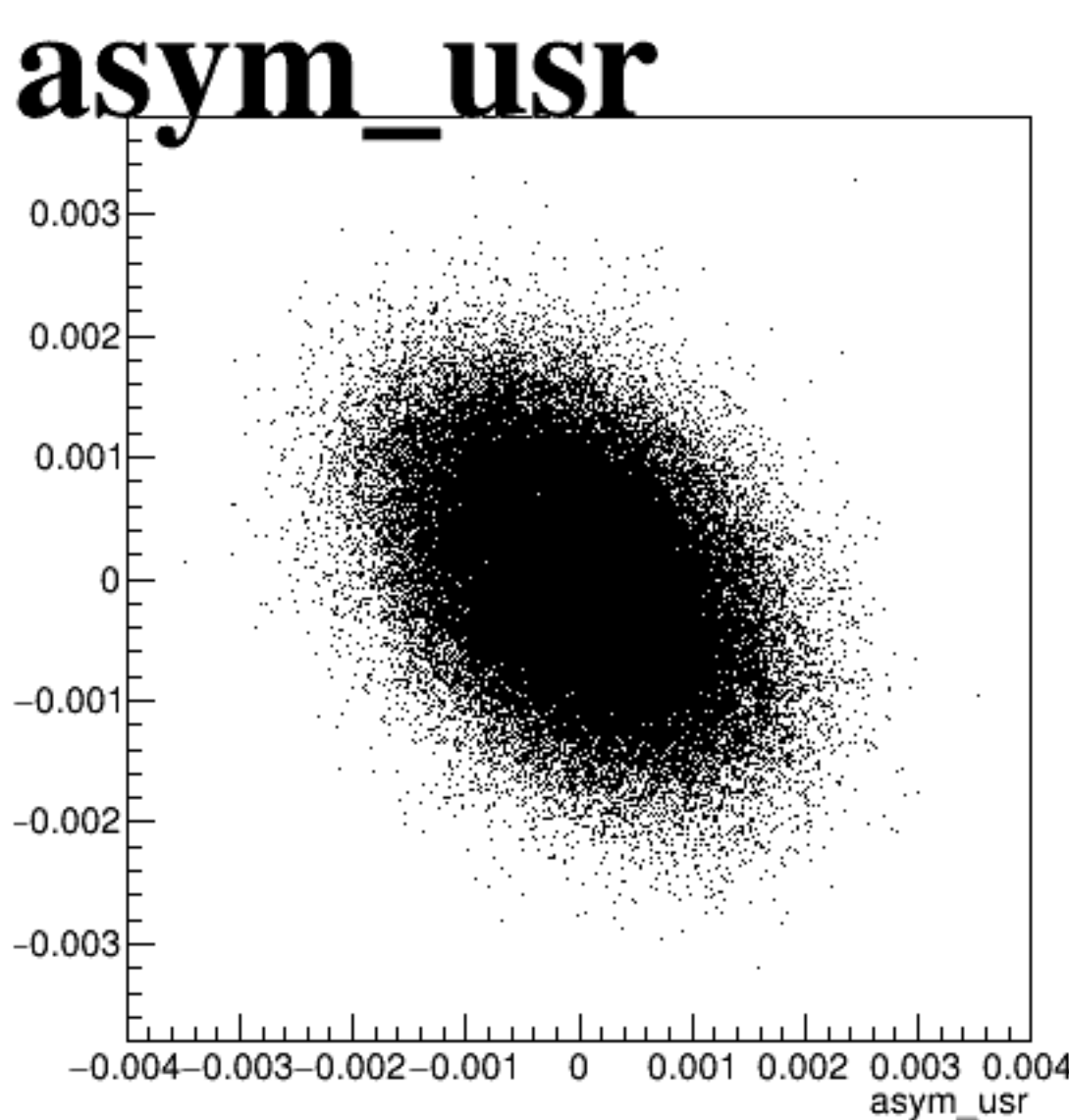
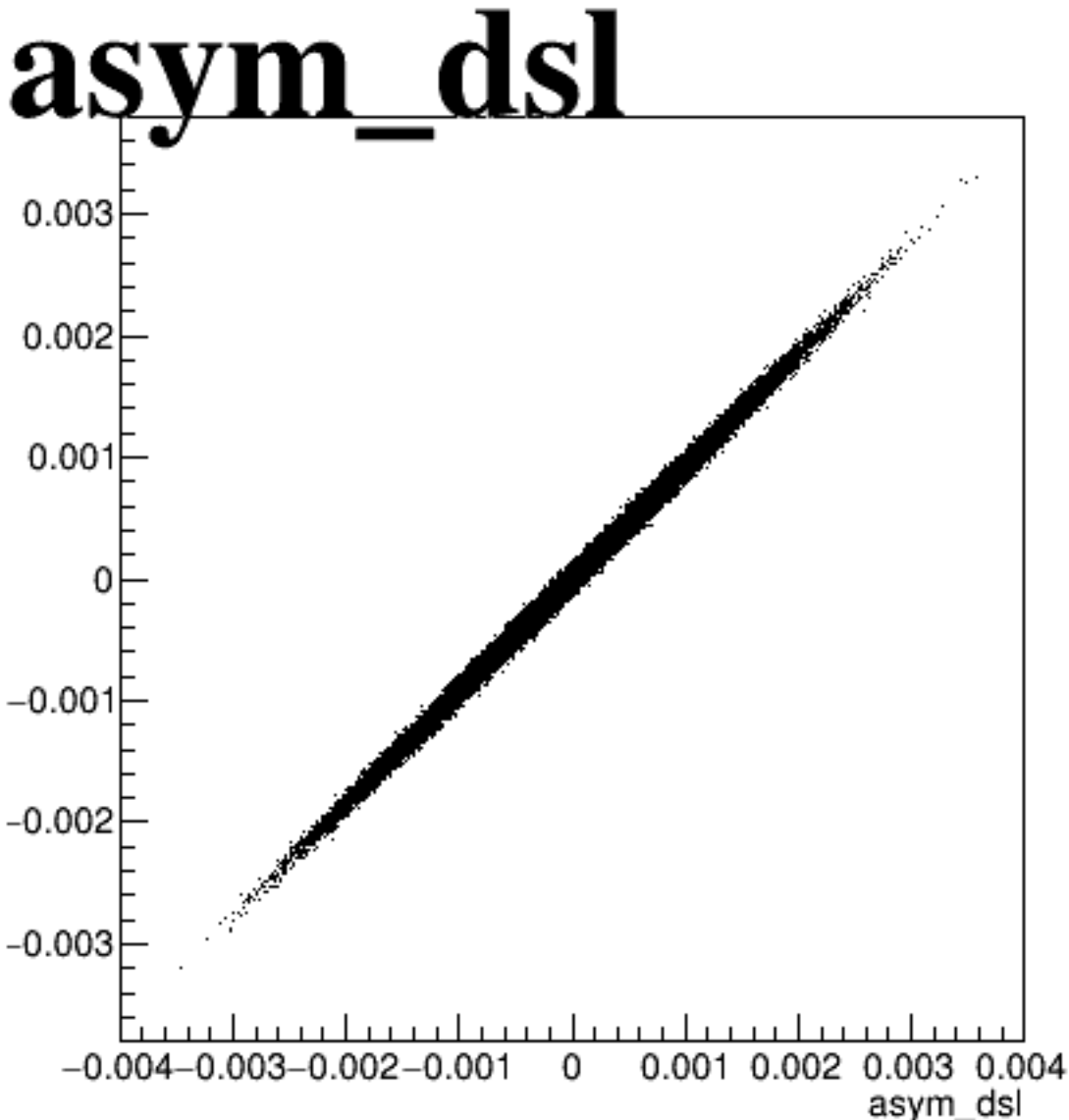
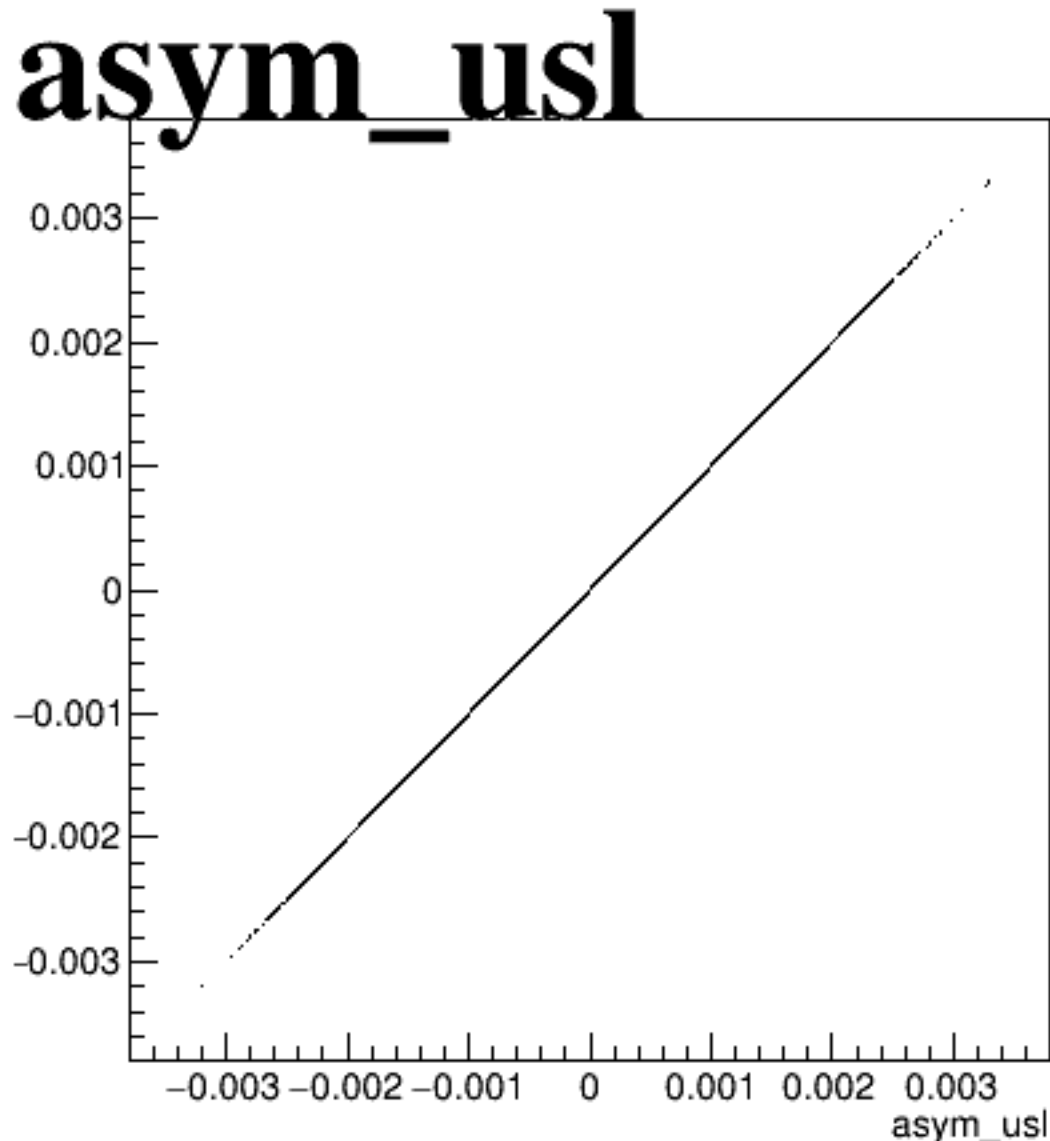


asym\_dsr

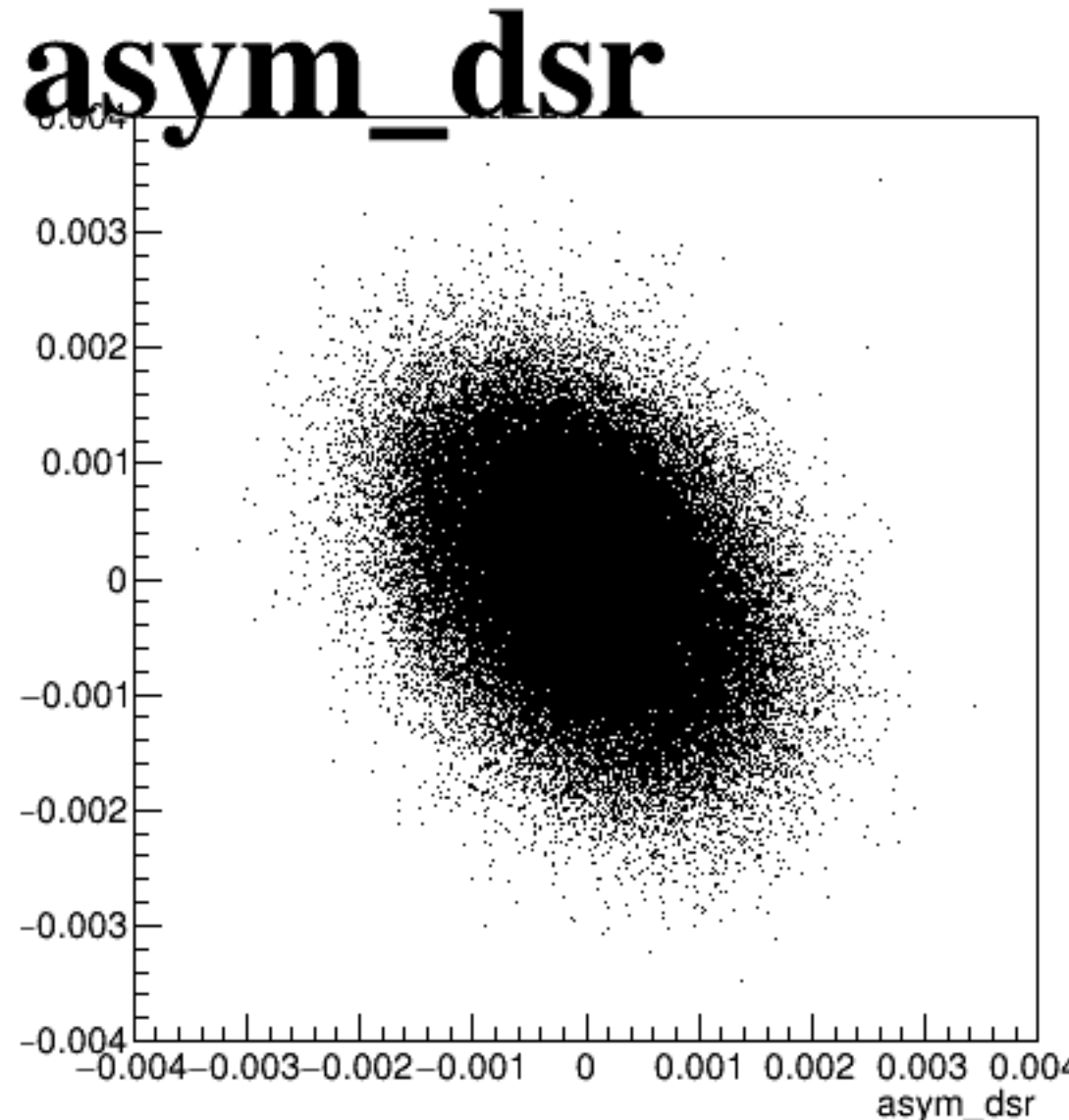
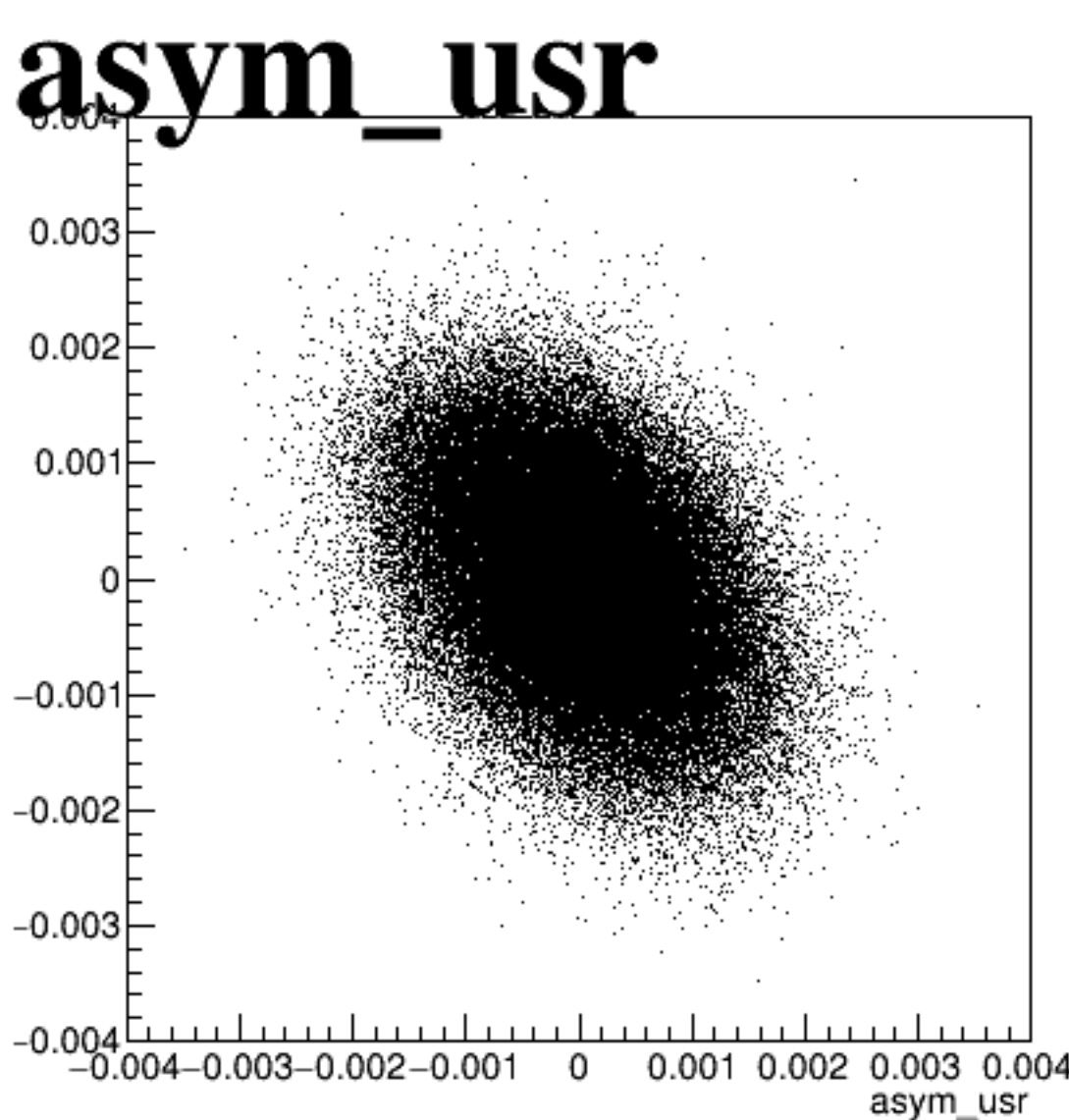
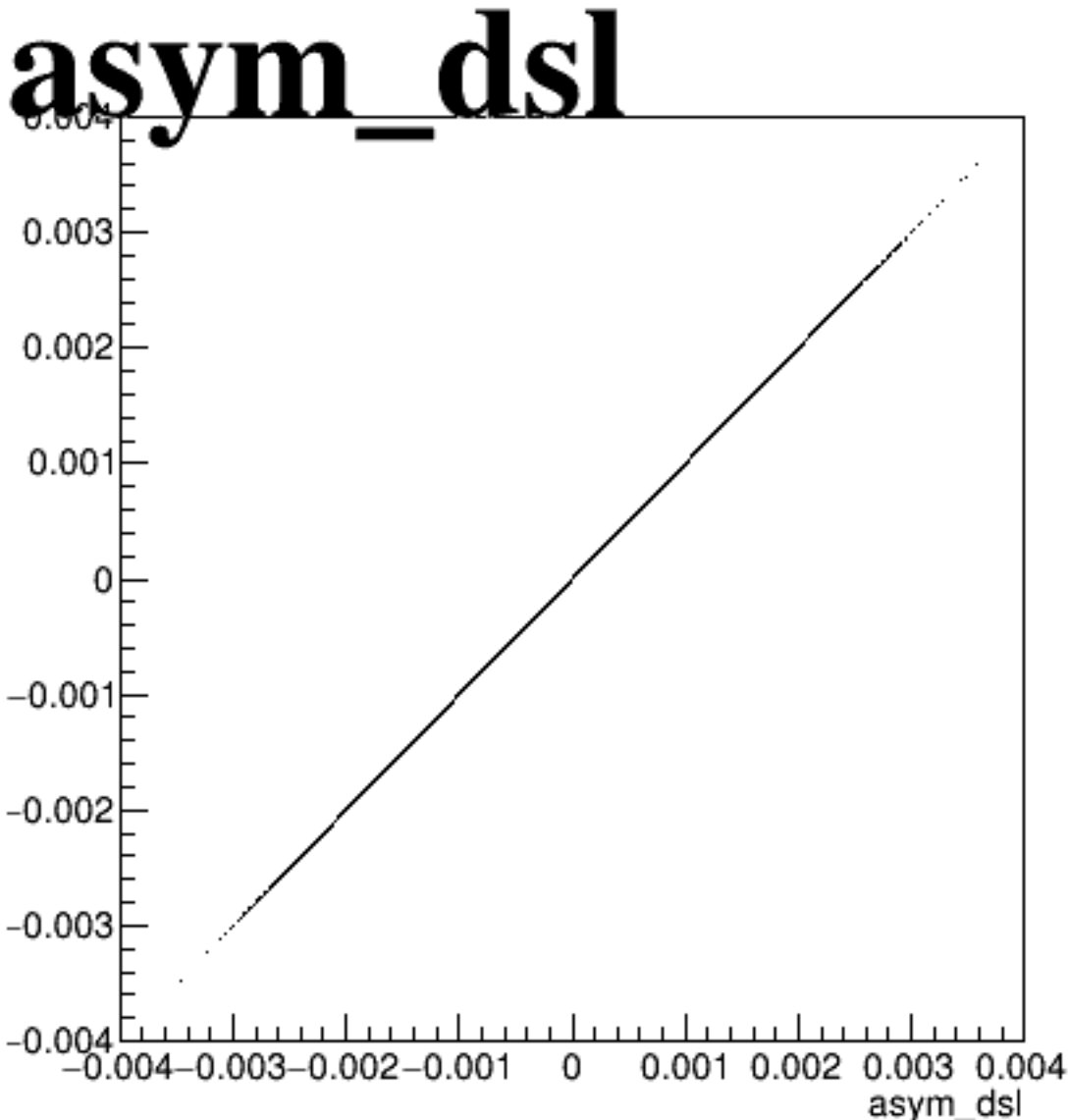
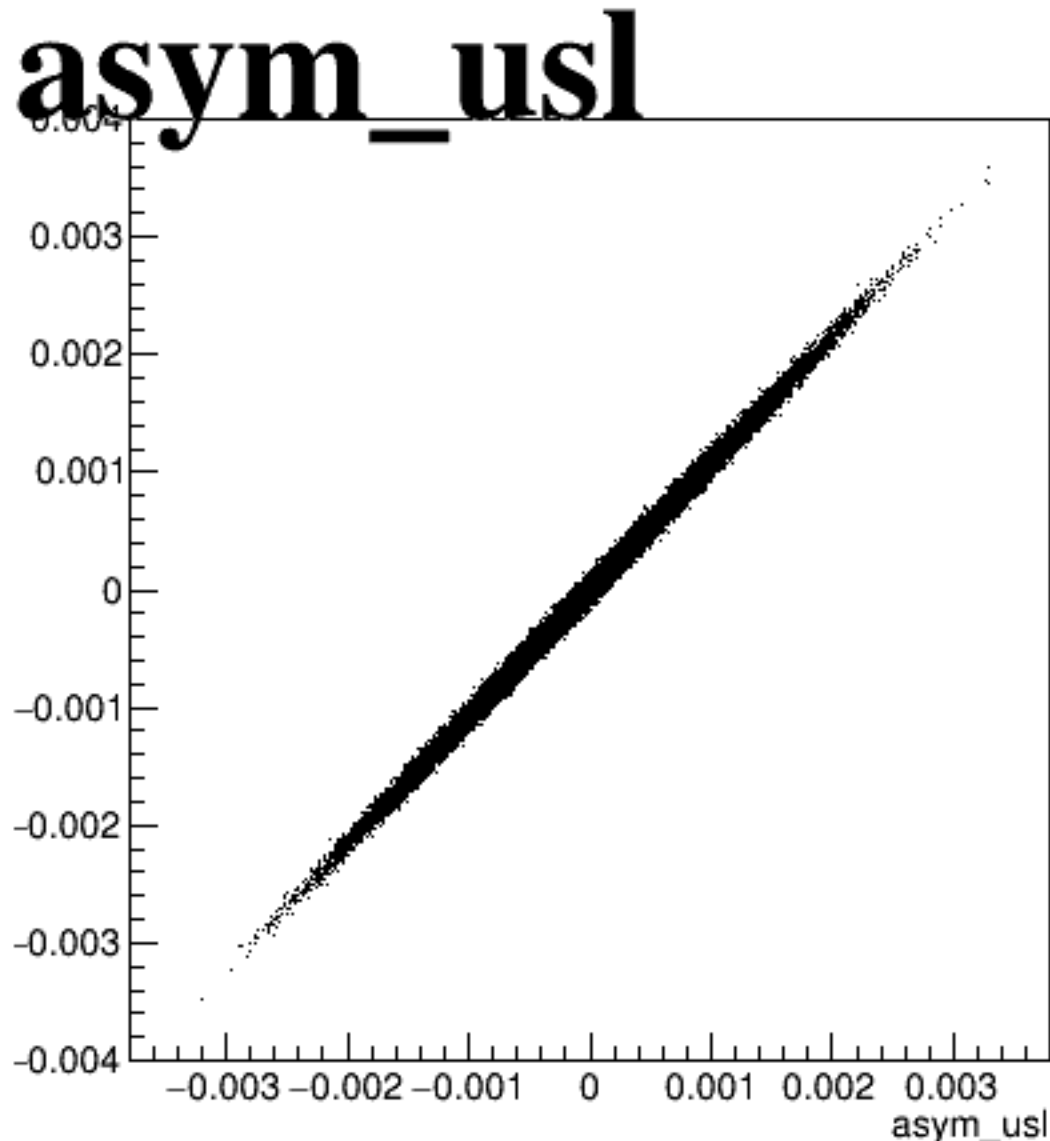




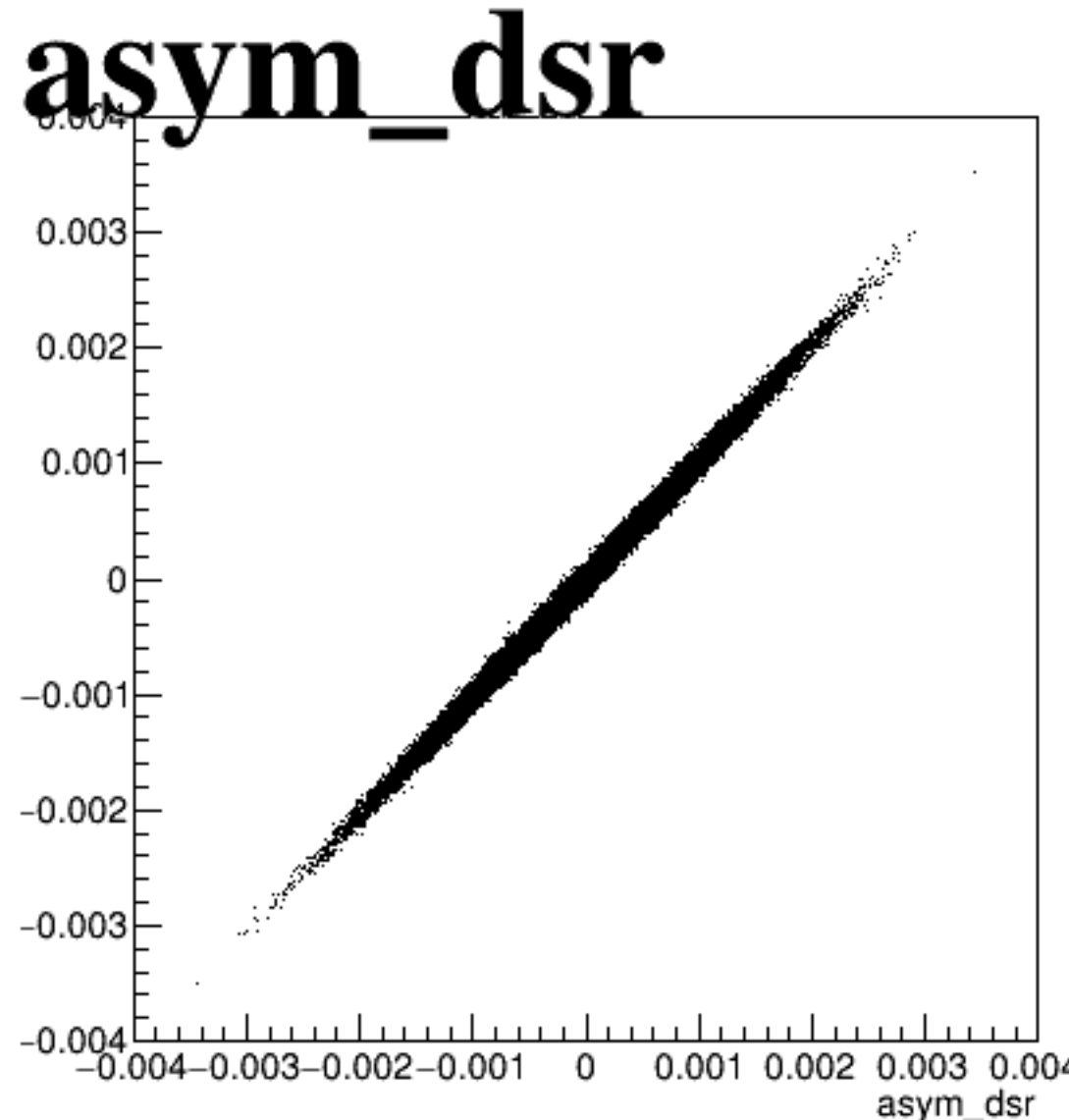
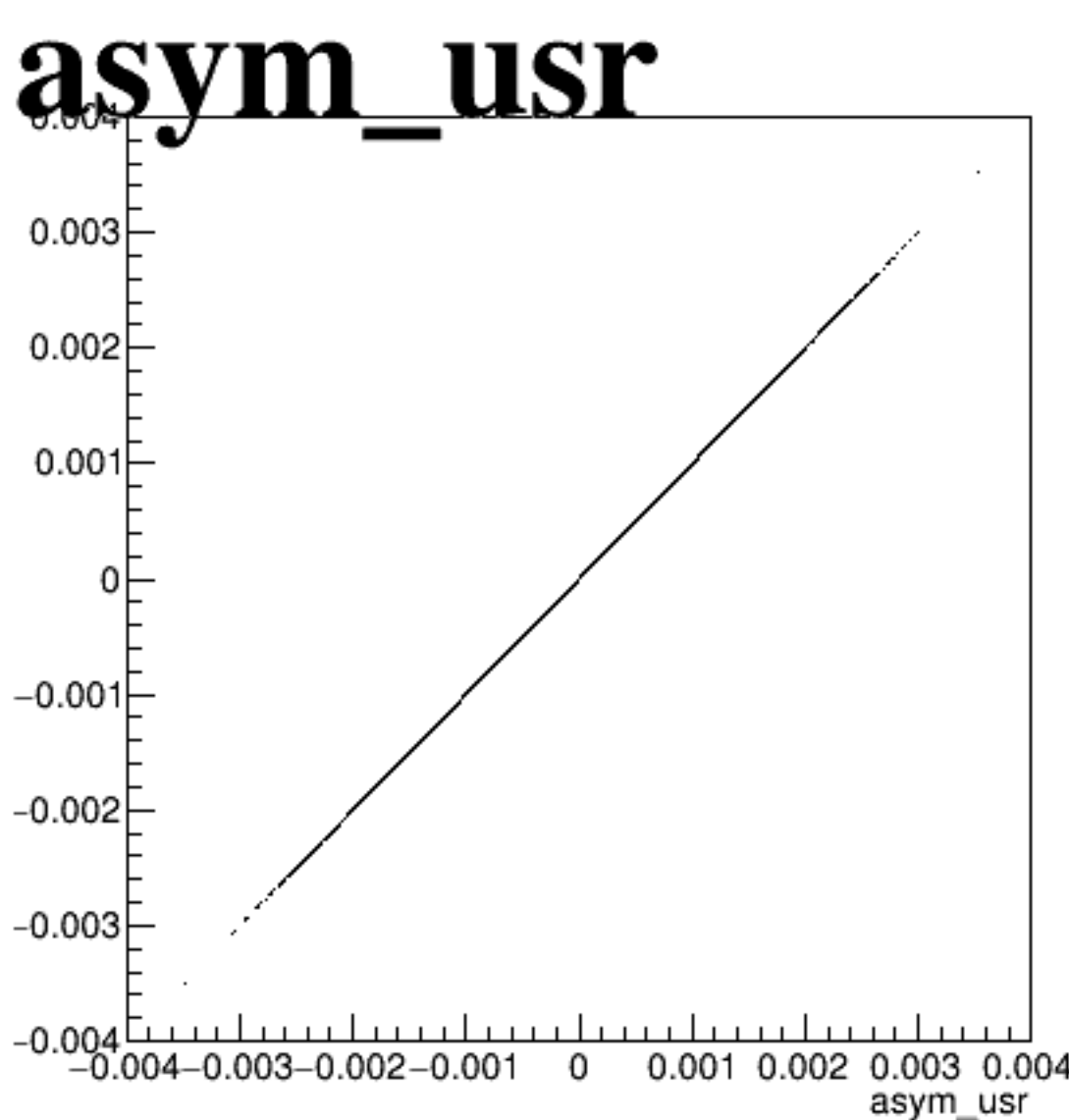
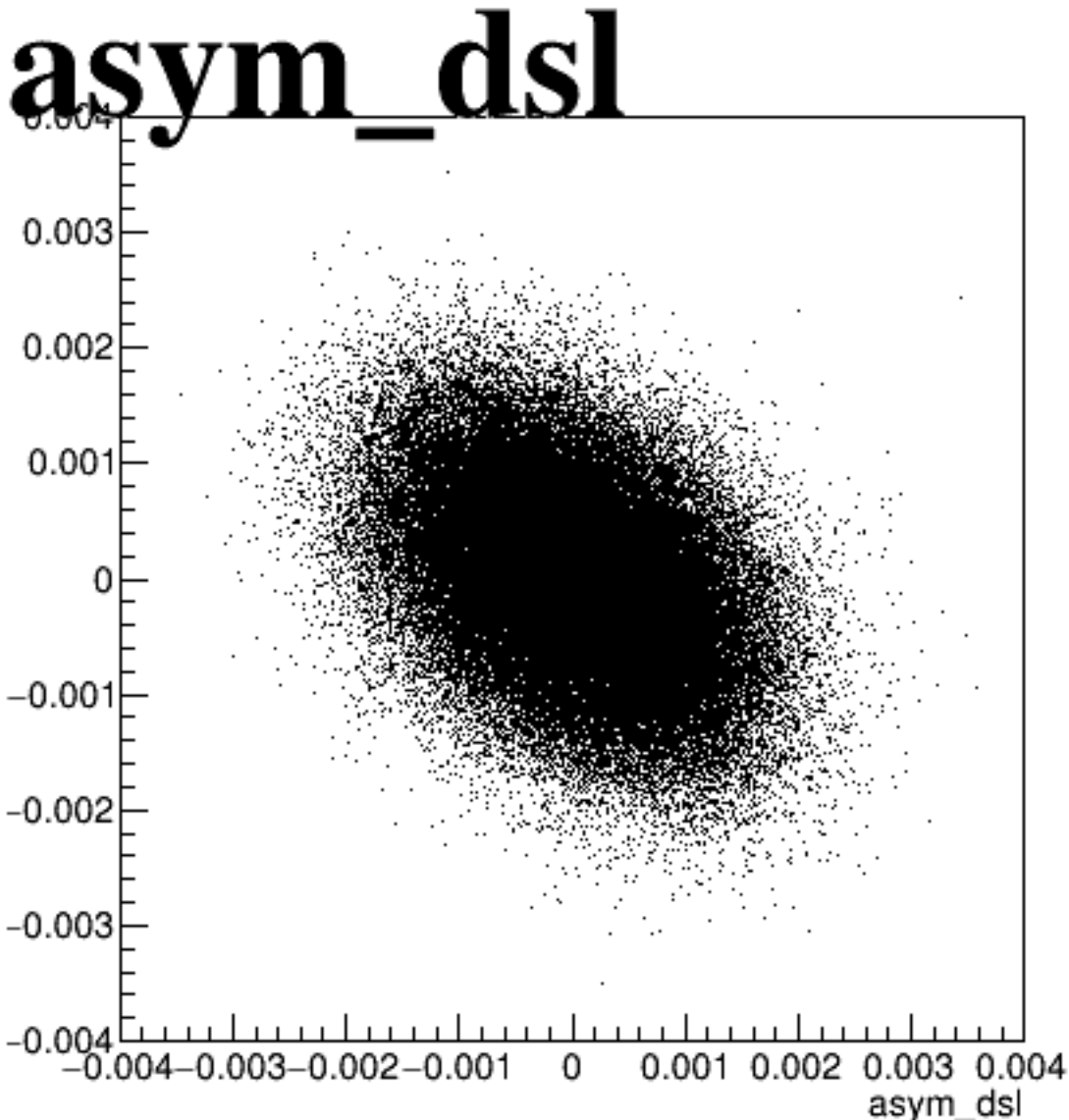
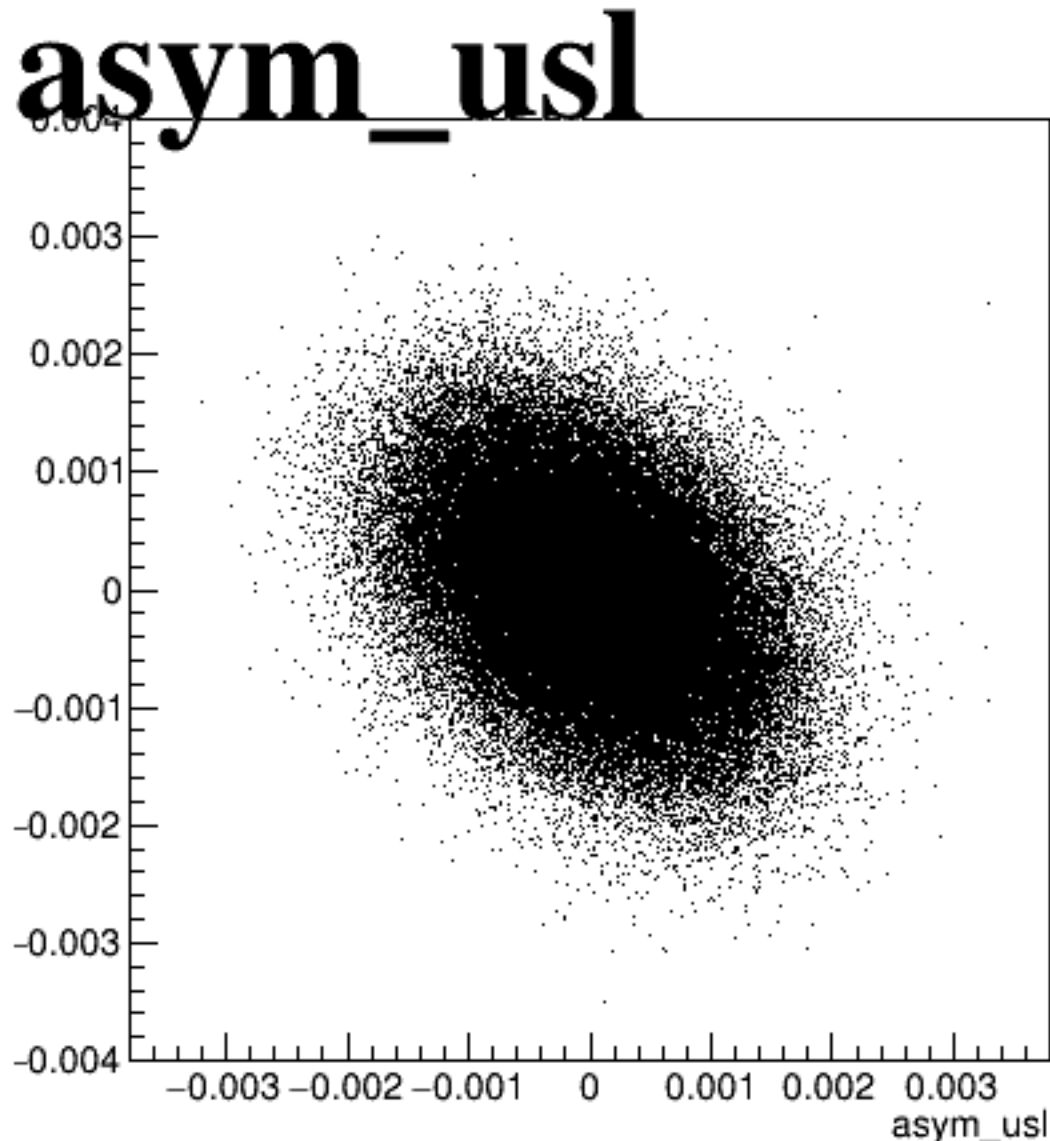
asym\_usl



asym\_dsl



asym\_usr



asym\_dsr

