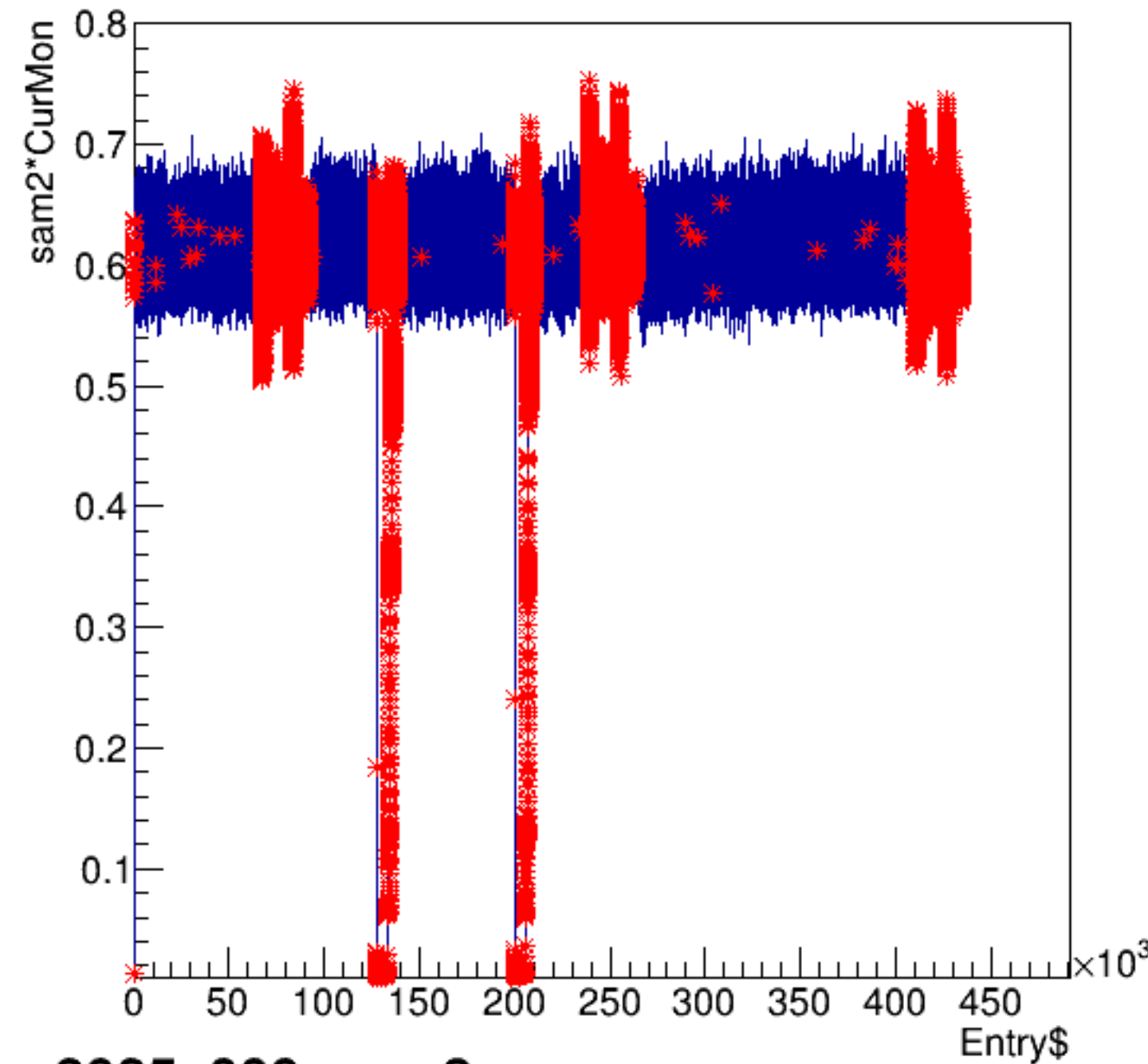
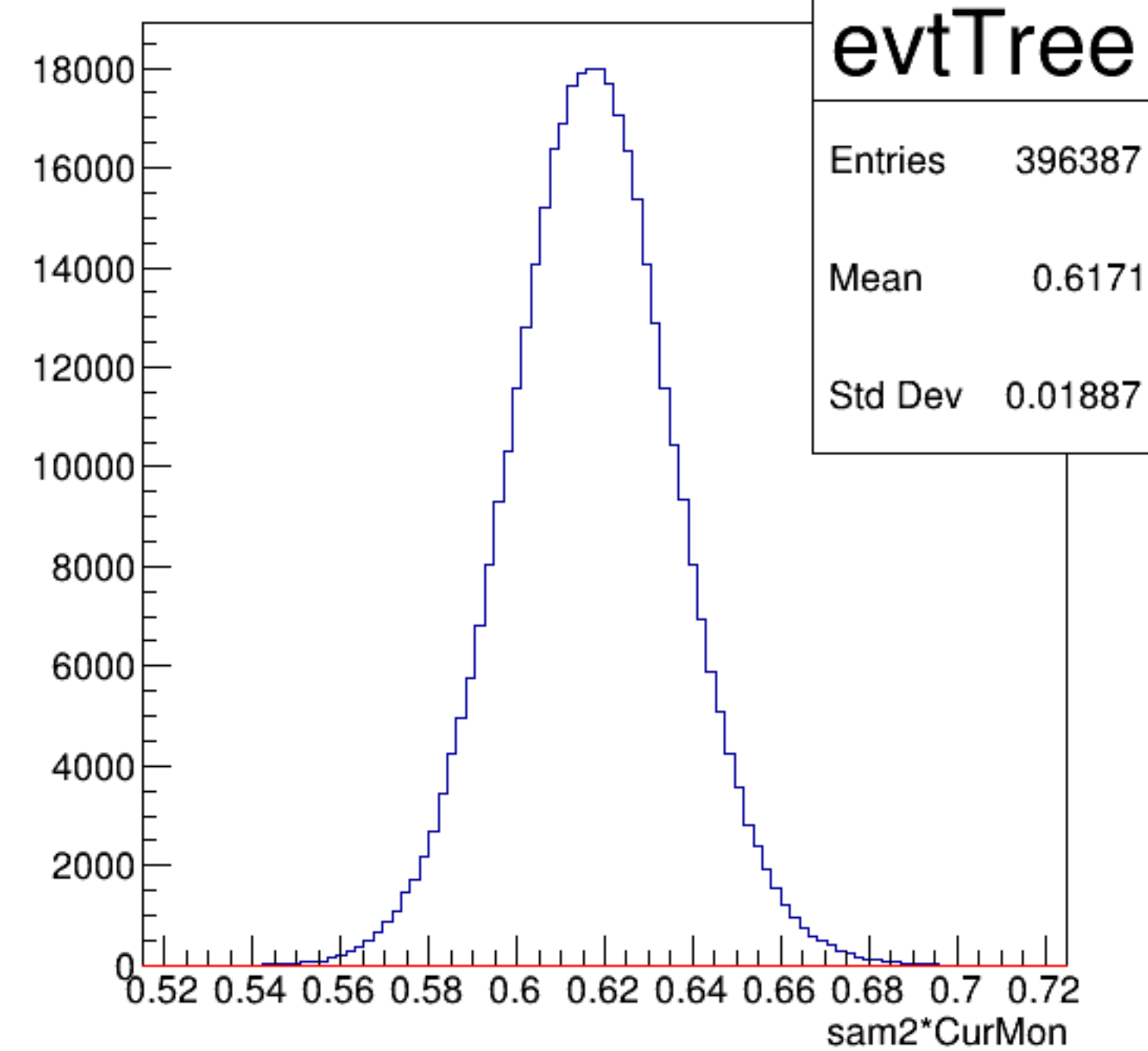


sam2*CurMon:Entry\$

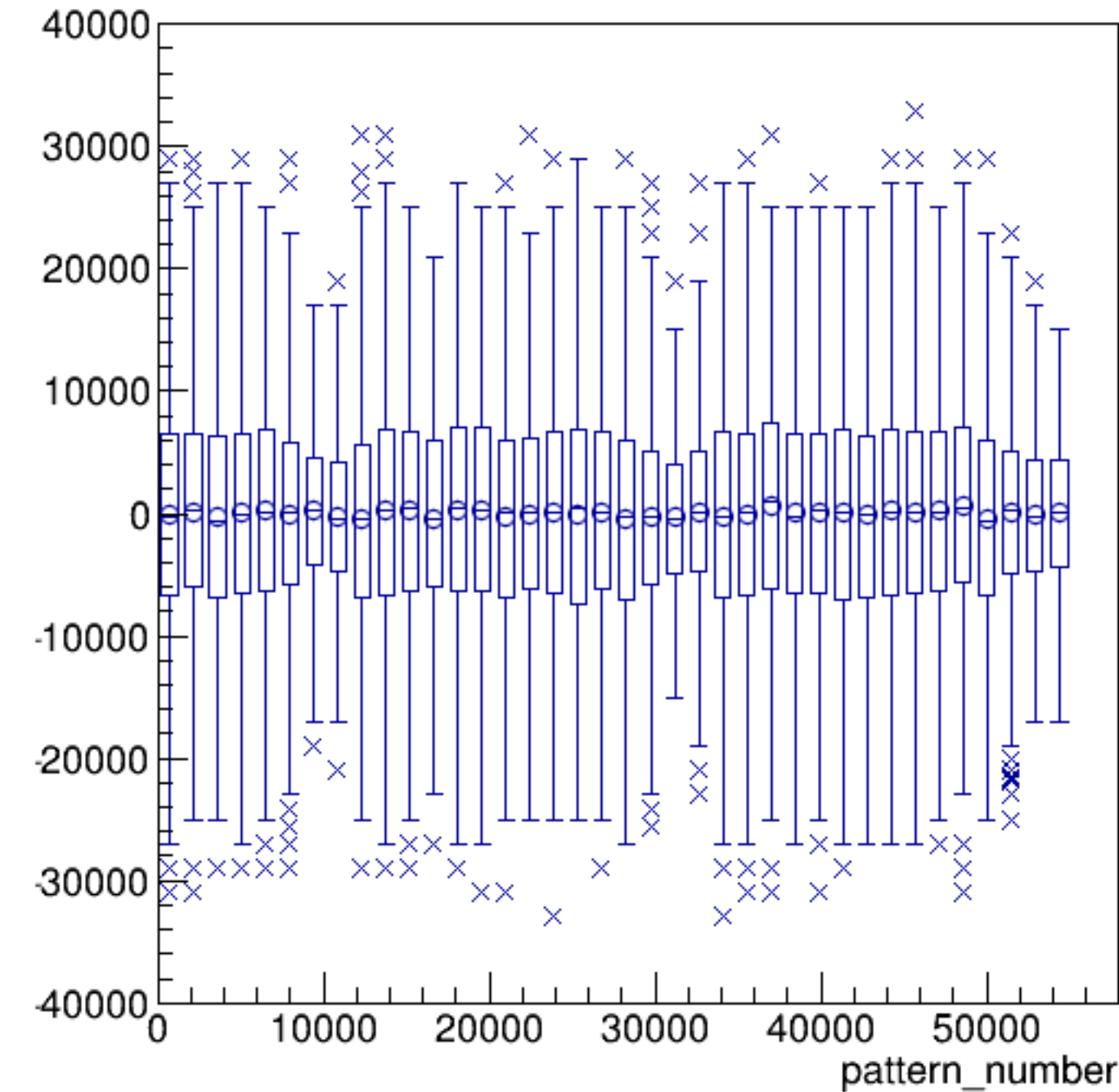


run3925_000_sam2.png

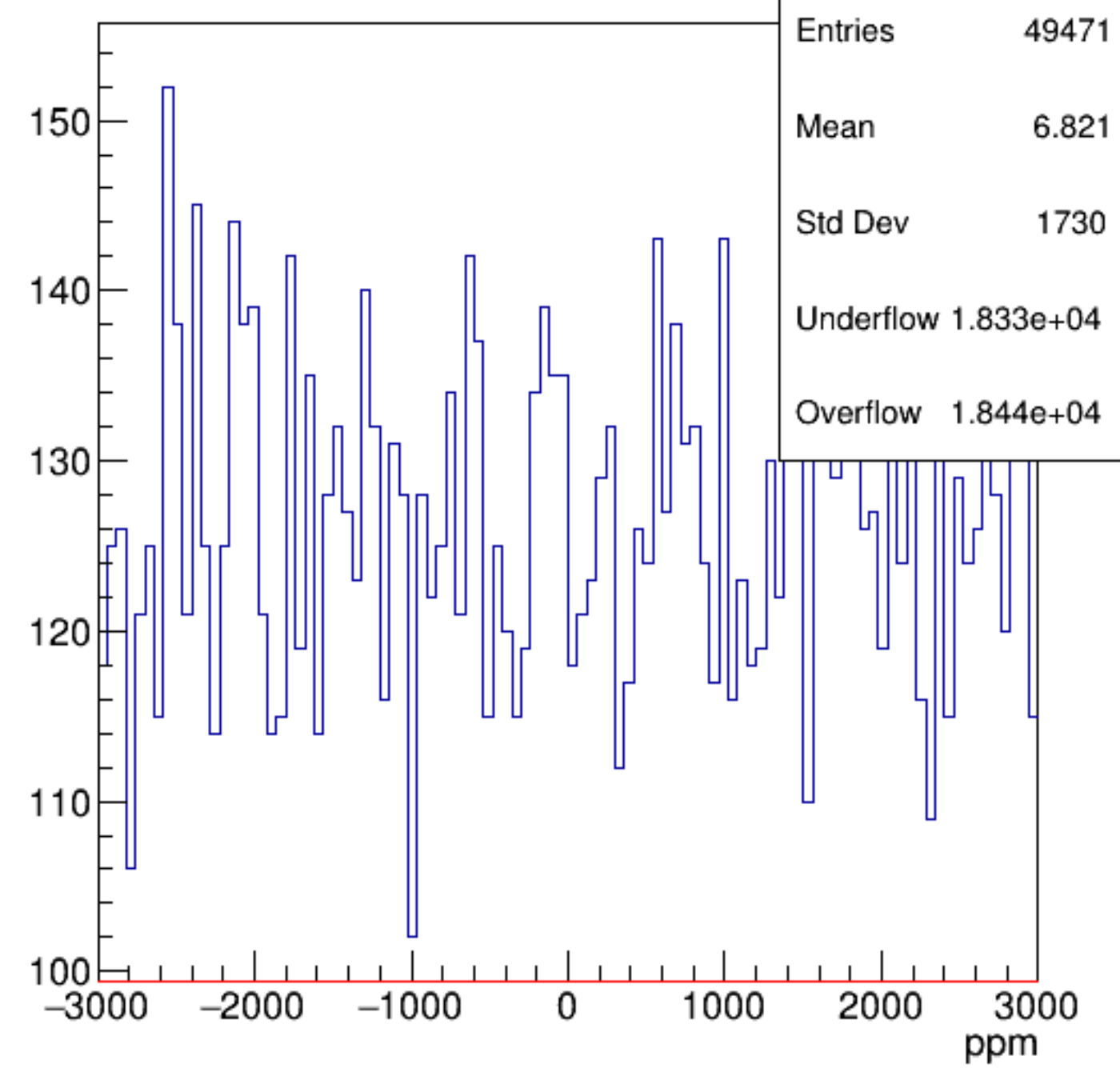
sam2*CurMon {ErrorFlag==0}



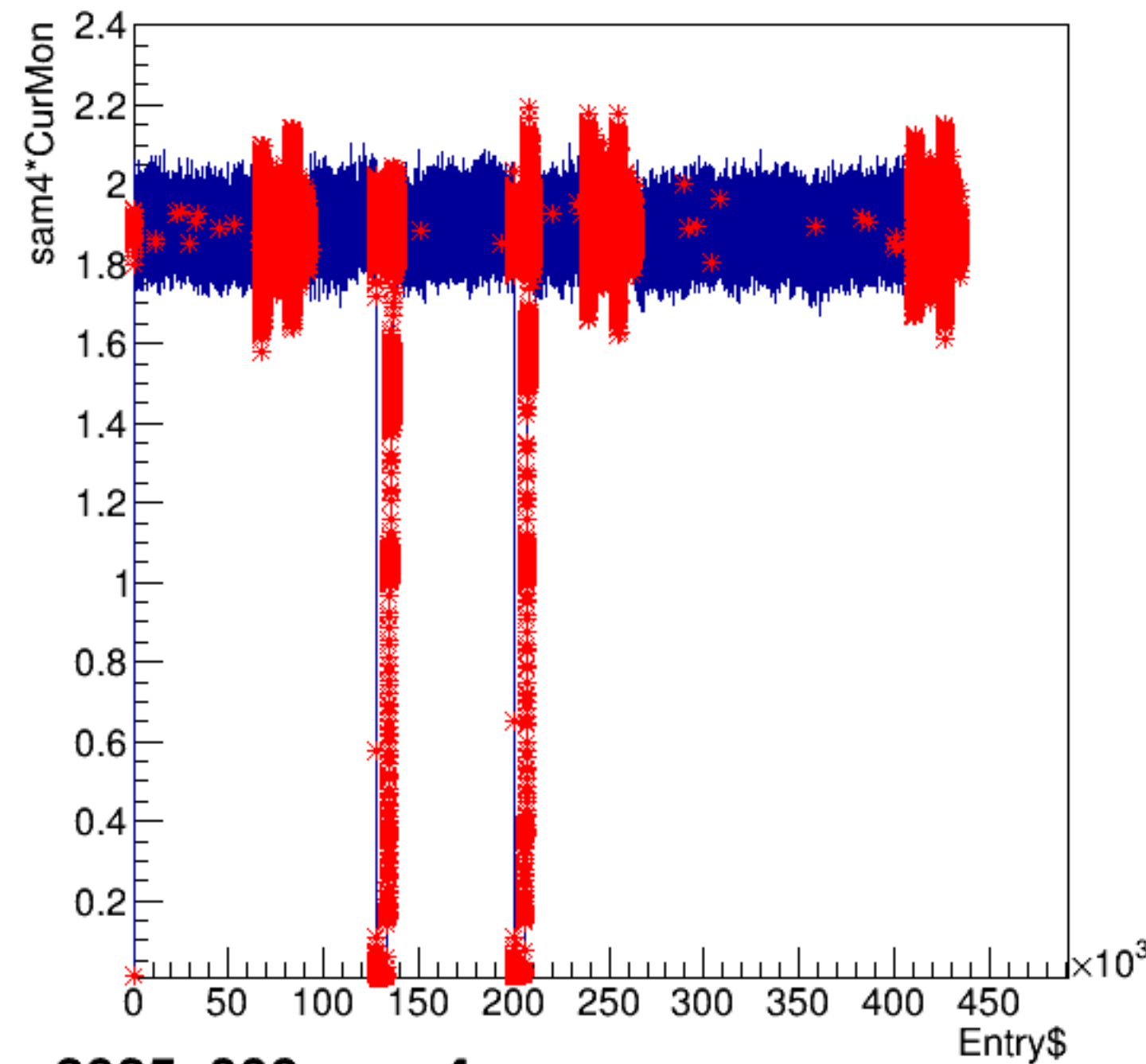
asym_sam2/ppm:pattern_number {ErrorFlag==0}



asym_sam2

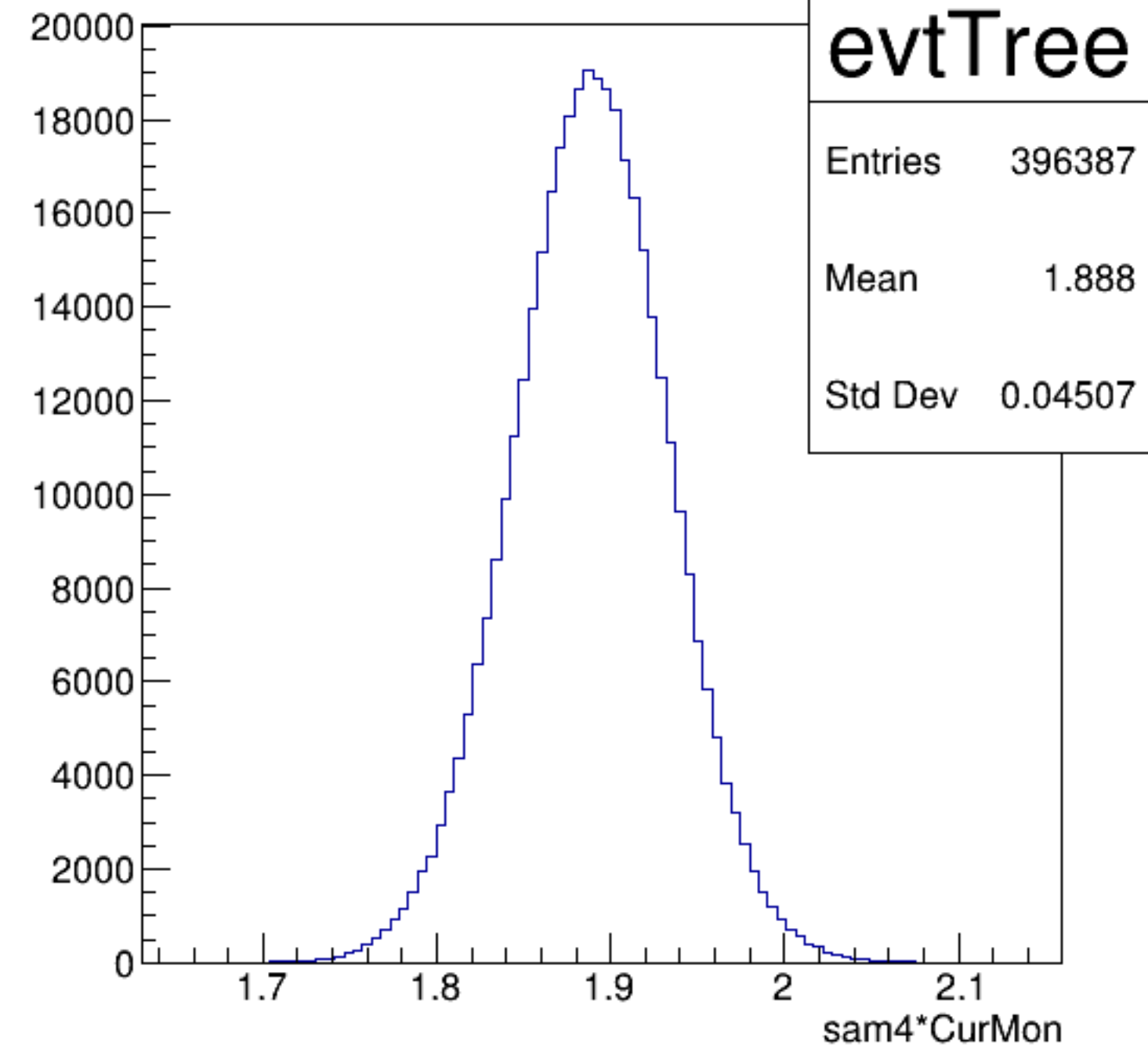


sam4*CurMon:Entry\$

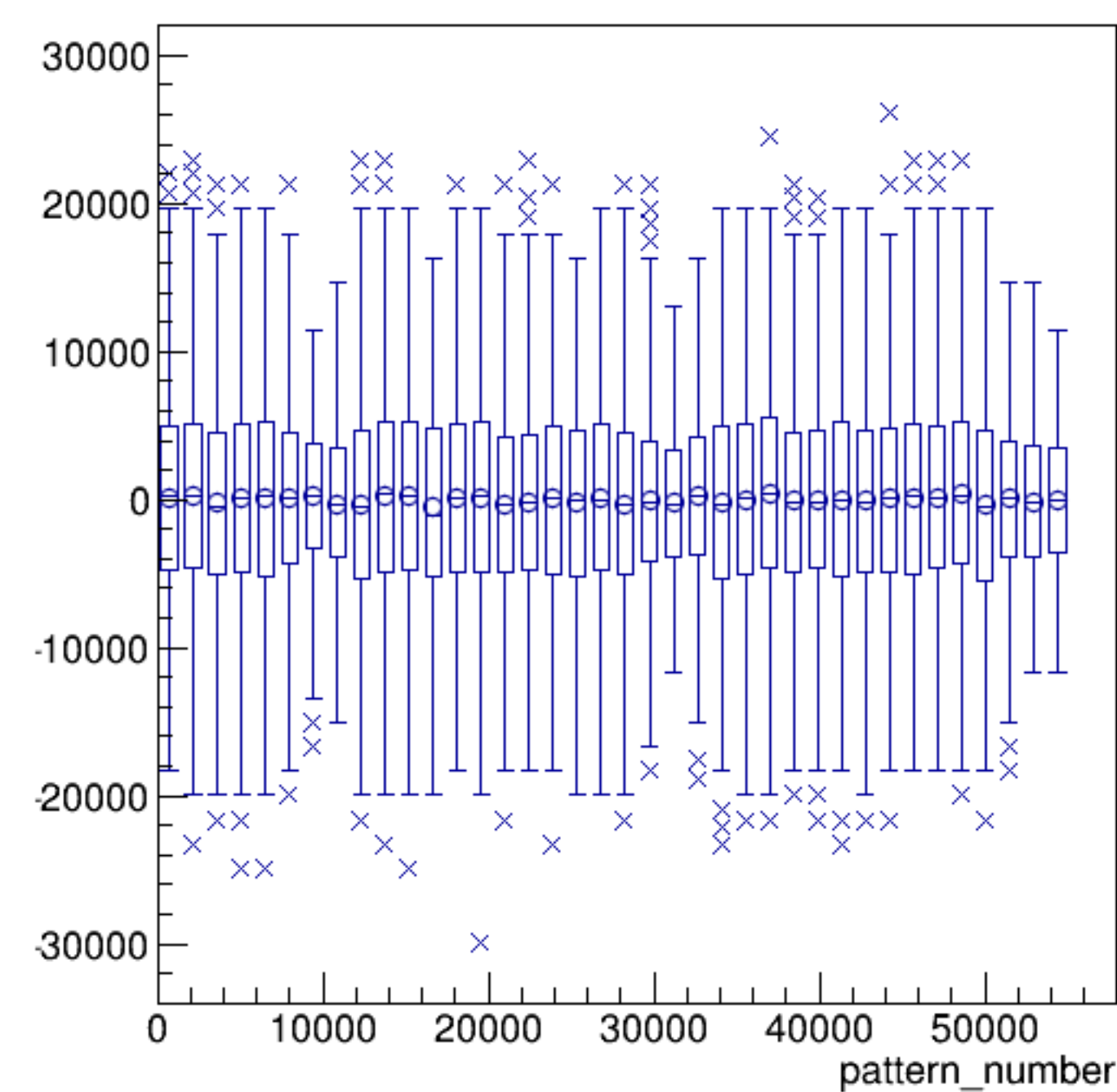


run3925_000_sam4.png

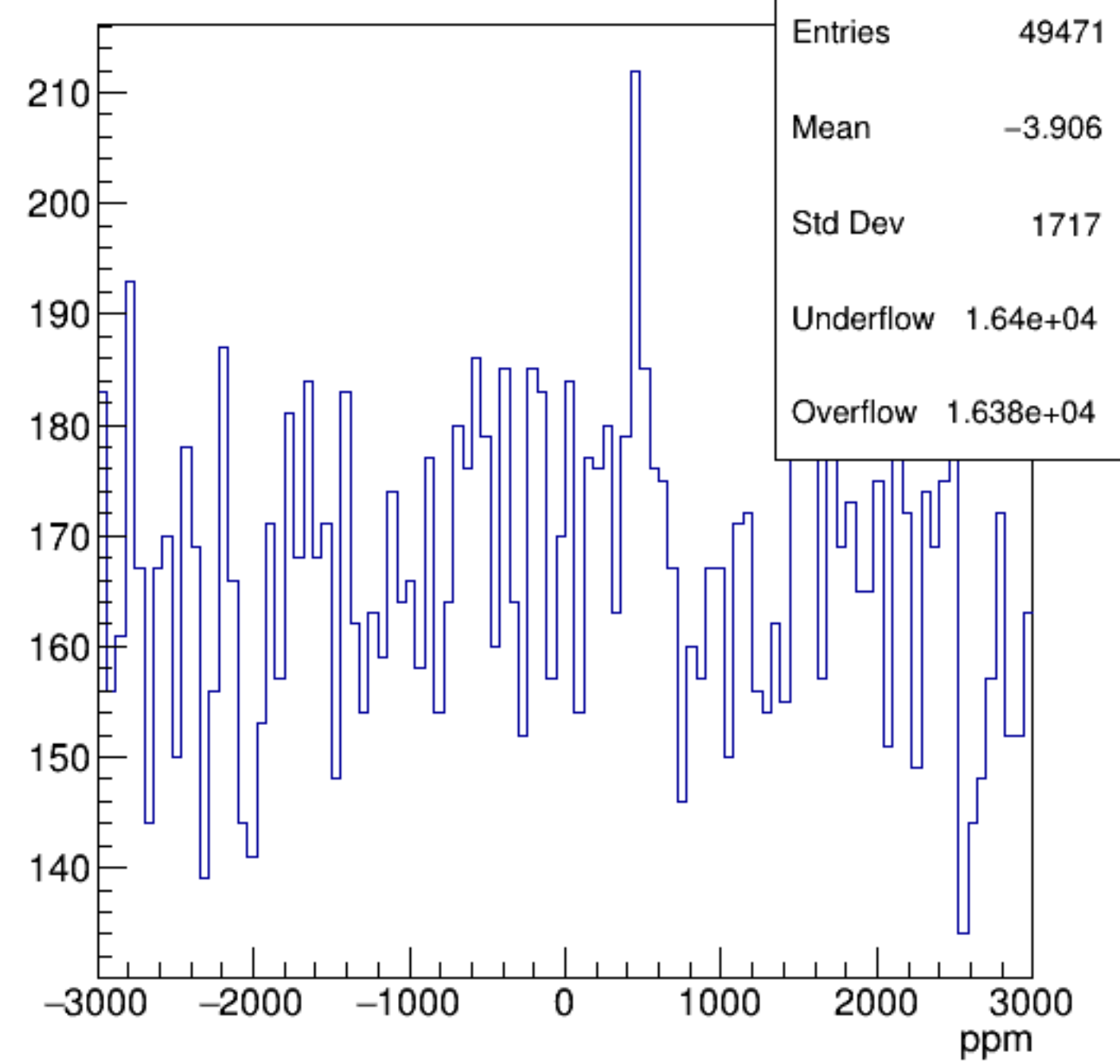
sam4*CurMon {ErrorFlag==0}



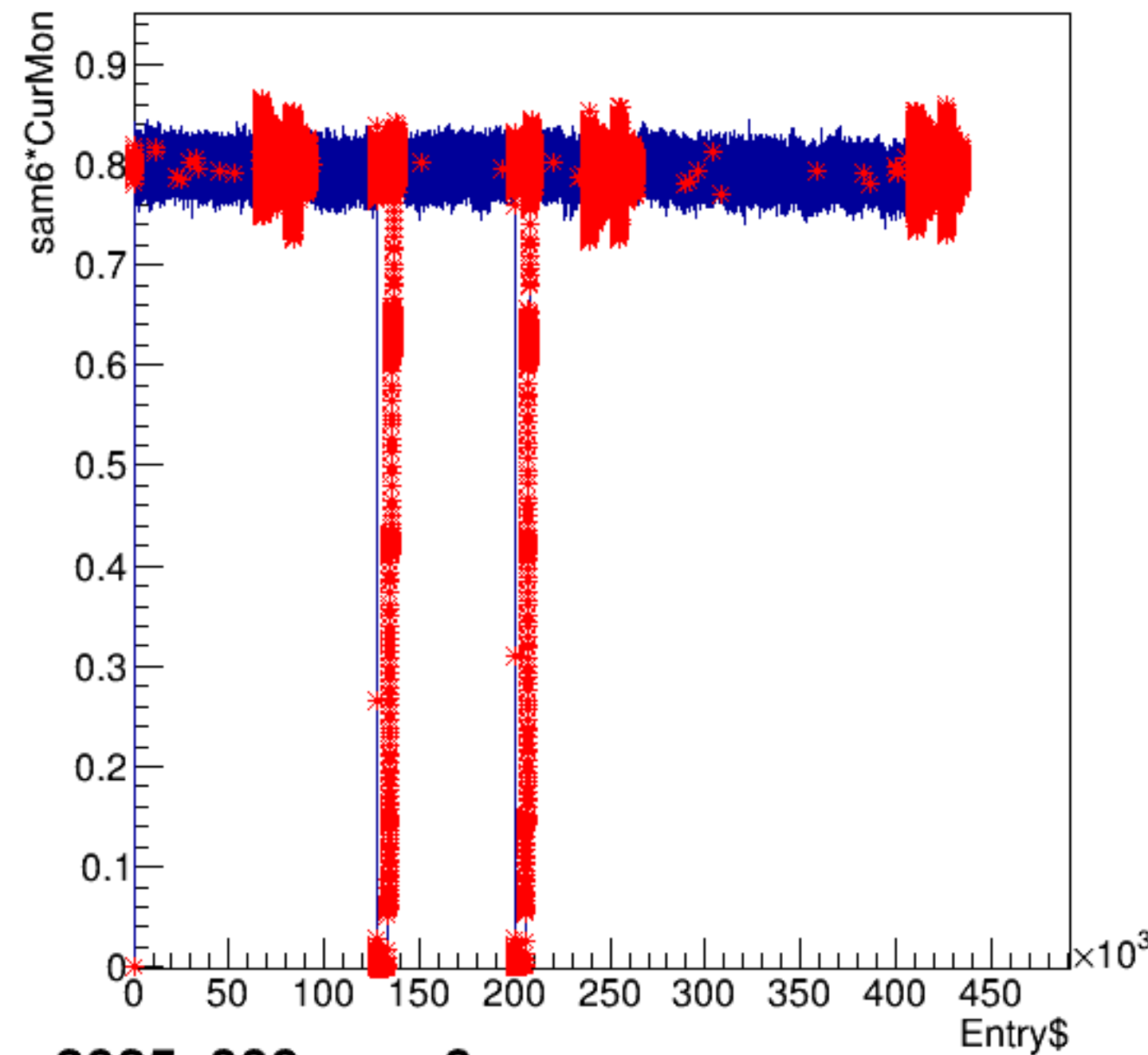
asym_sam4/ppm:pattern_number {ErrorFlag==0}



asym_sam4

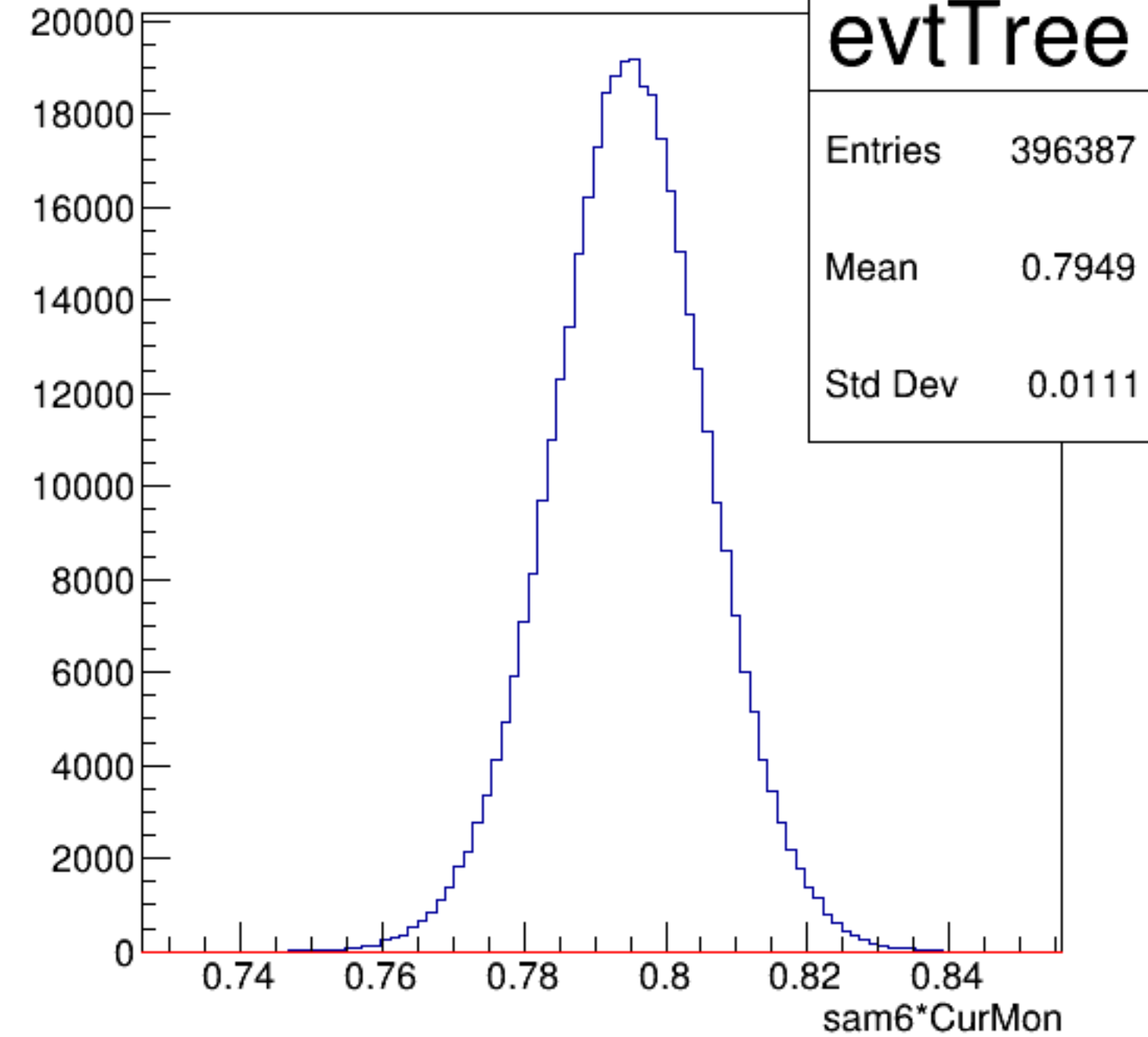


sam6*CurMon:Entry\$

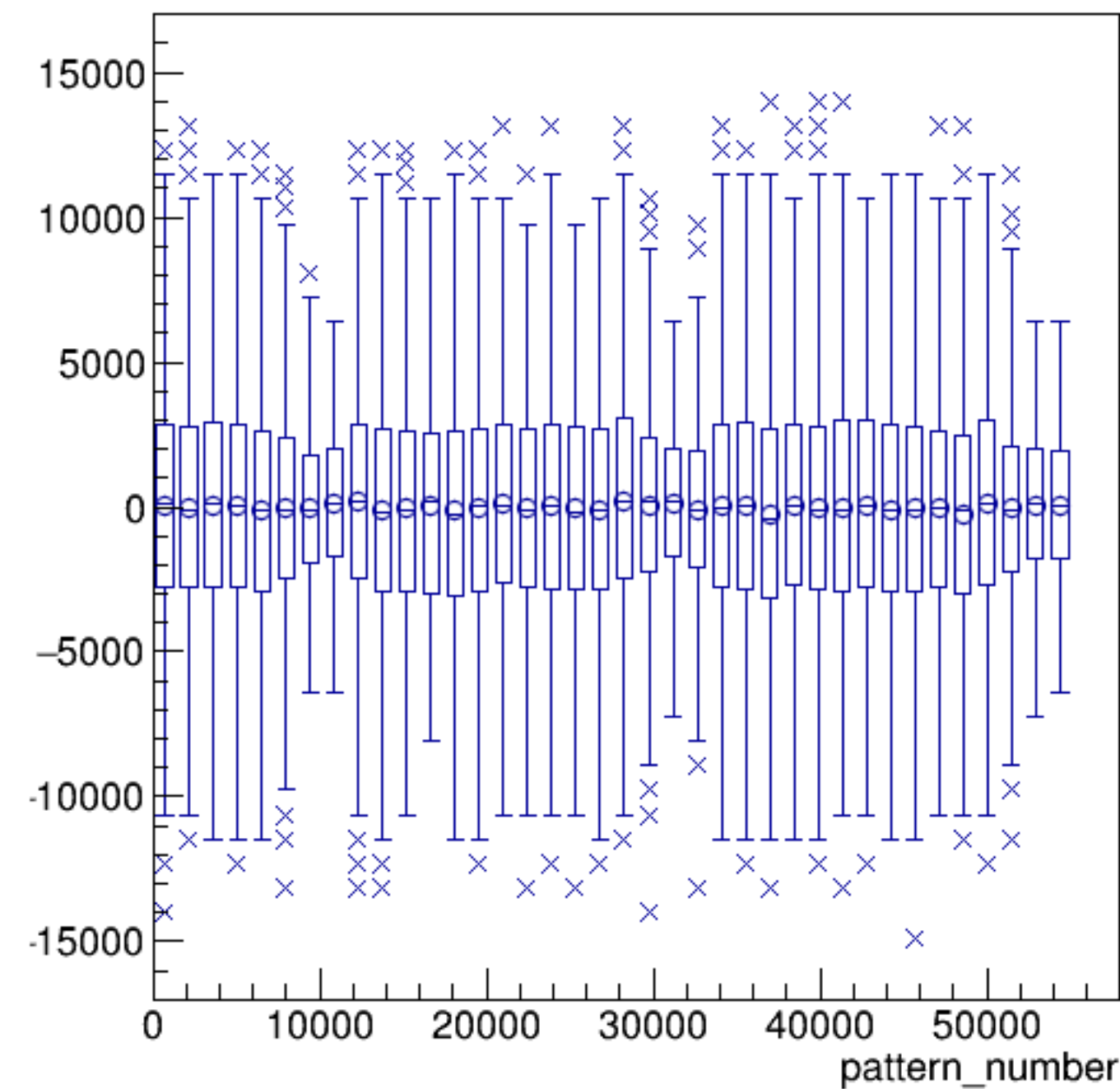


run3925_000_sam6.png

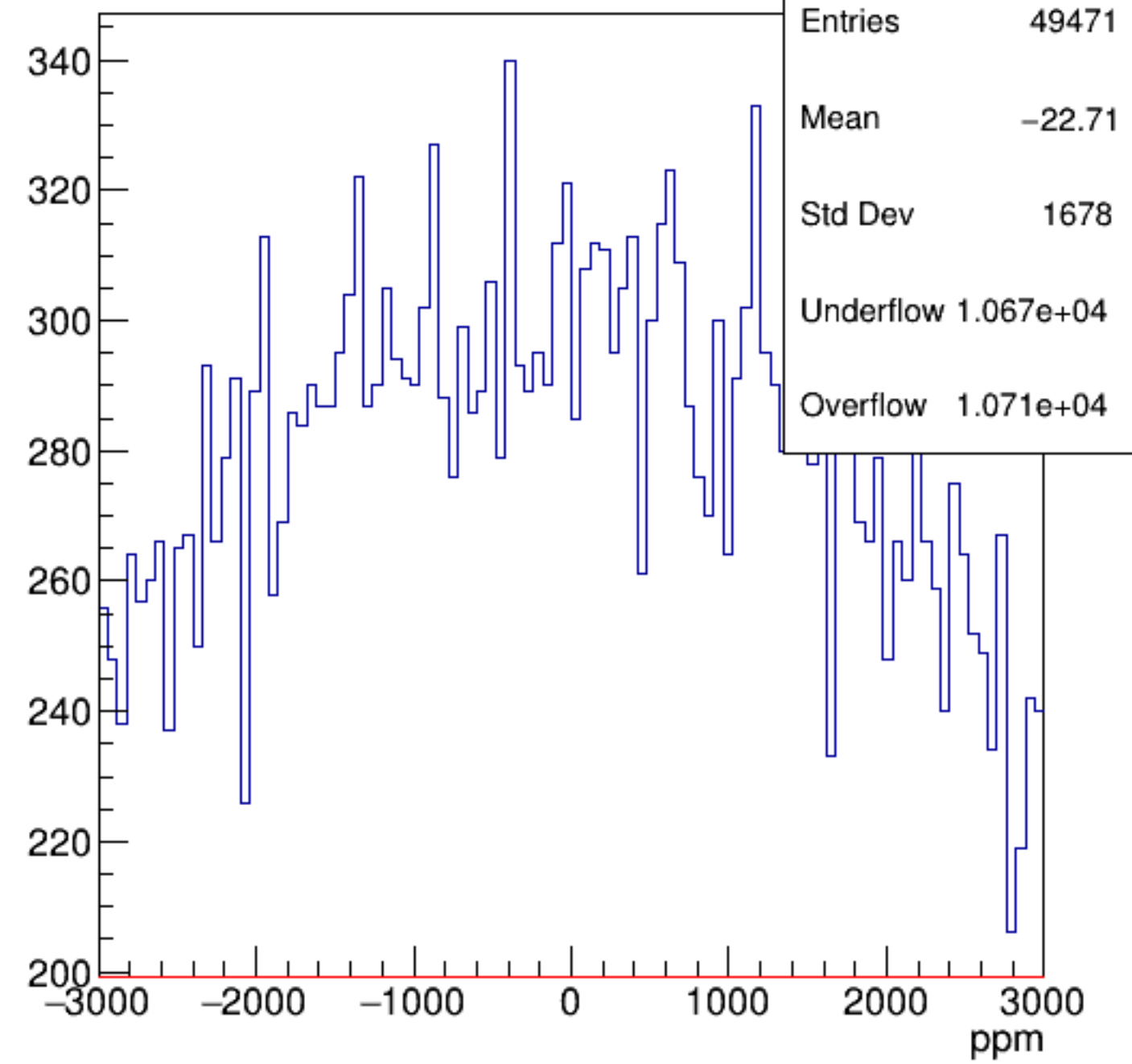
sam6*CurMon {ErrorFlag==0}



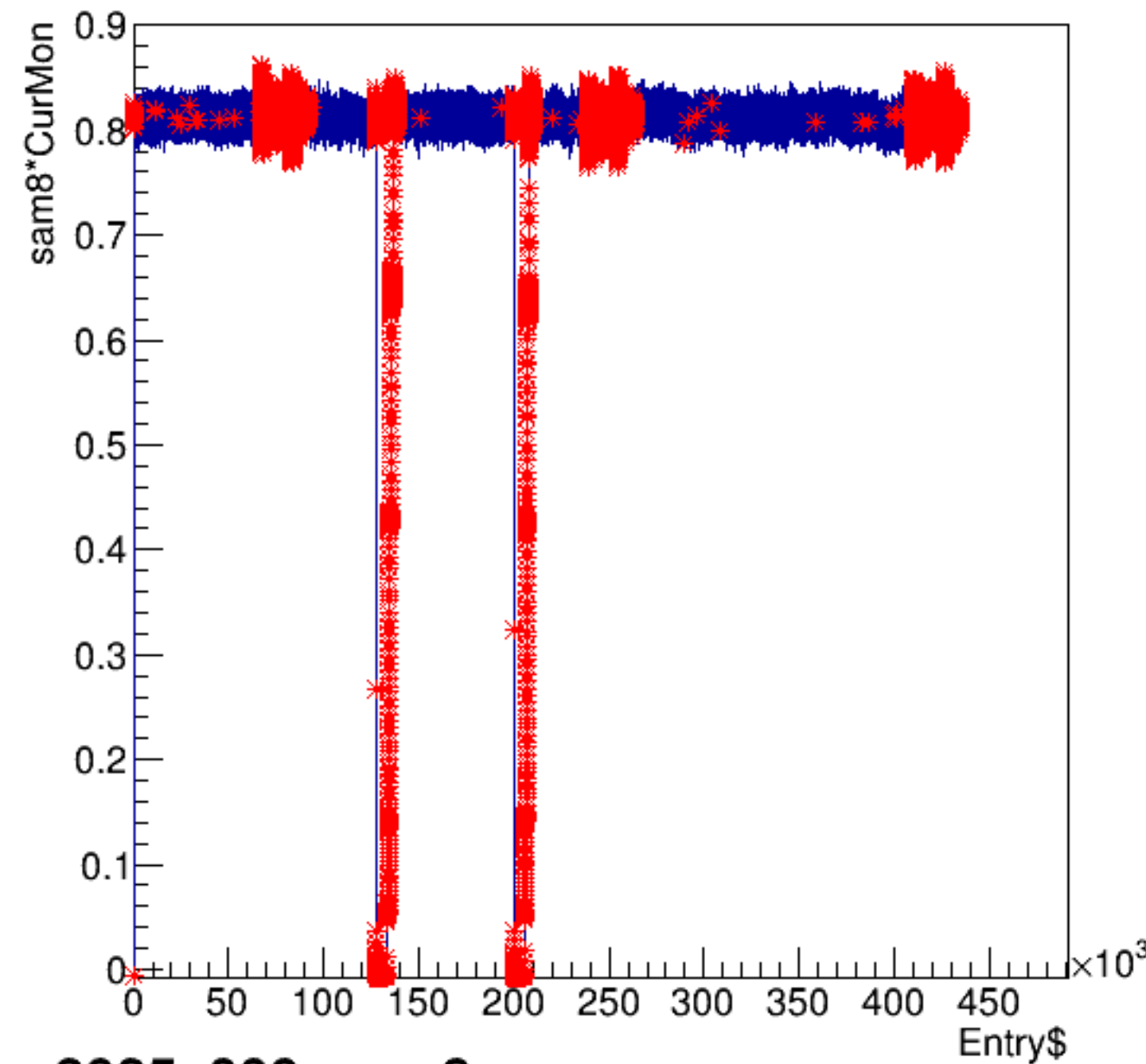
asym_sam6/ppm:pattern_number {ErrorFlag==0}



asym_sam6

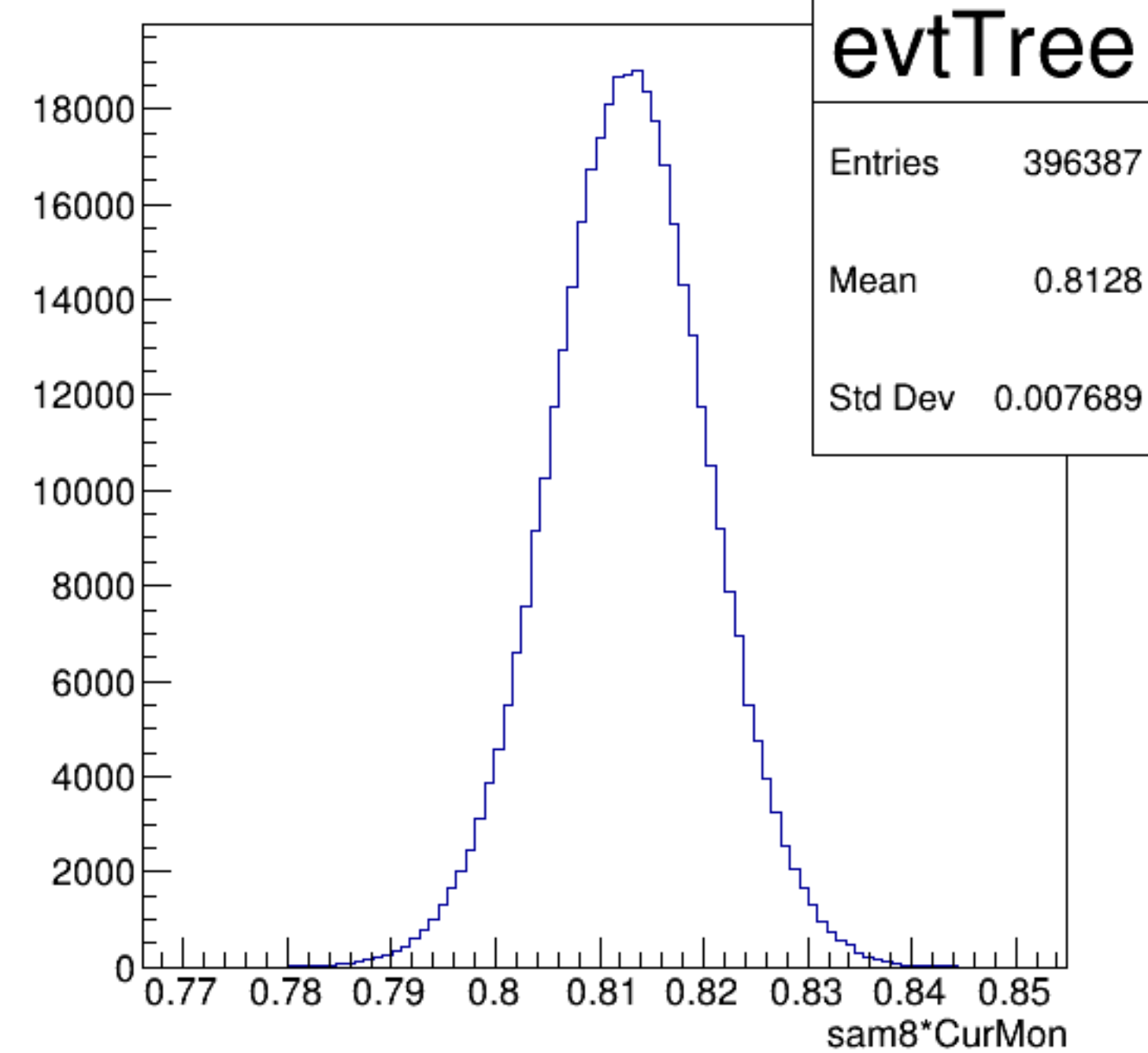


sam8*CurMon:Entry\$



run3925_000_sam8.png

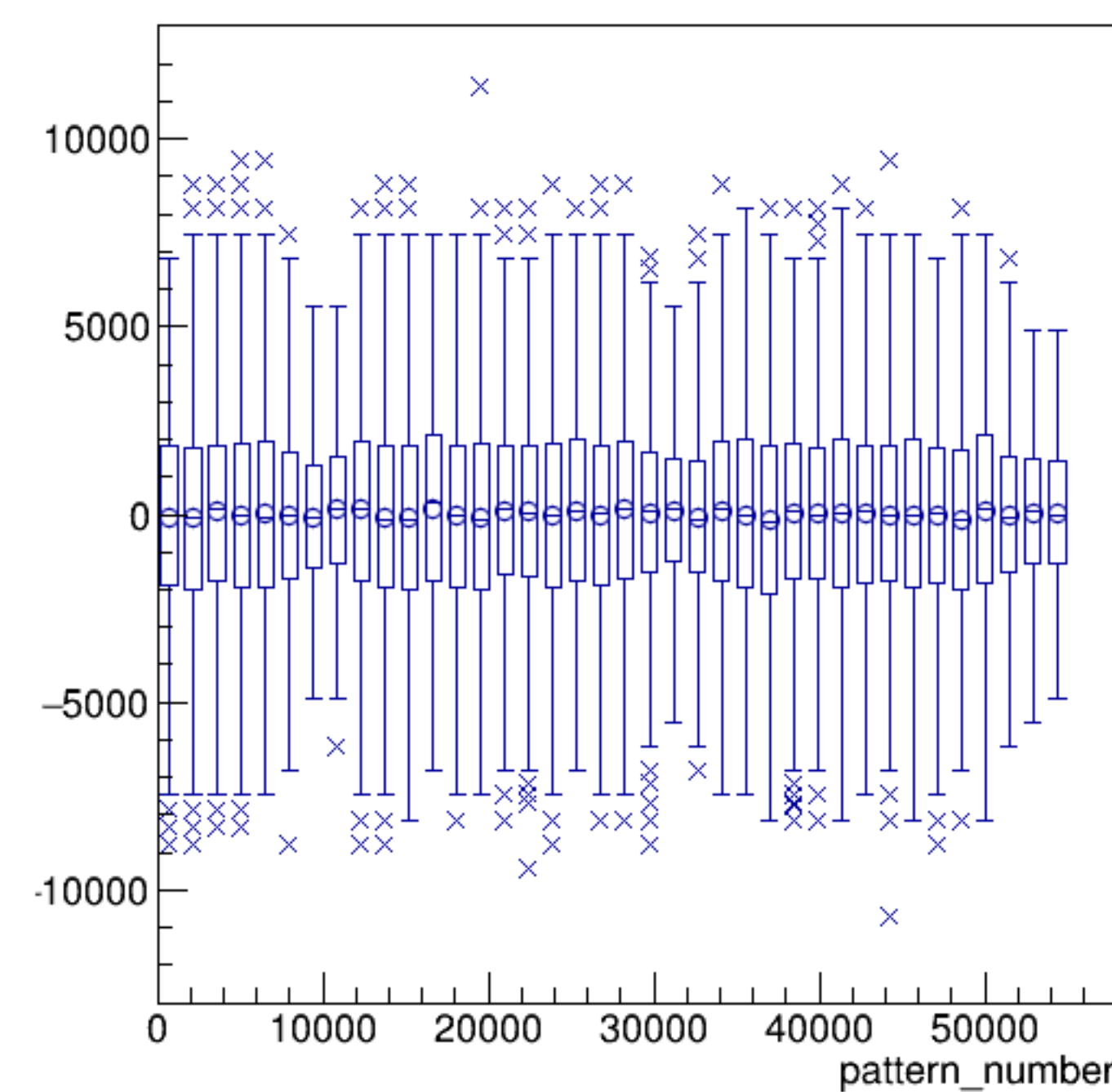
sam8*CurMon {ErrorFlag==0}



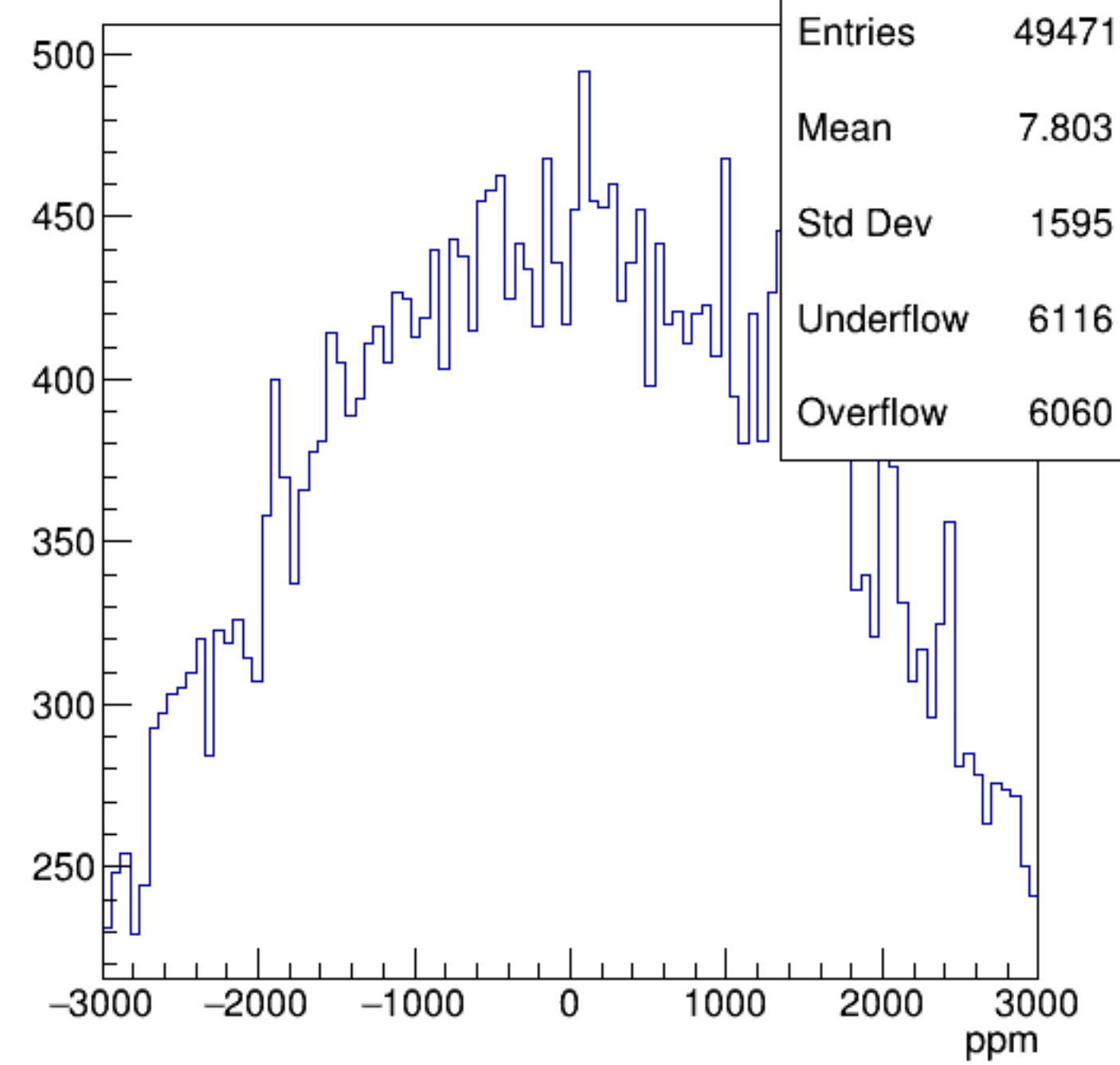
evtTree

Entries	396387
Mean	0.8128
Std Dev	0.007689

asym_sam8/ppm:pattern_number {ErrorFlag==0}

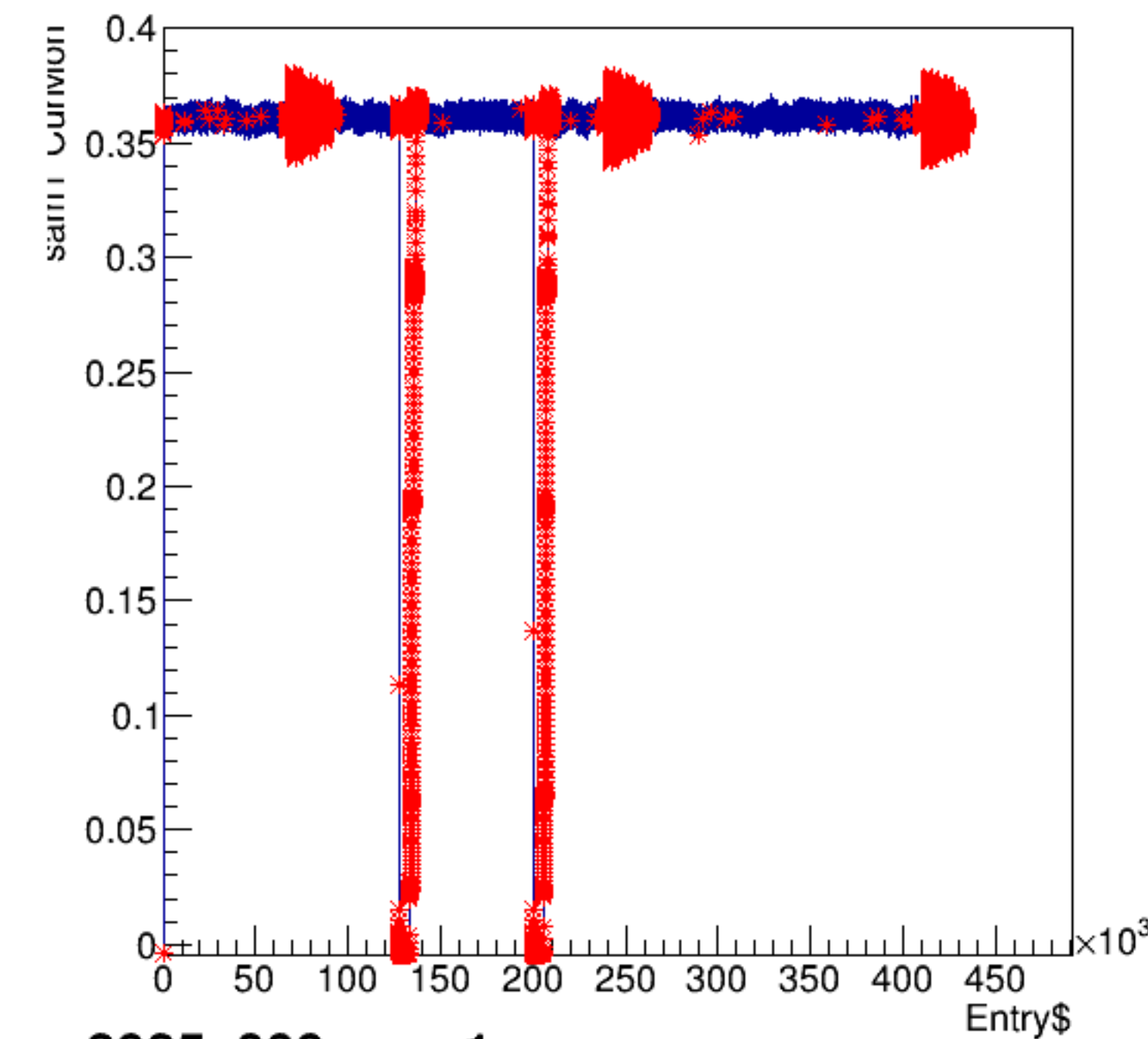


asym_sam8



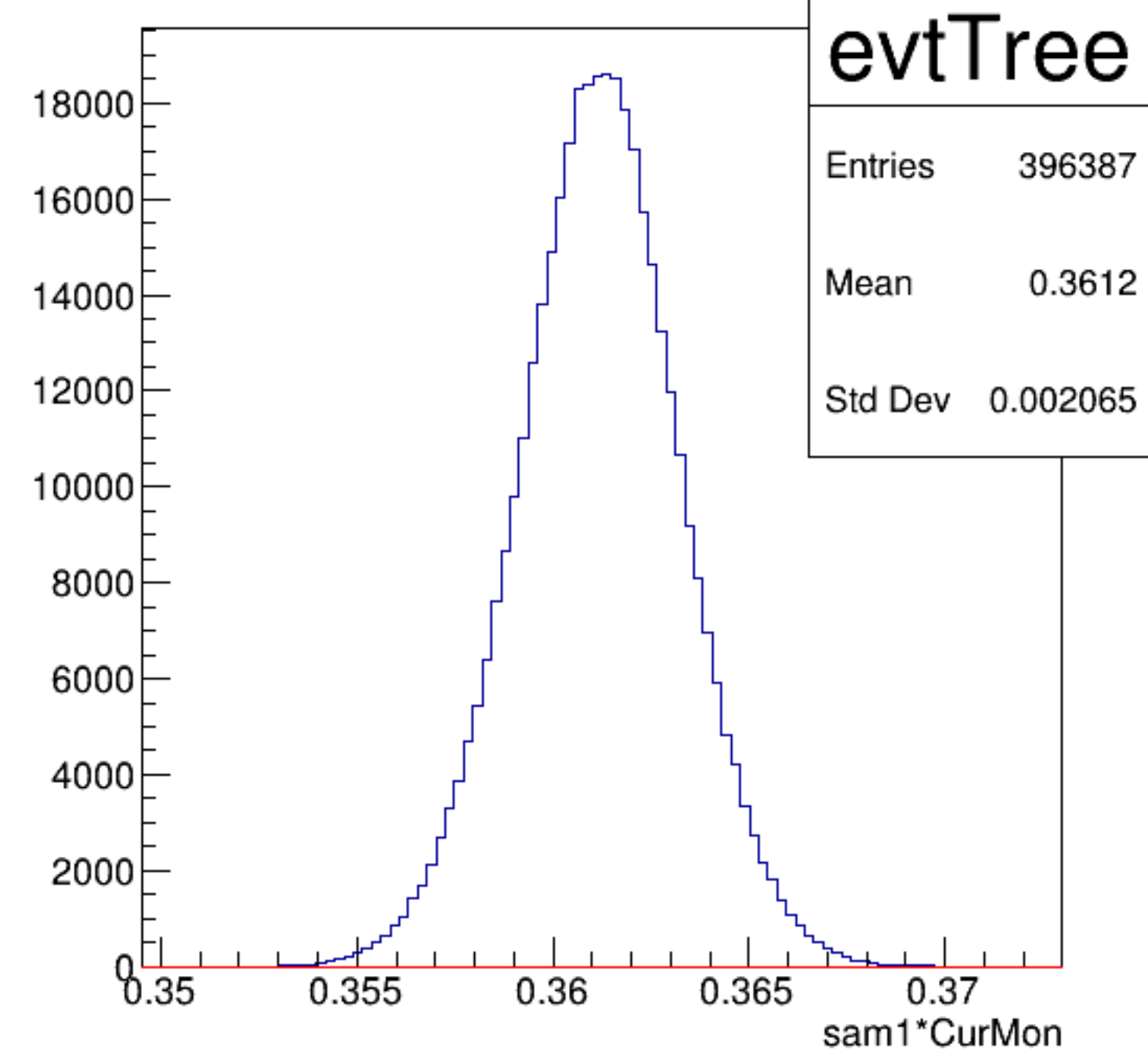
Entries	49471
Mean	7.803
Std Dev	1595
Underflow	6116
Overflow	6060

sam1*CurMon:Entry\$

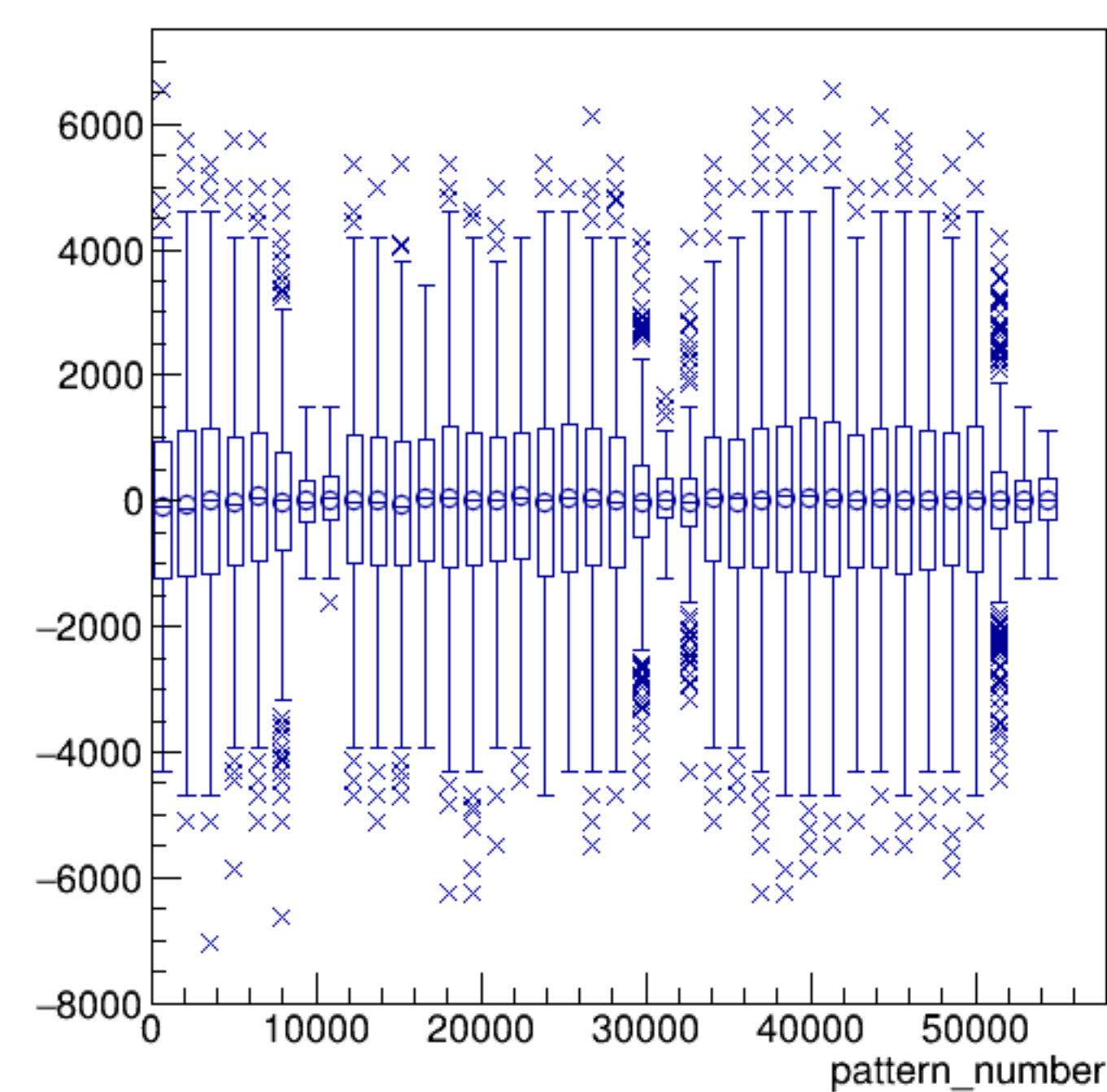


run3925_000_sam1.png

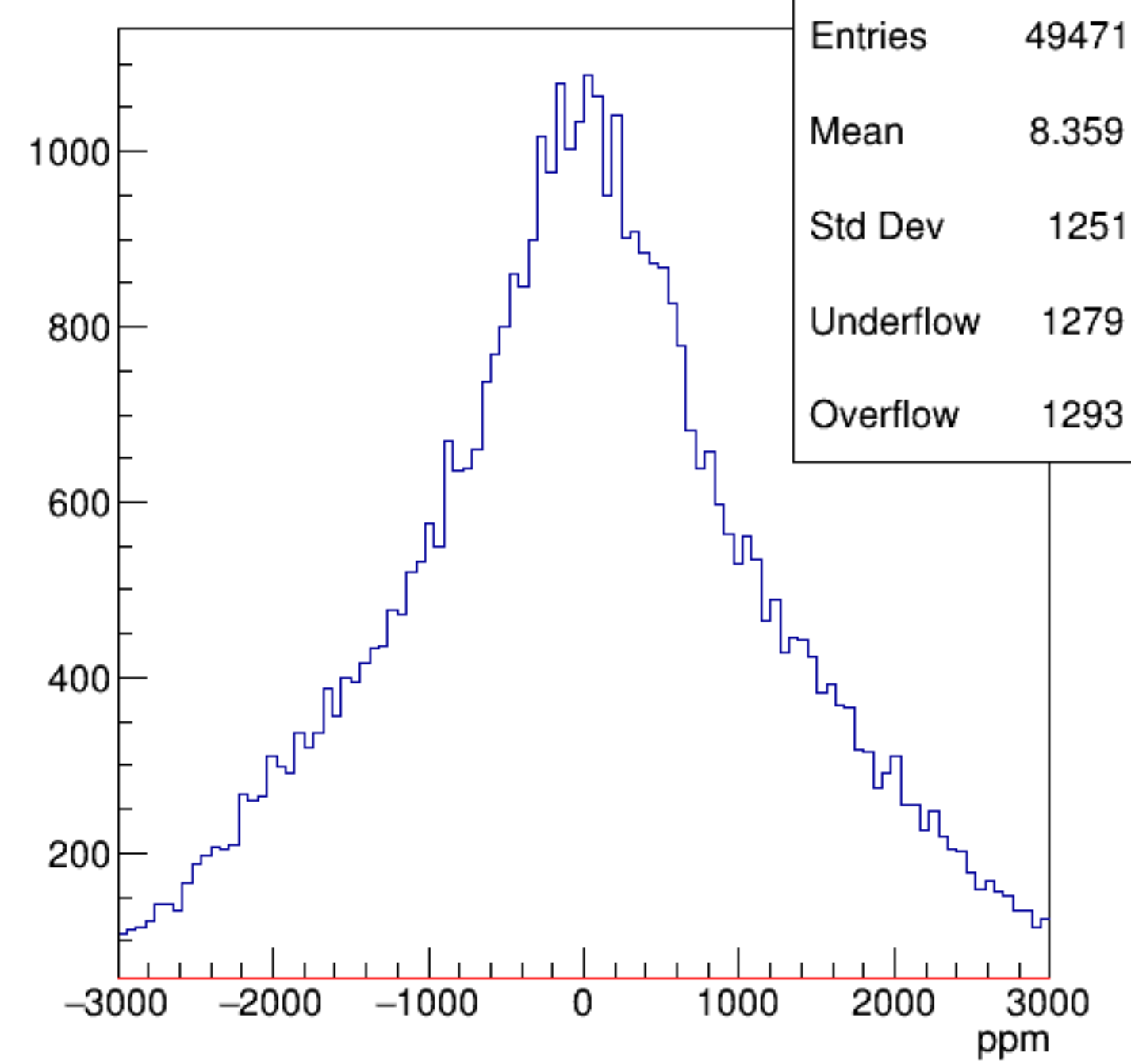
sam1*CurMon {ErrorFlag==0}



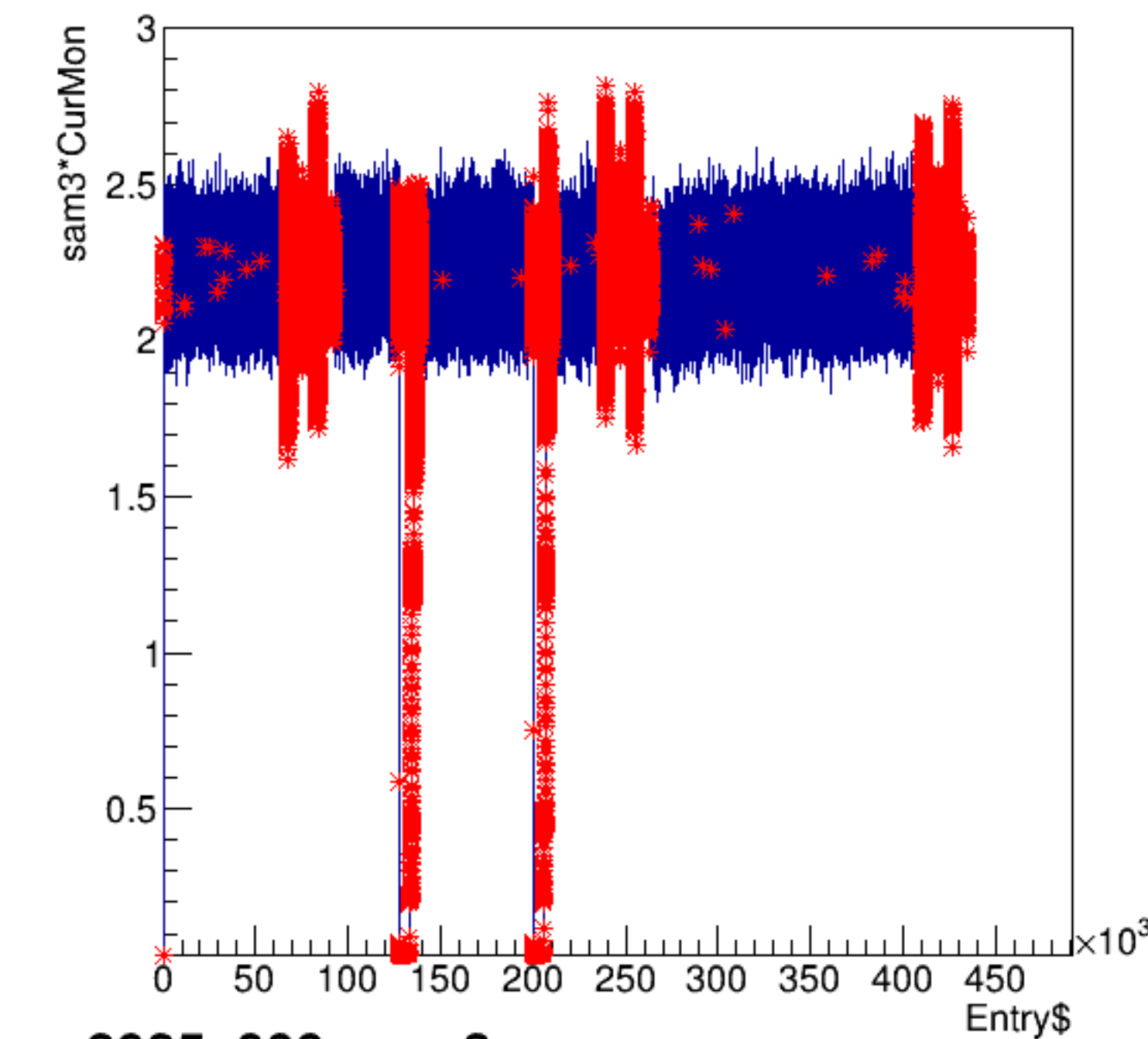
asym_sam1/ppm:pattern_number {ErrorFlag==0}



asym_sam1

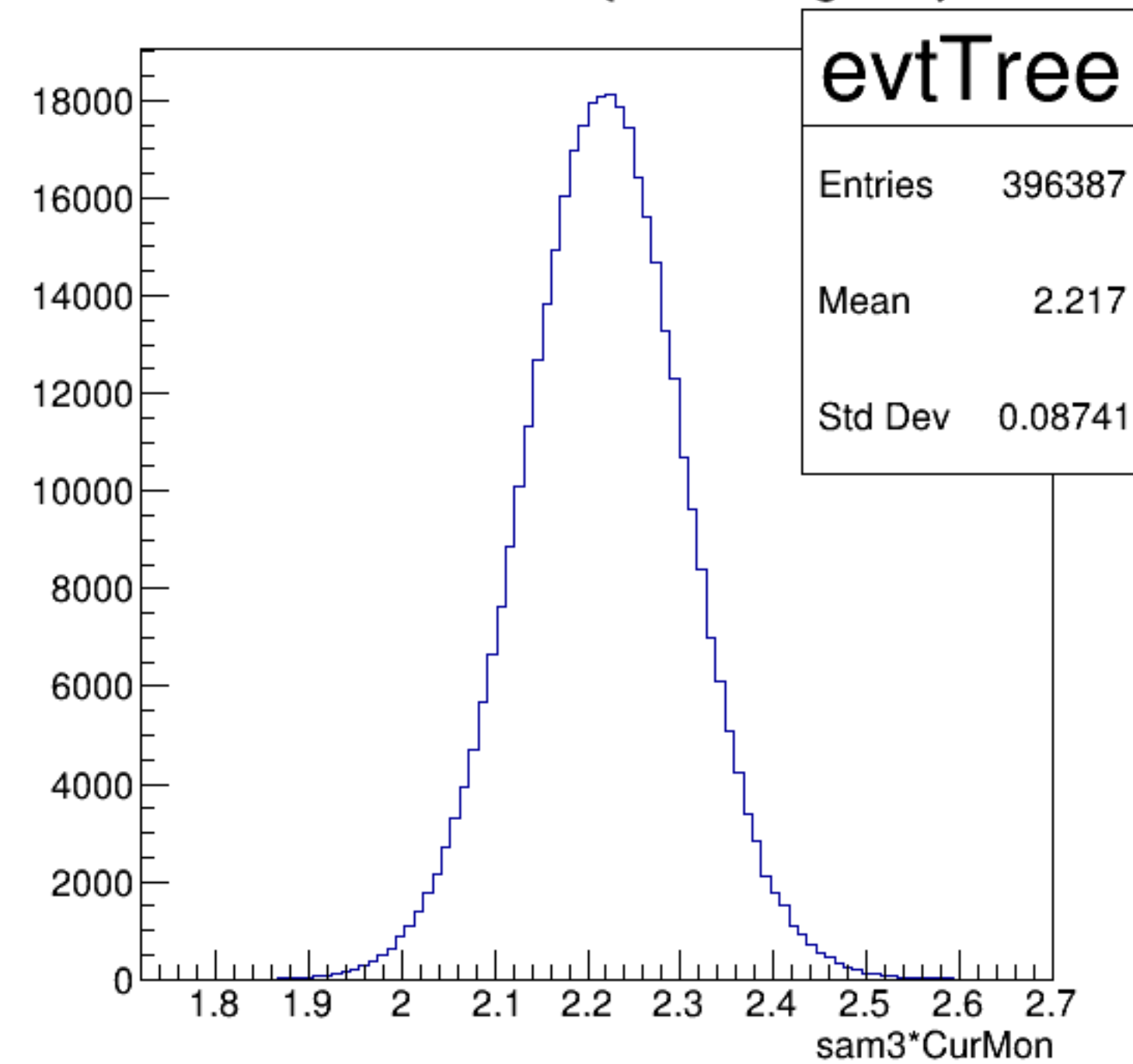


sam3*CurMon:Entry\$



run3925_000_sam3.png

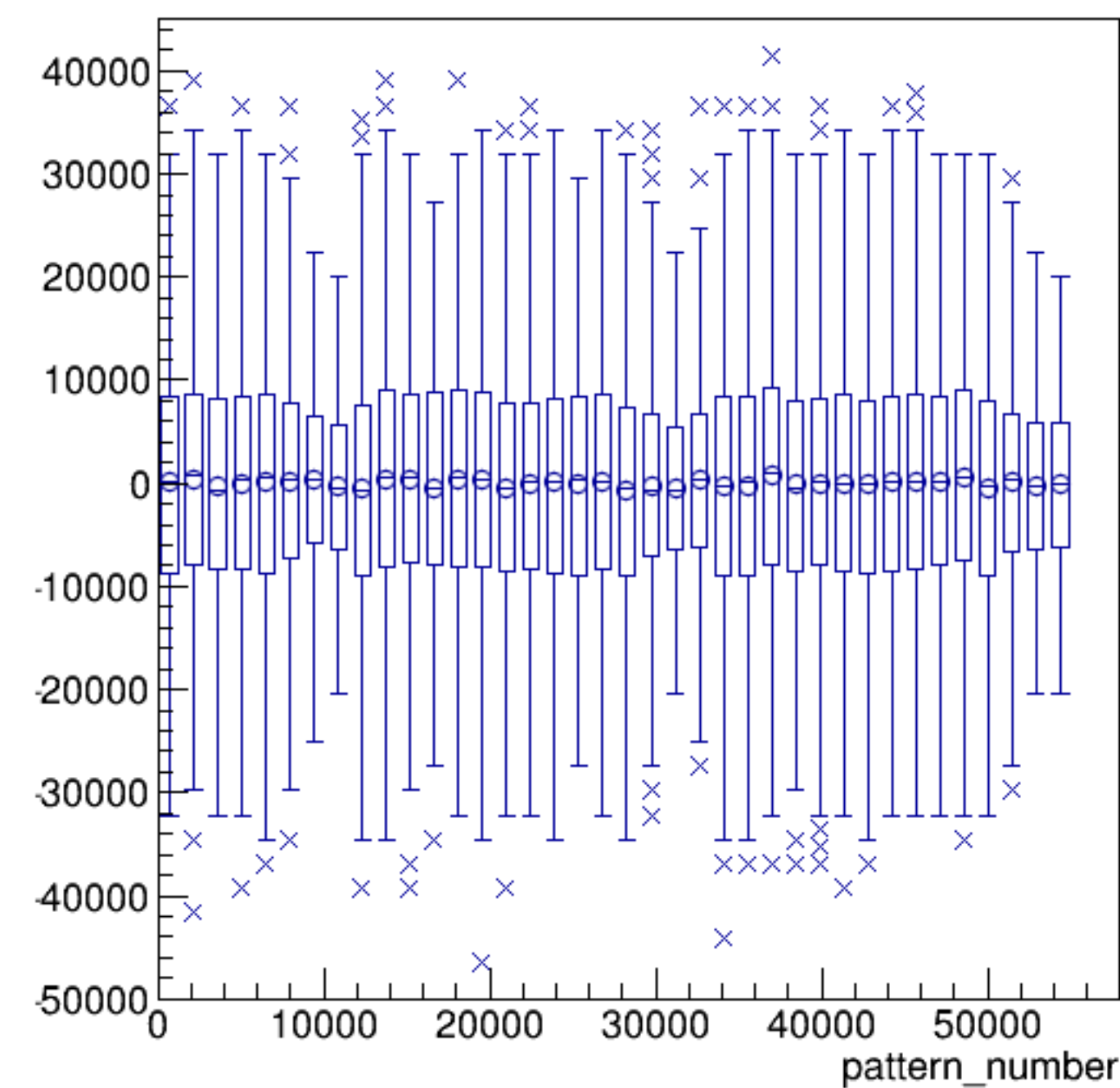
sam3*CurMon {ErrorFlag==0}



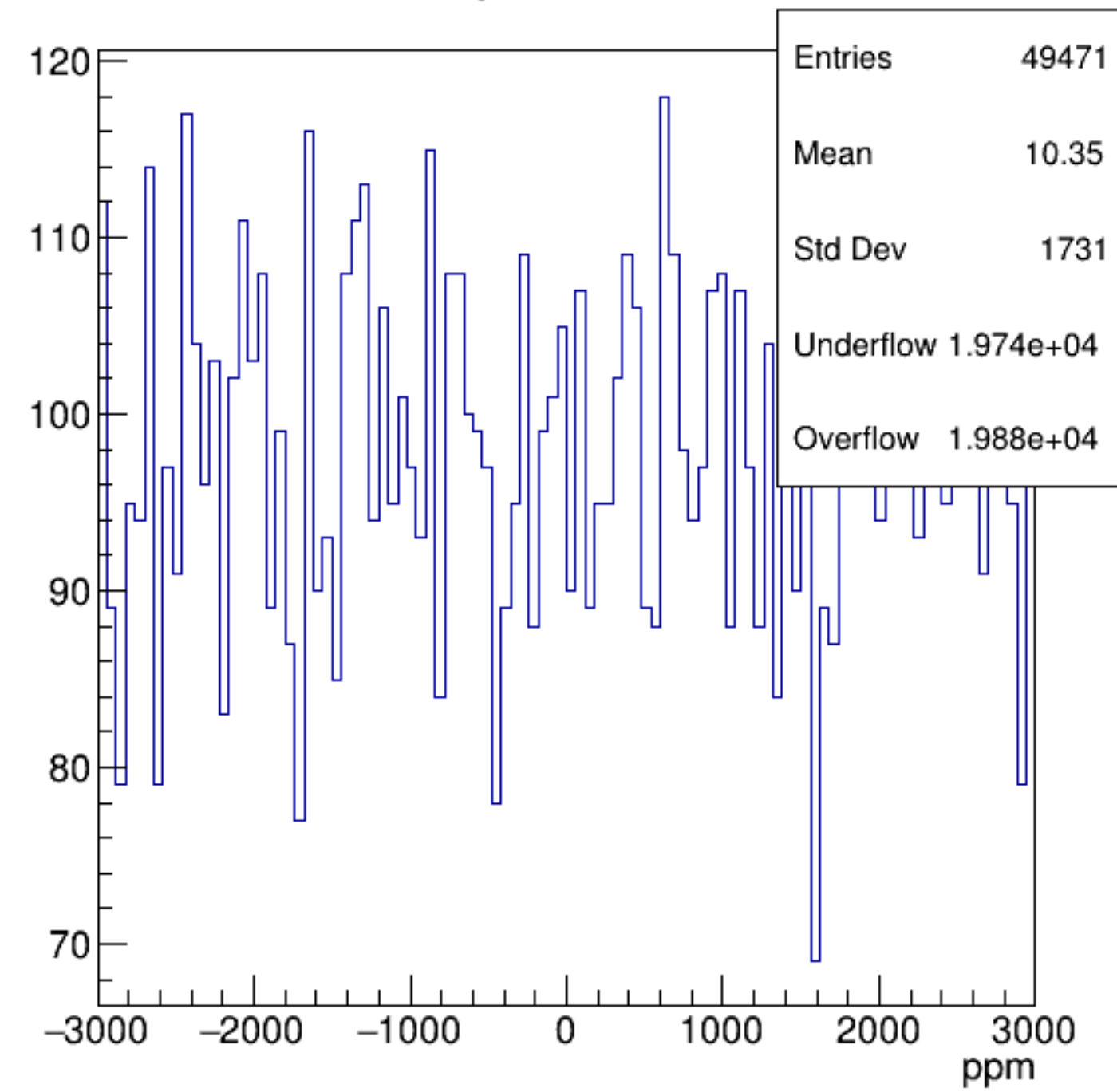
evtTree

Entries	396387
Mean	2.217
Std Dev	0.08741

asym_sam3/ppm:pattern_number {ErrorFlag==0}

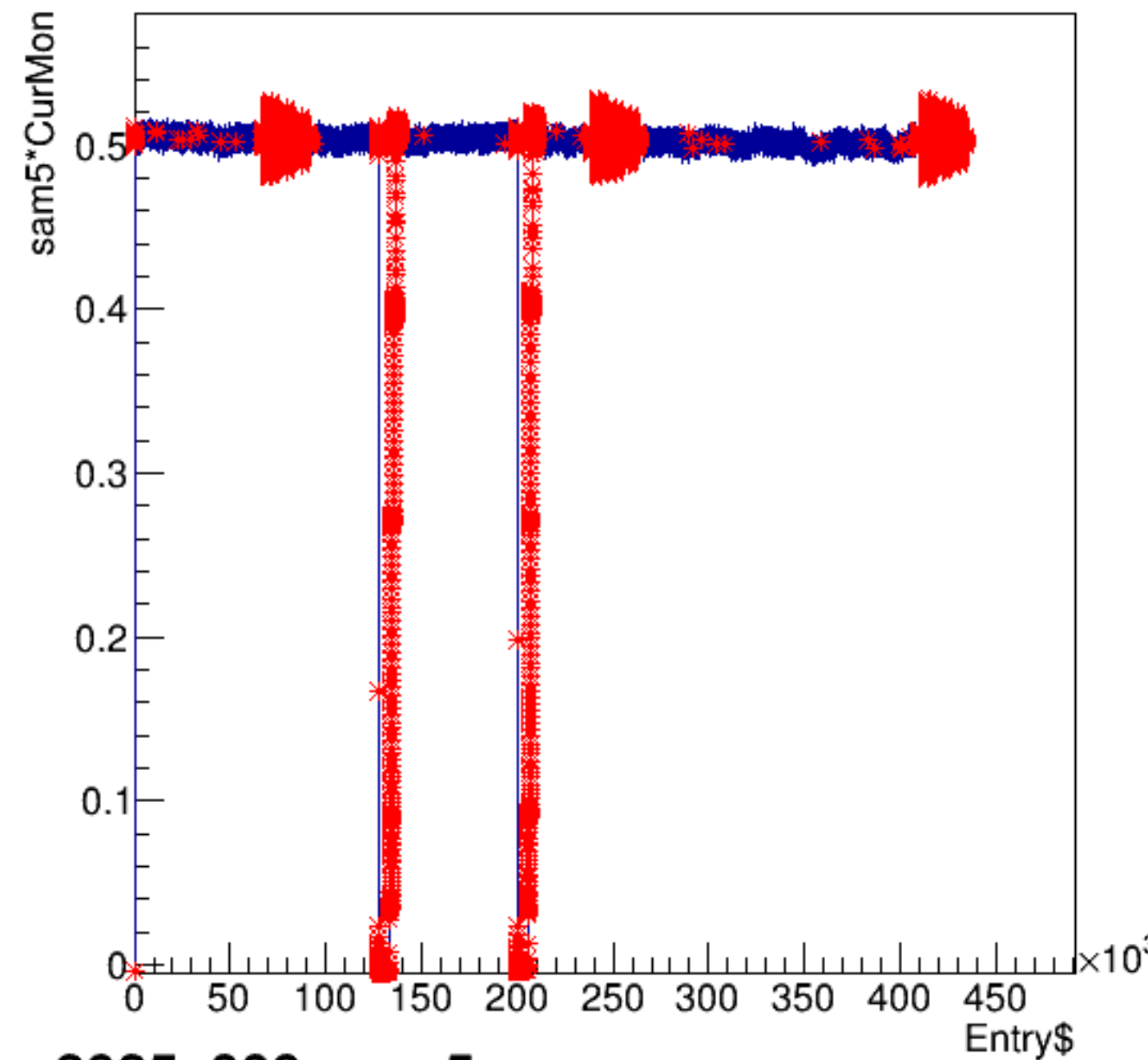


asym_sam3



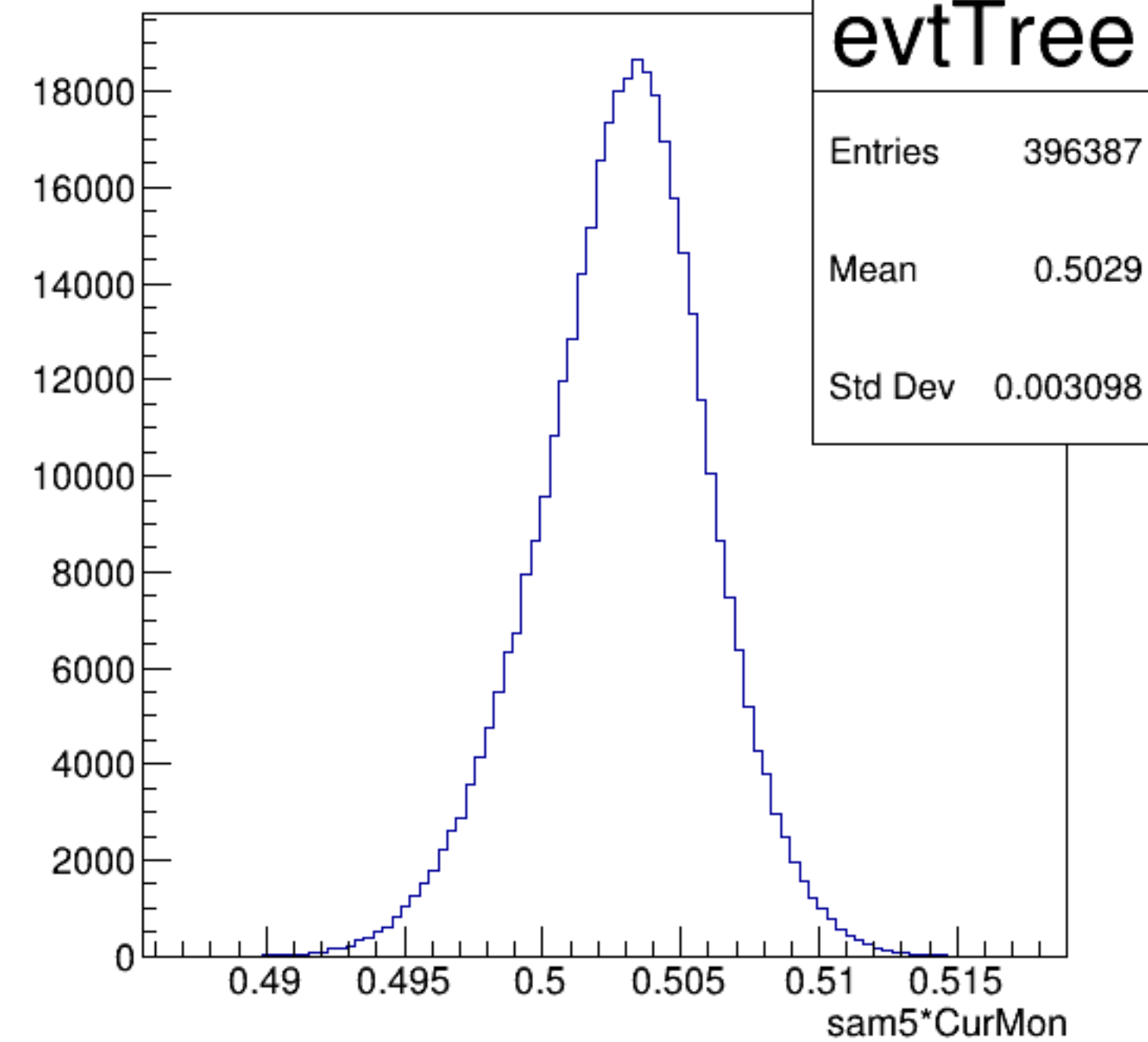
Entries	49471
Mean	10.35
Std Dev	1731
Underflow	1.974e+04
Overflow	1.988e+04

sam5*CurMon:Entry\$

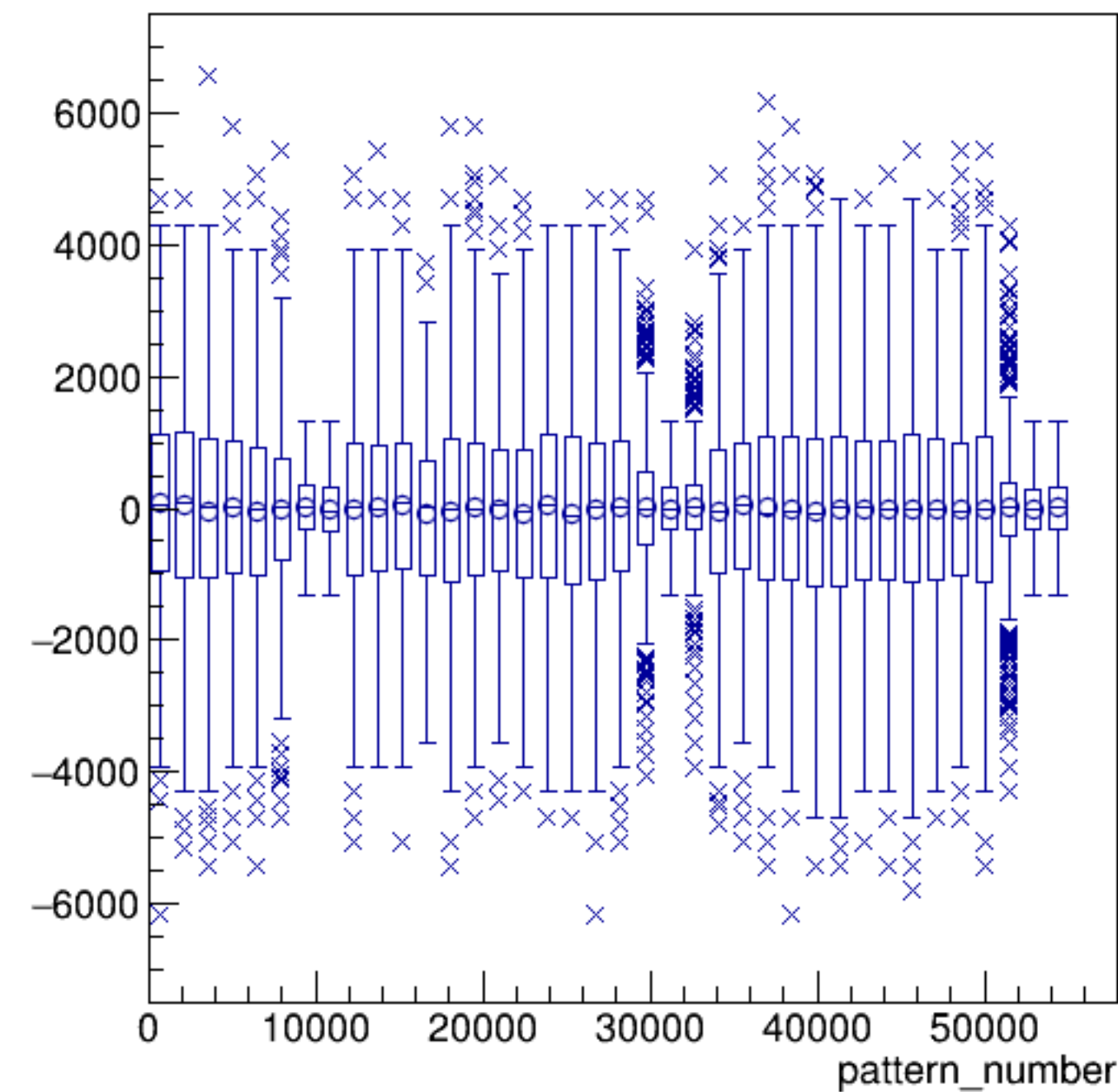


run3925_000_sam5.png

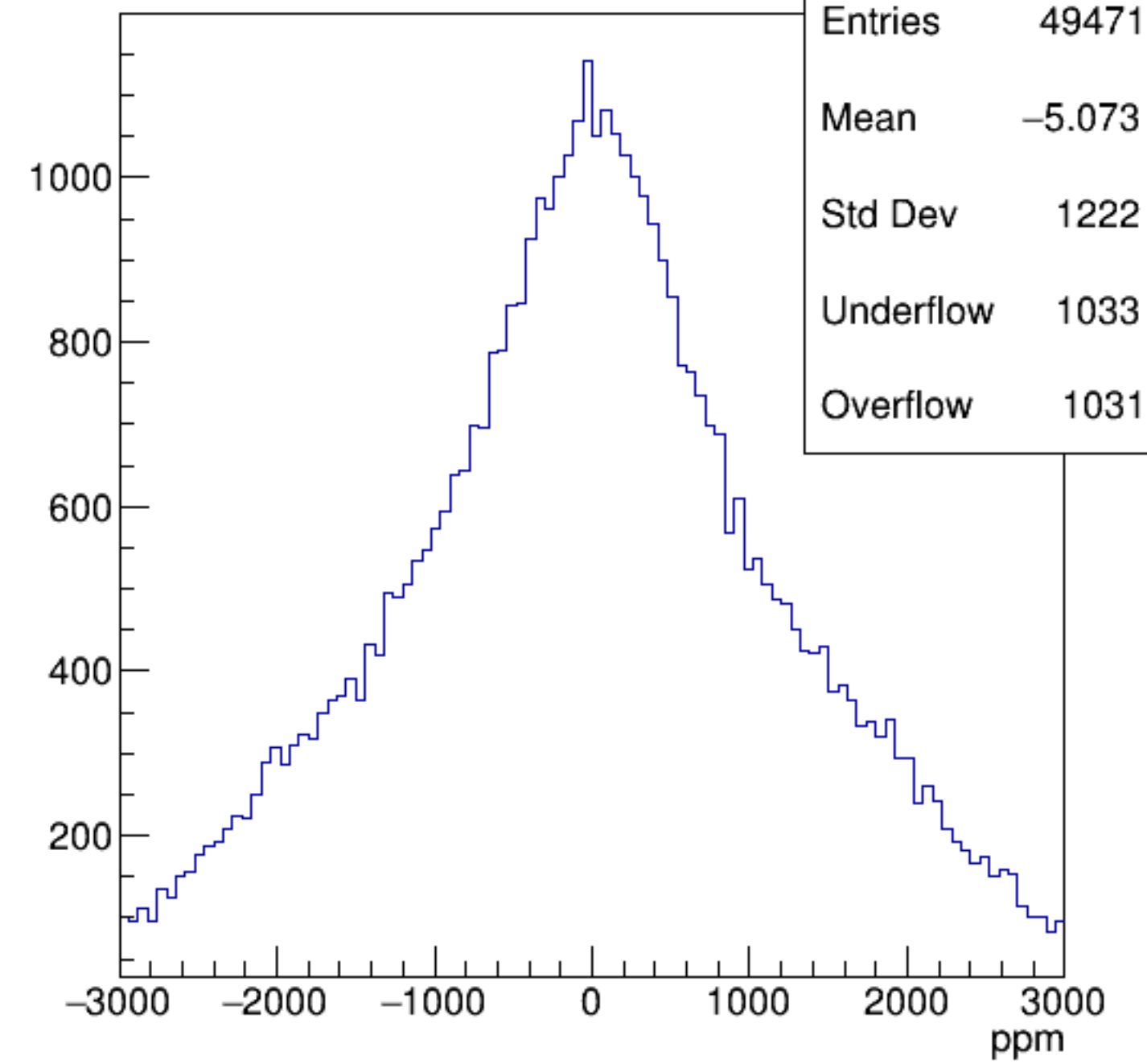
sam5*CurMon {ErrorFlag==0}



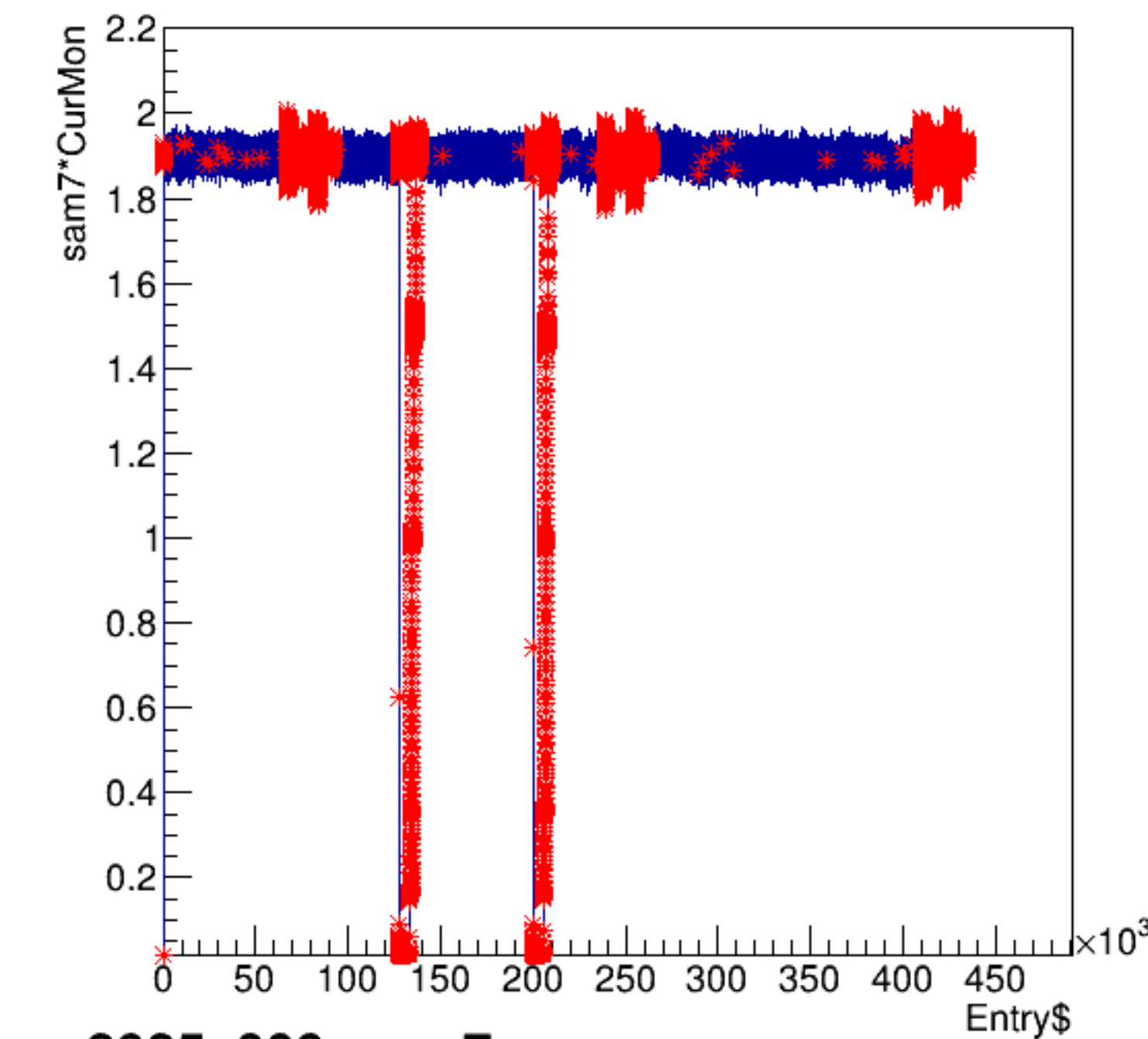
asym_sam5/ppm:pattern_number {ErrorFlag==0}



asym_sam5

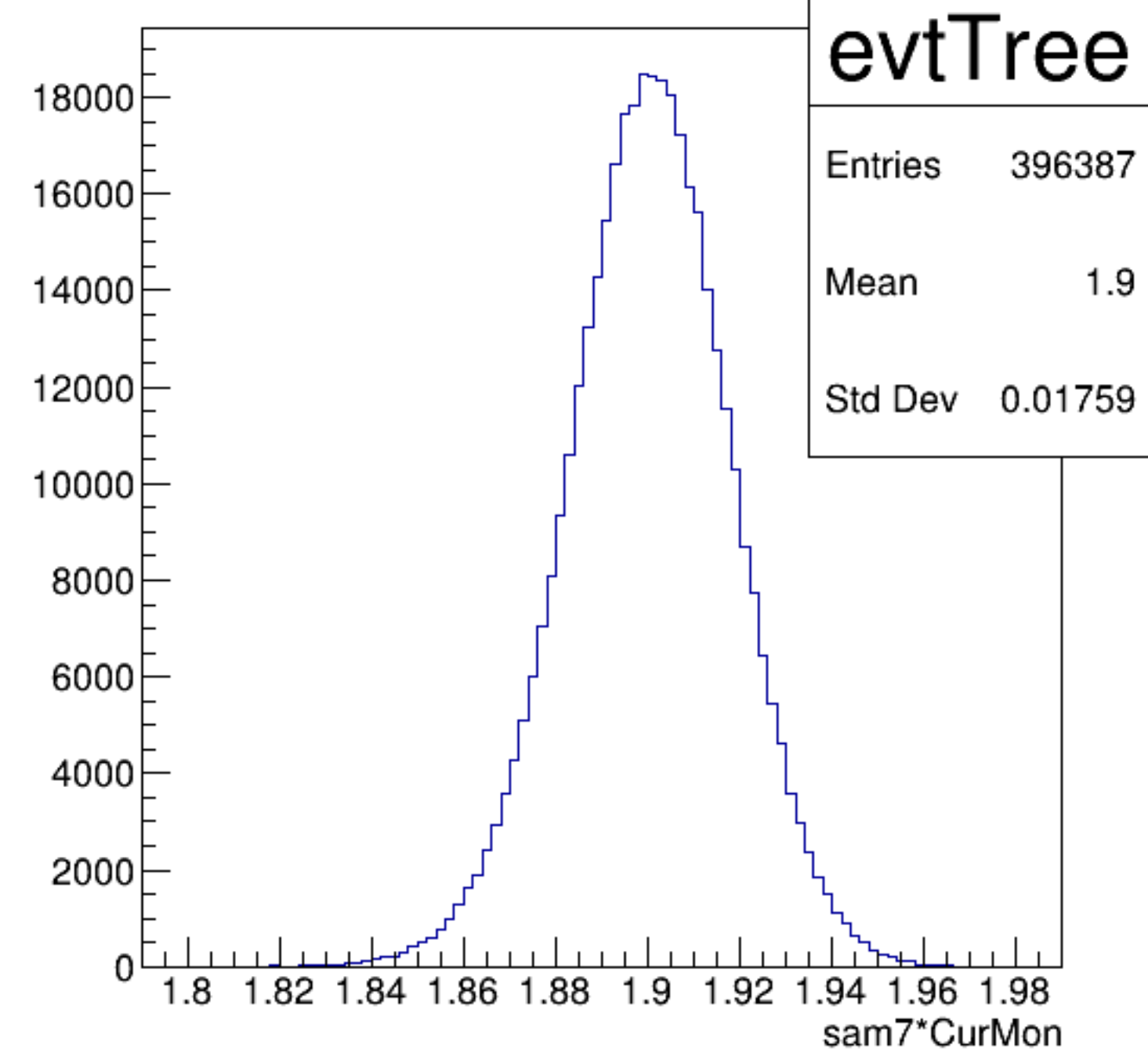


sam7*CurMon:Entry\$



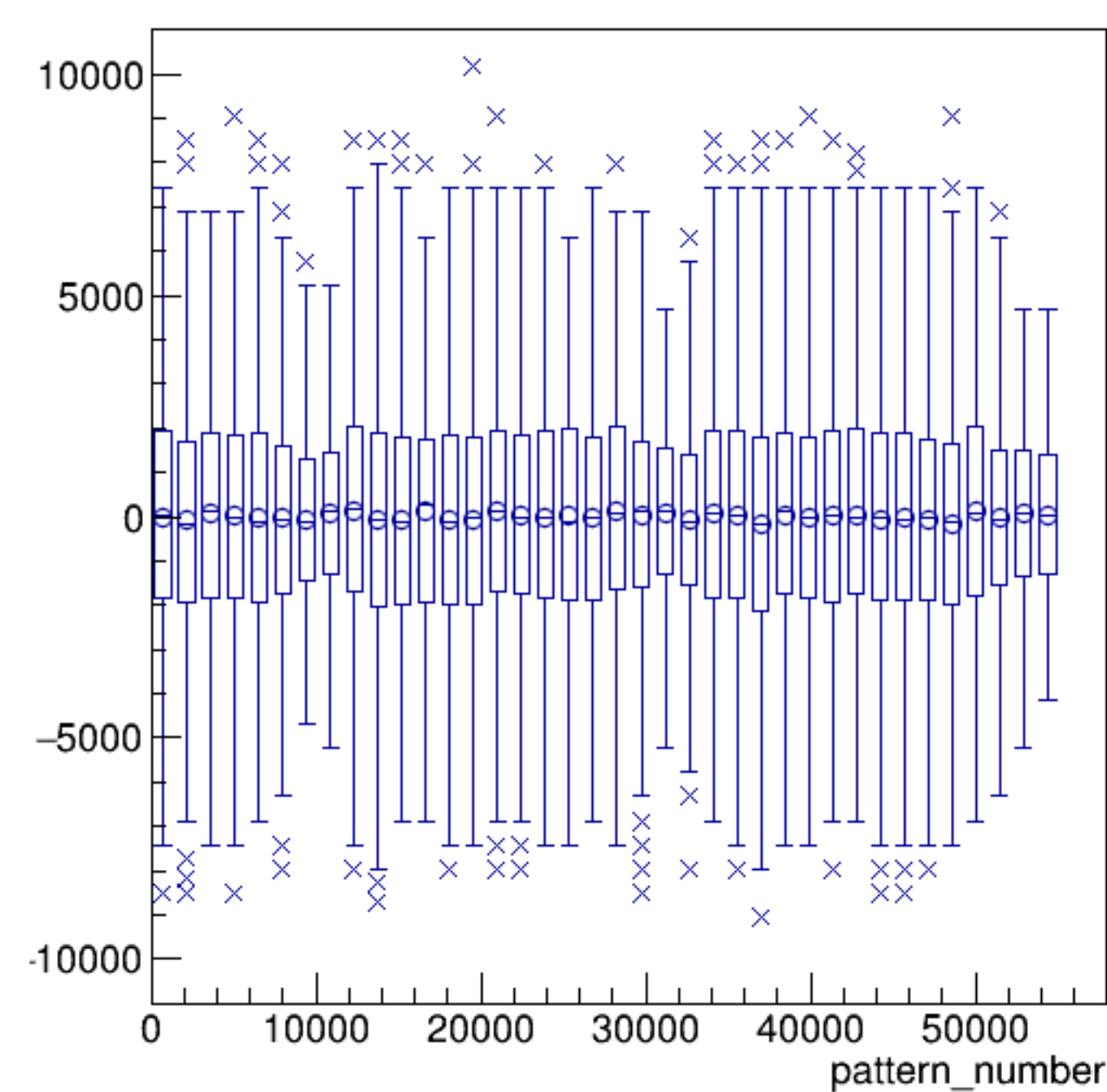
run3925_000_sam7.png

sam7*CurMon {ErrorFlag==0}

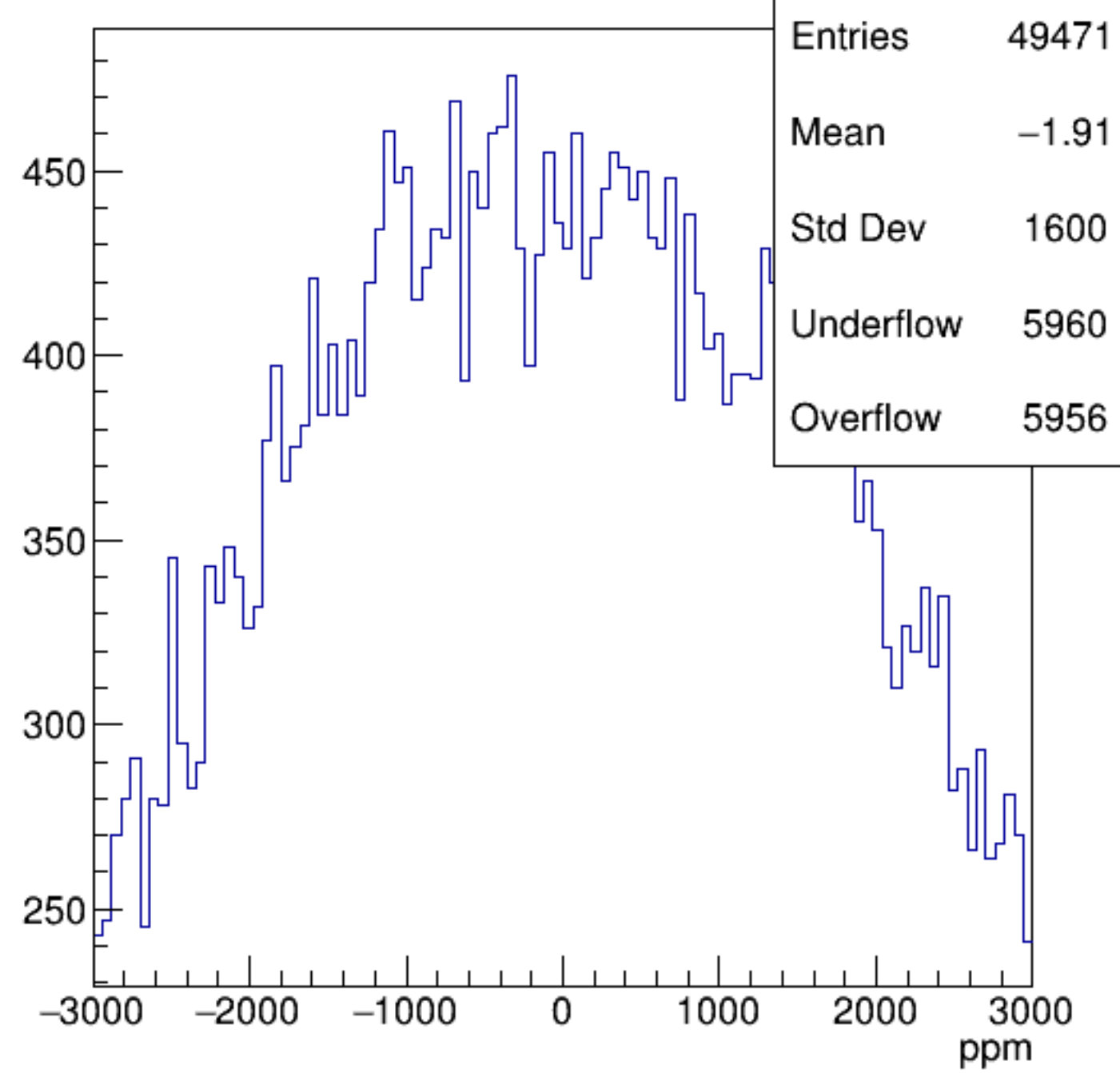


evtTree	
Entries	396387
Mean	1.9
Std Dev	0.01759

asym_sam7/ppm:pattern_number {ErrorFlag==0}

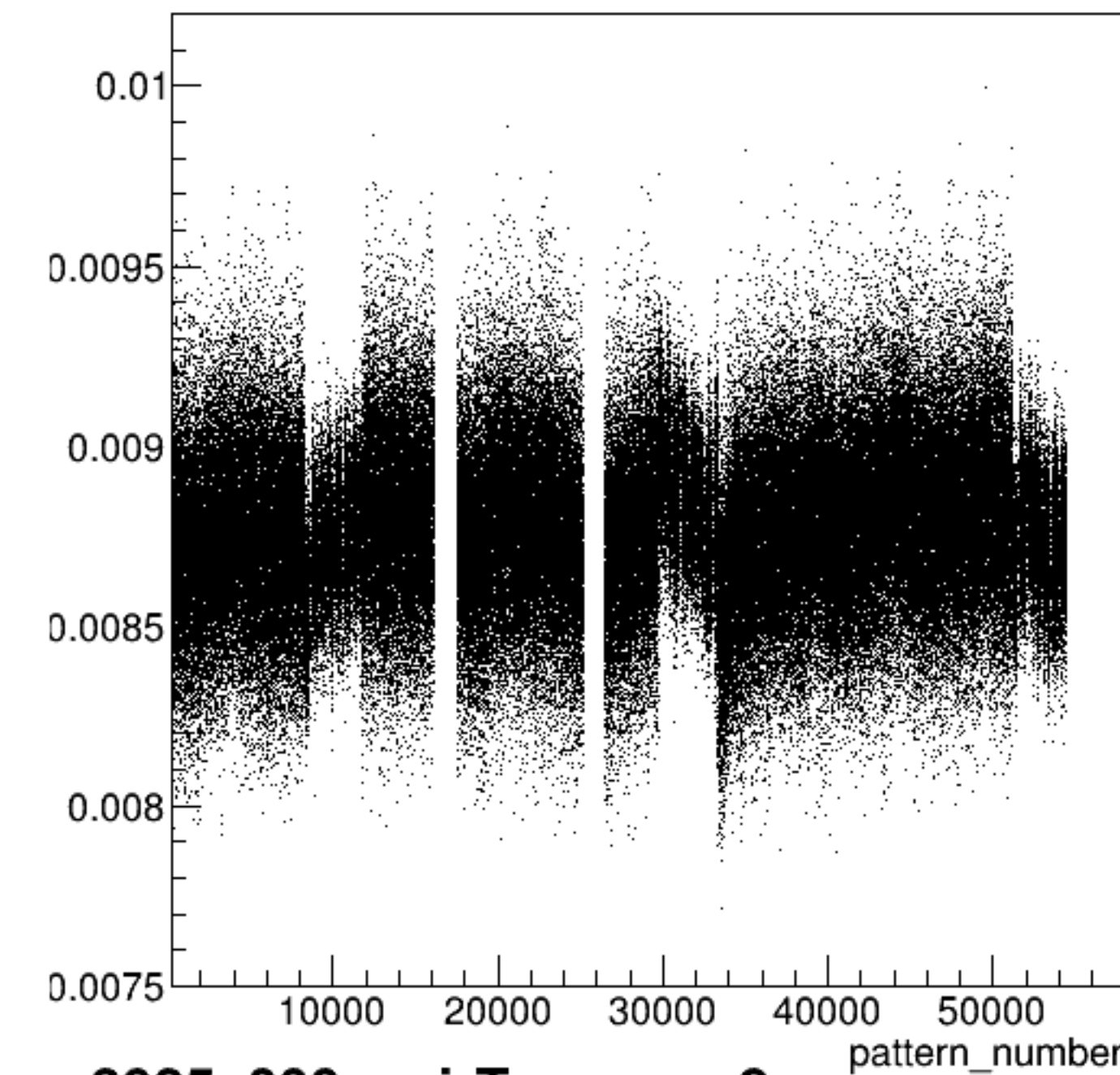


asym_sam7



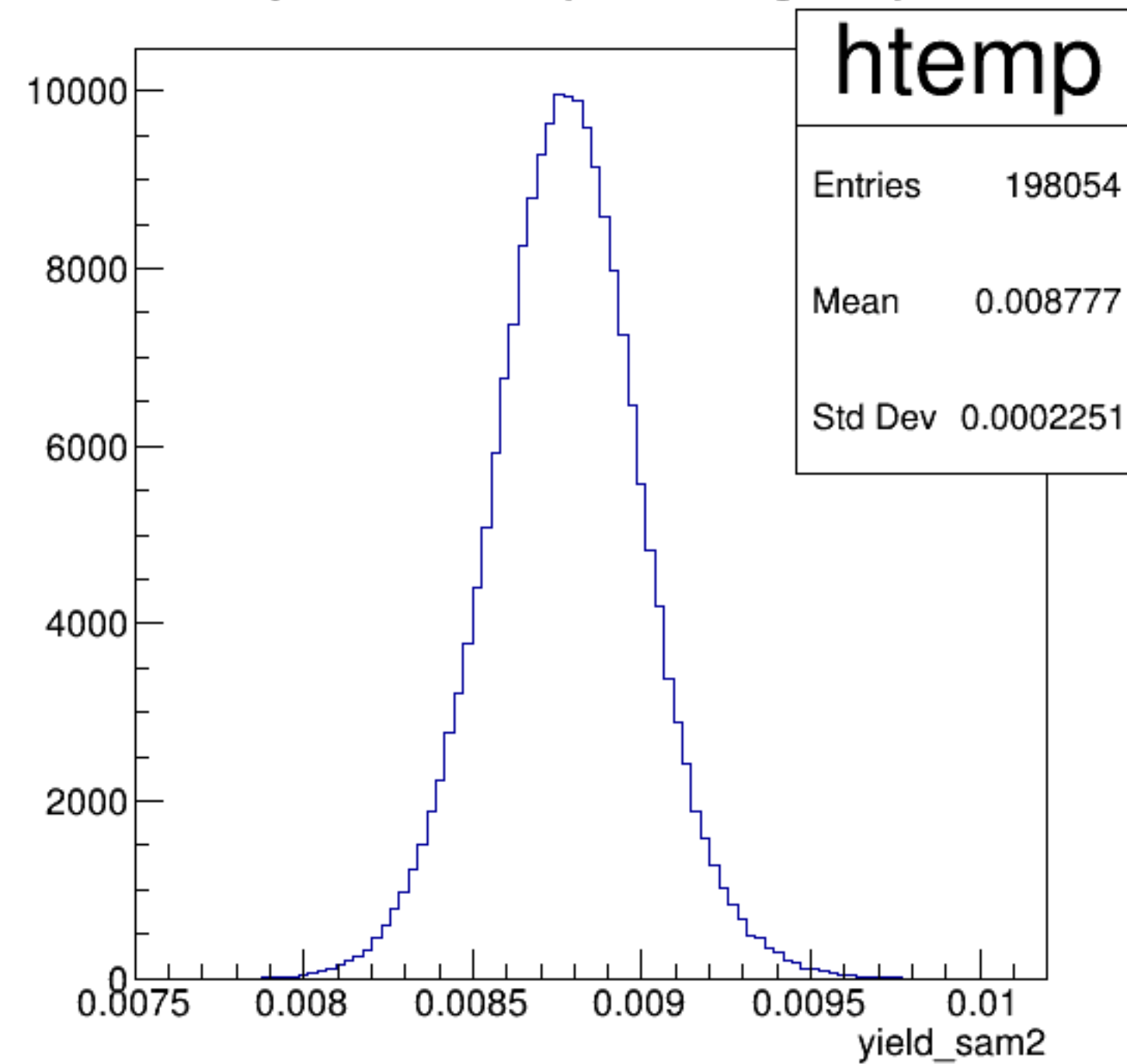
Entries	49471
Mean	-1.91
Std Dev	1600
Underflow	5960
Overflow	5956

yield_sam2:pattern_number {ErrorFlag==0}

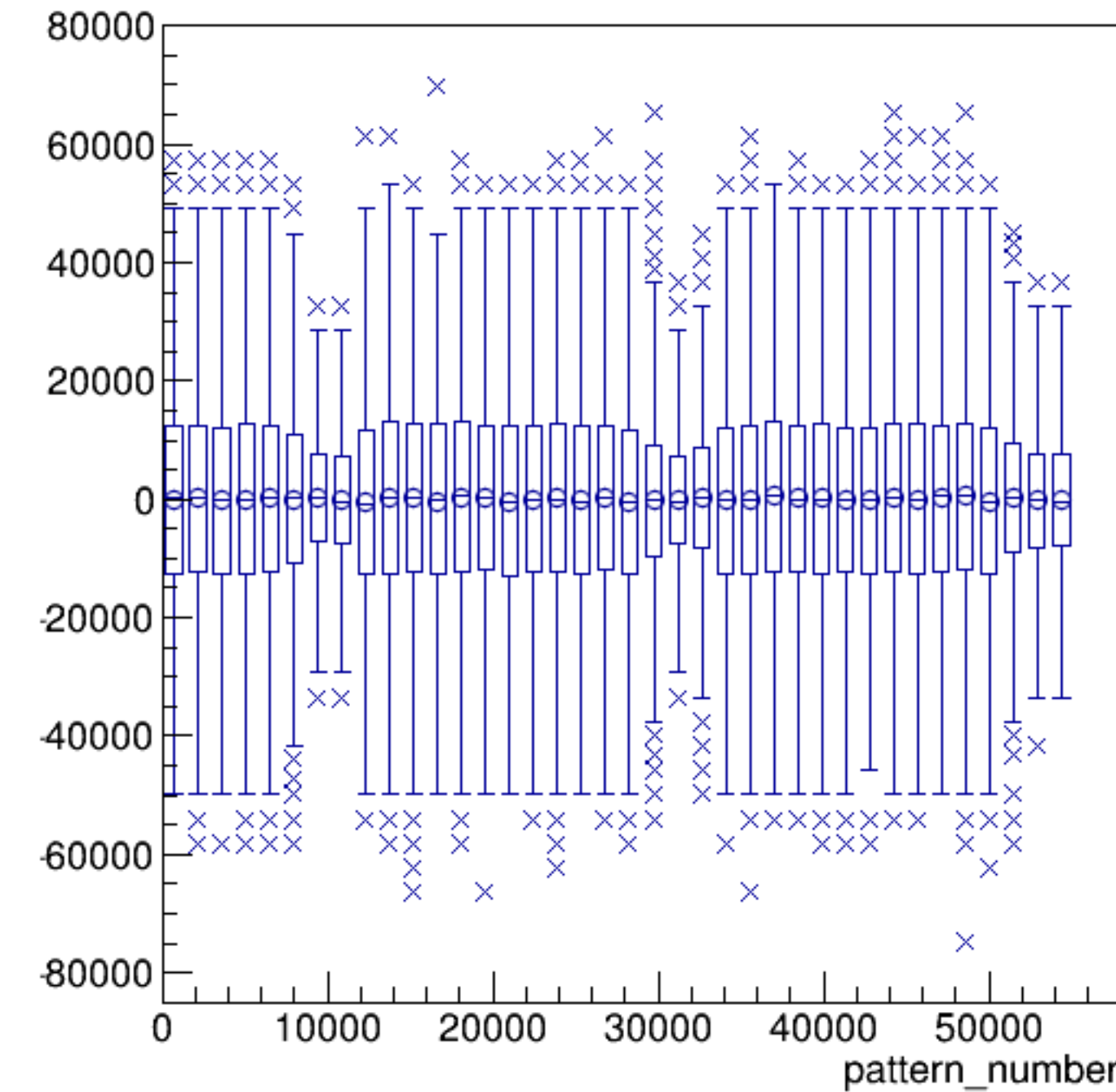


run3925_000_pairTree_sam2.png

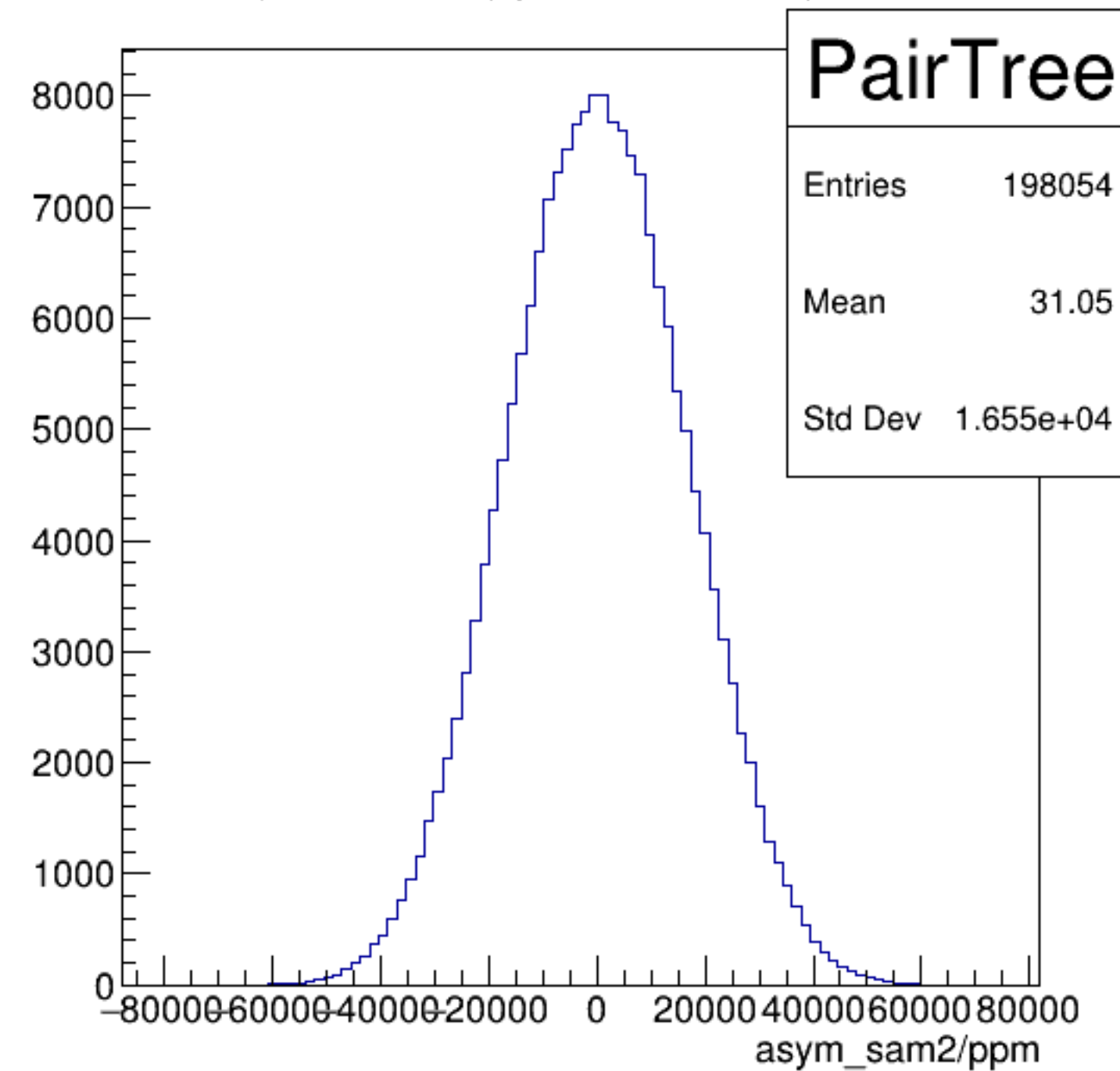
yield_sam2 {ErrorFlag==0}



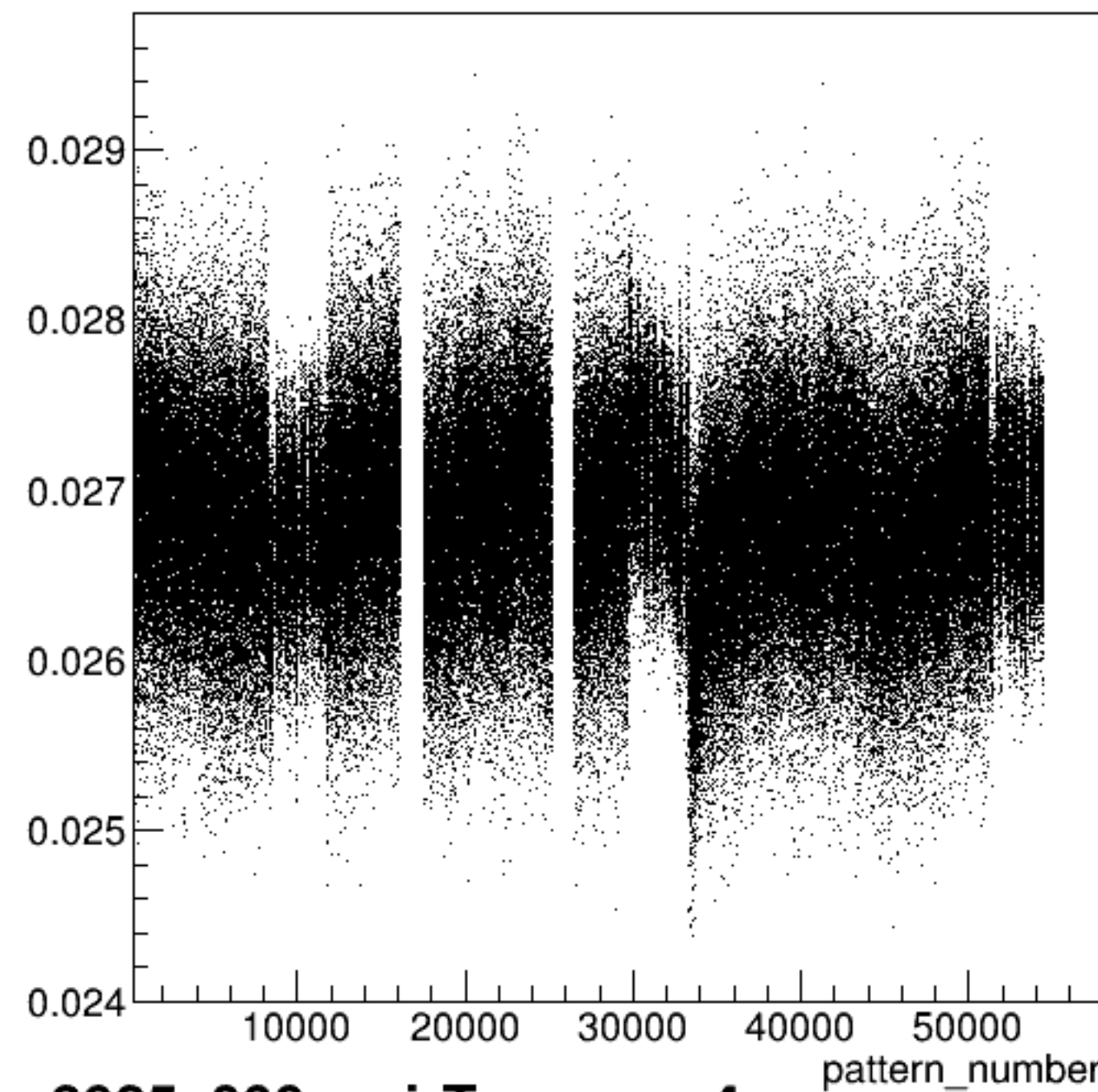
asym_sam2/ppm:pattern_number {ErrorFlag==0}



asym_sam2/ppm {ErrorFlag==0}

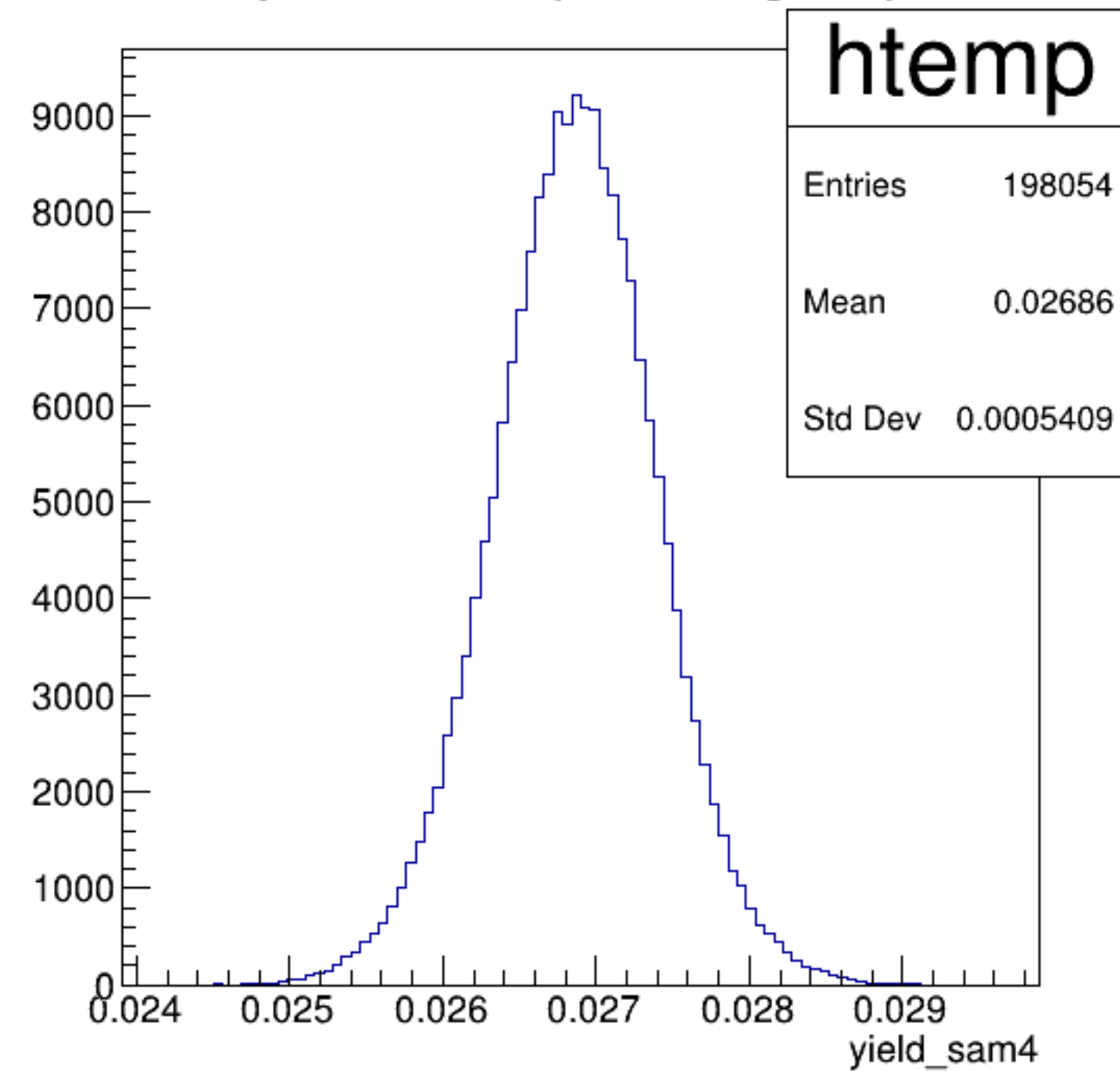


yield_sam4:pattern_number {ErrorFlag==0}

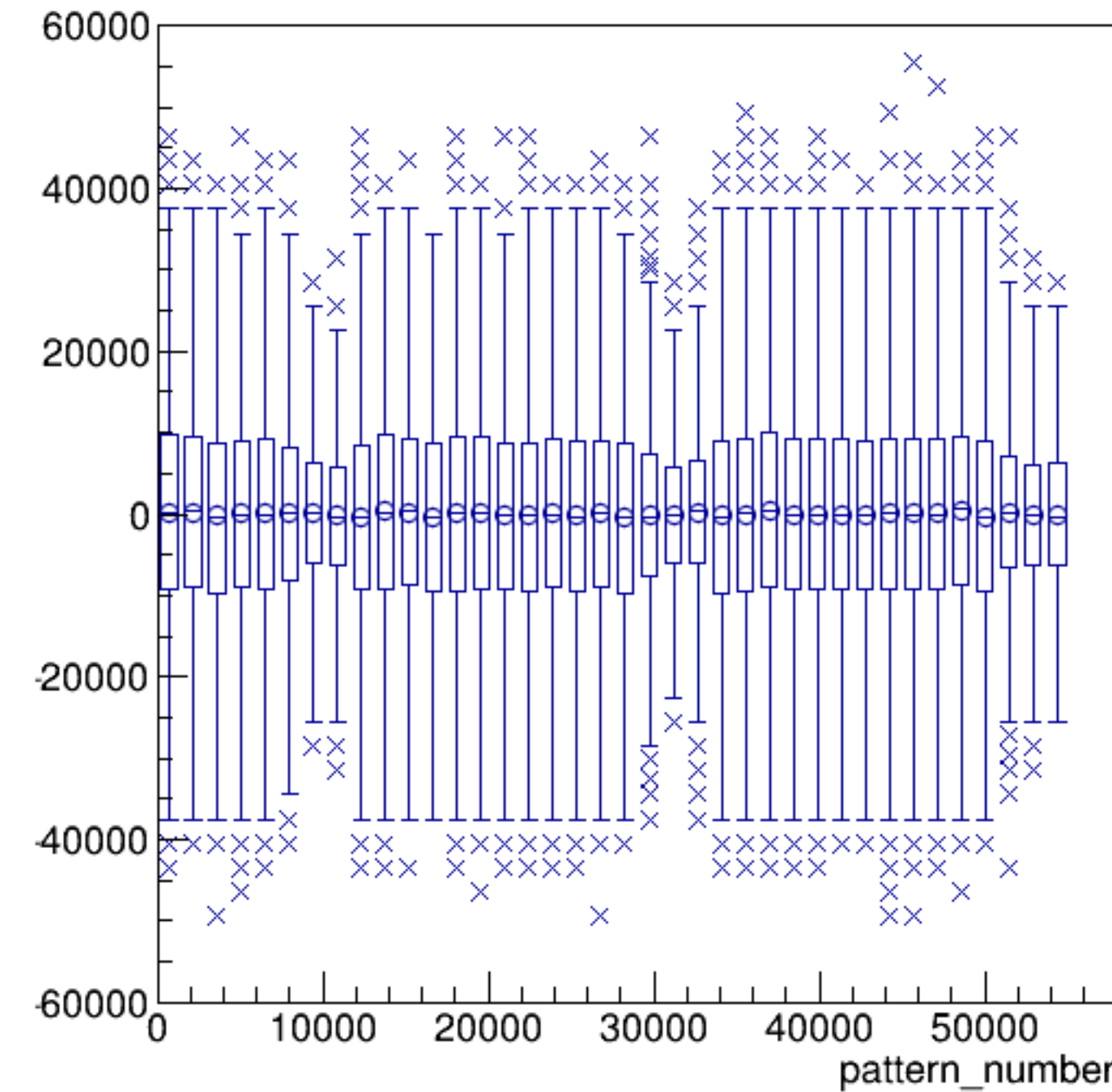


run3925_000_pairTree_sam4.png

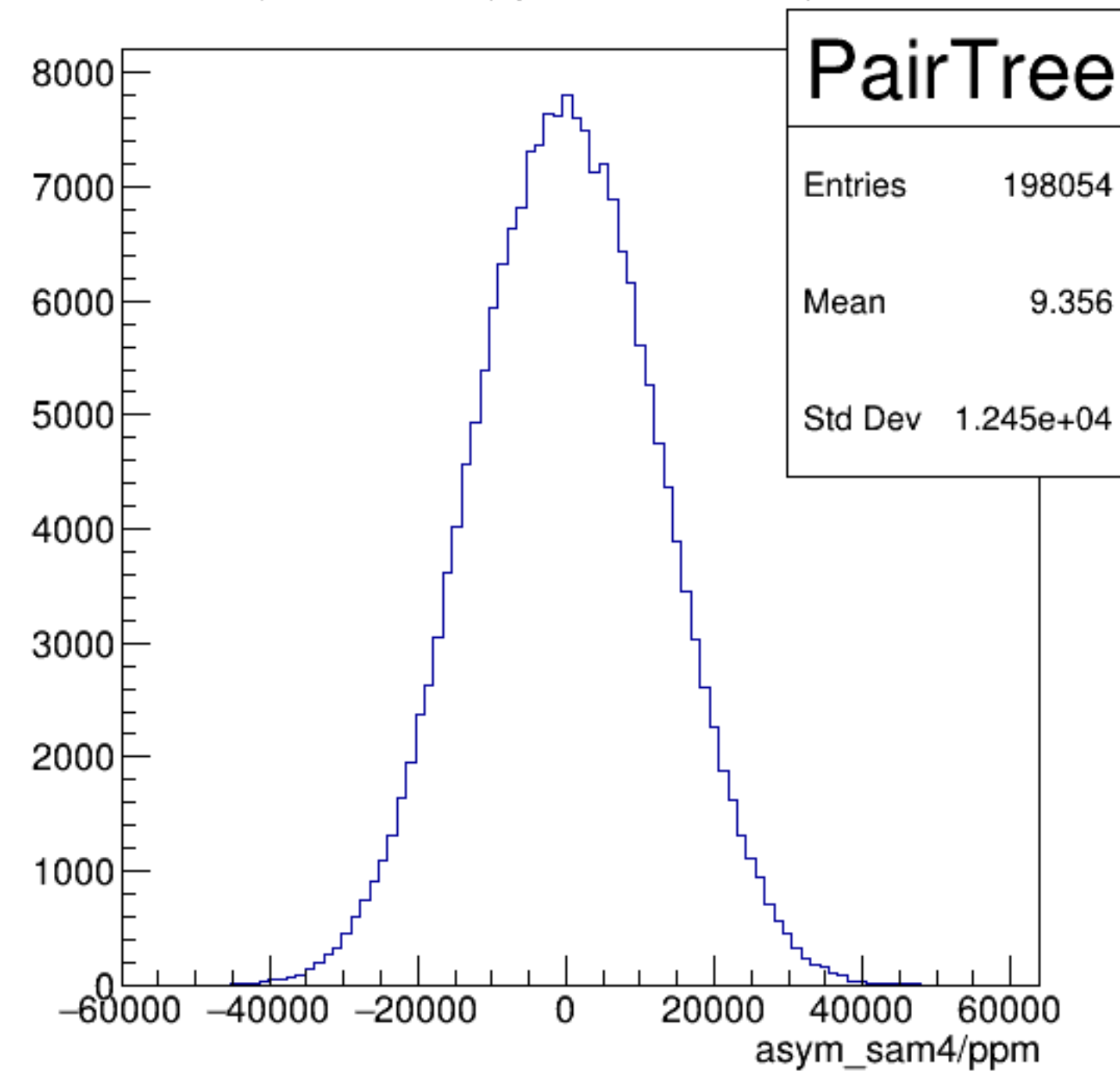
yield_sam4 {ErrorFlag==0}



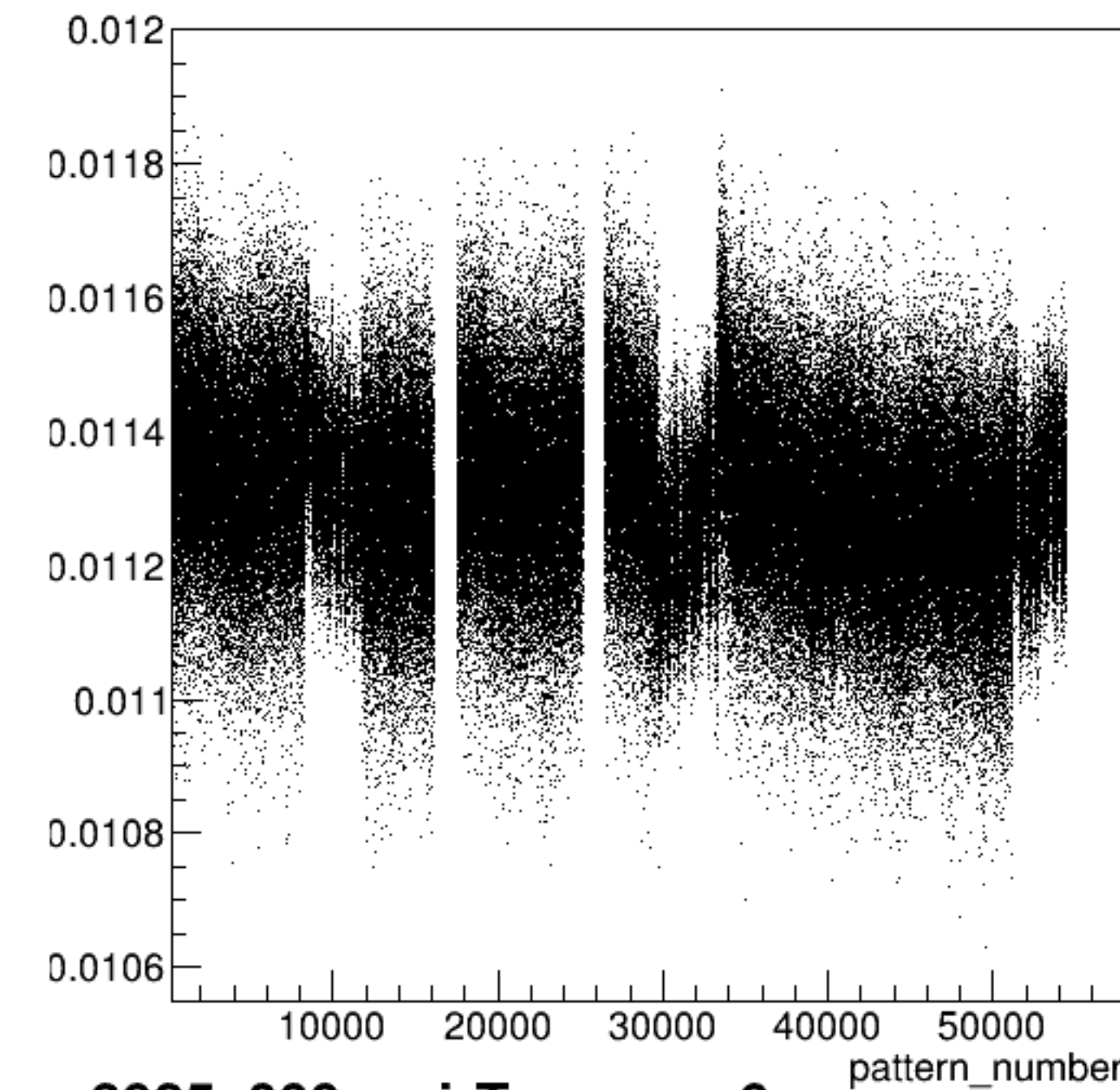
asym_sam4/ppm:pattern_number {ErrorFlag==0}



asym_sam4/ppm {ErrorFlag==0}

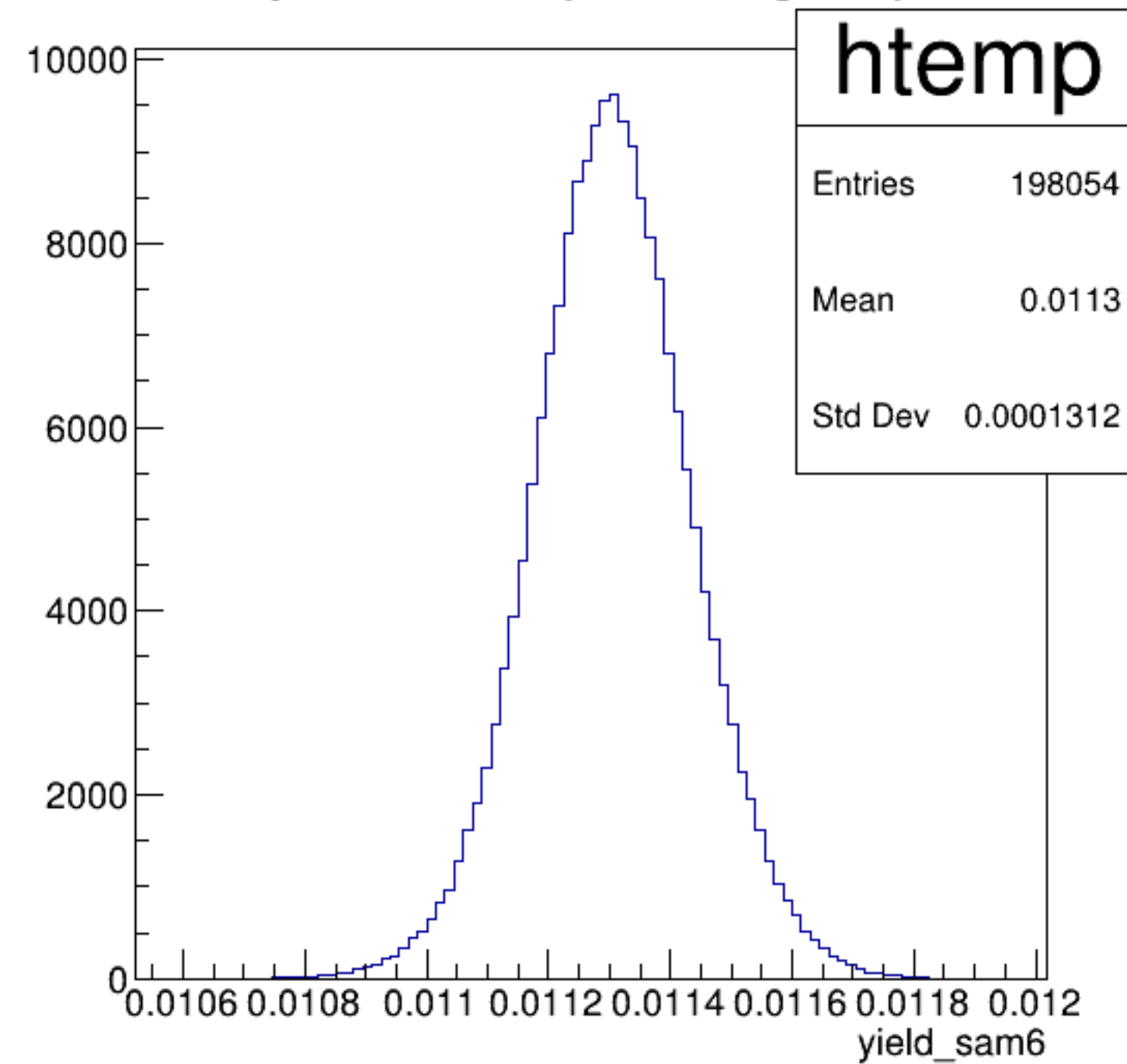


yield_sam6:pattern_number {ErrorFlag==0}

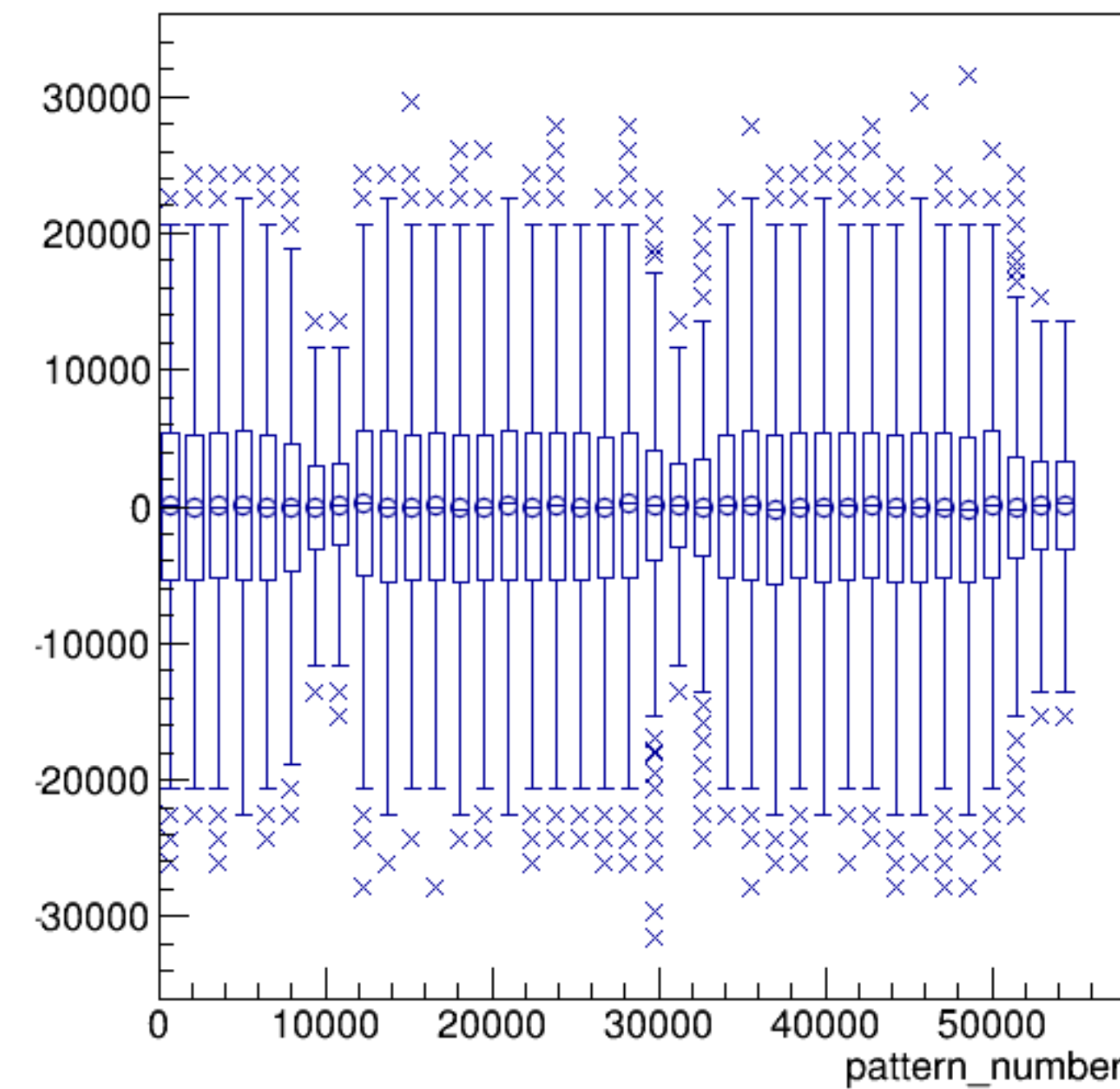


run3925_000_pairTree_sam6.png

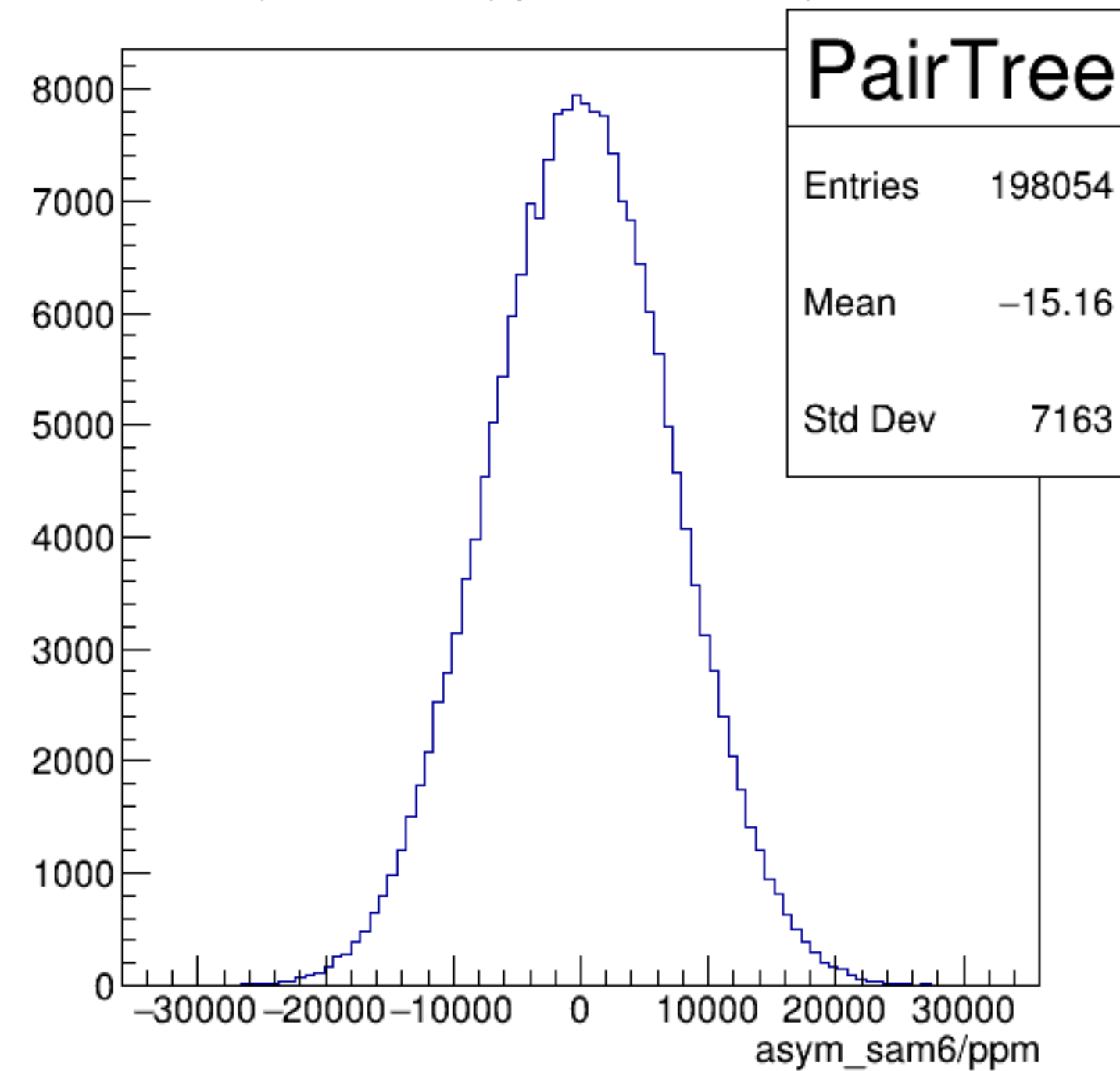
yield_sam6 {ErrorFlag==0}

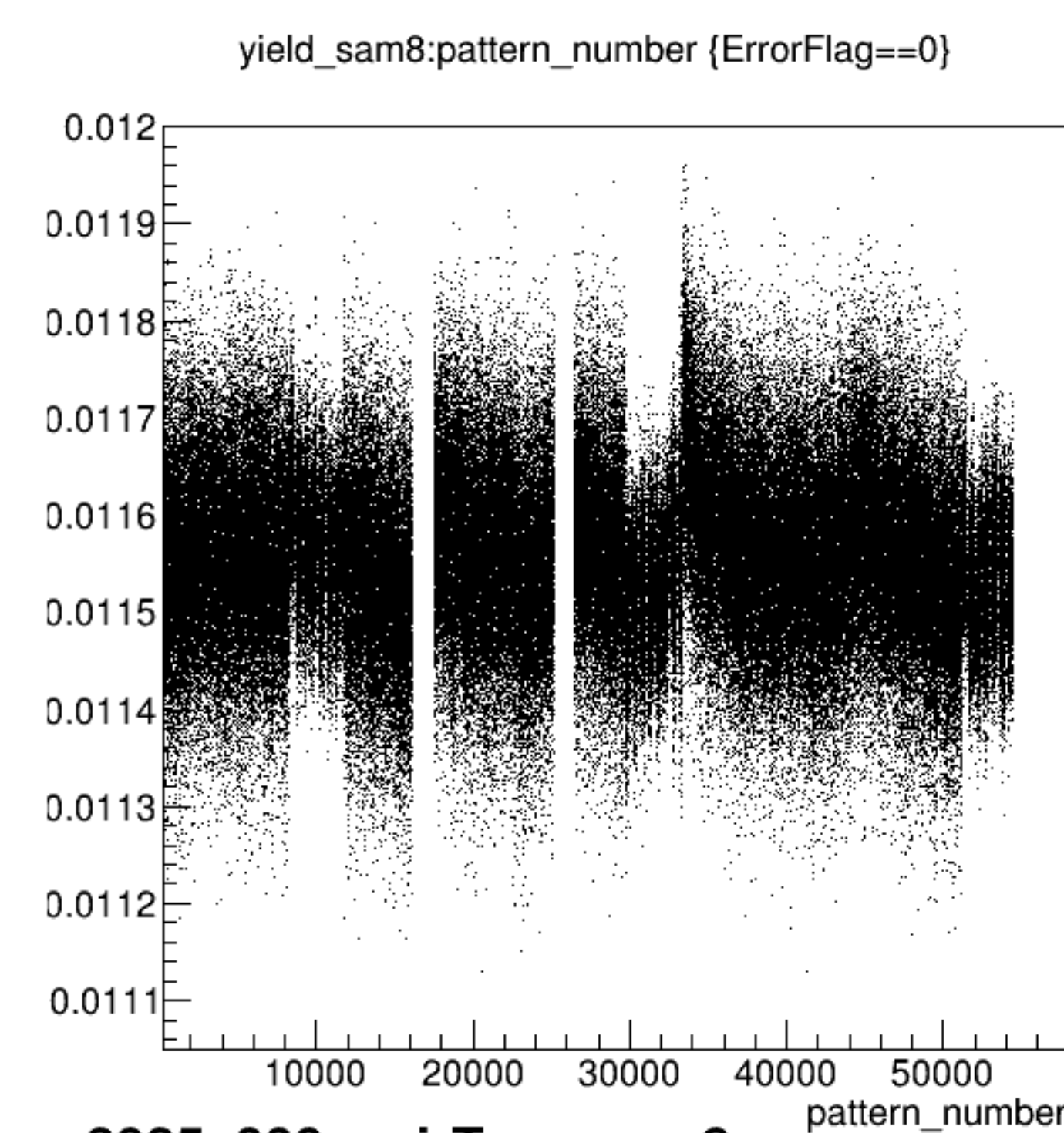


asym_sam6/ppm:pattern_number {ErrorFlag==0}

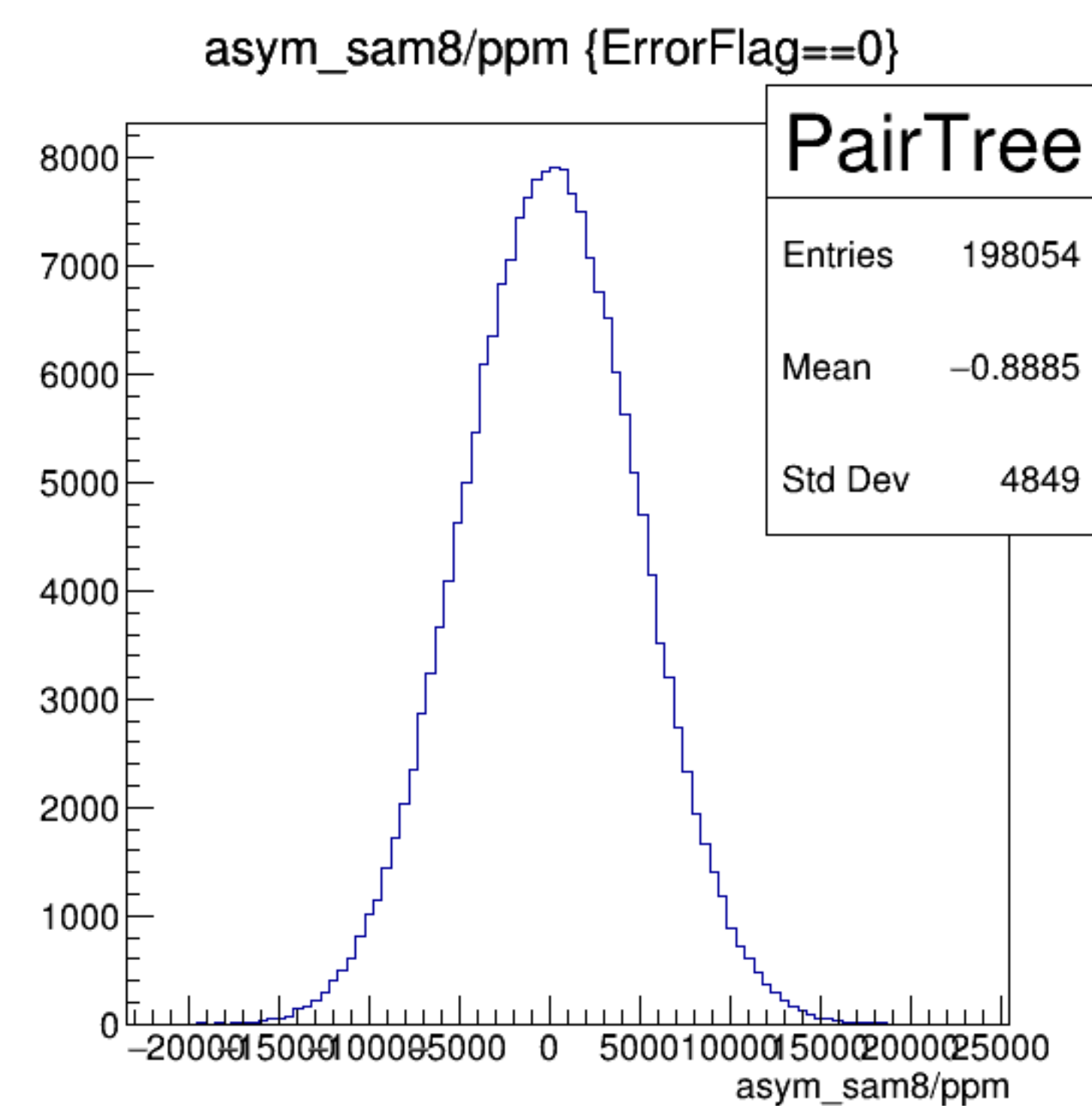
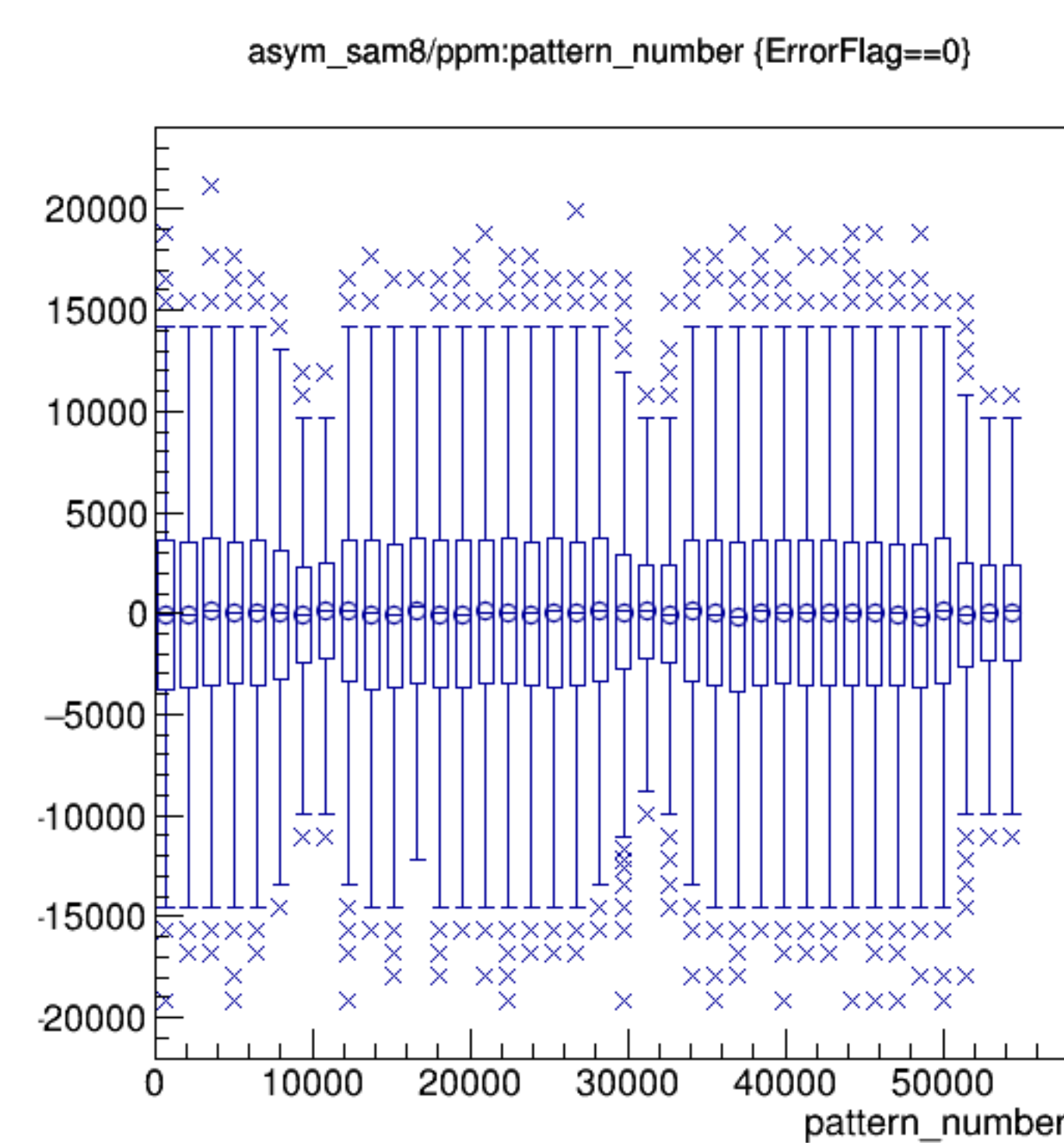
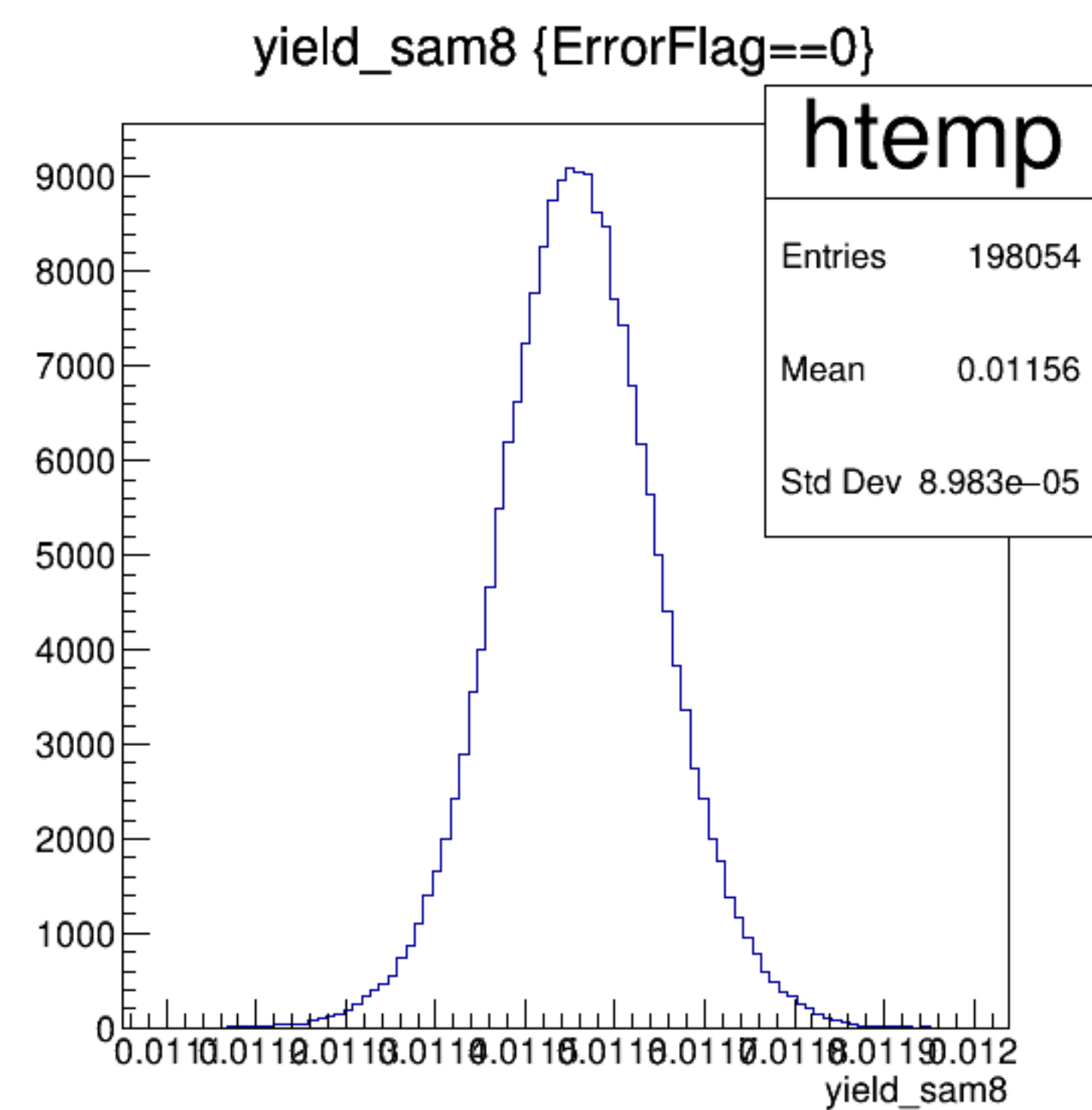


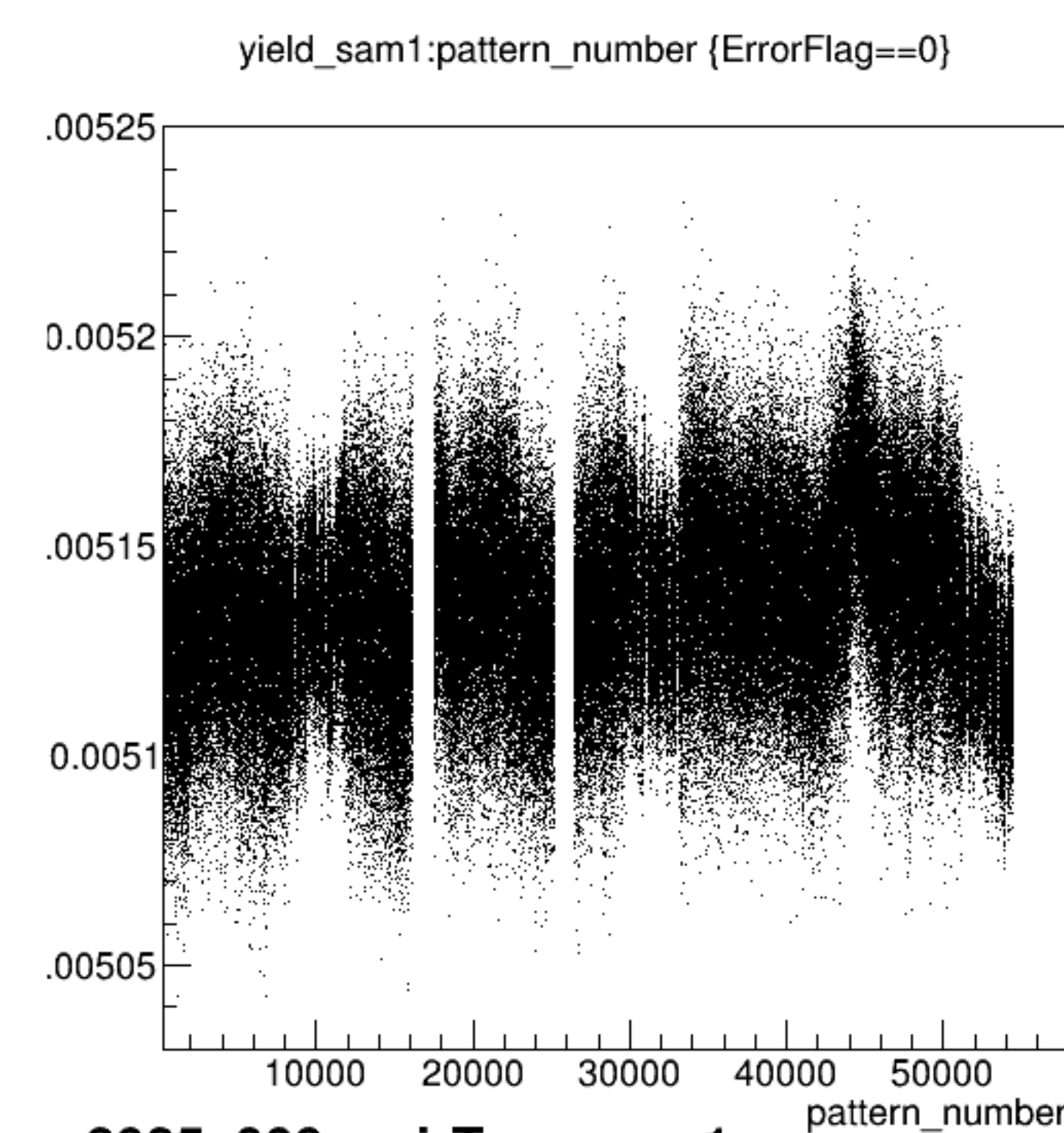
asym_sam6/ppm {ErrorFlag==0}



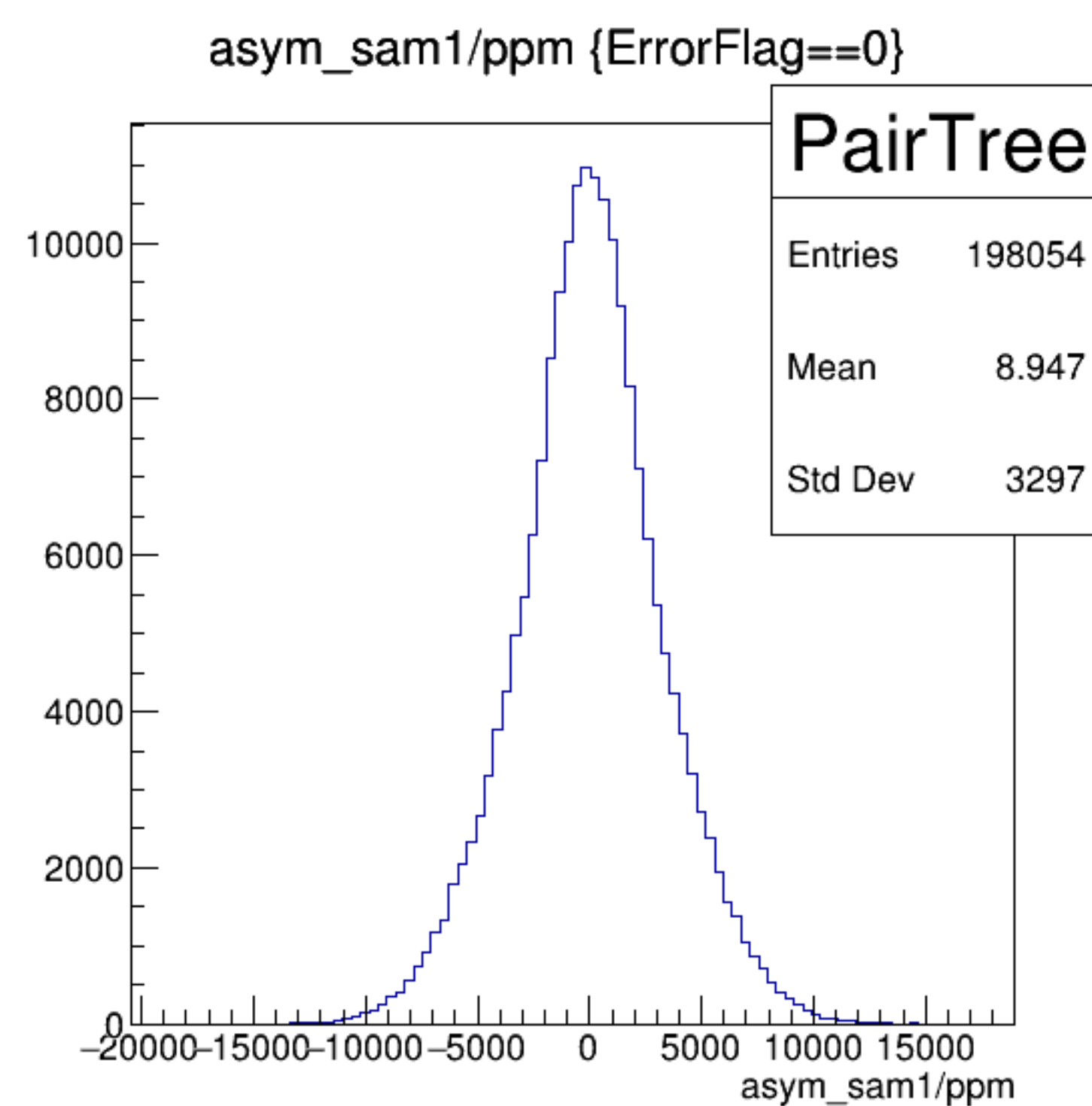
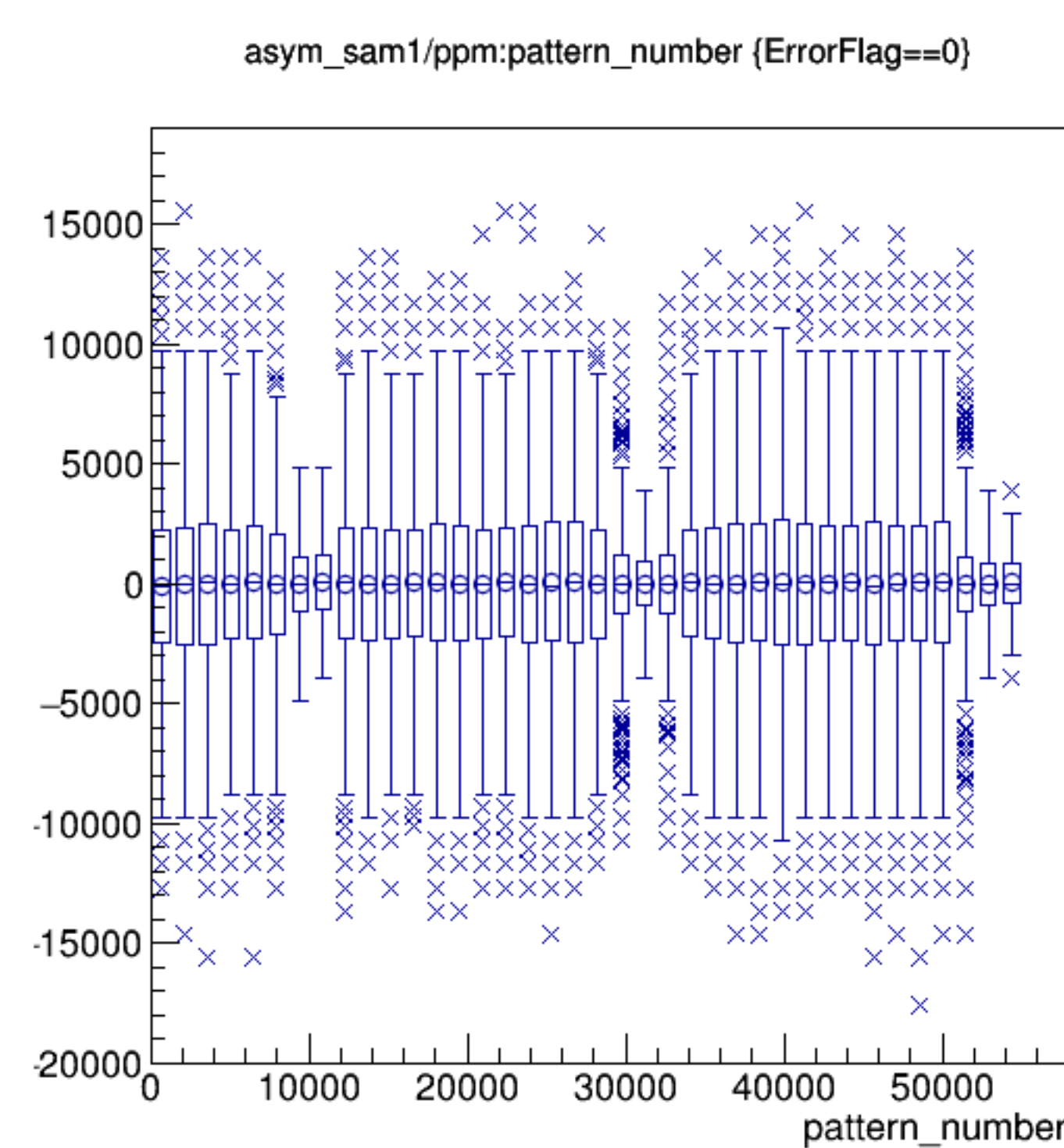
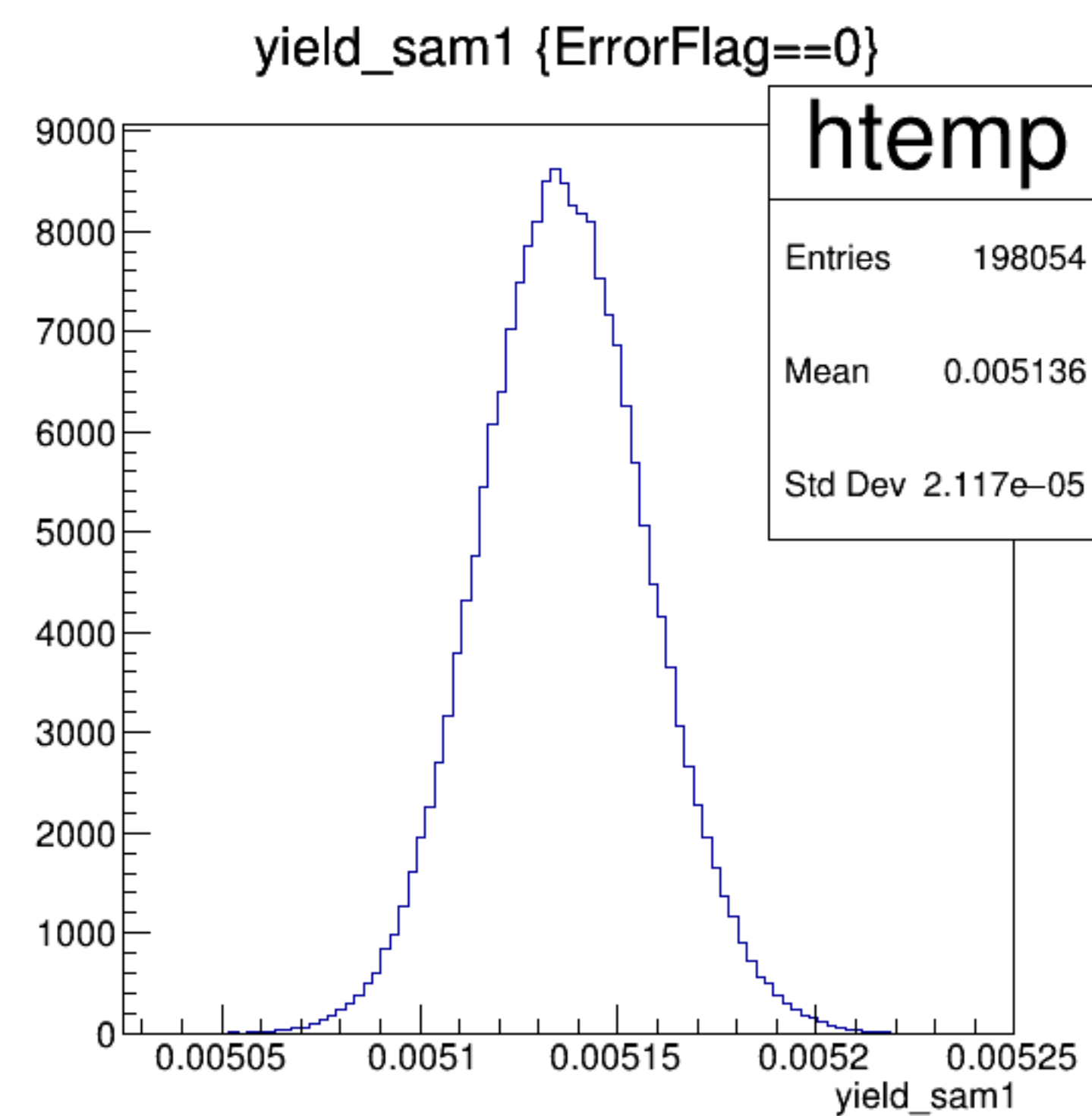


run3925_000_pairTree_sam8.png

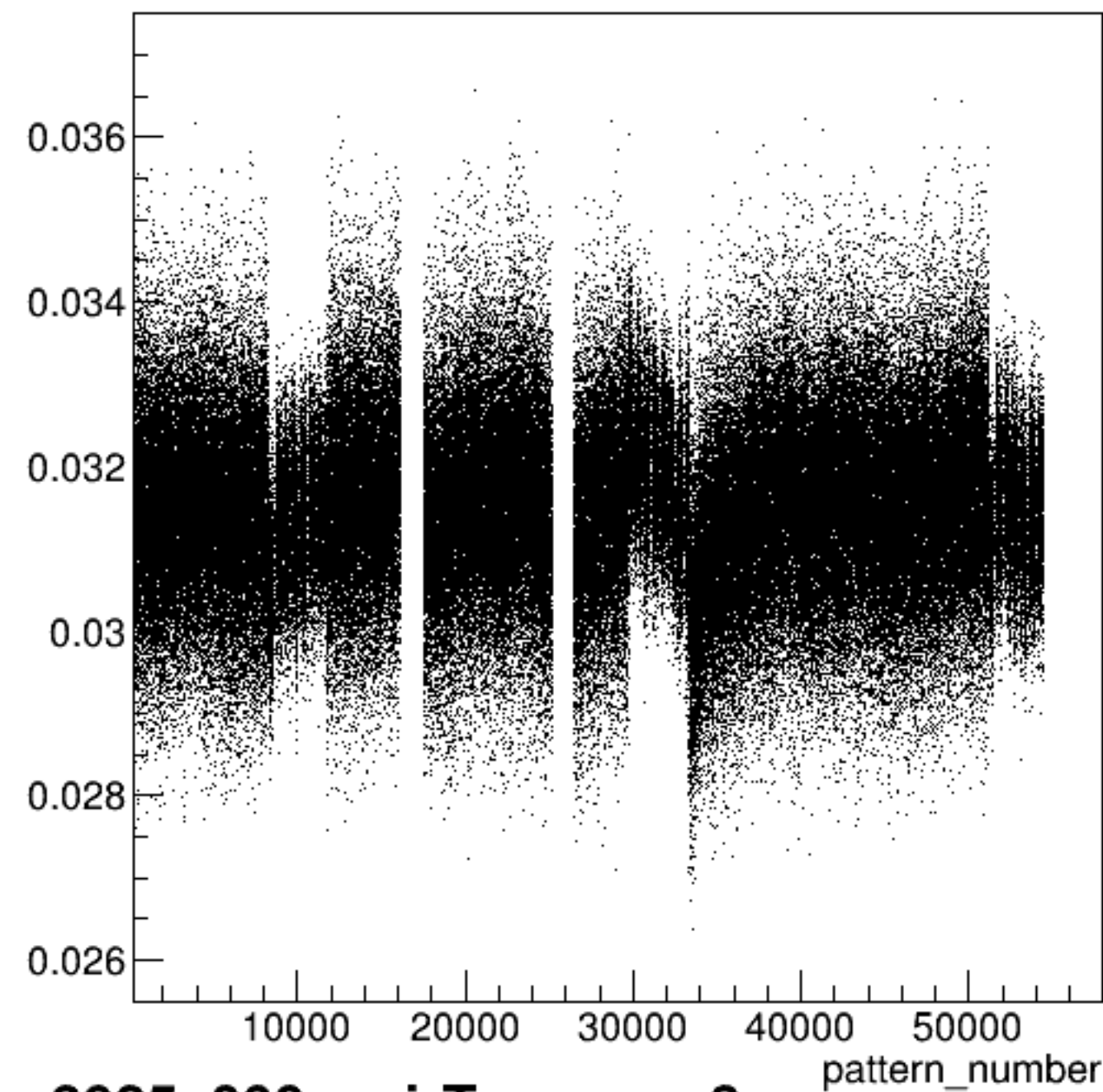




run3925_000_pairTree_sam1.png

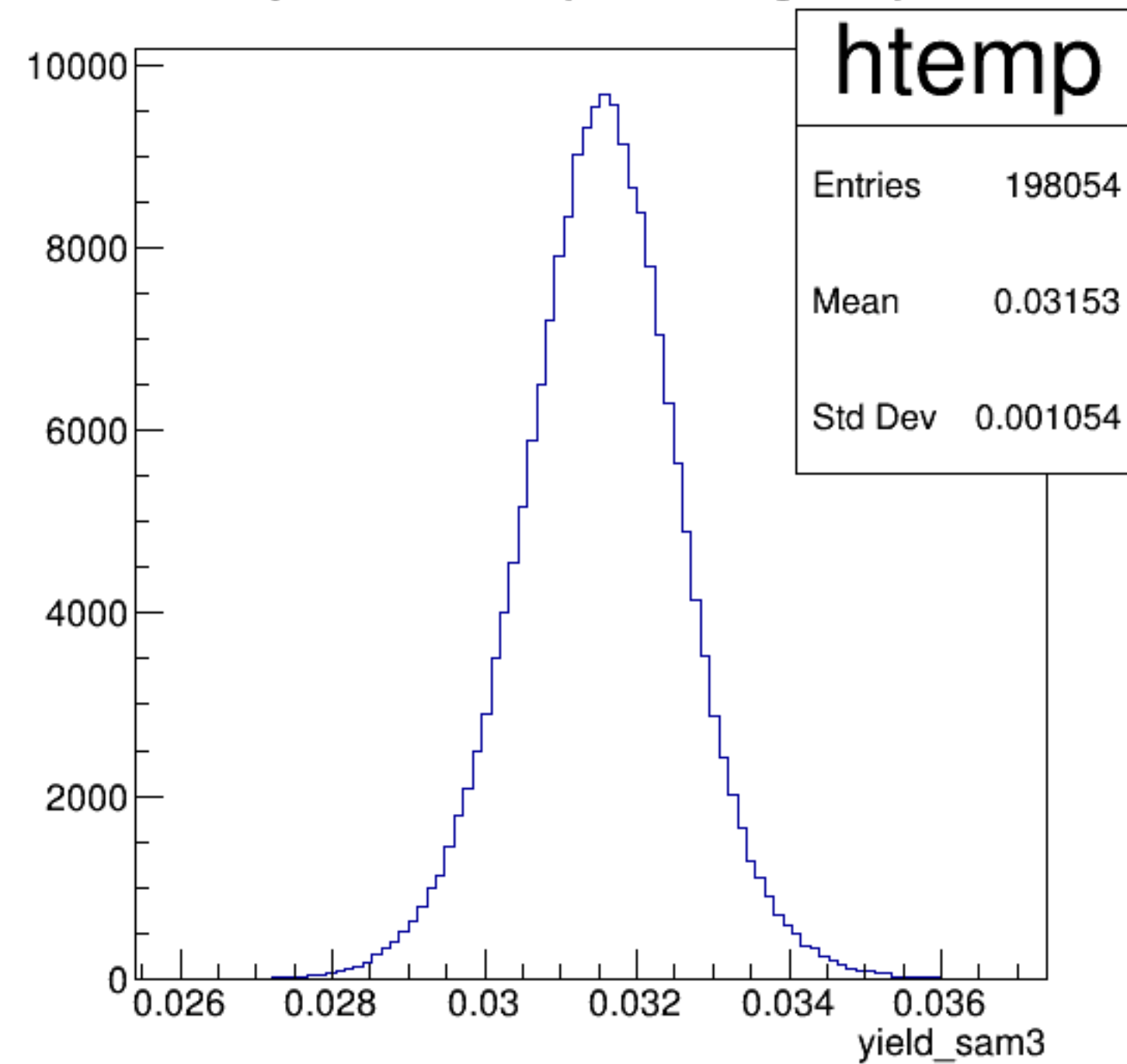


yield_sam3:pattern_number {ErrorFlag==0}

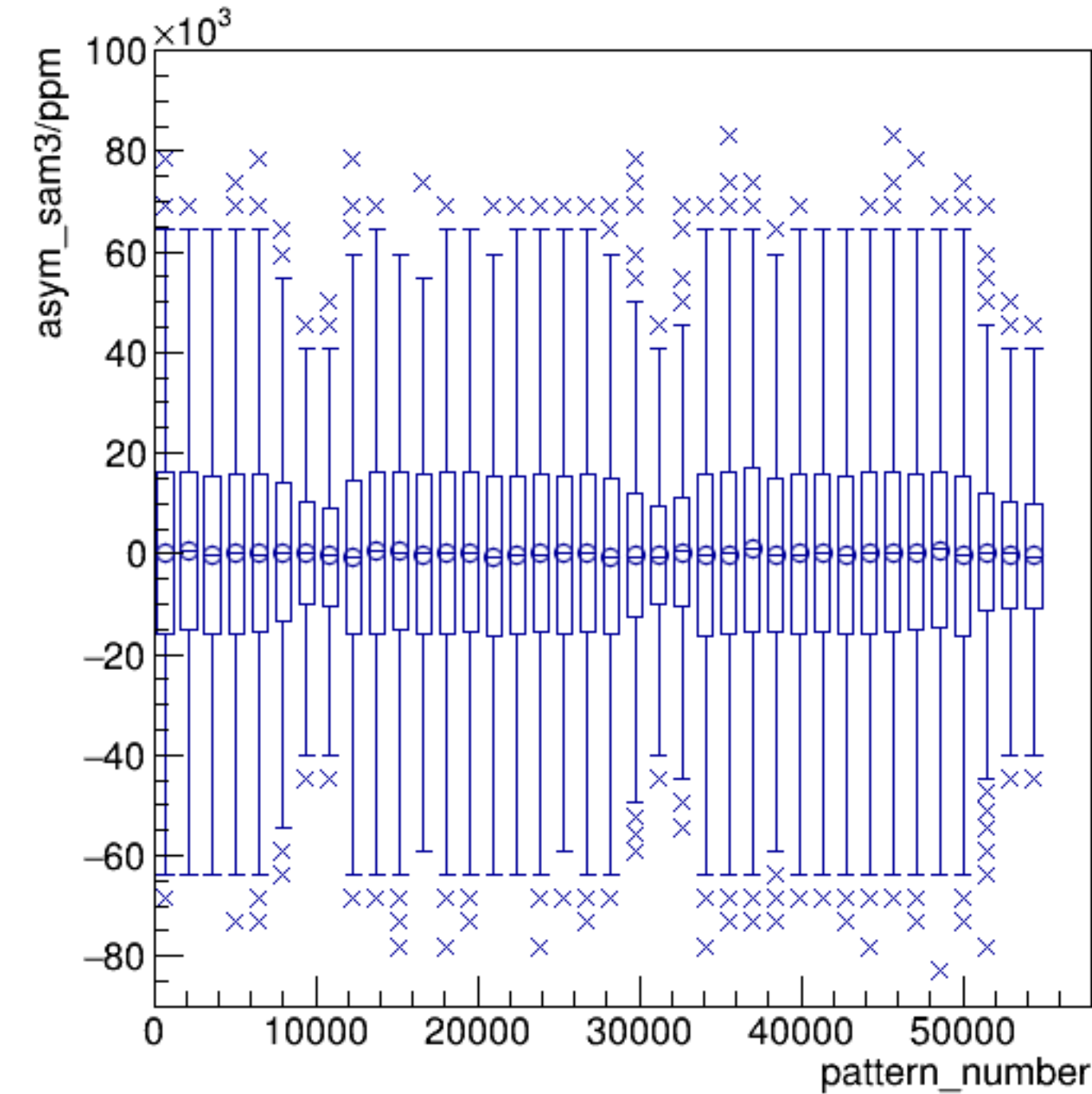


run3925_000_pairTree_sam3.png

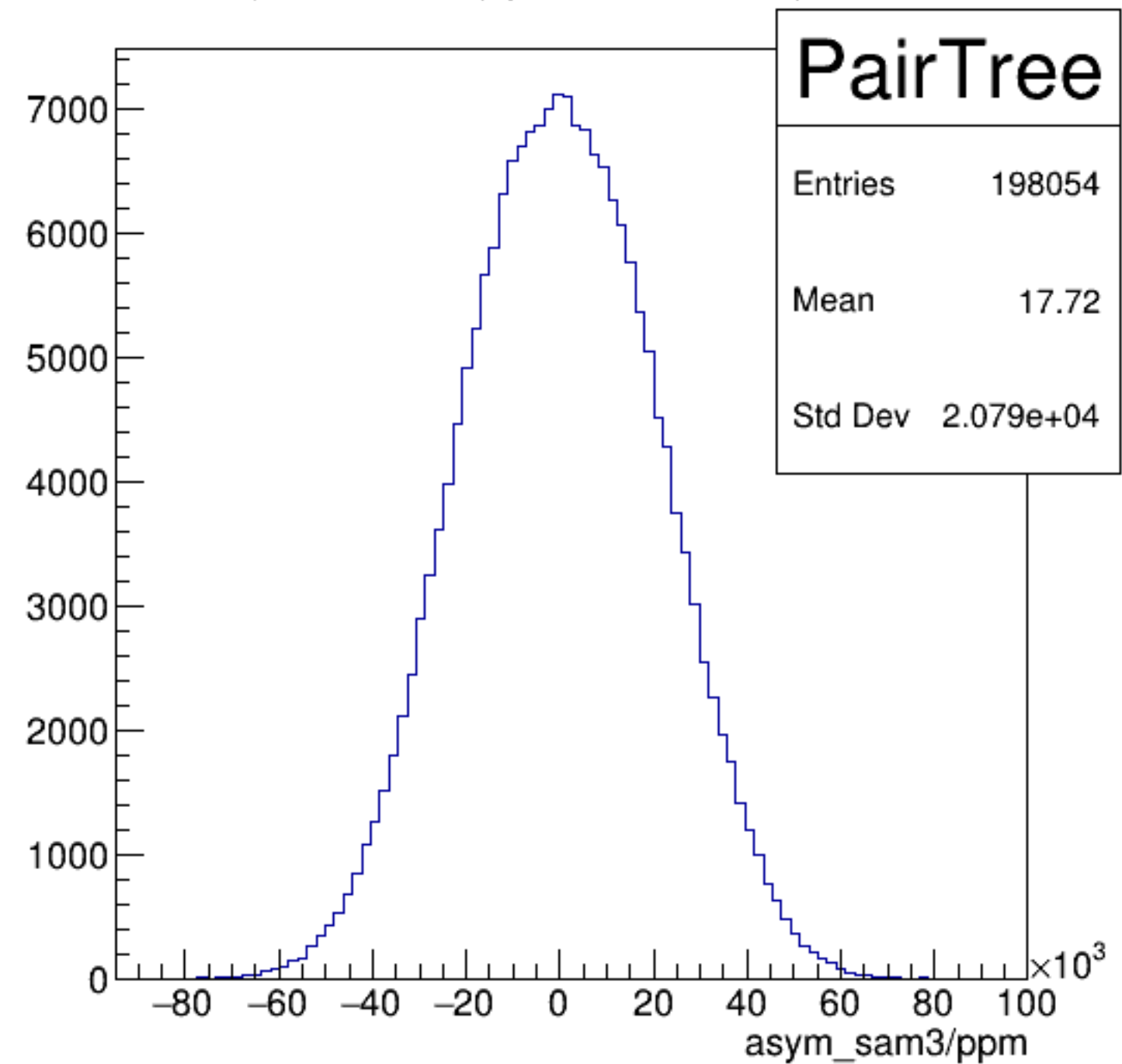
yield_sam3 {ErrorFlag==0}



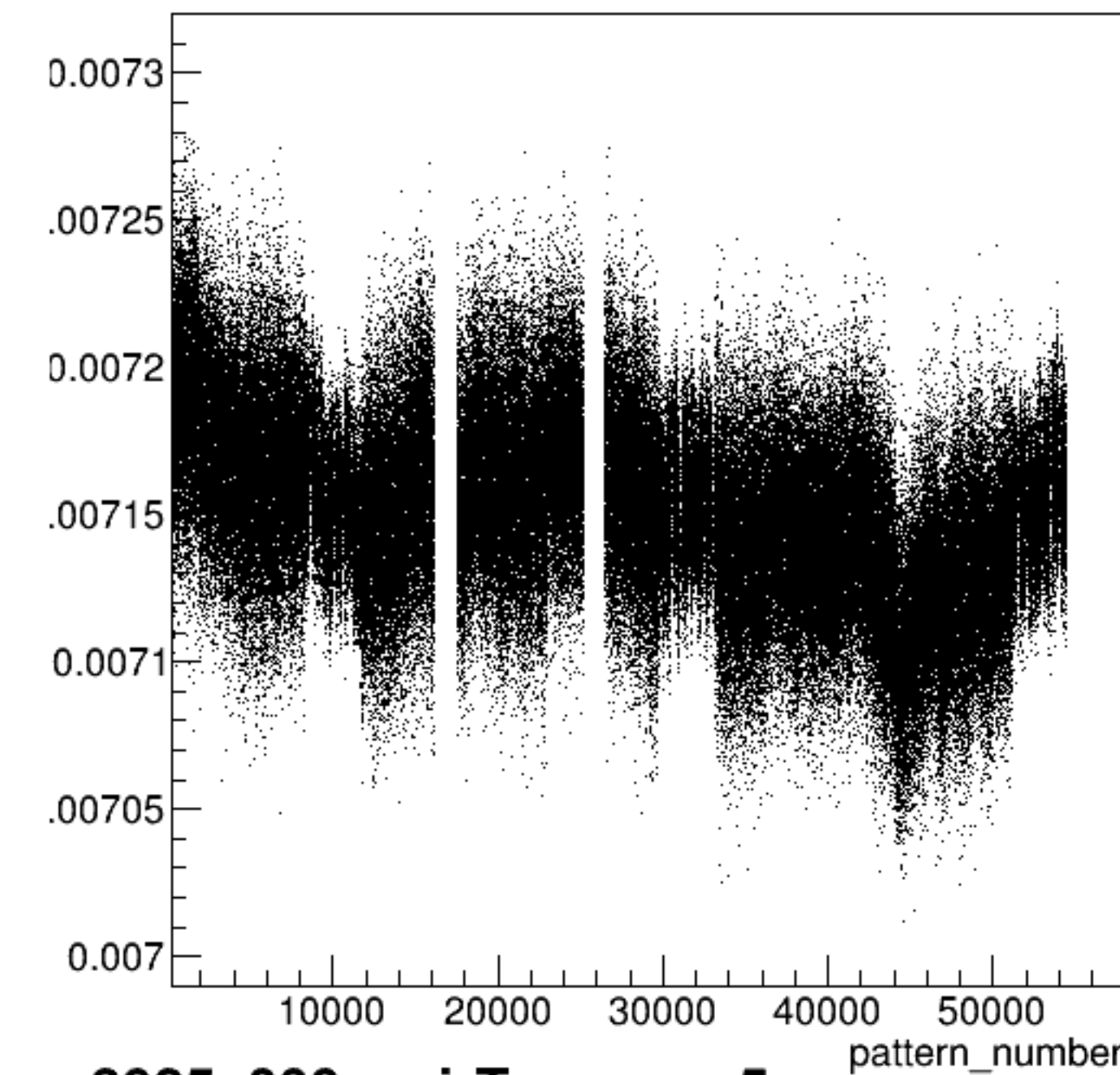
asym_sam3/ppm:pattern_number {ErrorFlag==0}



asym_sam3/ppm {ErrorFlag==0}

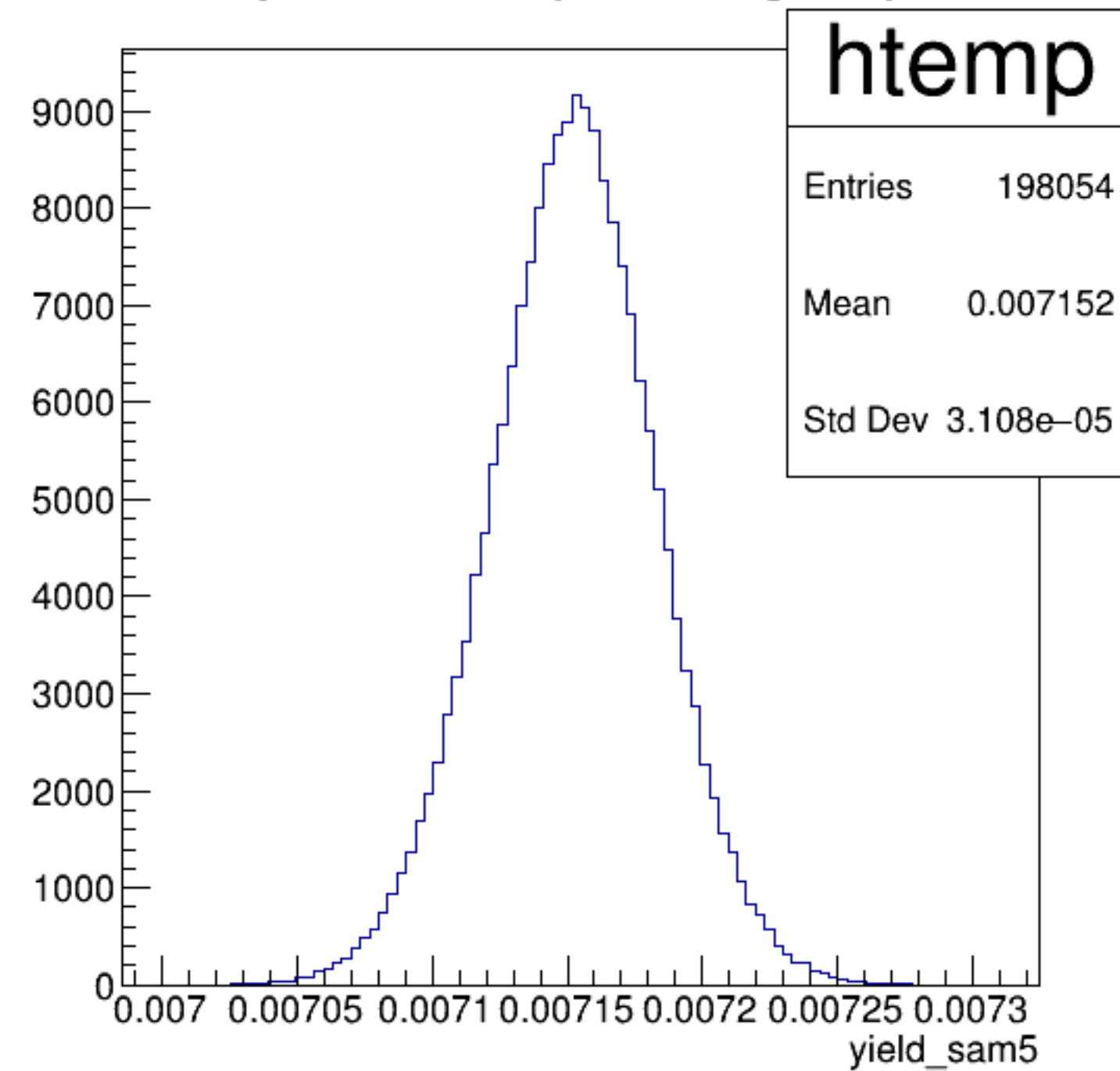


yield_sam5:pattern_number {ErrorFlag==0}

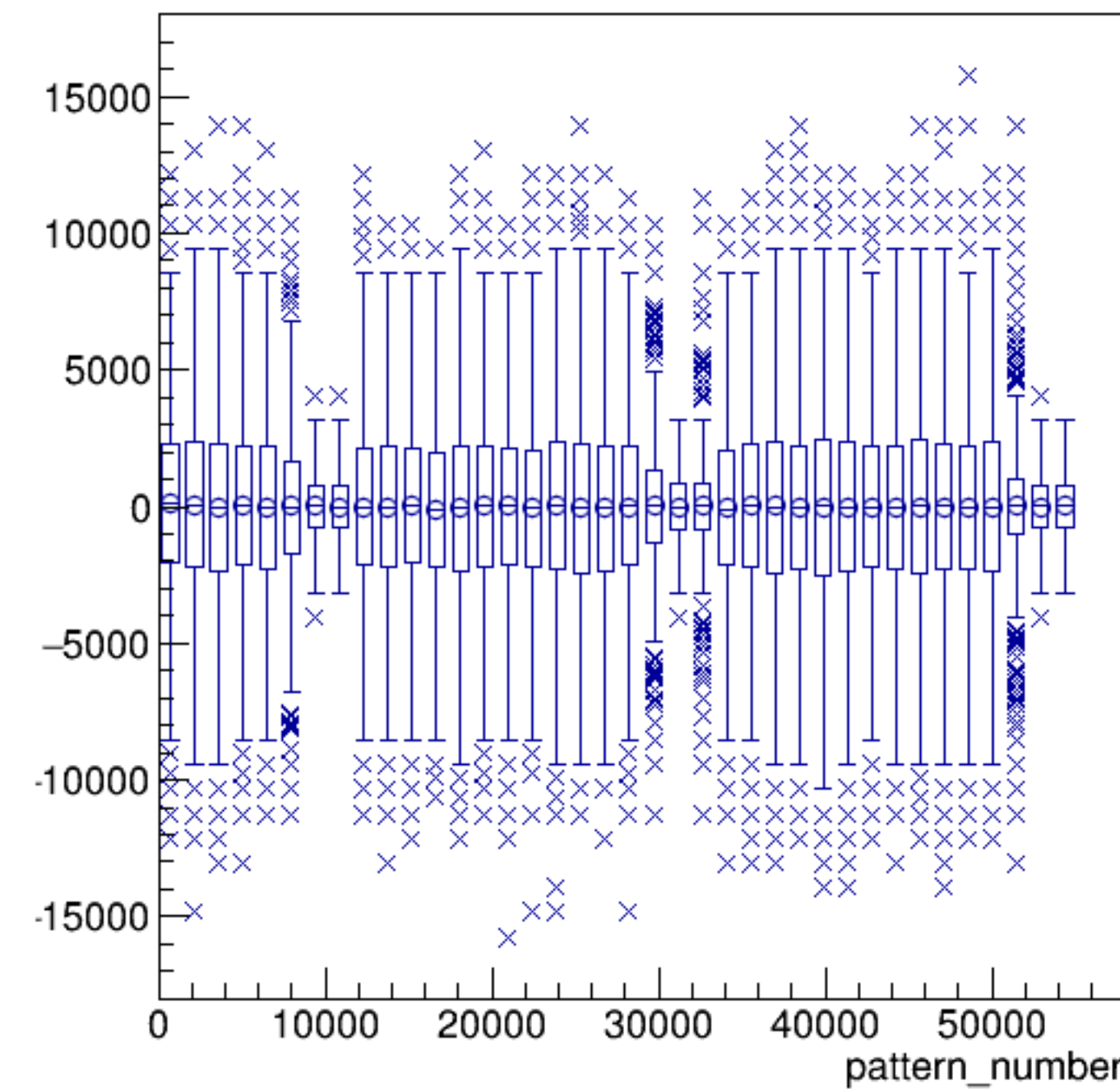


run3925_000_pairTree_sam5.png

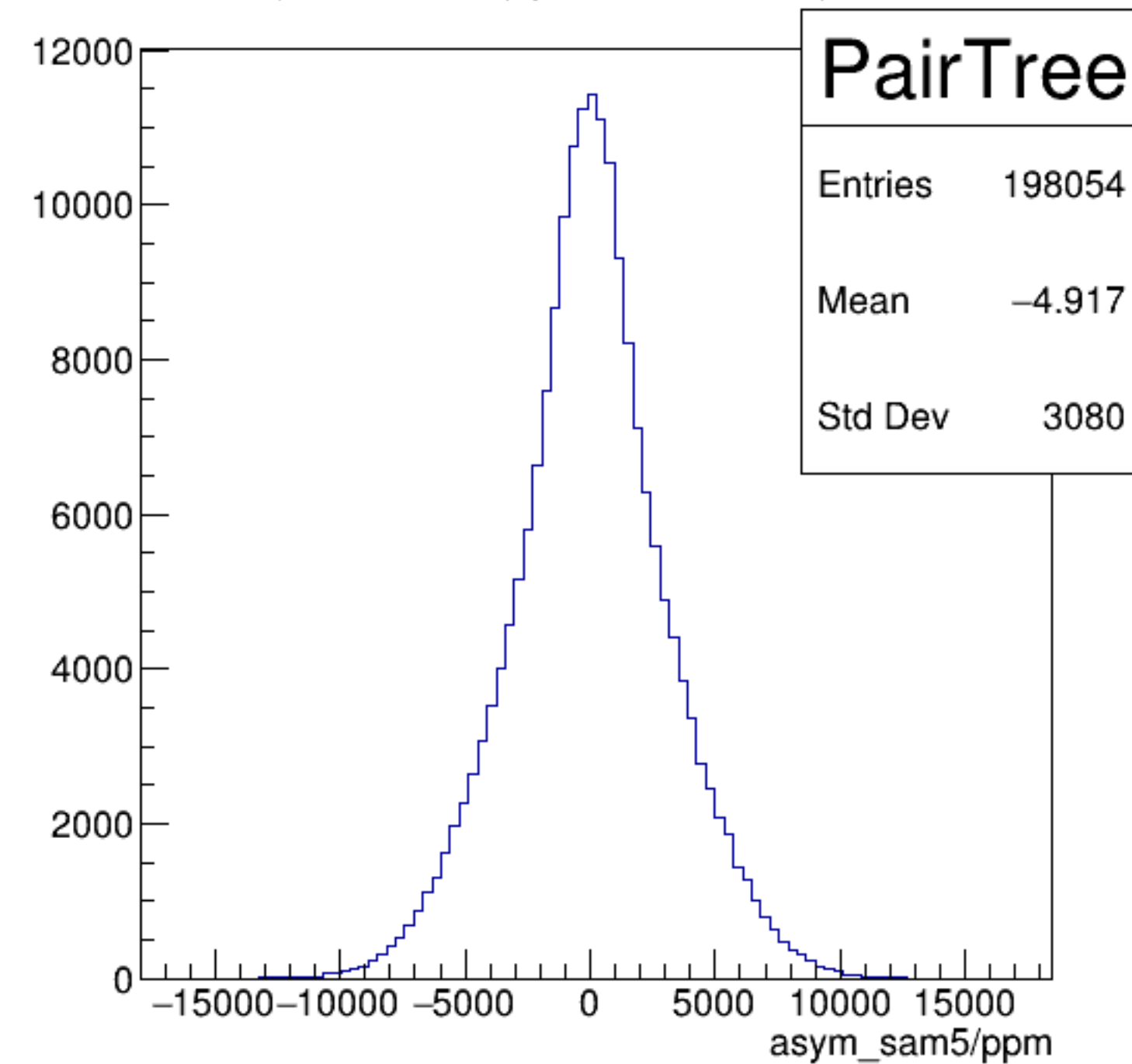
yield_sam5 {ErrorFlag==0}



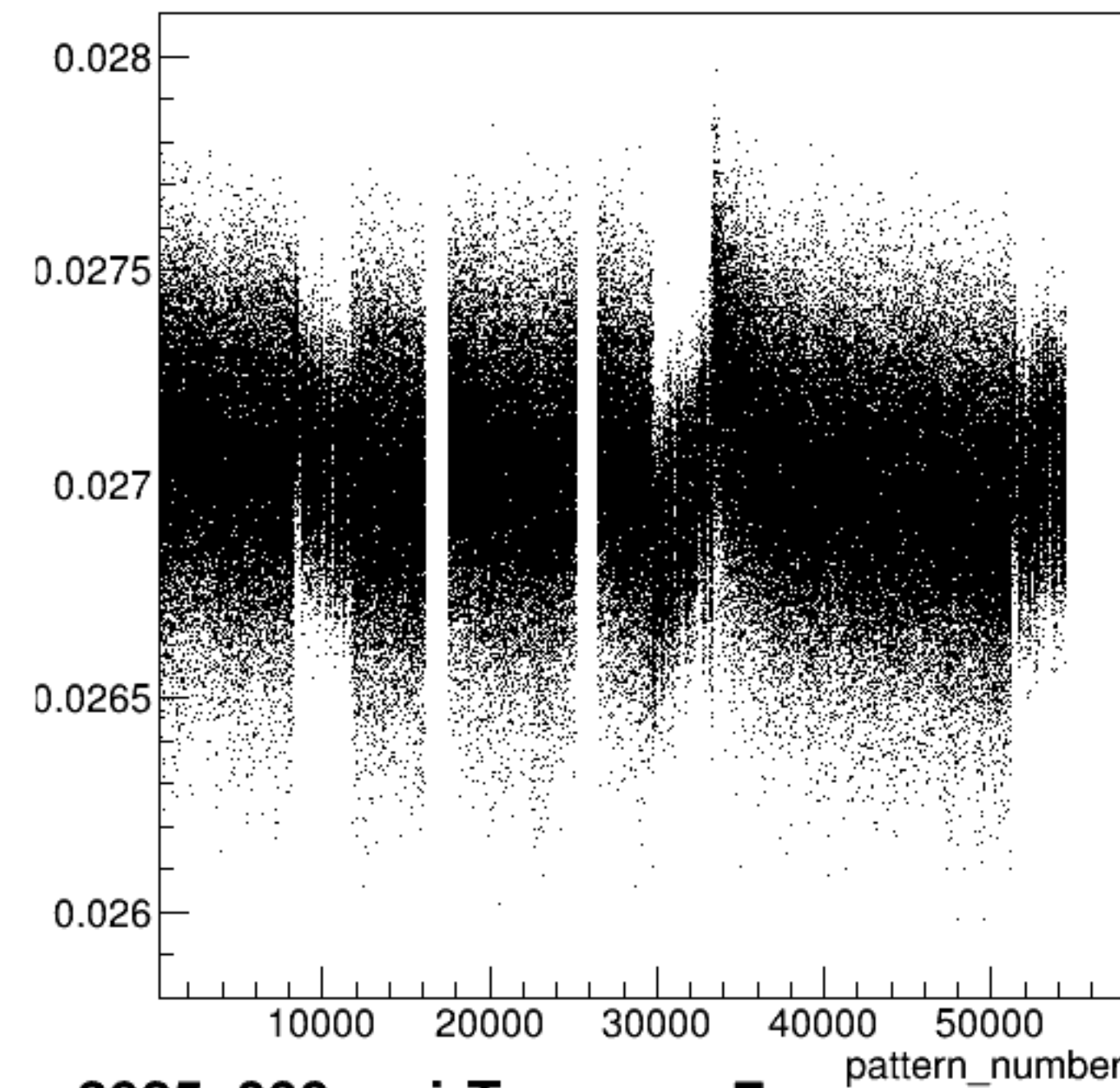
asym_sam5/ppm:pattern_number {ErrorFlag==0}



asym_sam5/ppm {ErrorFlag==0}

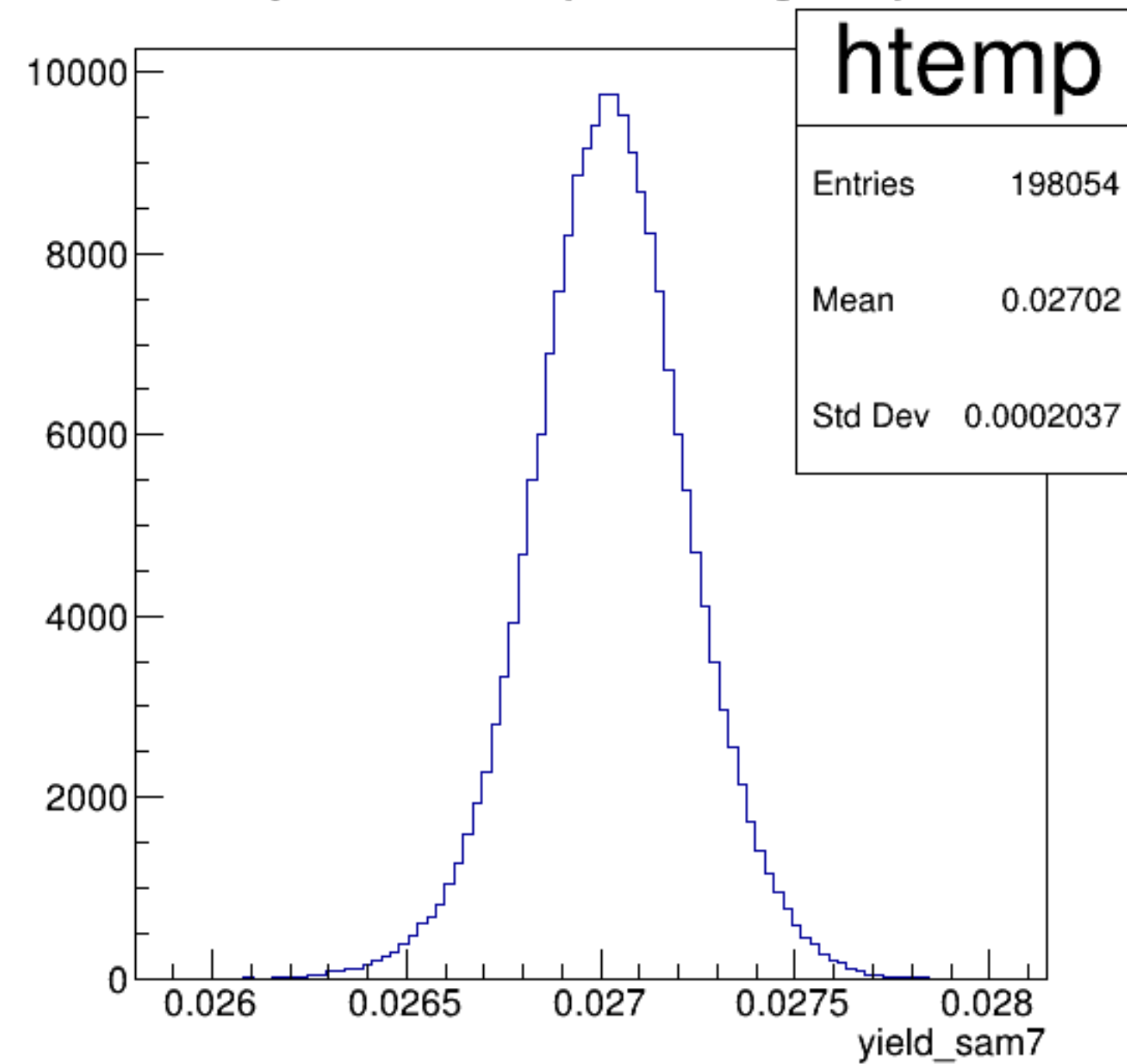


yield_sam7:pattern_number {ErrorFlag==0}

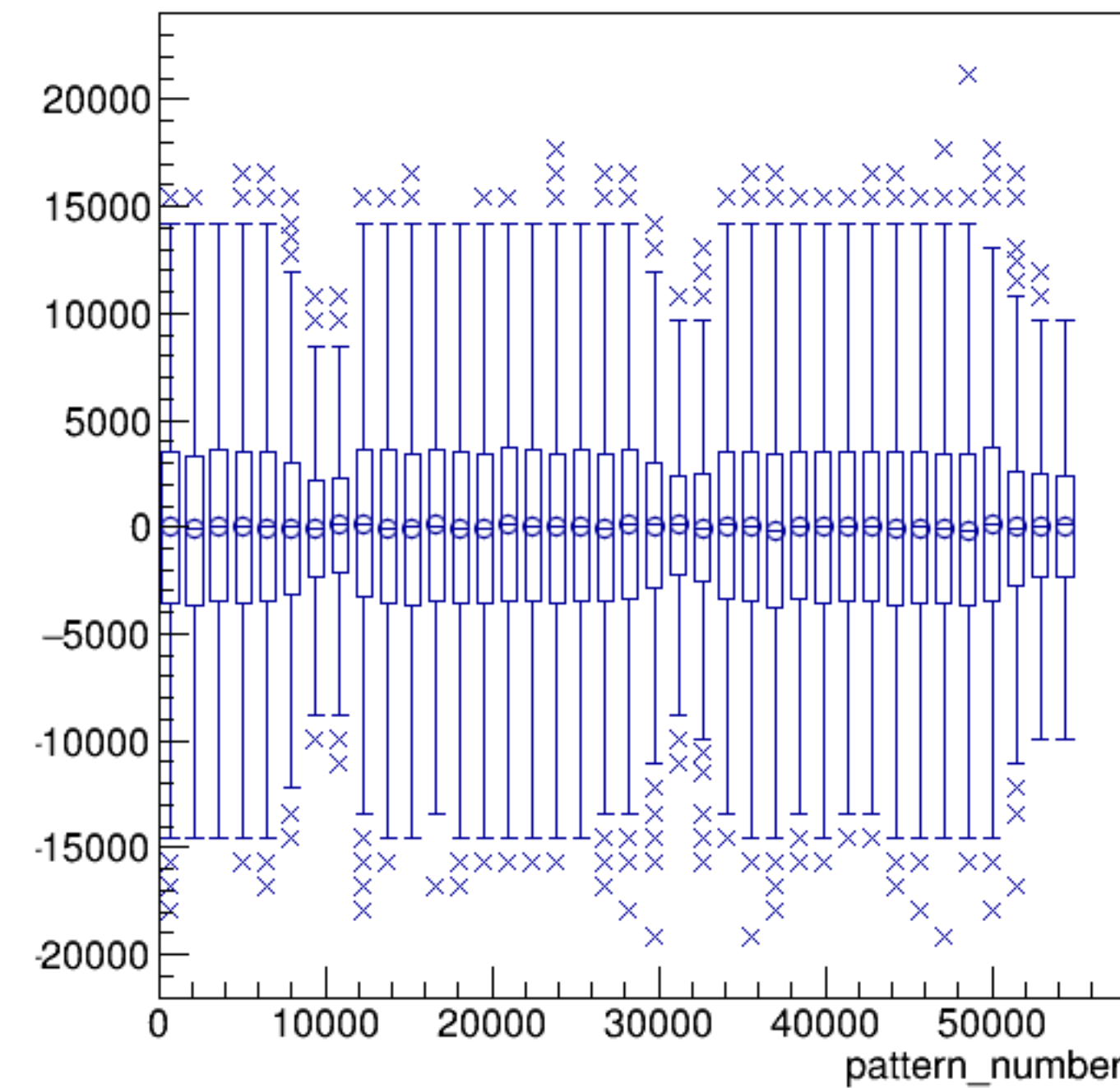


run3925_000_pairTree_sam7.png

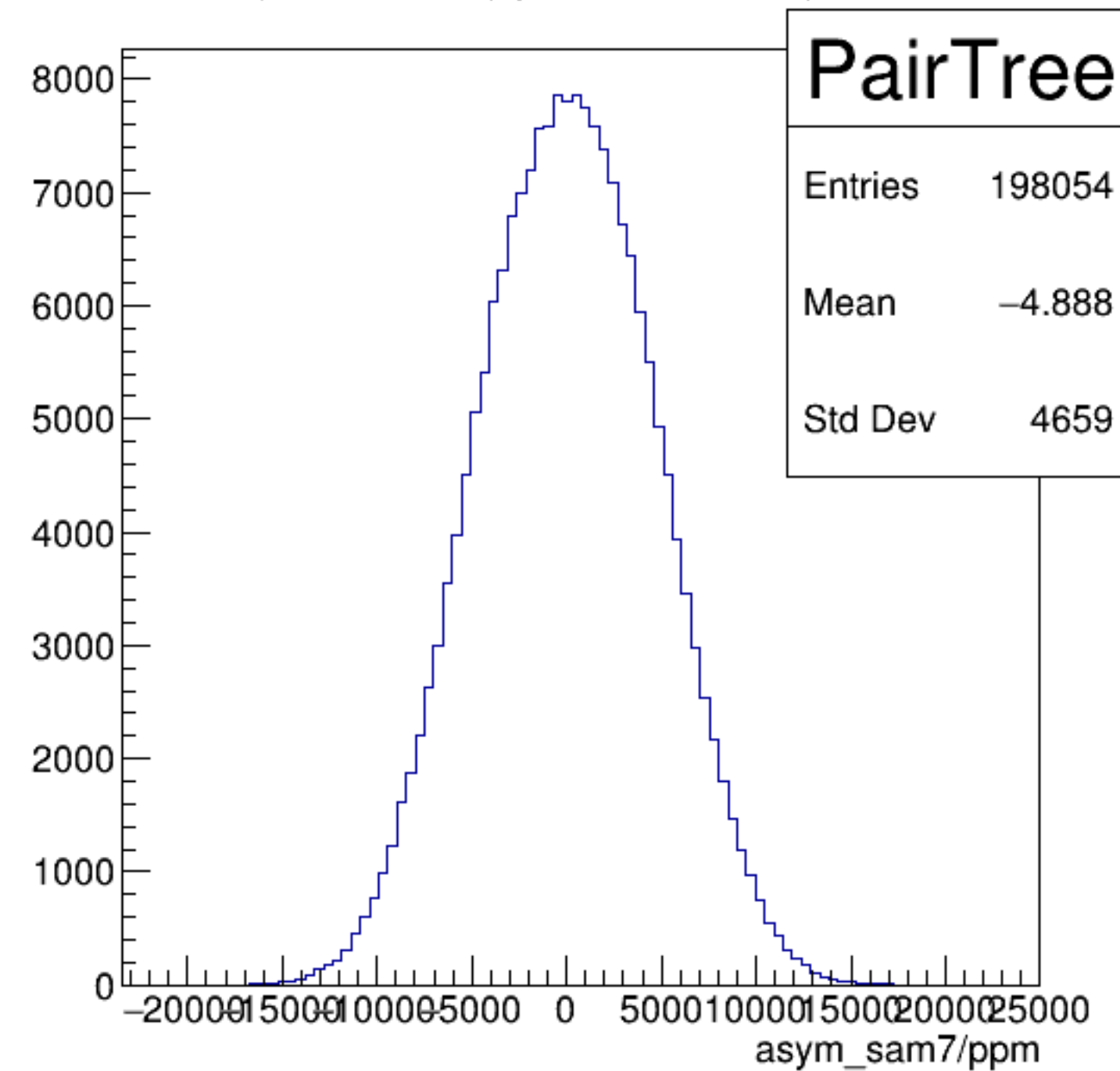
yield_sam7 {ErrorFlag==0}



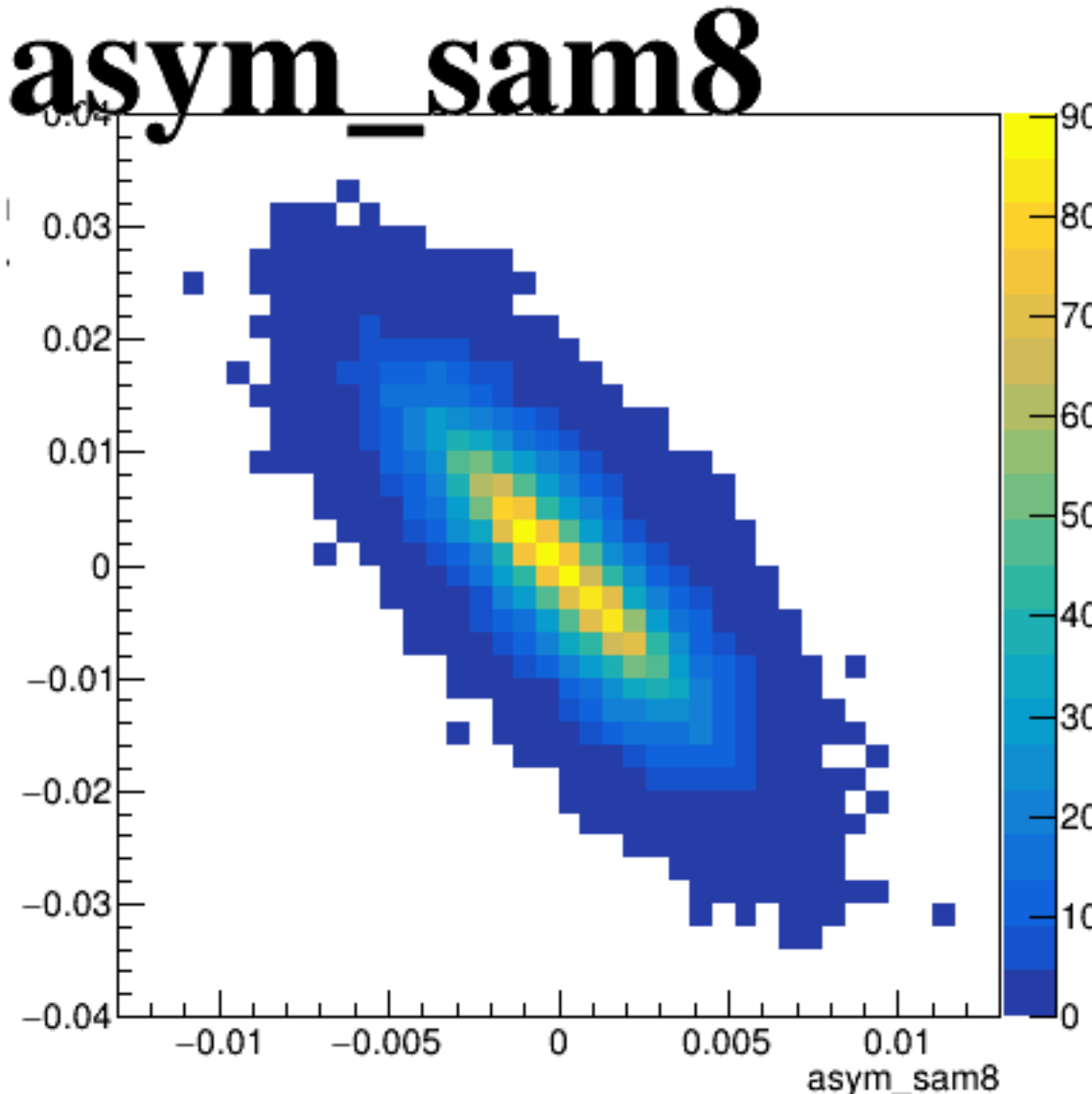
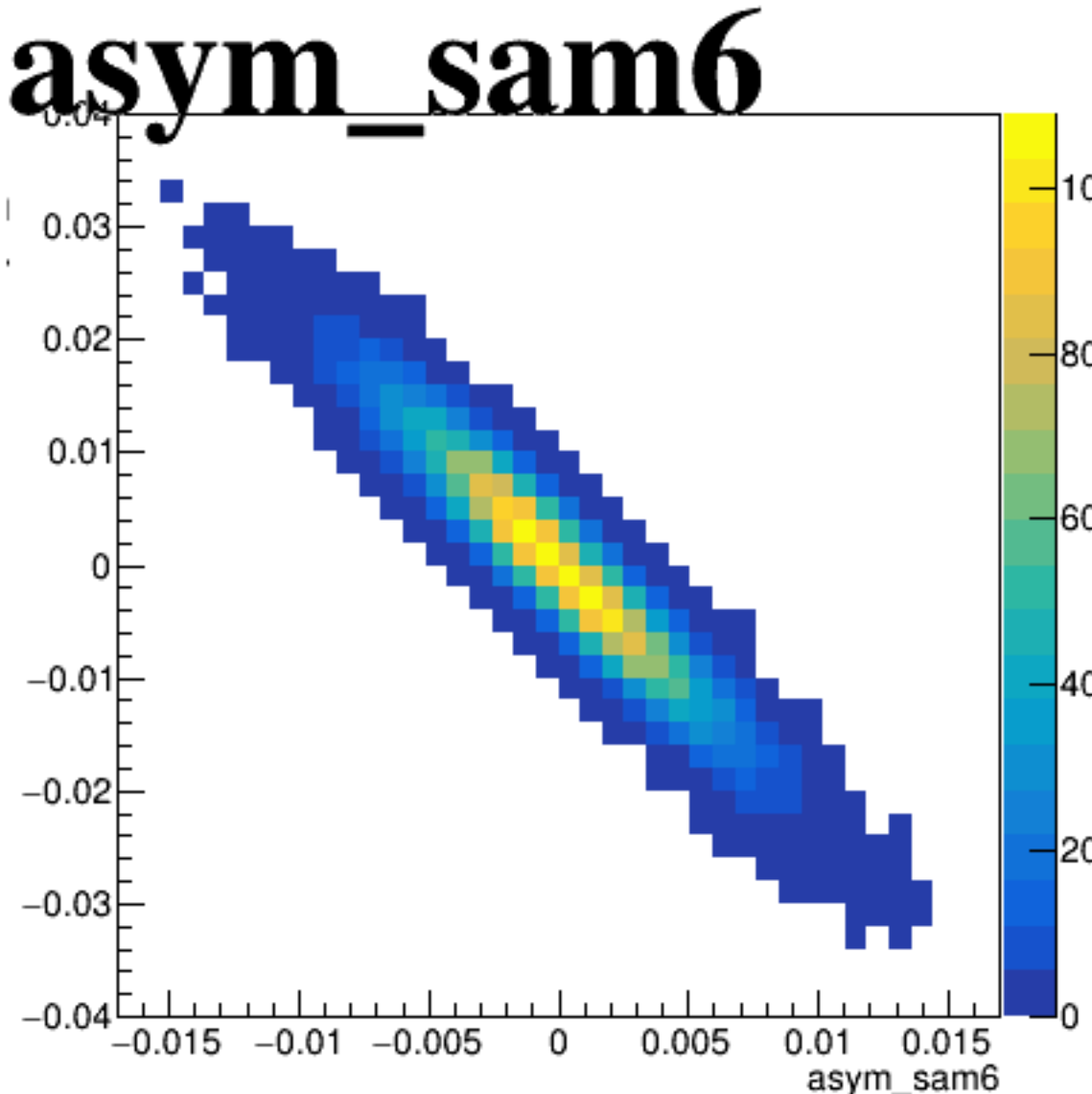
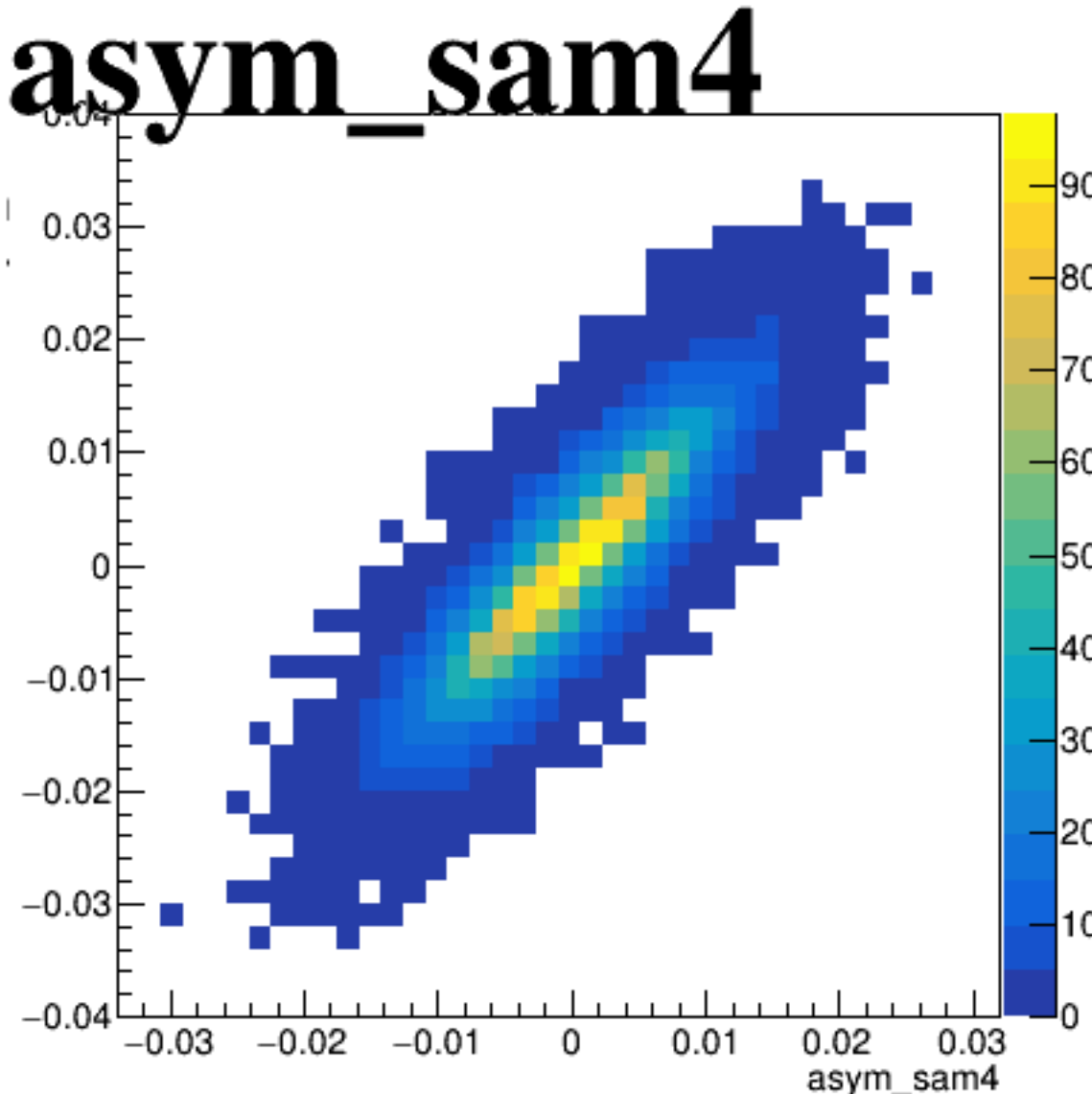
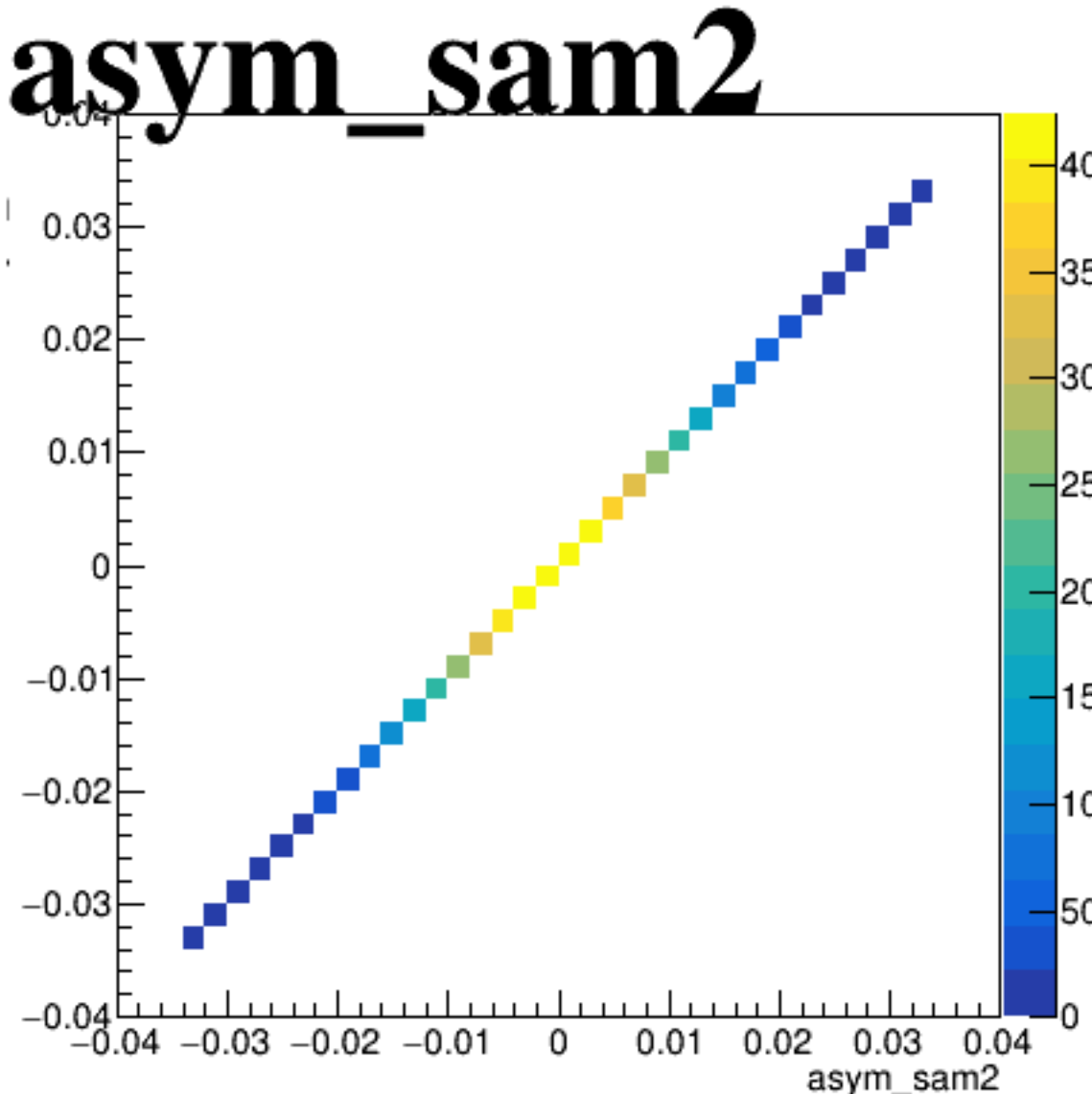
asym_sam7/ppm:pattern_number {ErrorFlag==0}



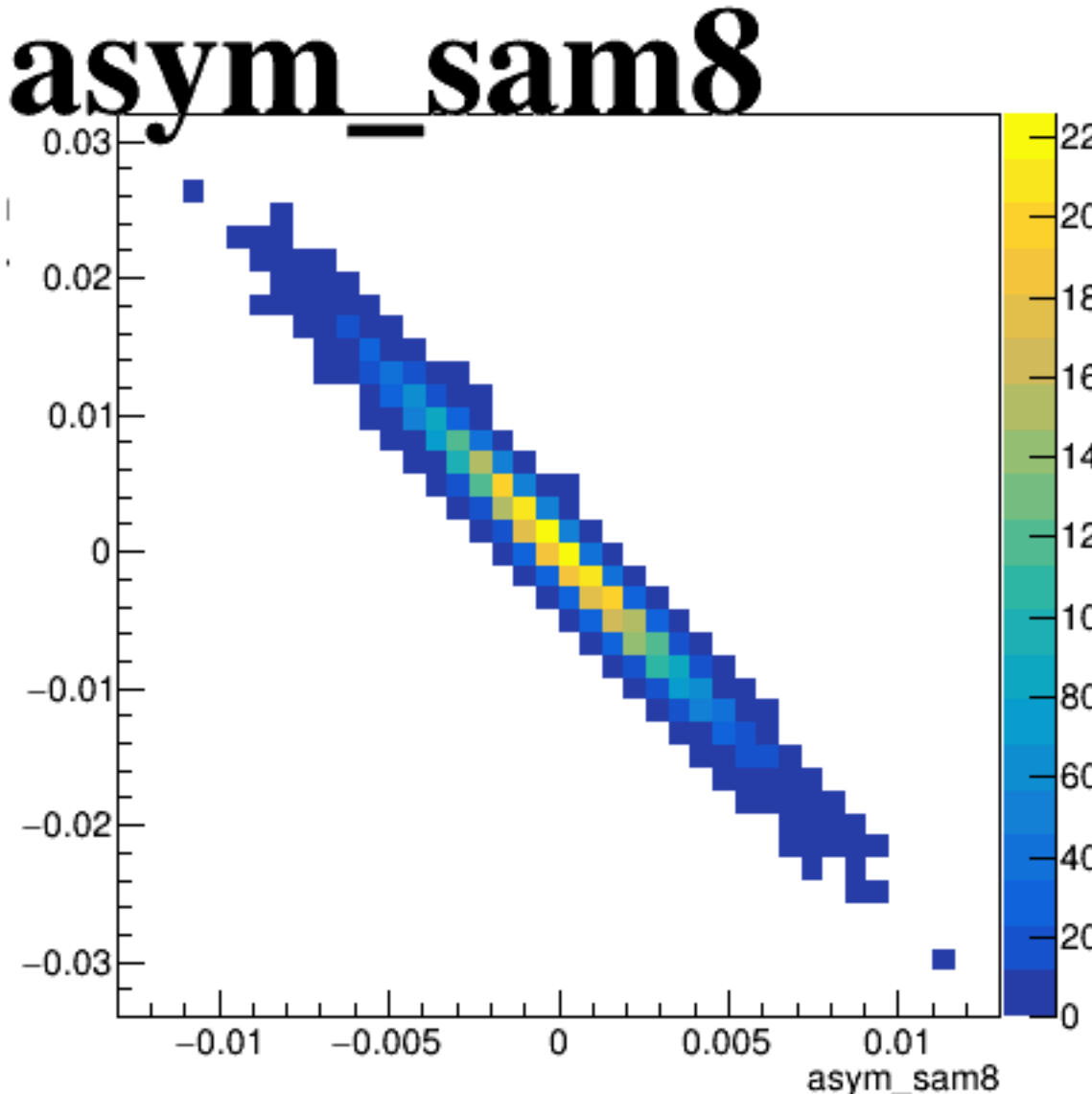
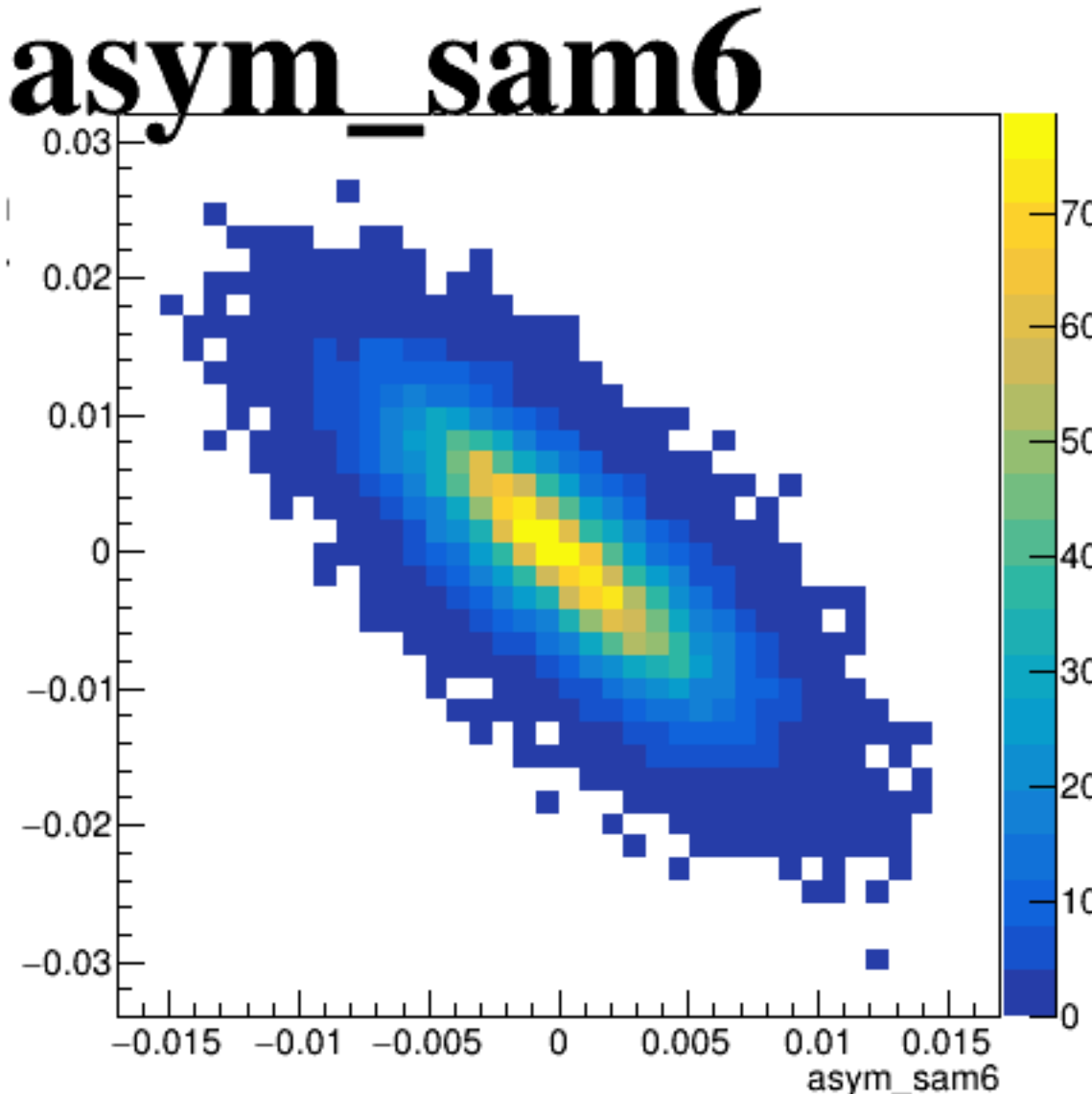
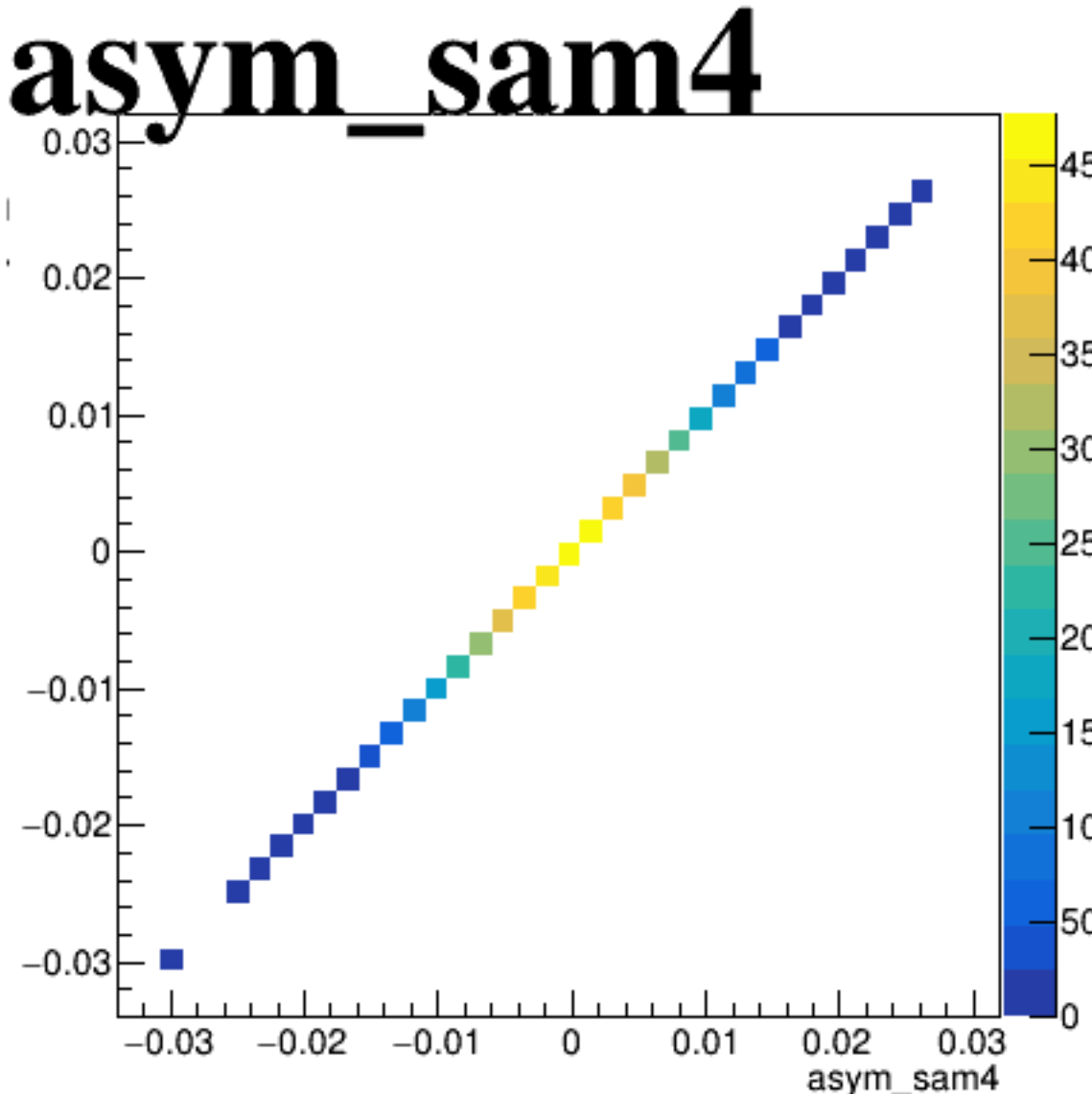
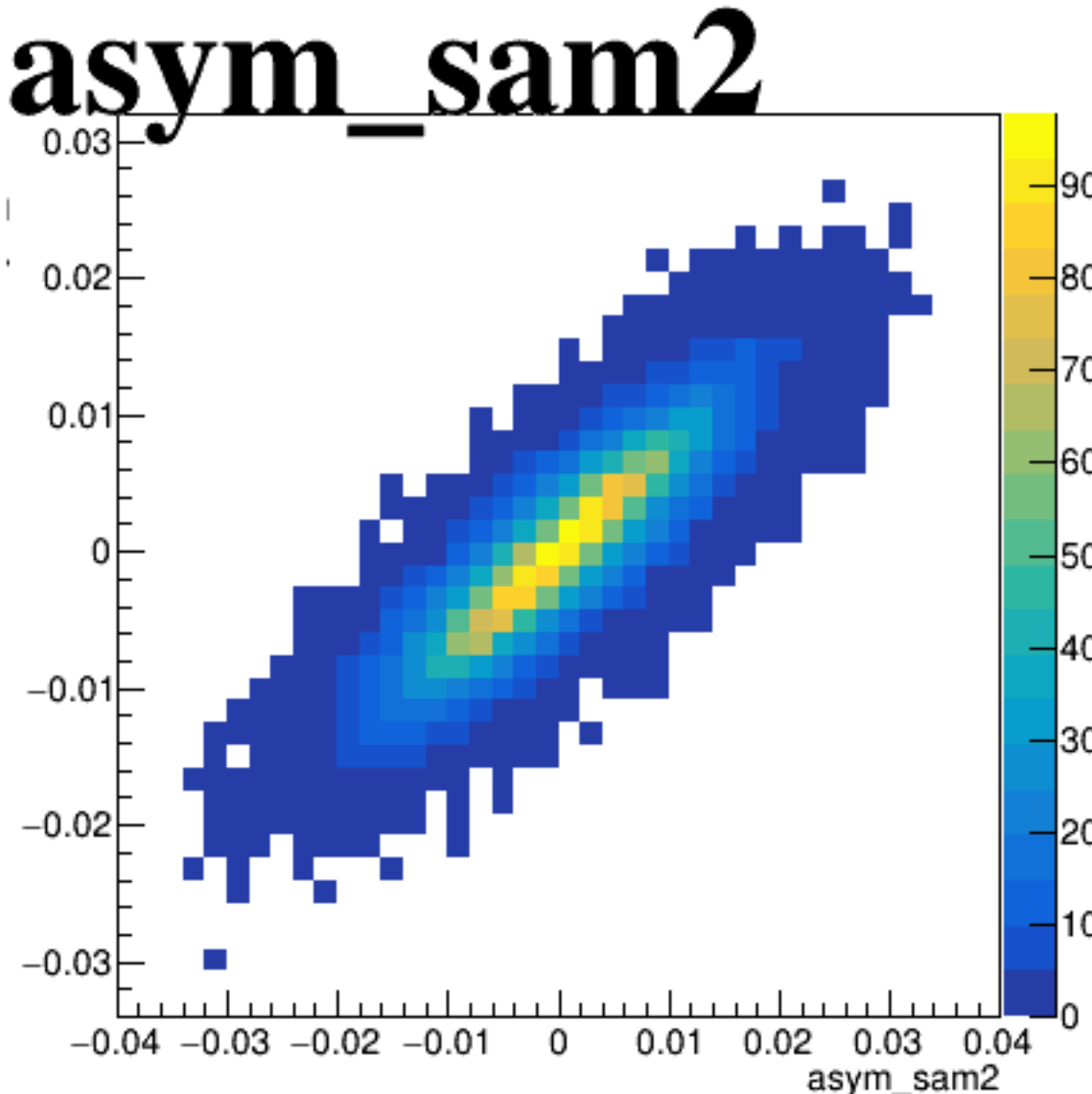
asym_sam7/ppm {ErrorFlag==0}



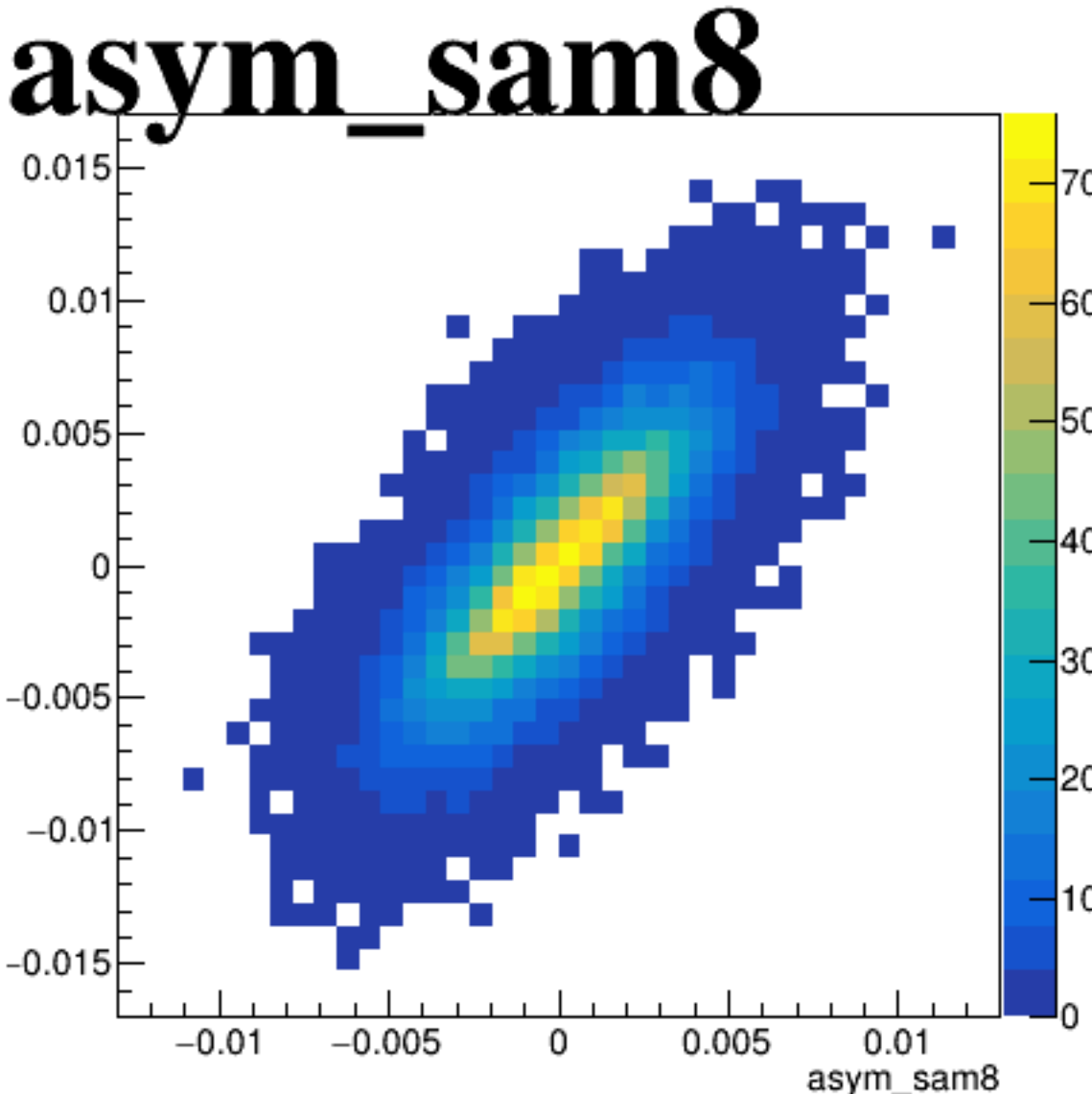
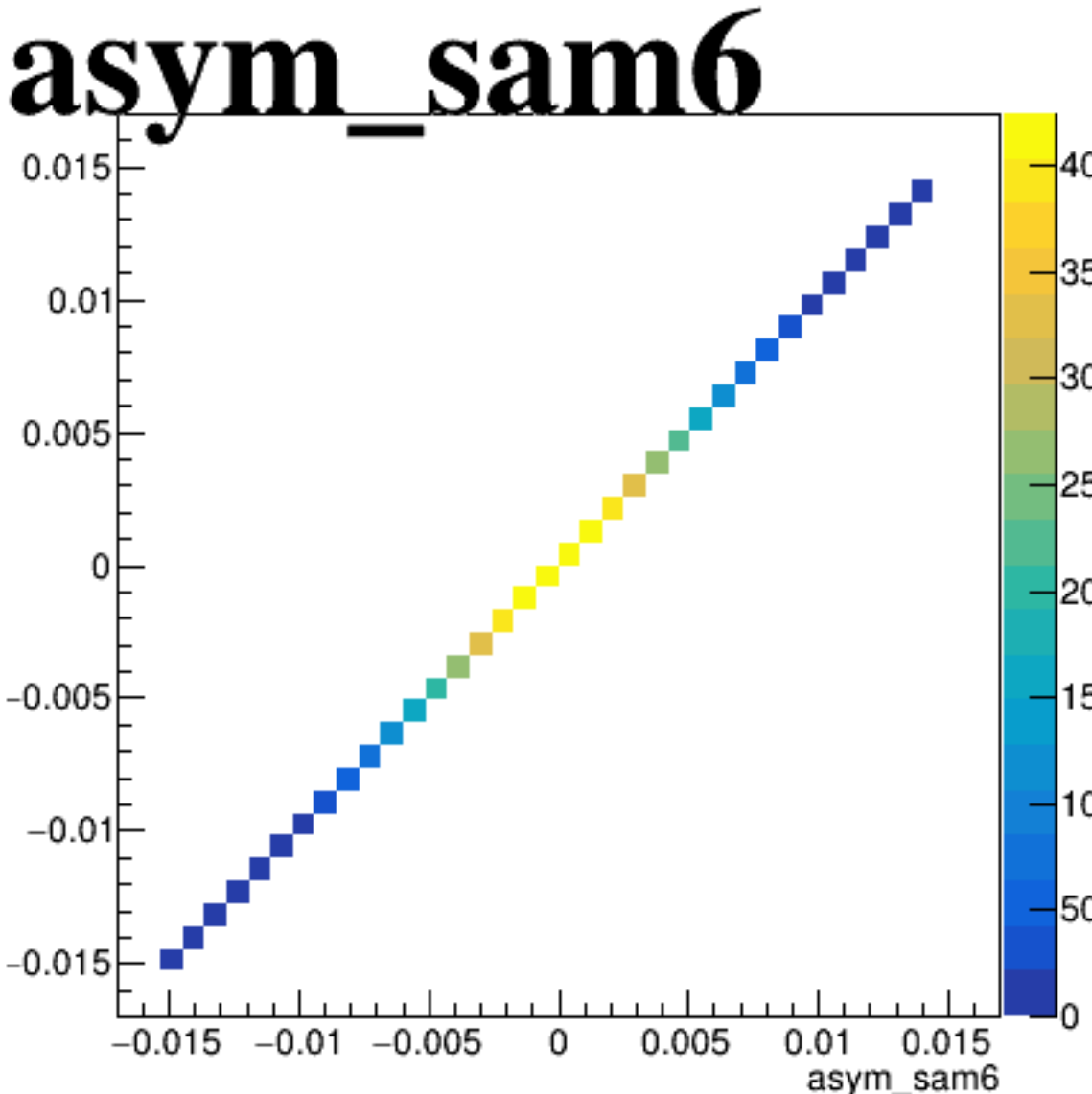
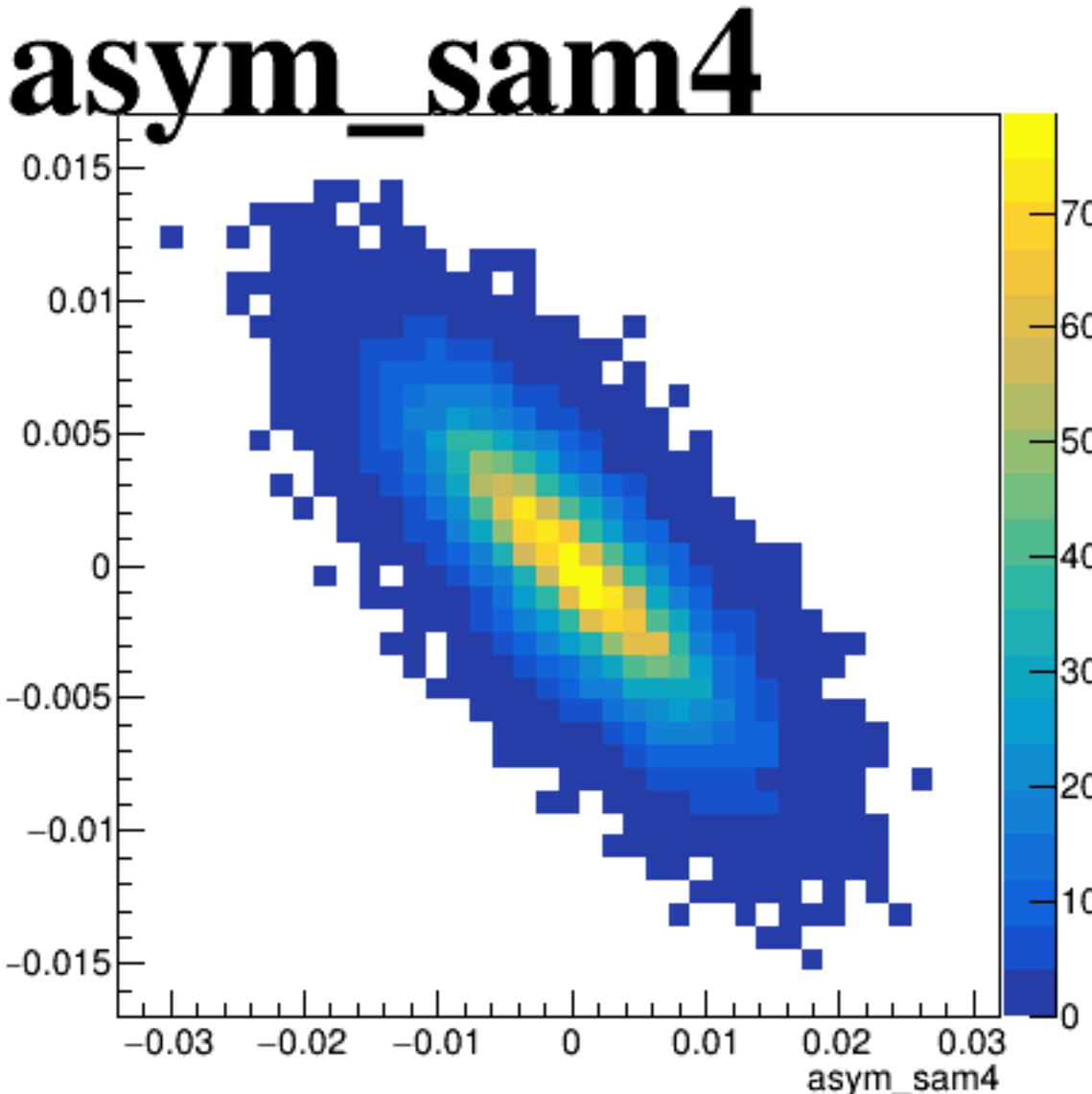
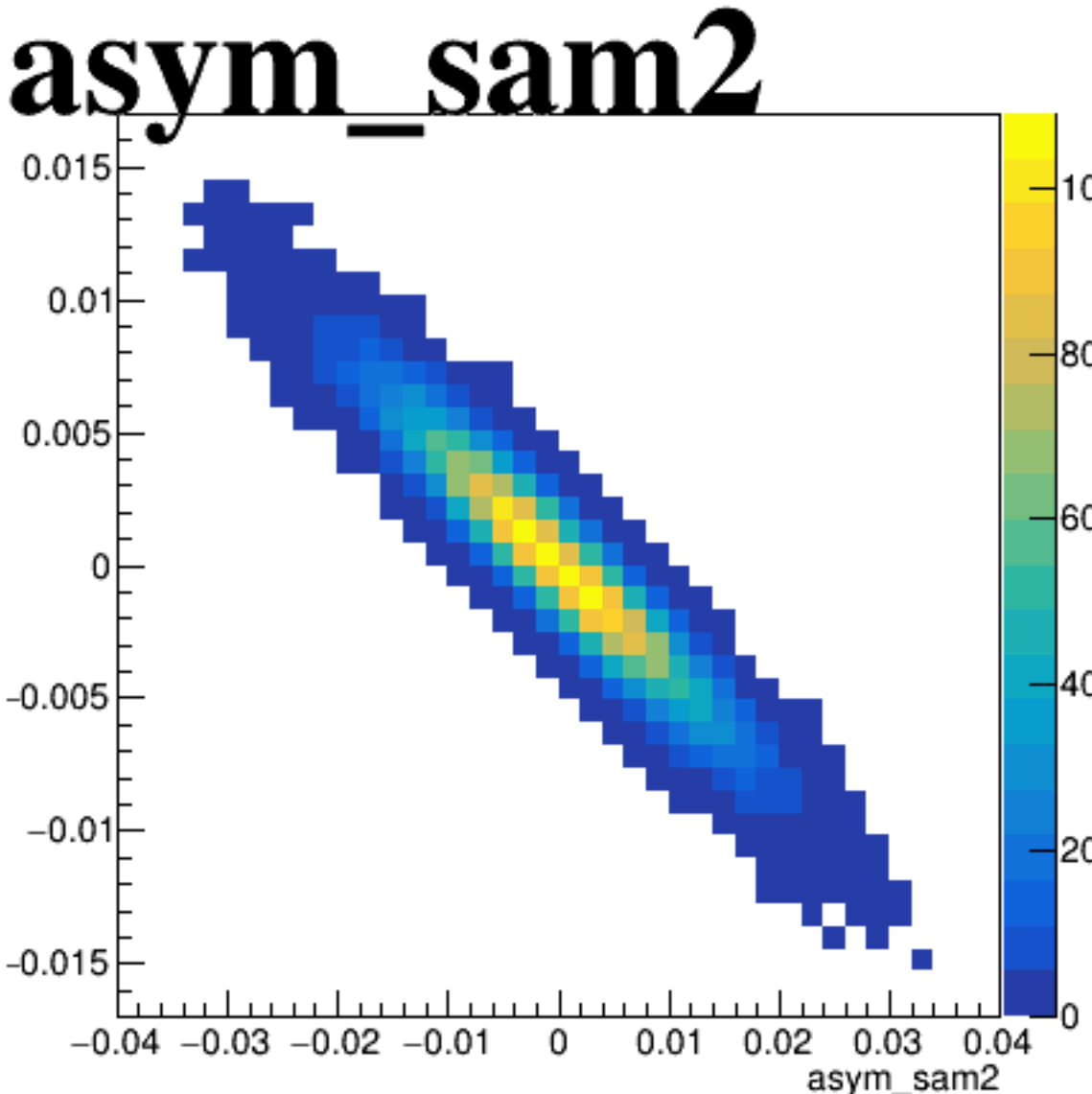
asym_sam2



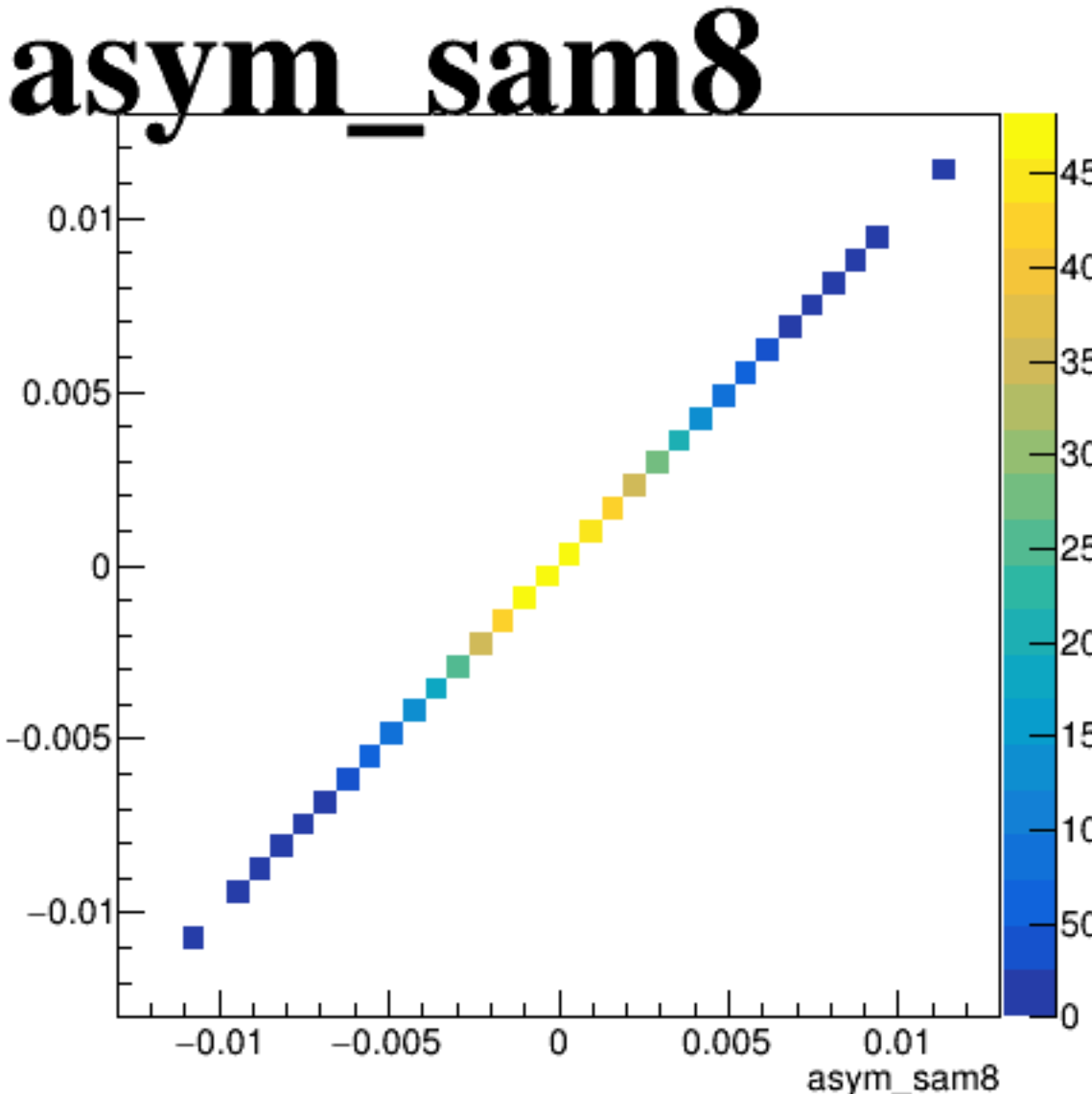
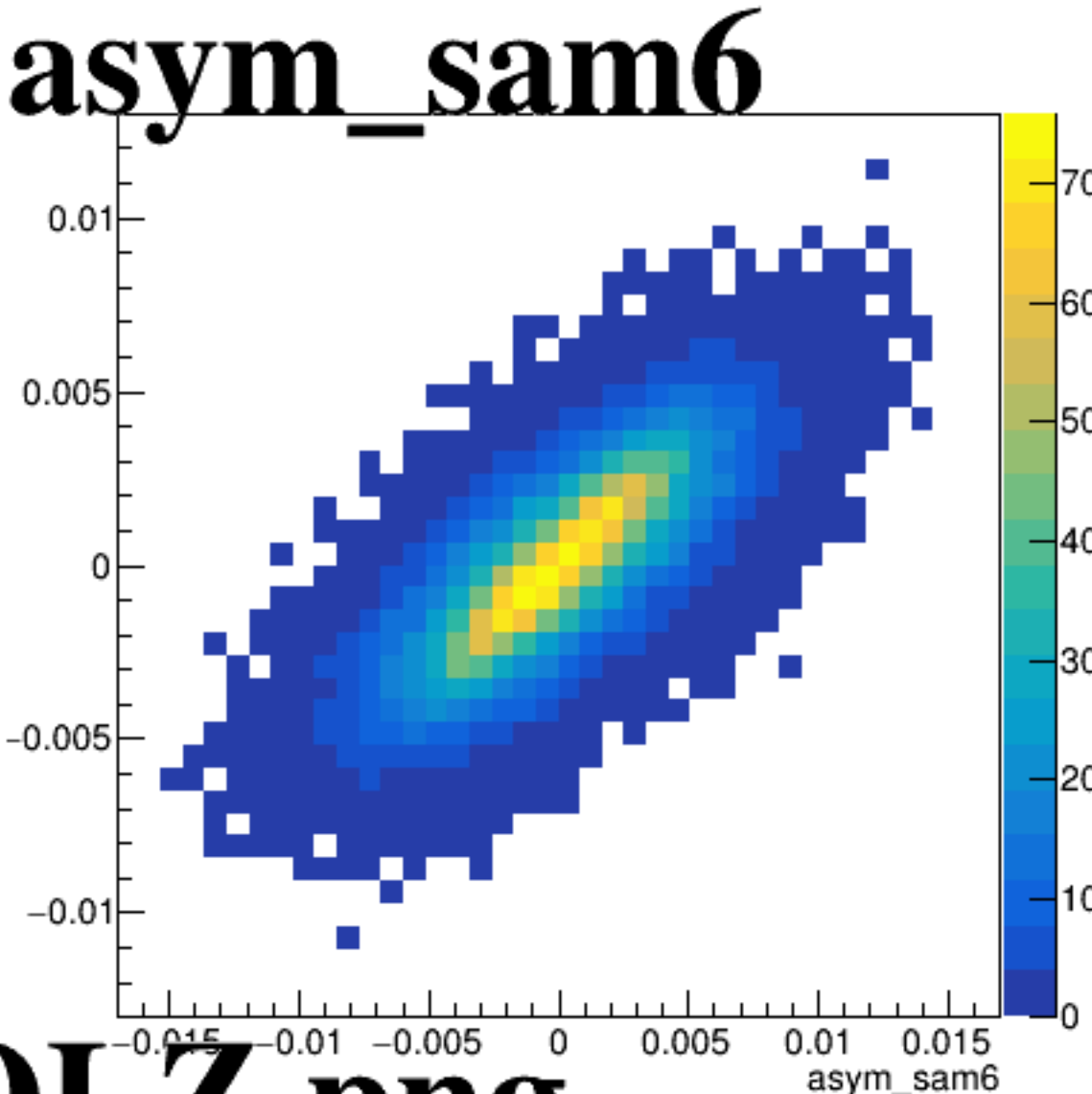
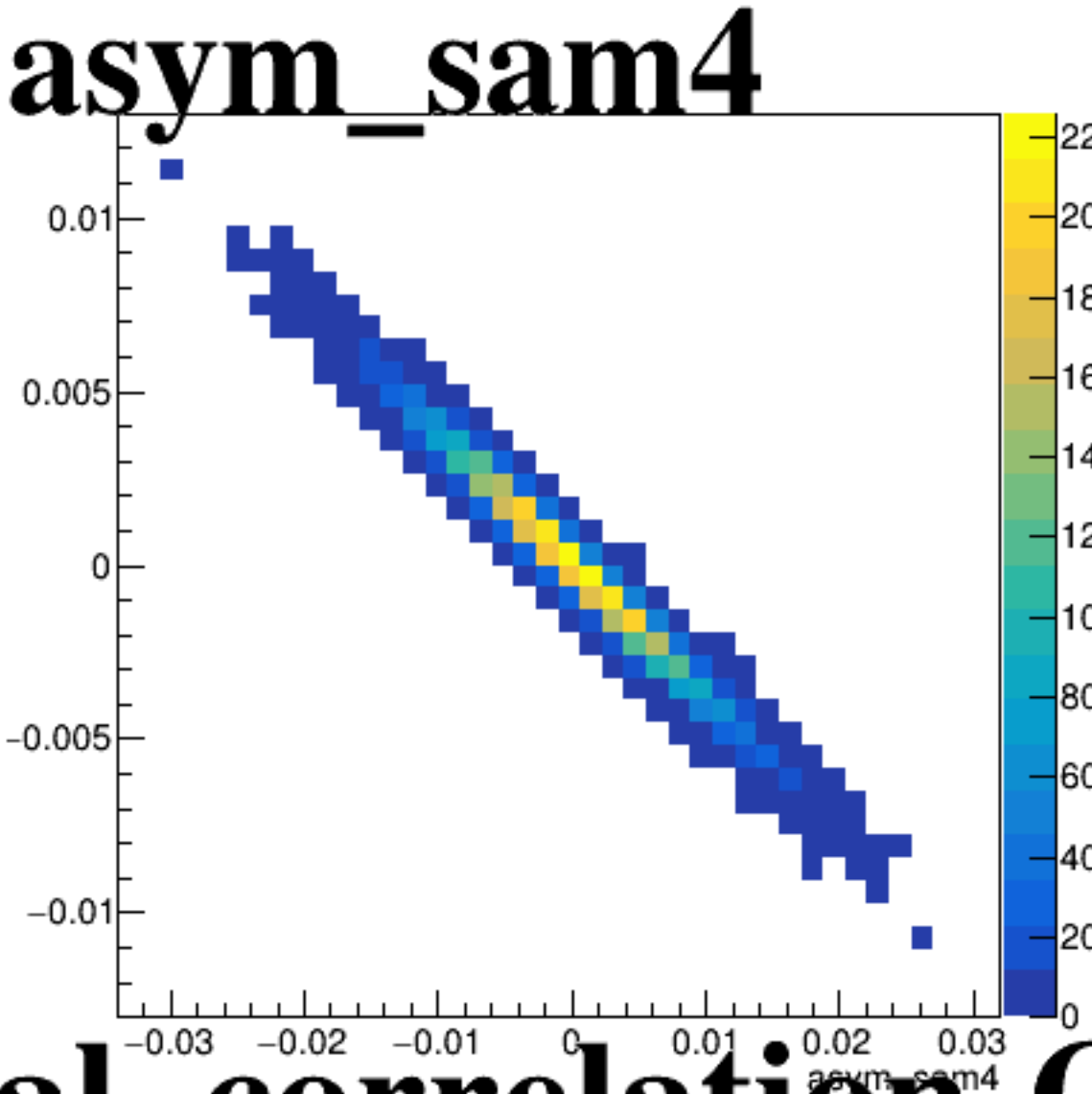
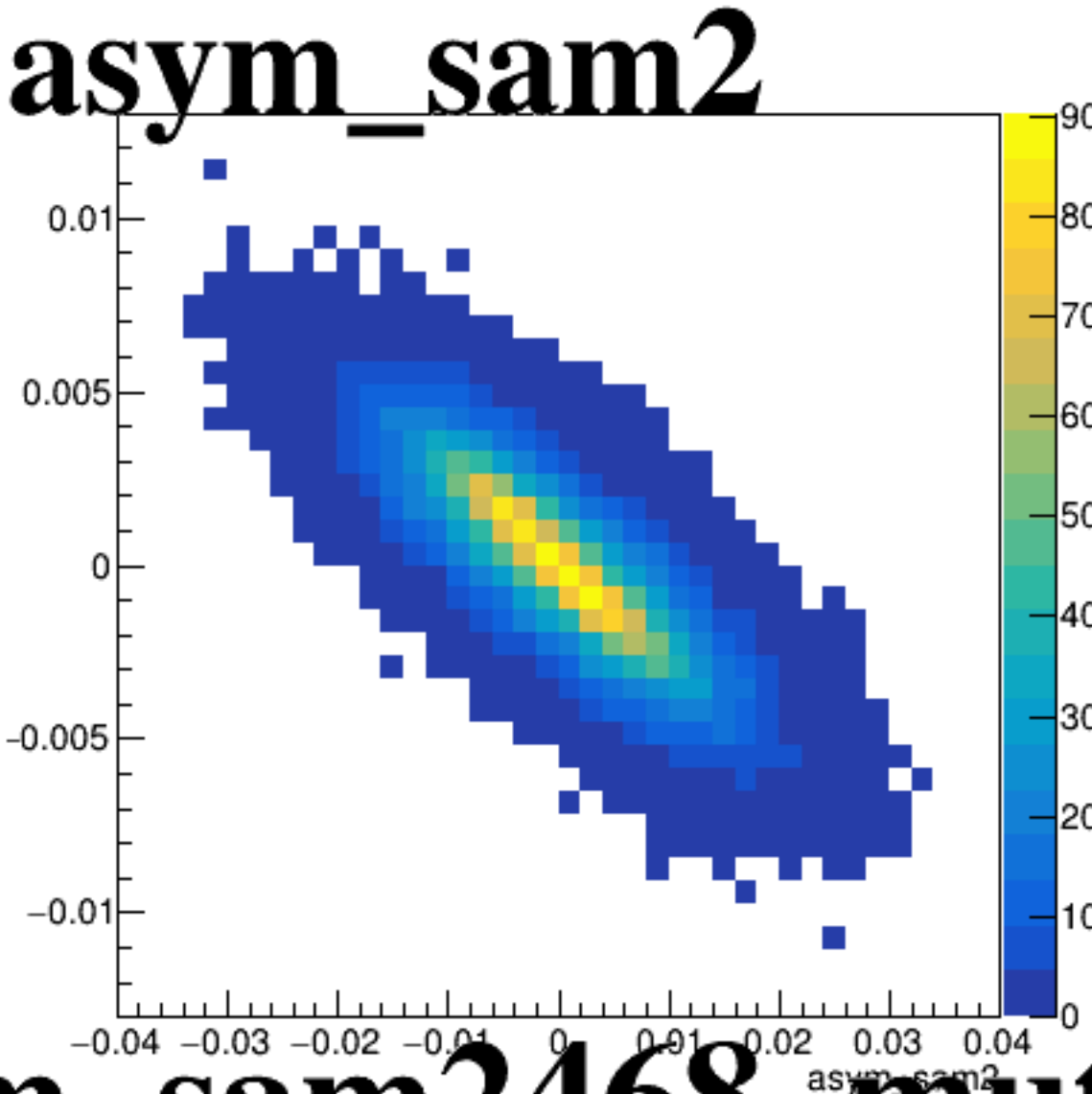
asym_sam4



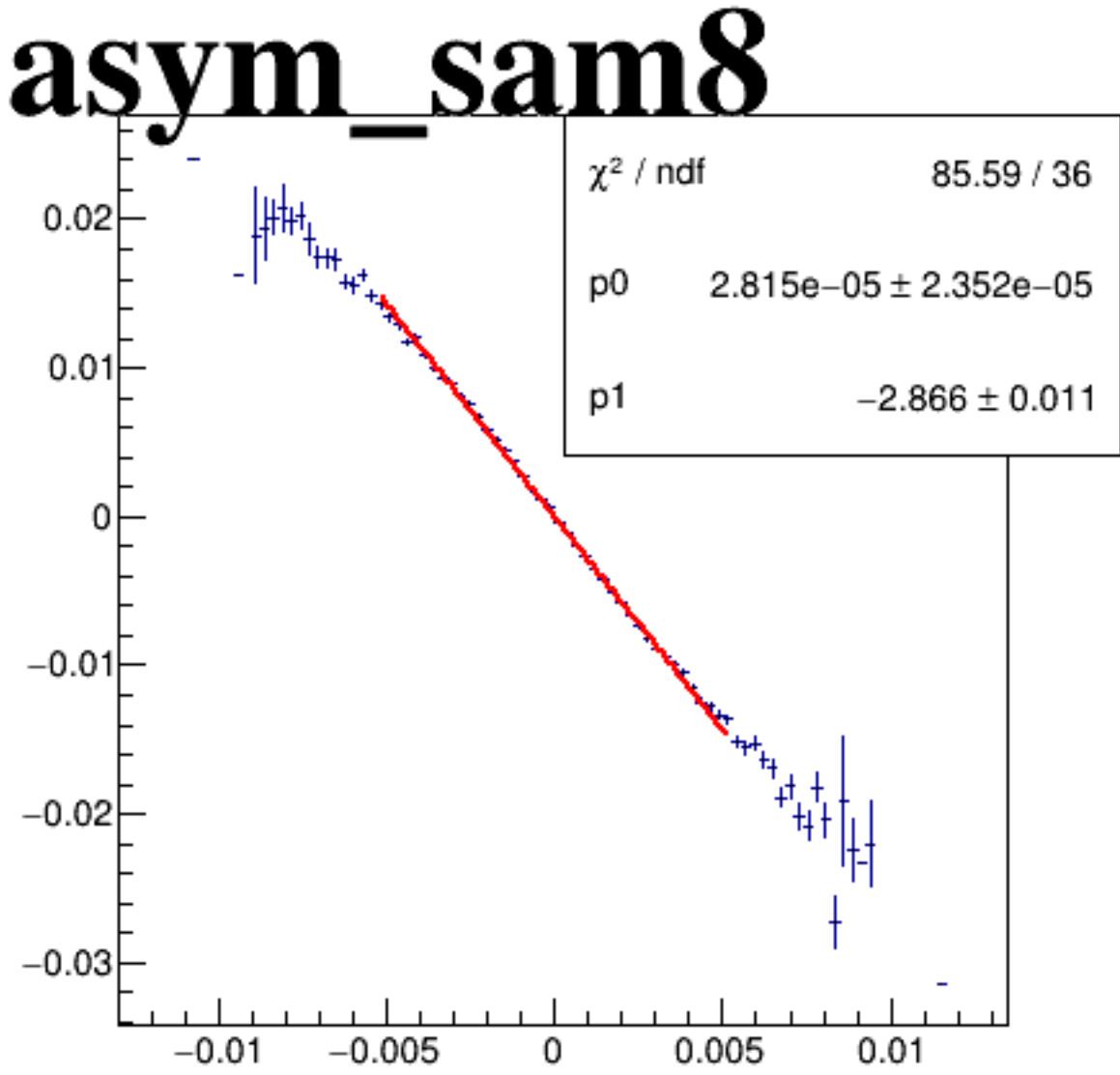
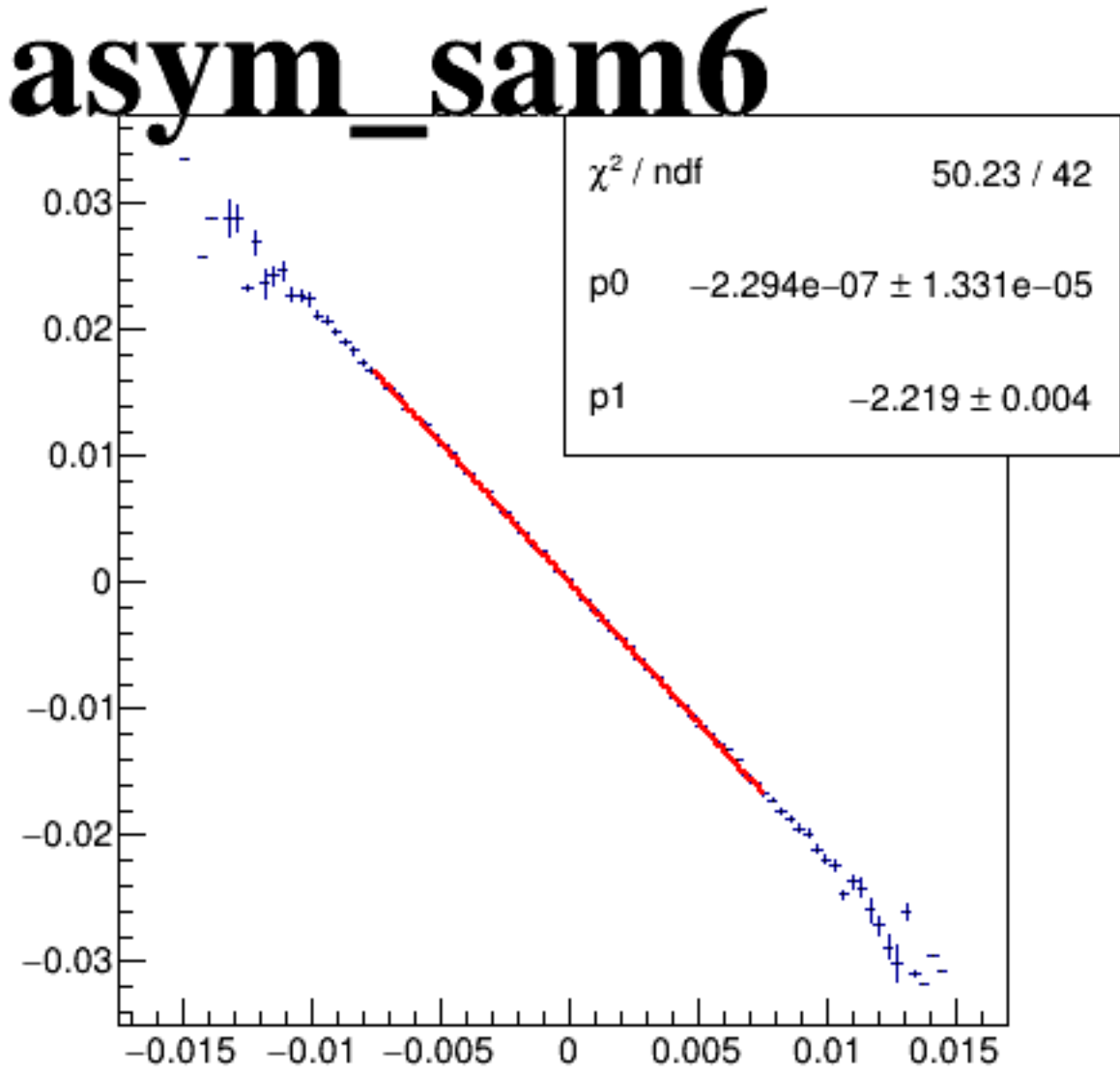
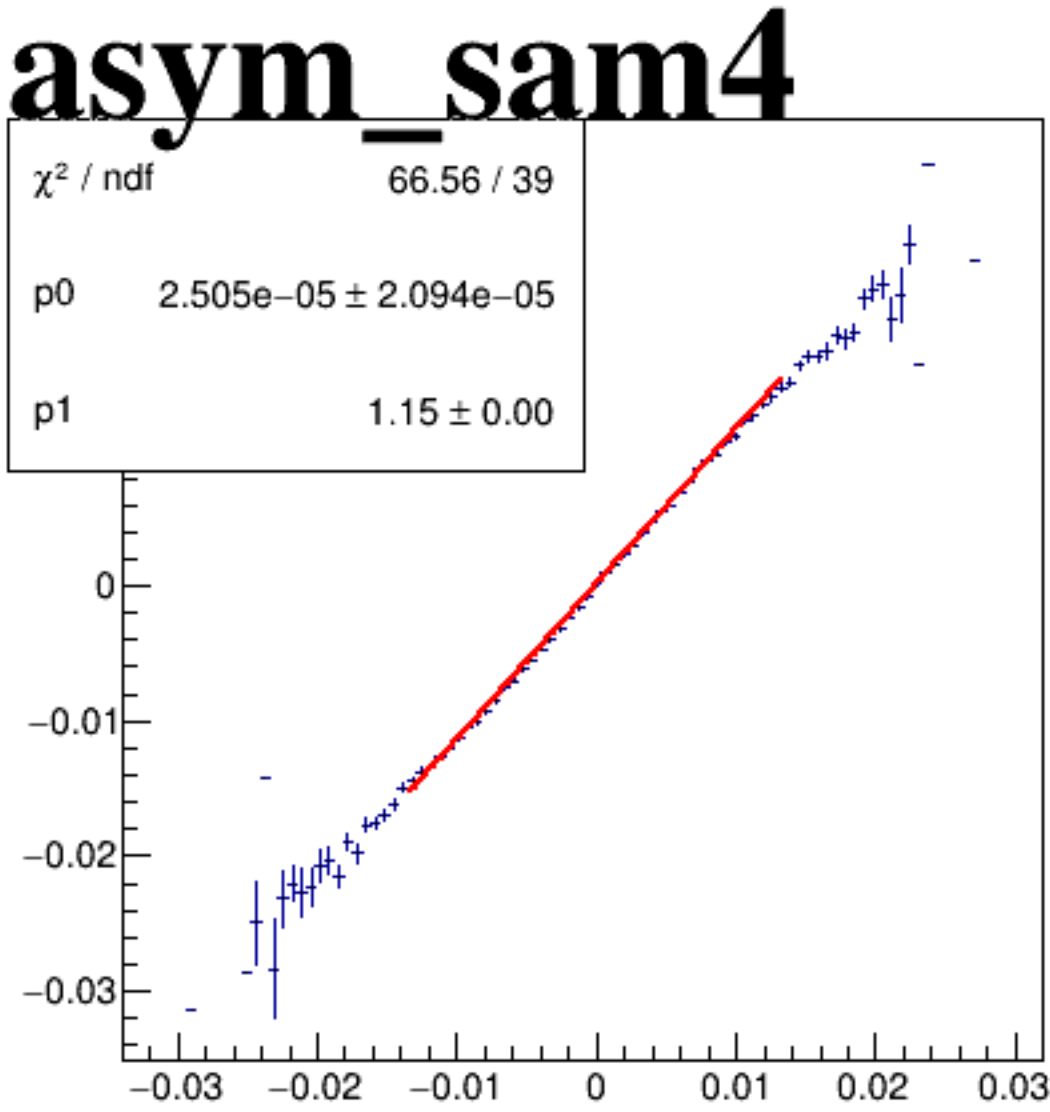
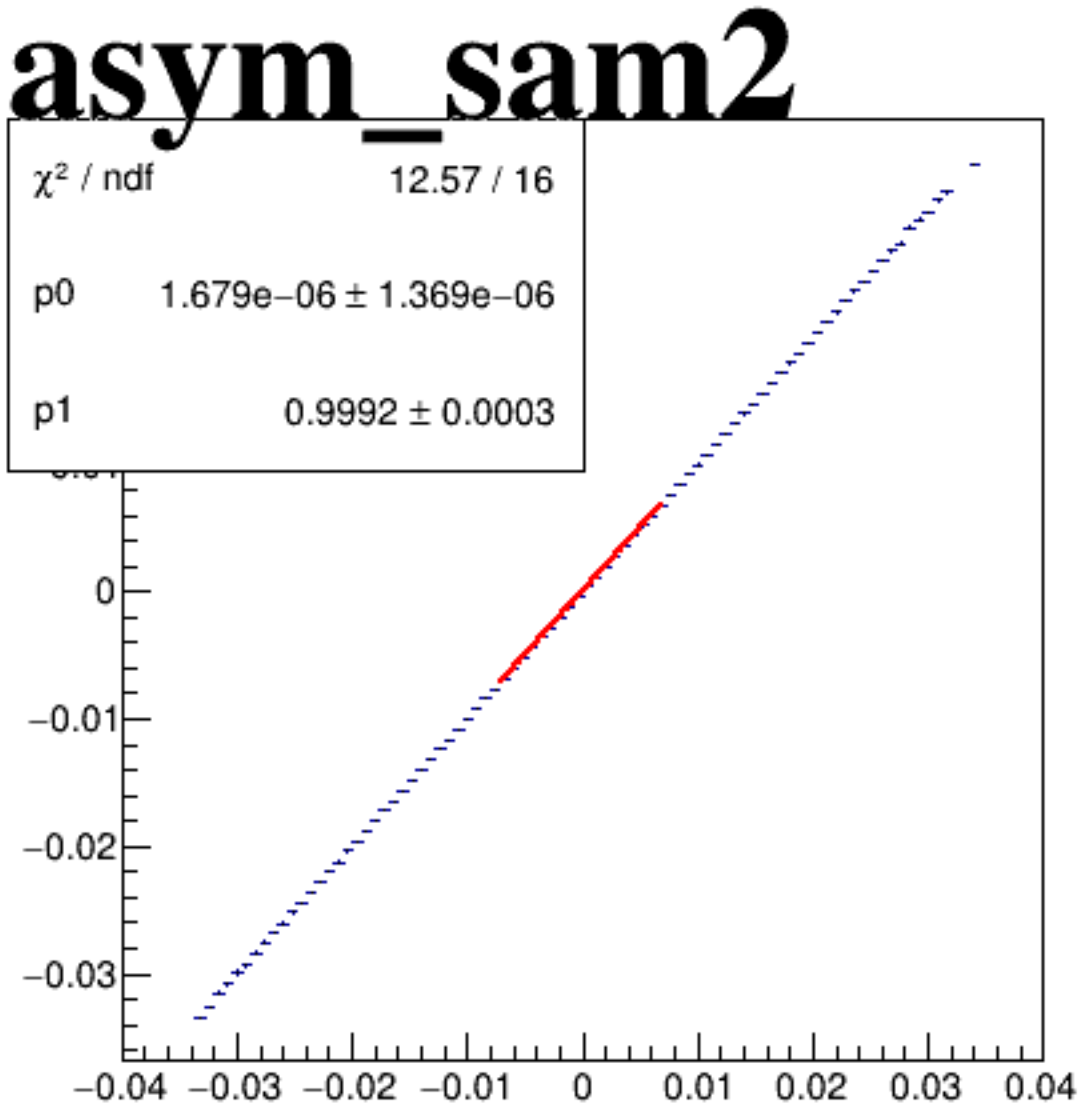
asym_sam6



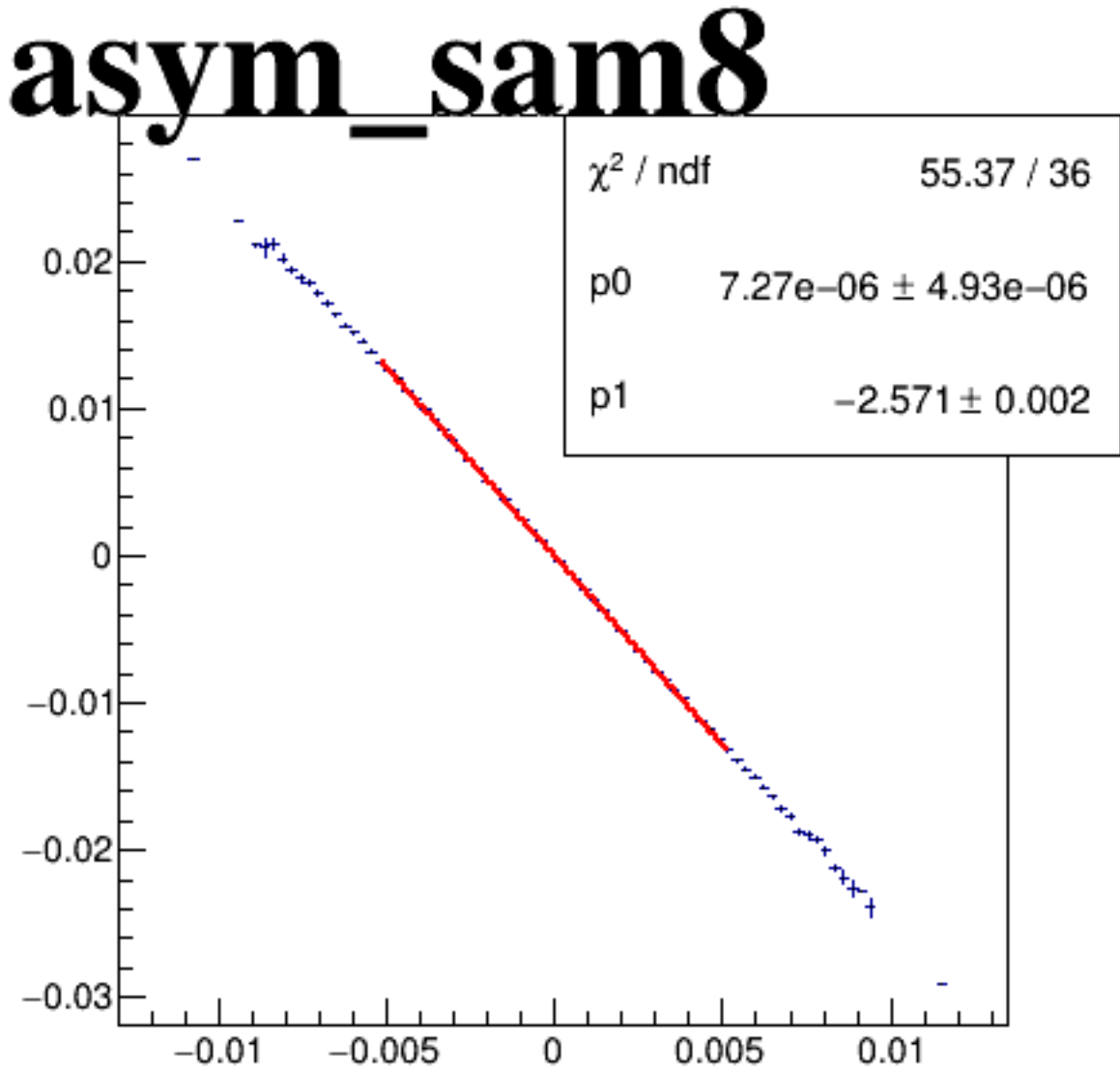
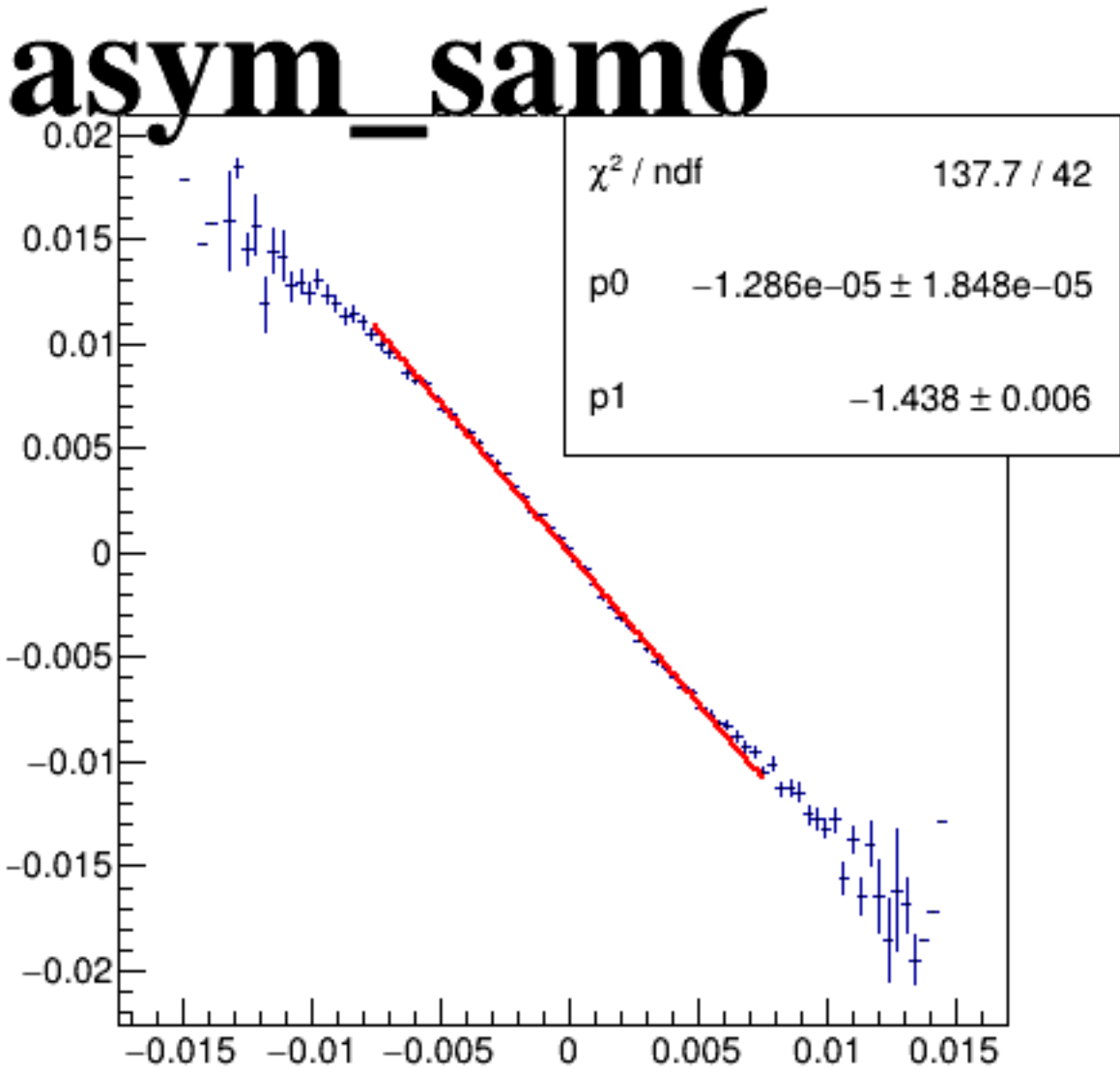
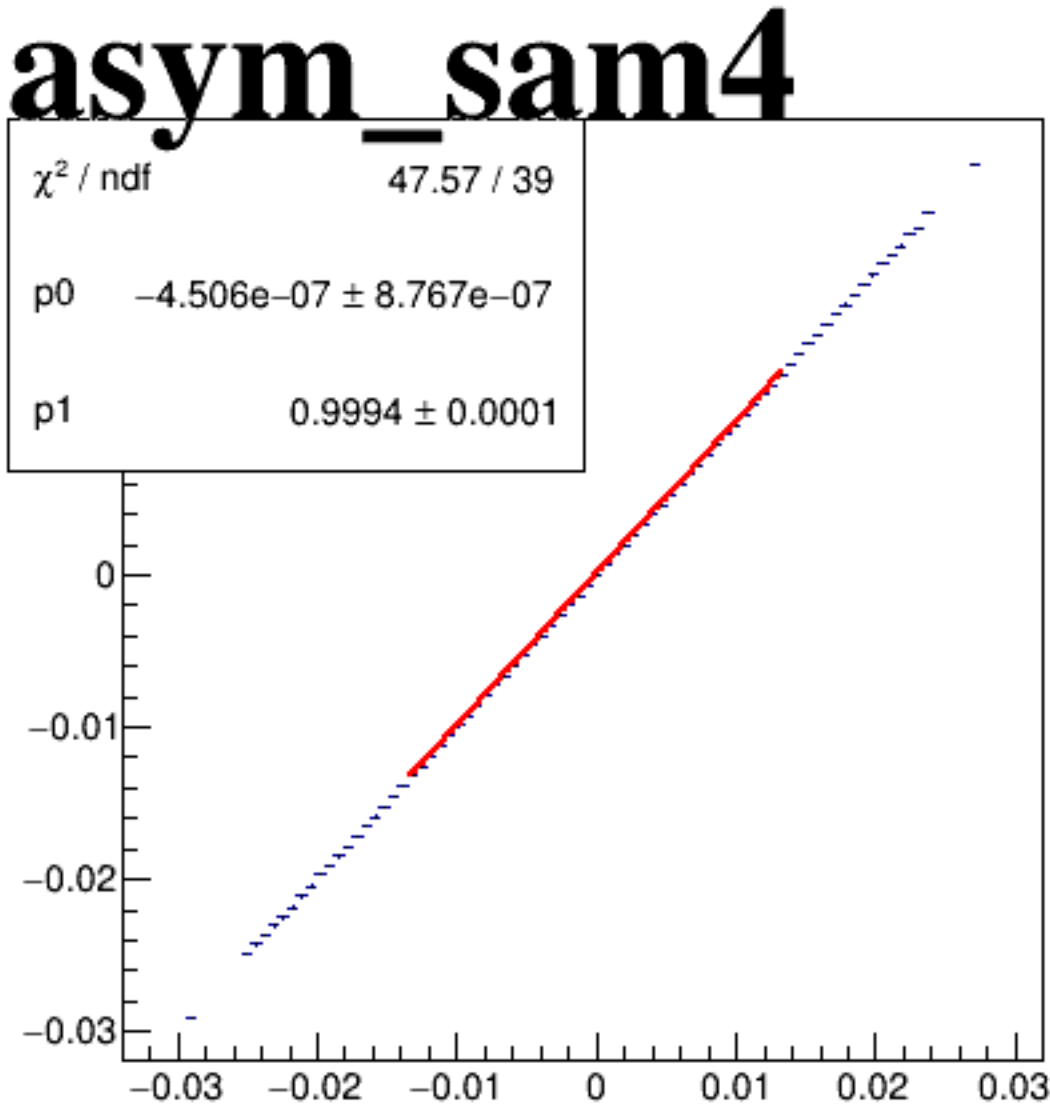
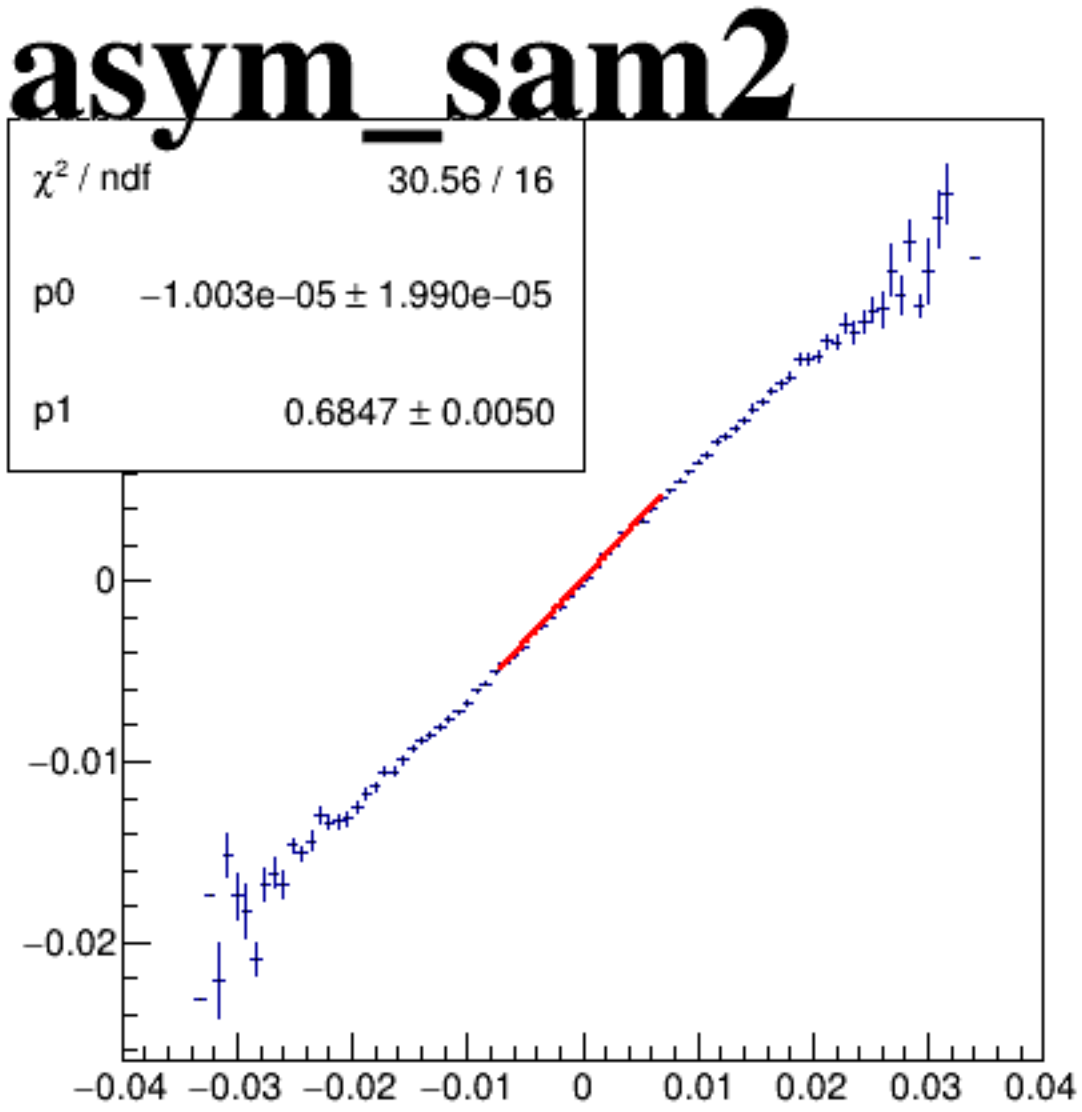
asym_sam8



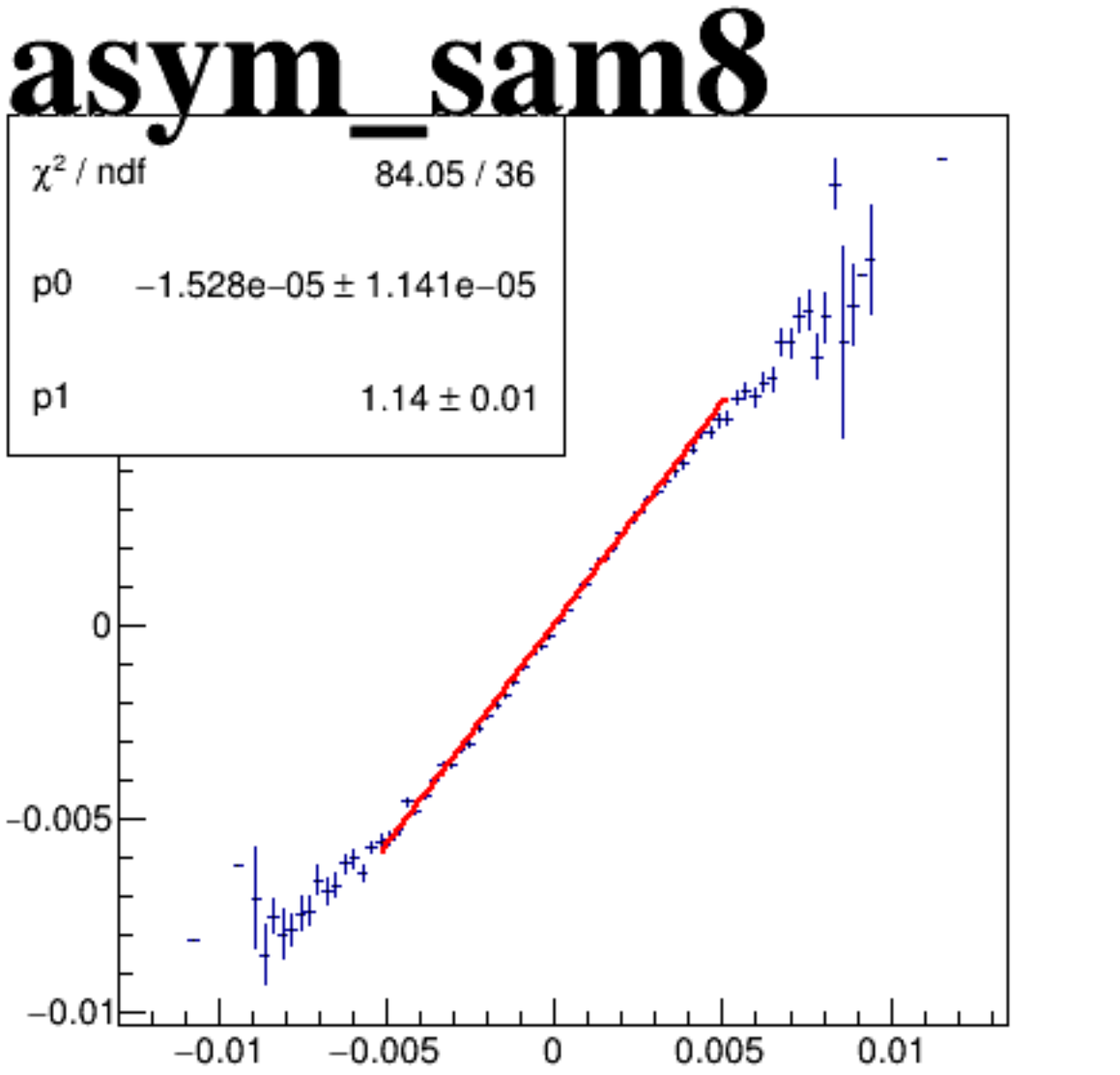
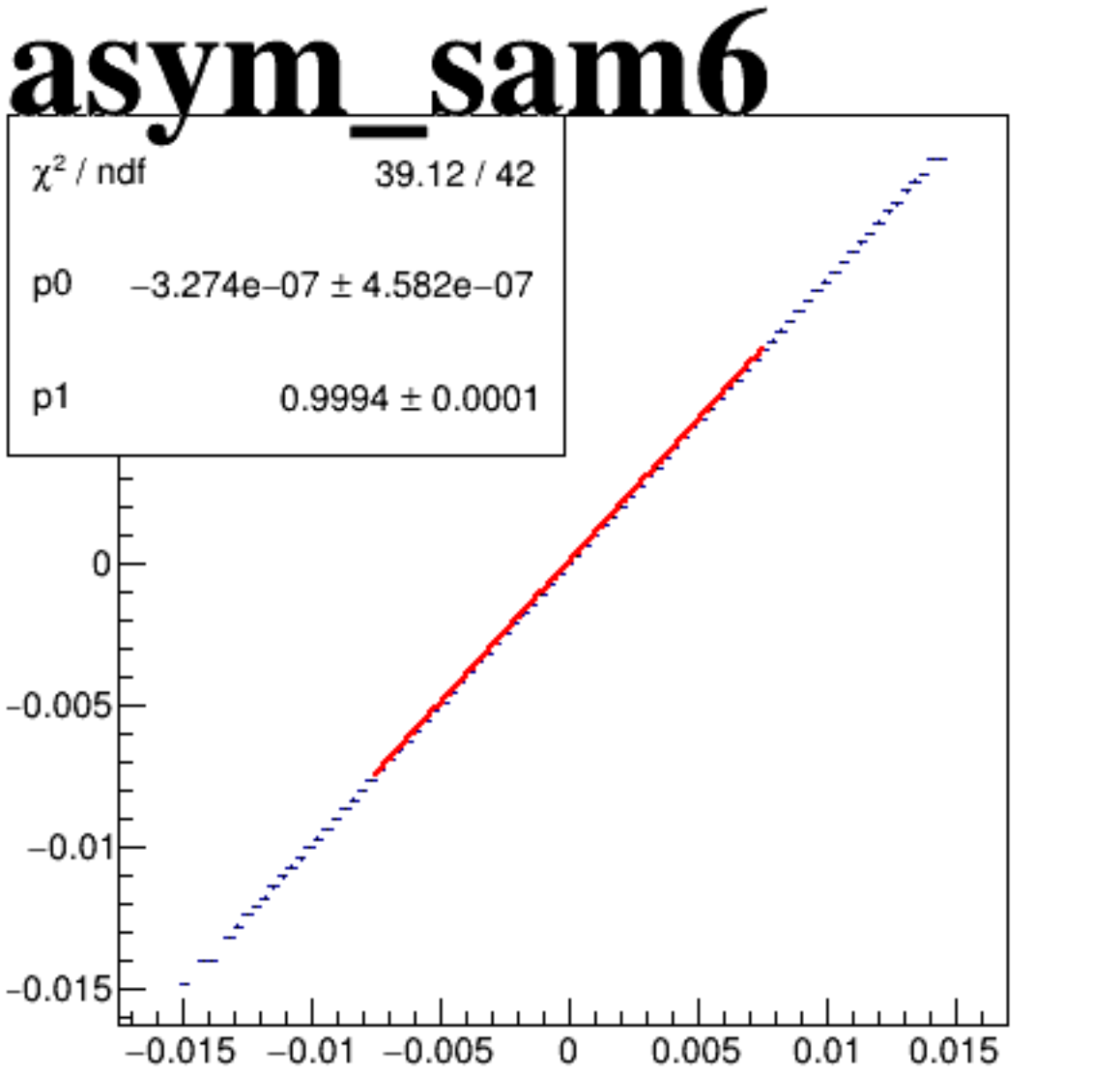
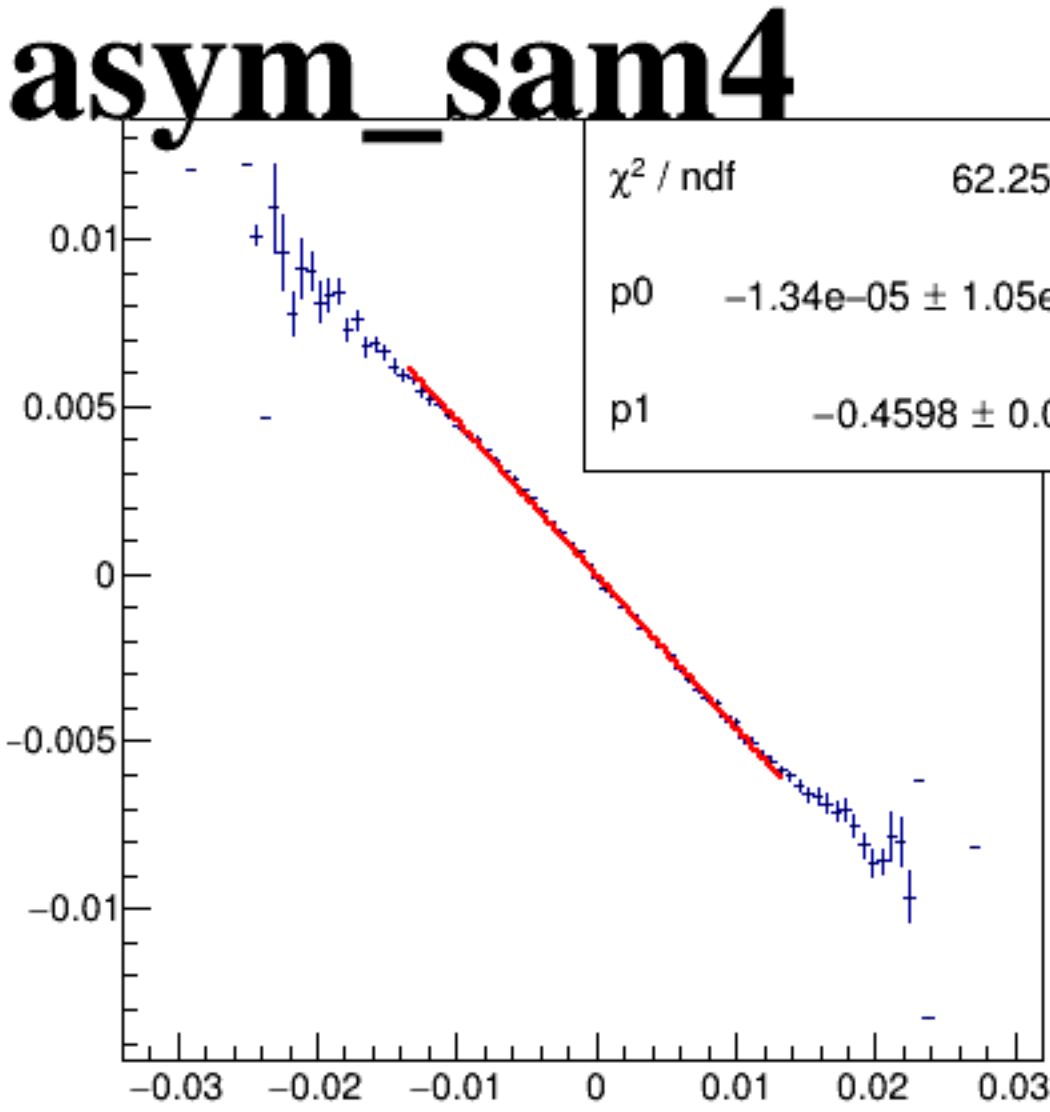
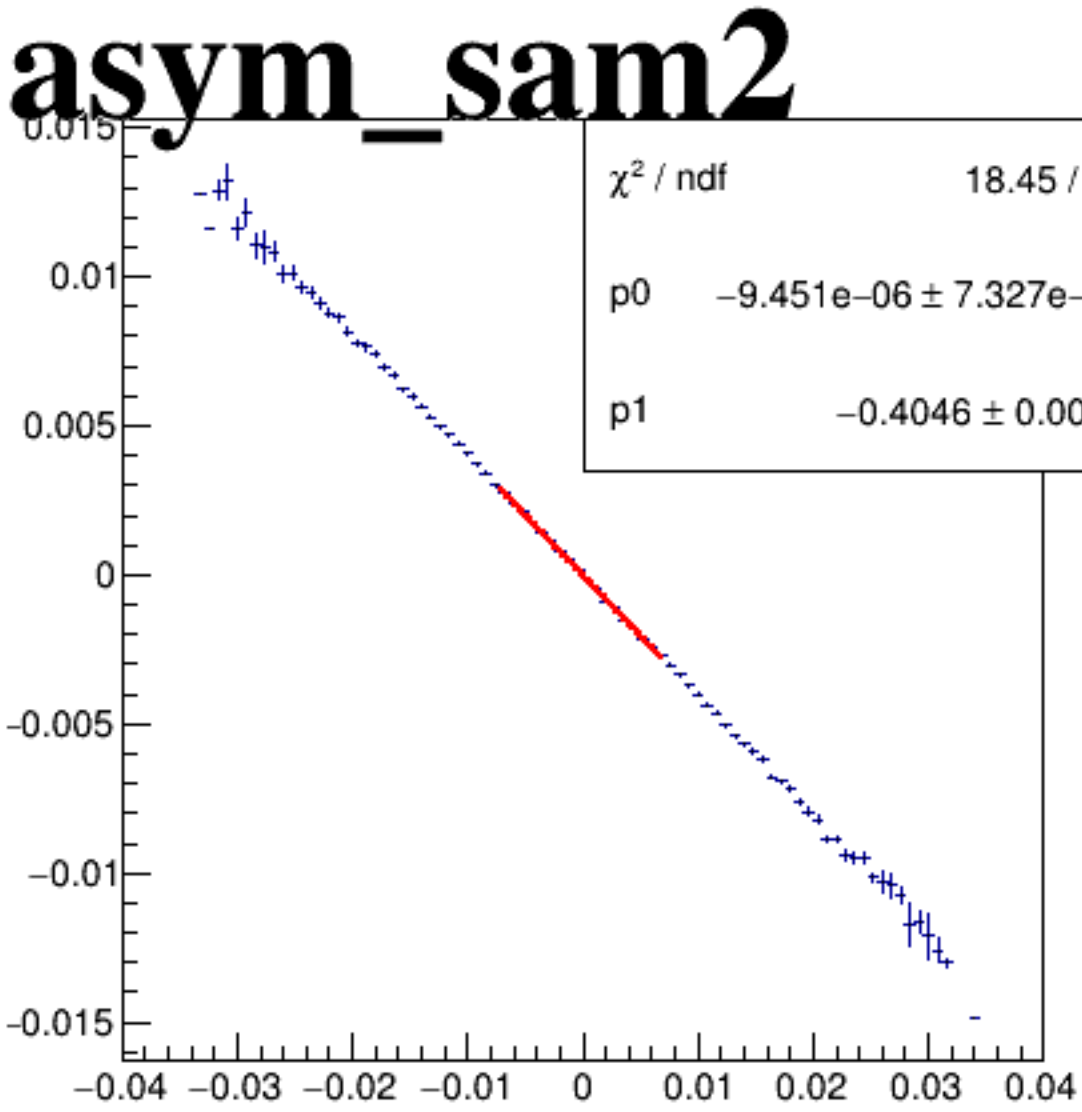
asym_sam2



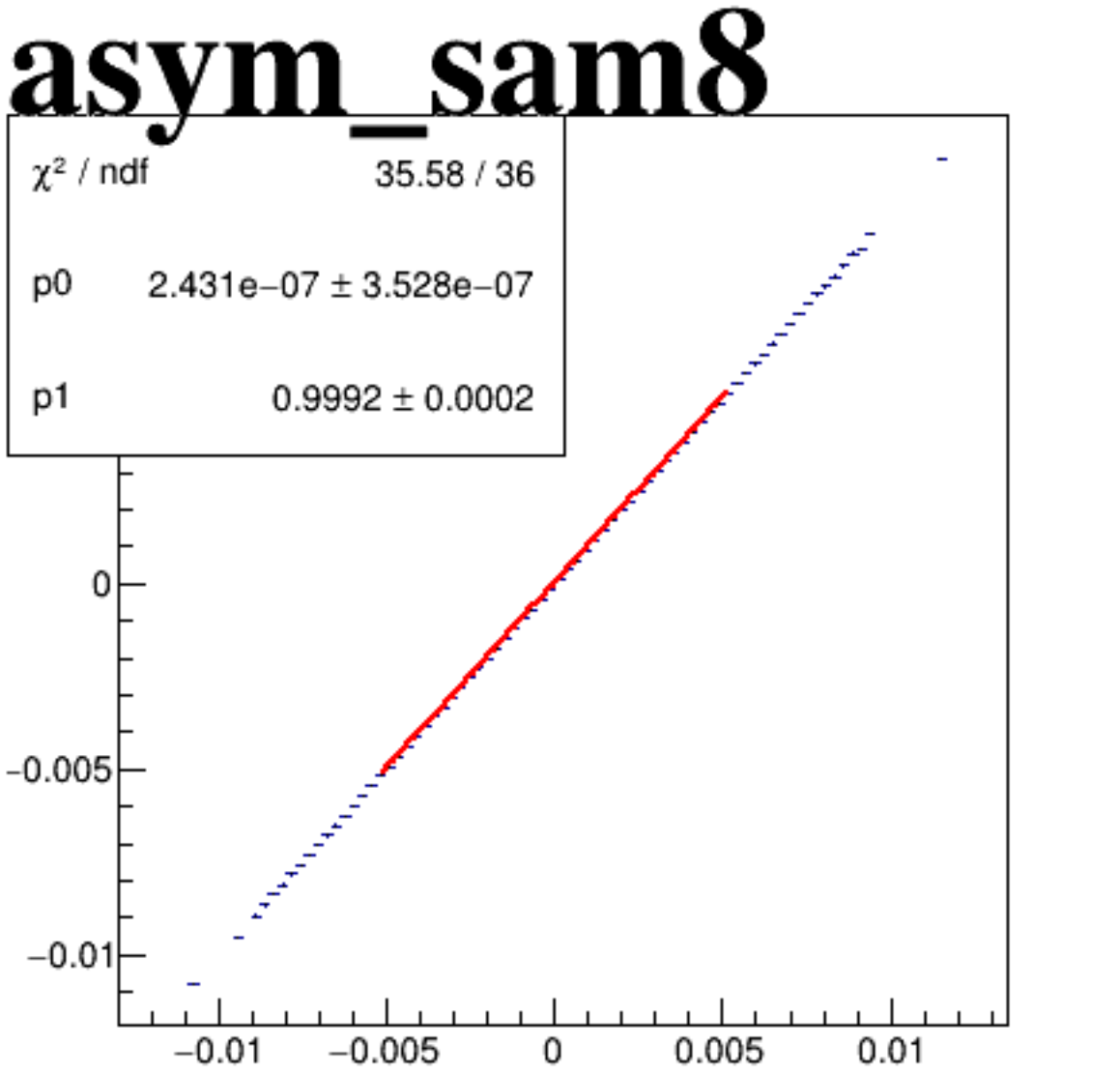
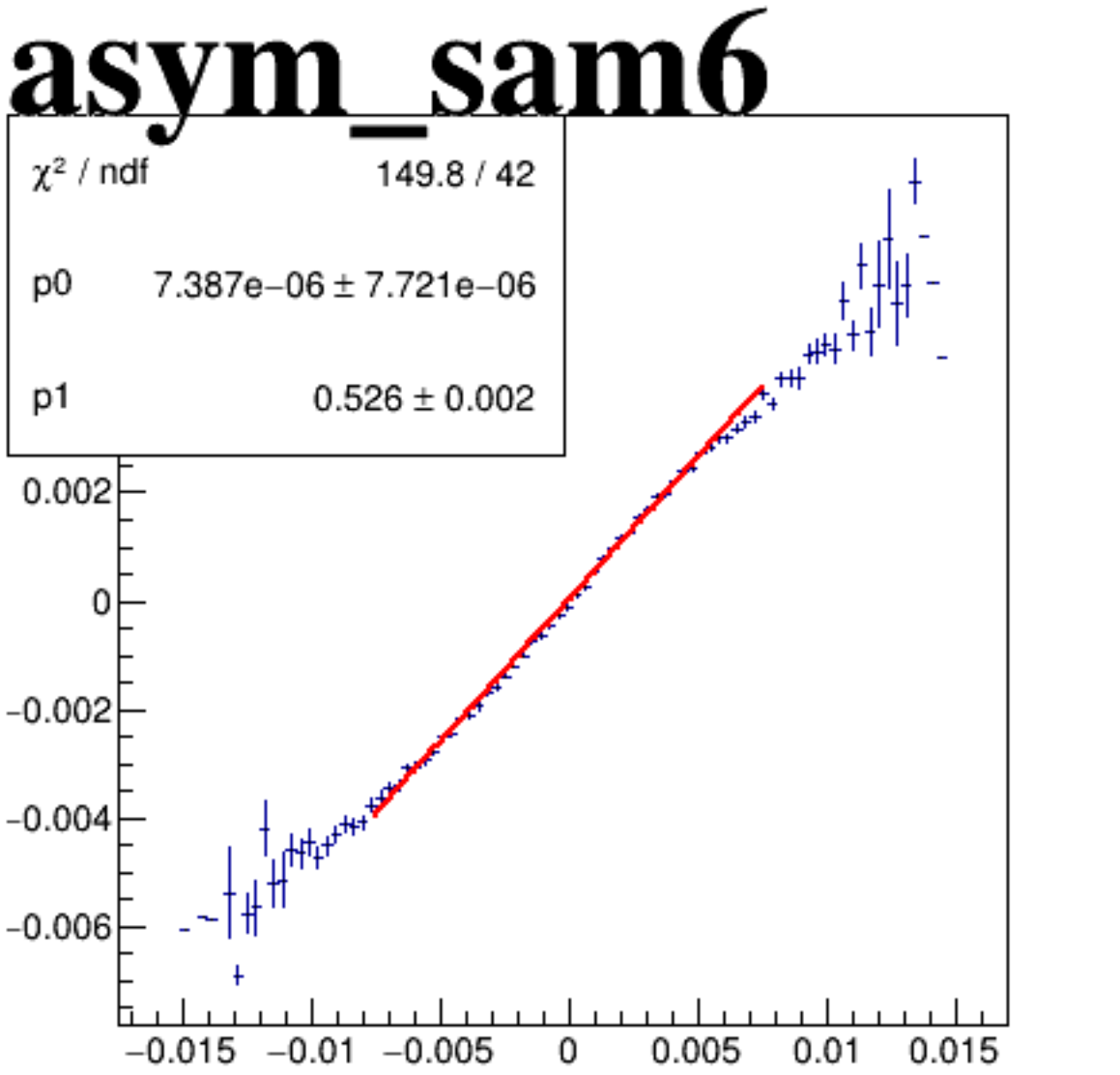
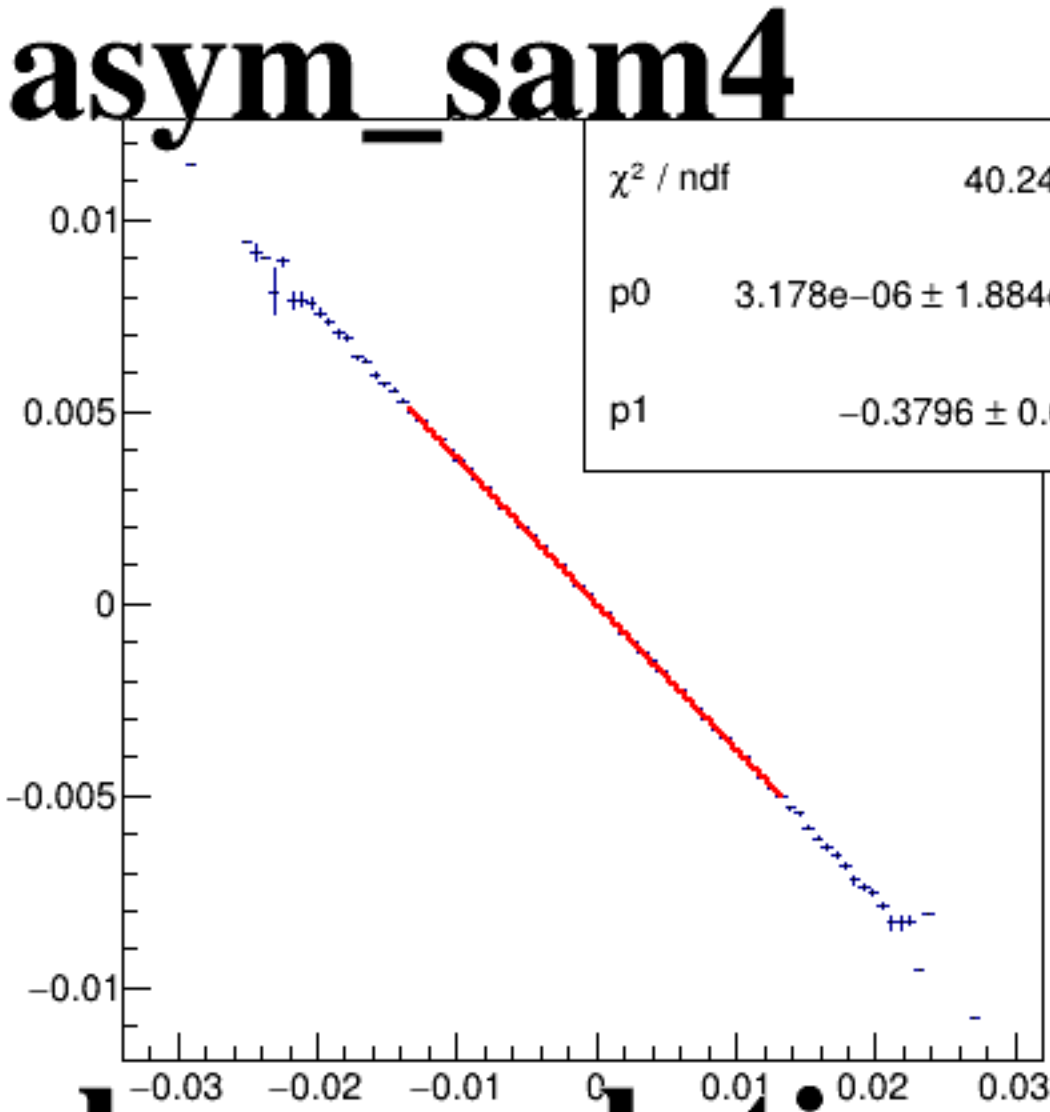
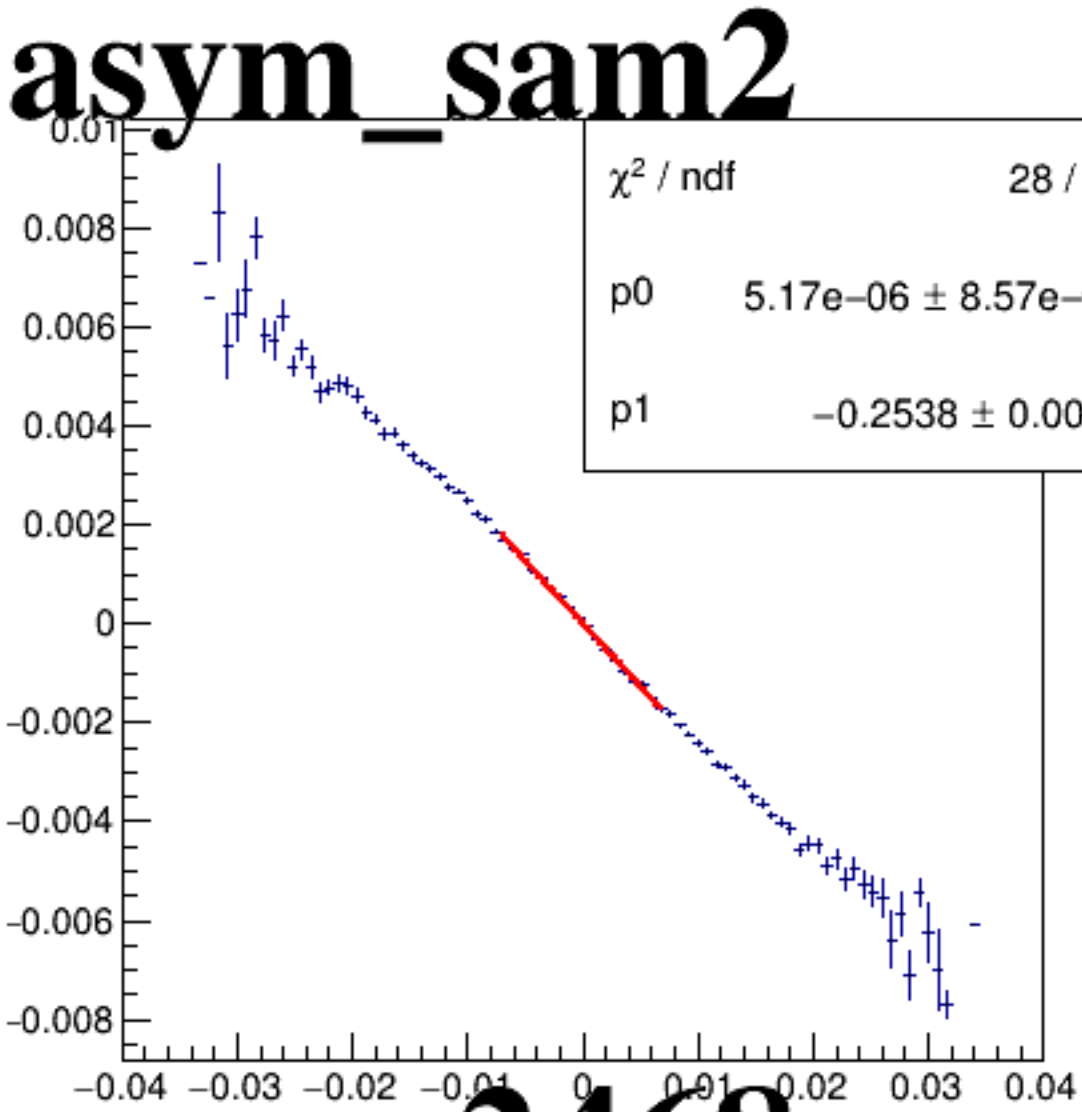
asym_sam4



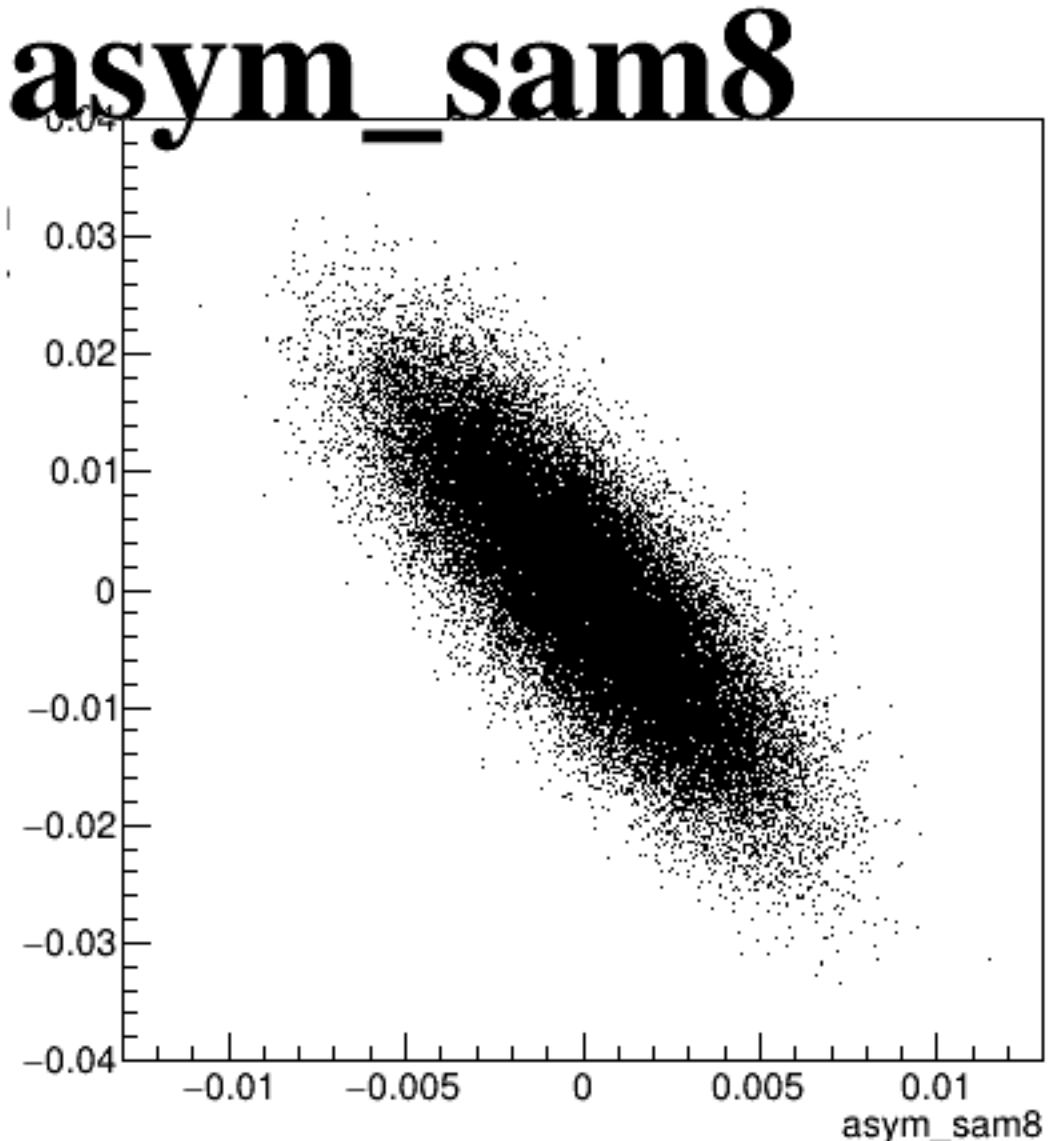
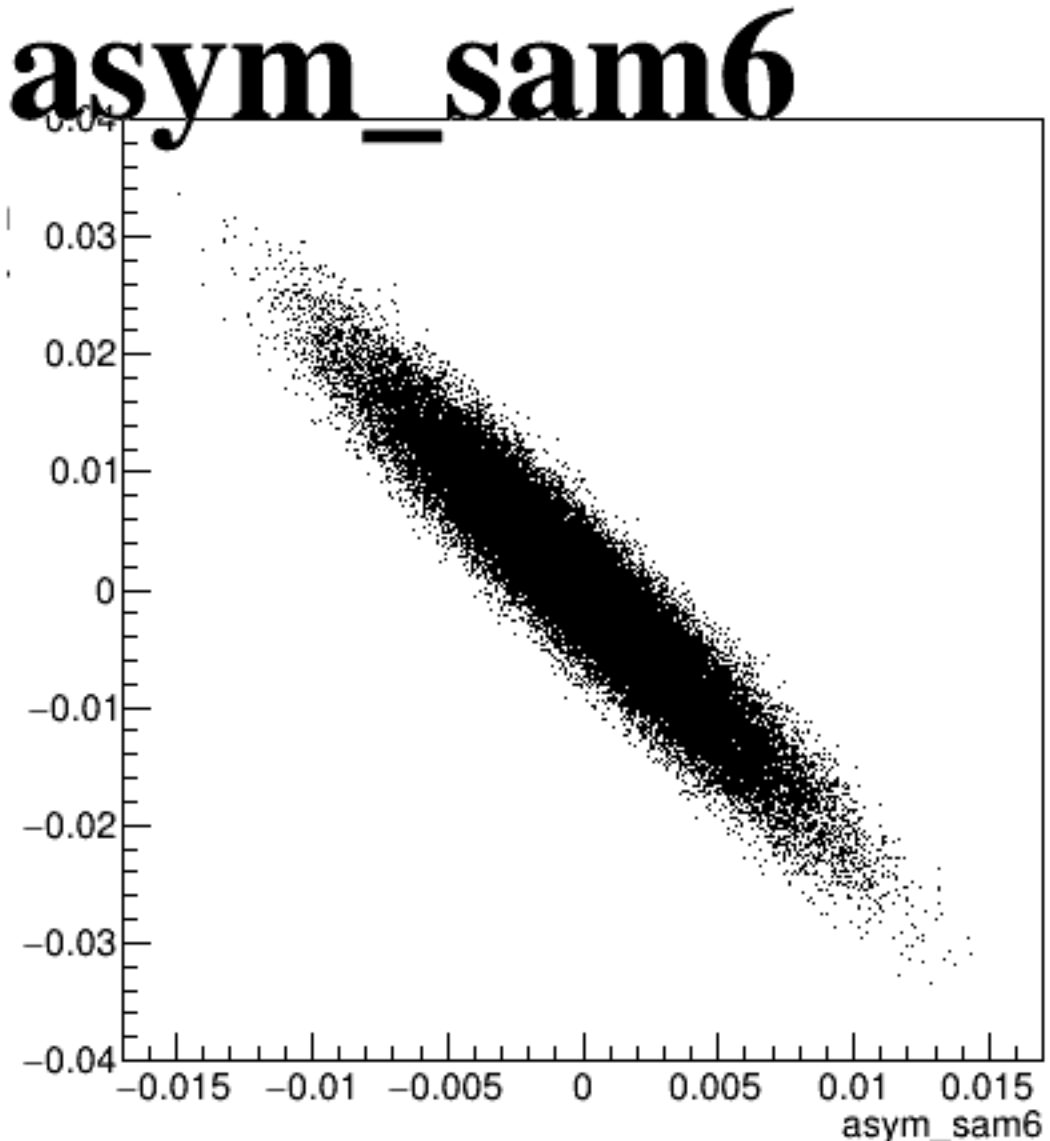
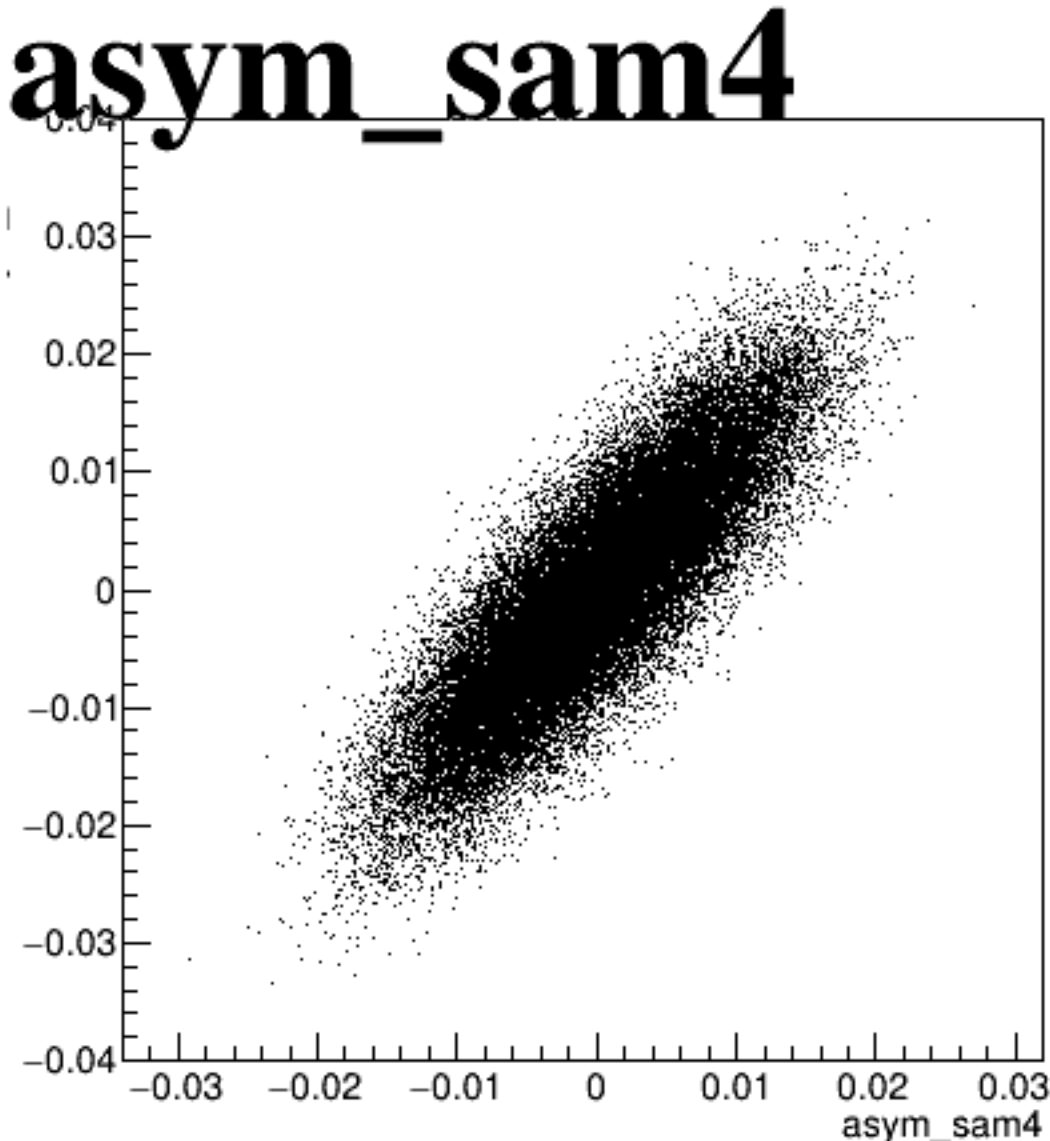
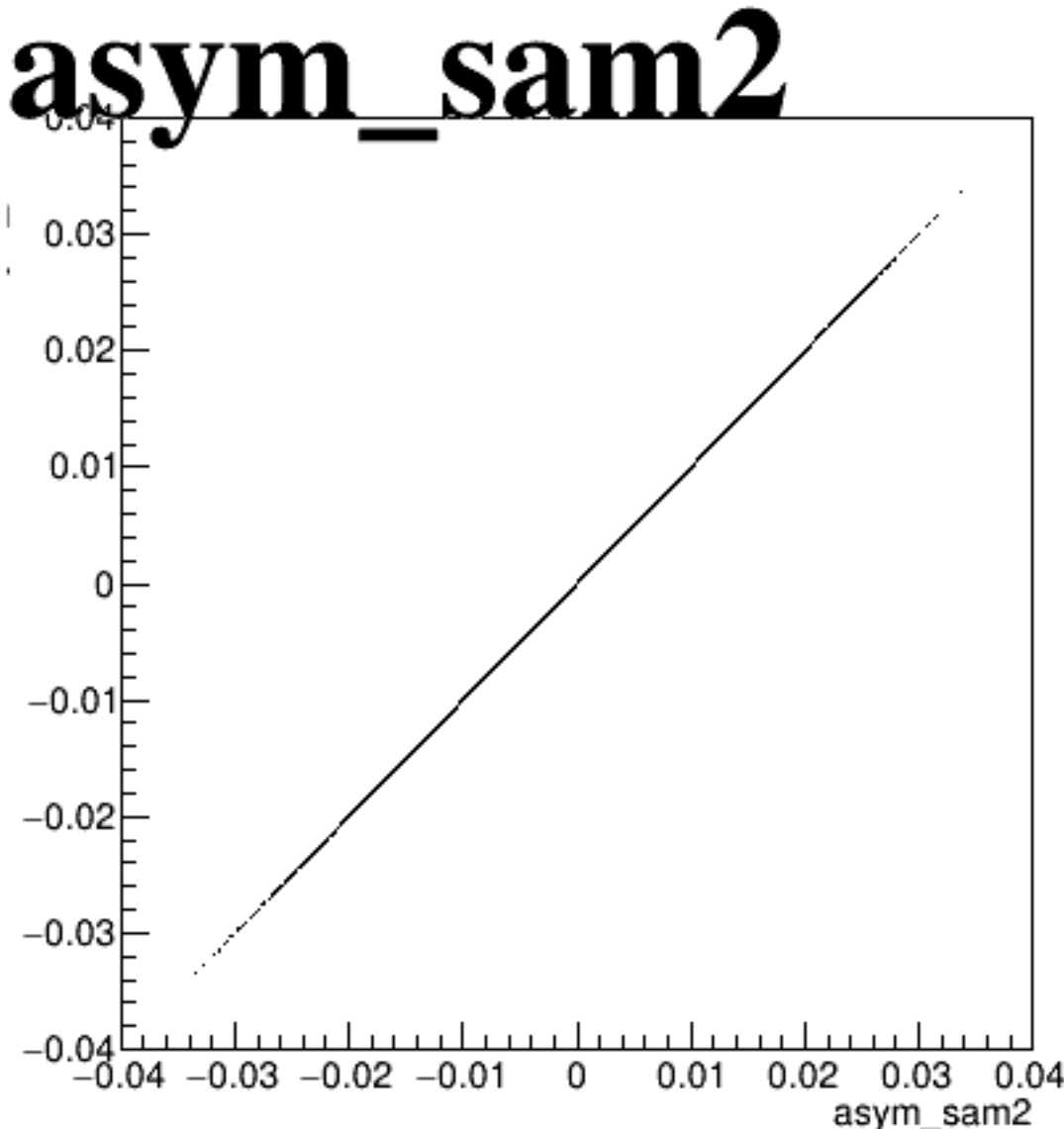
asym_sam6



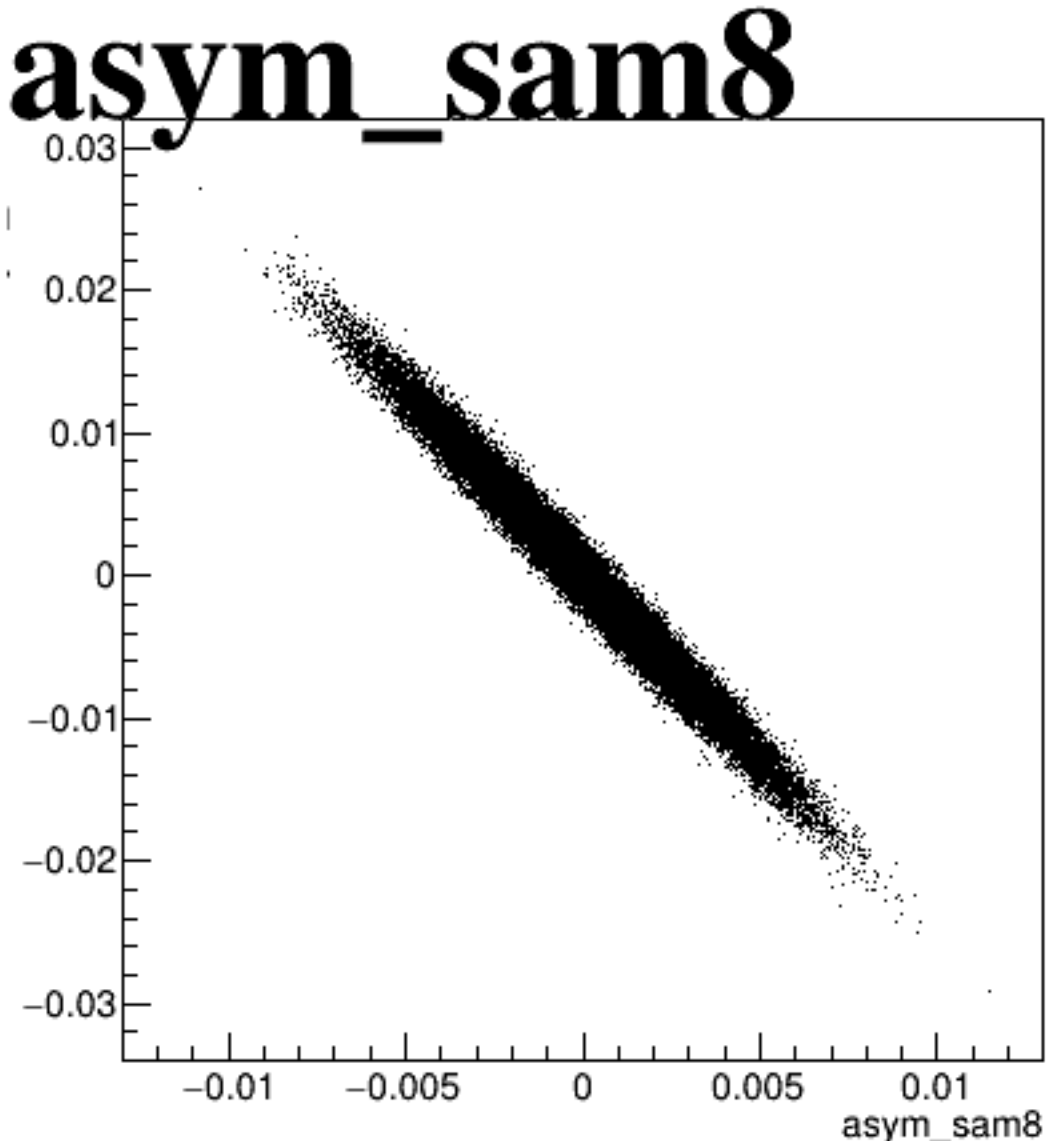
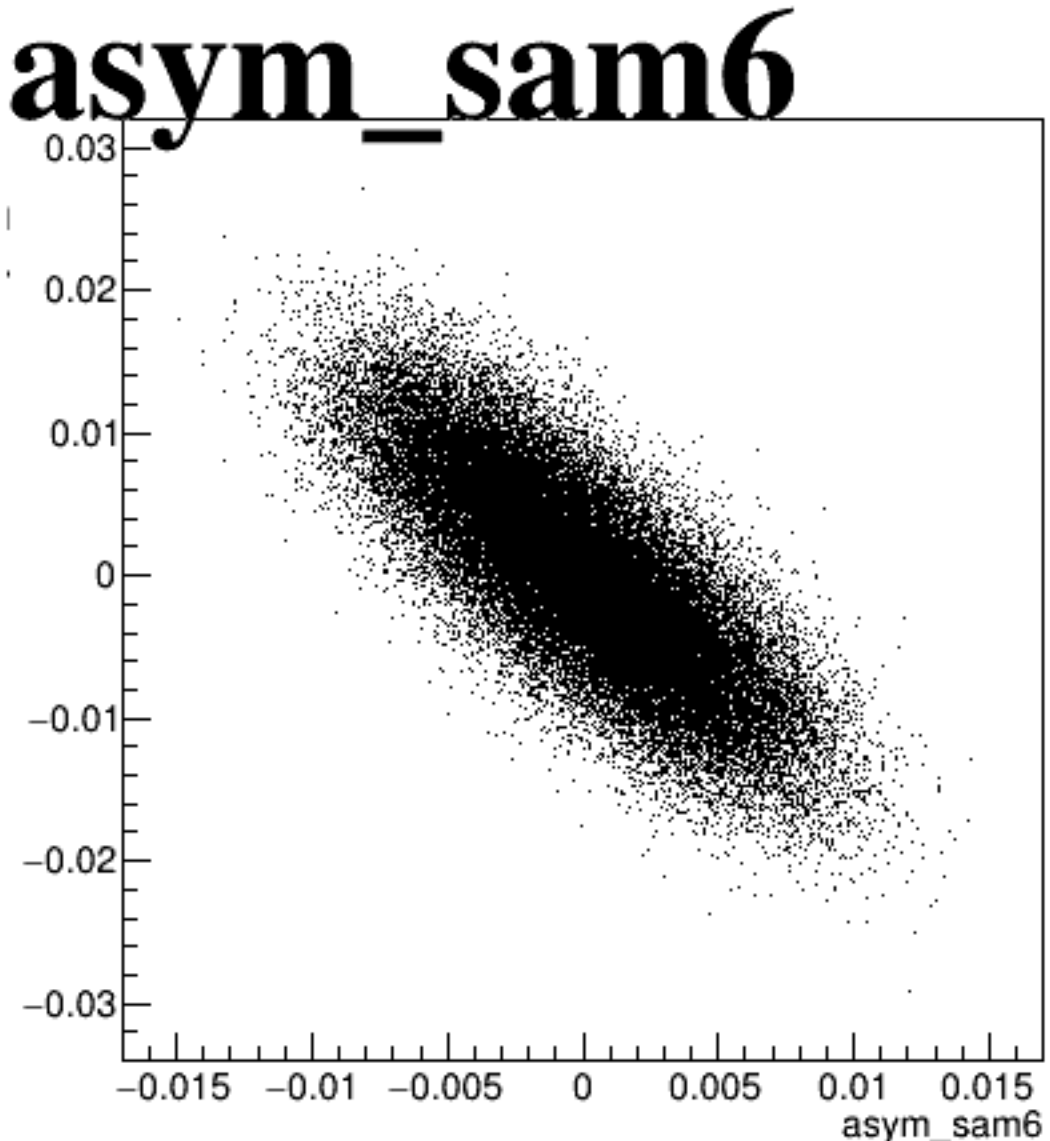
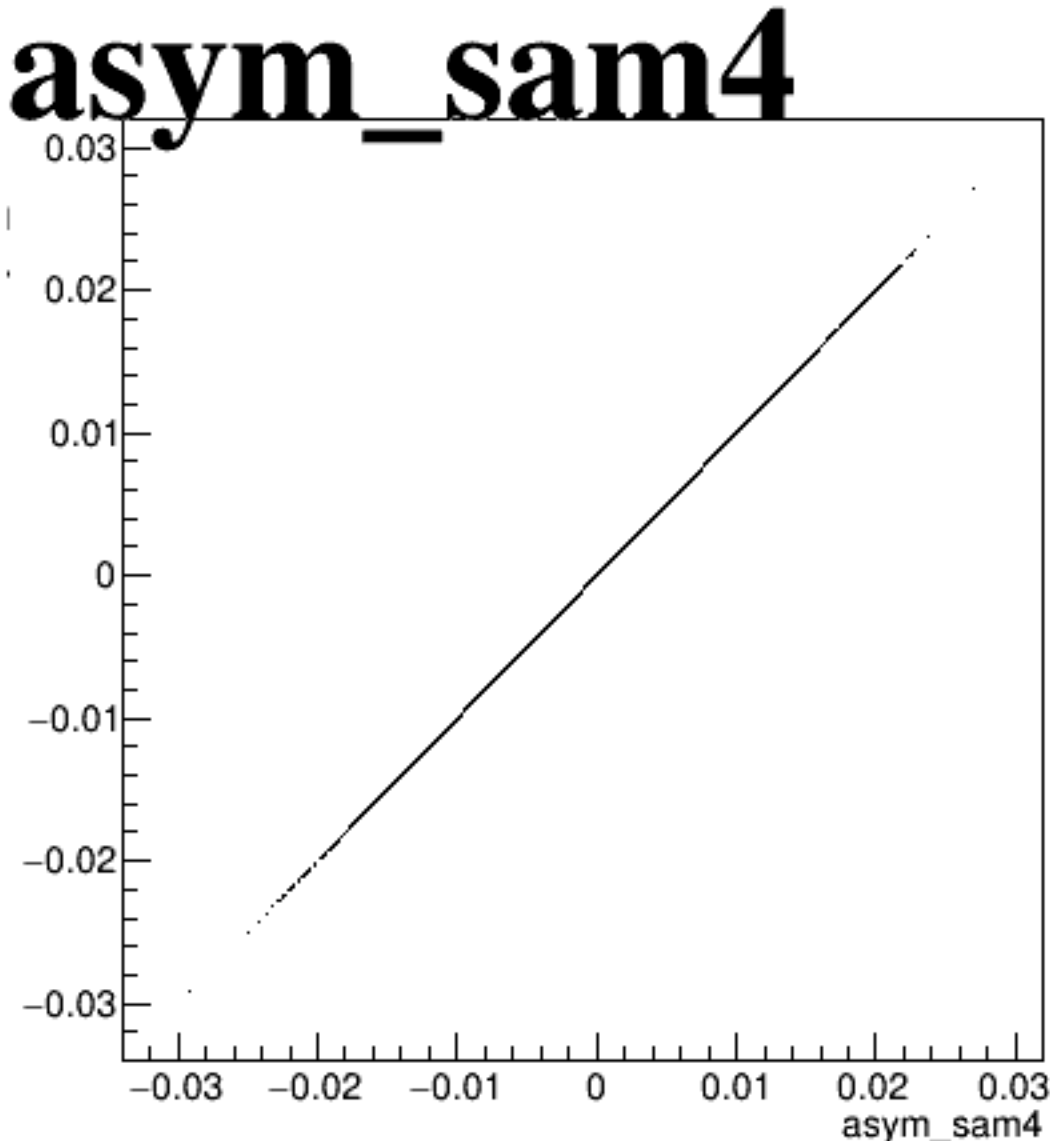
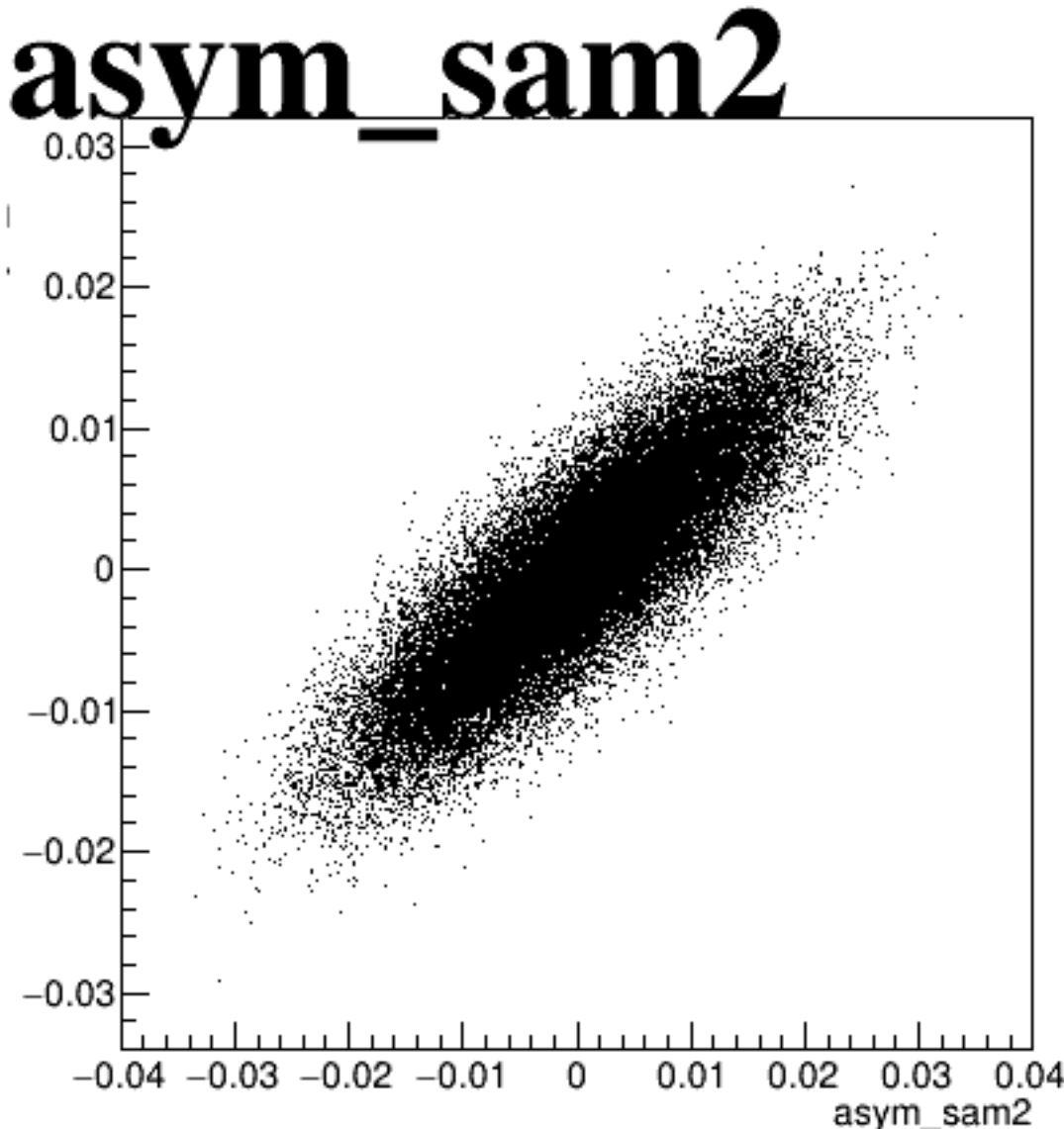
asym_sam8



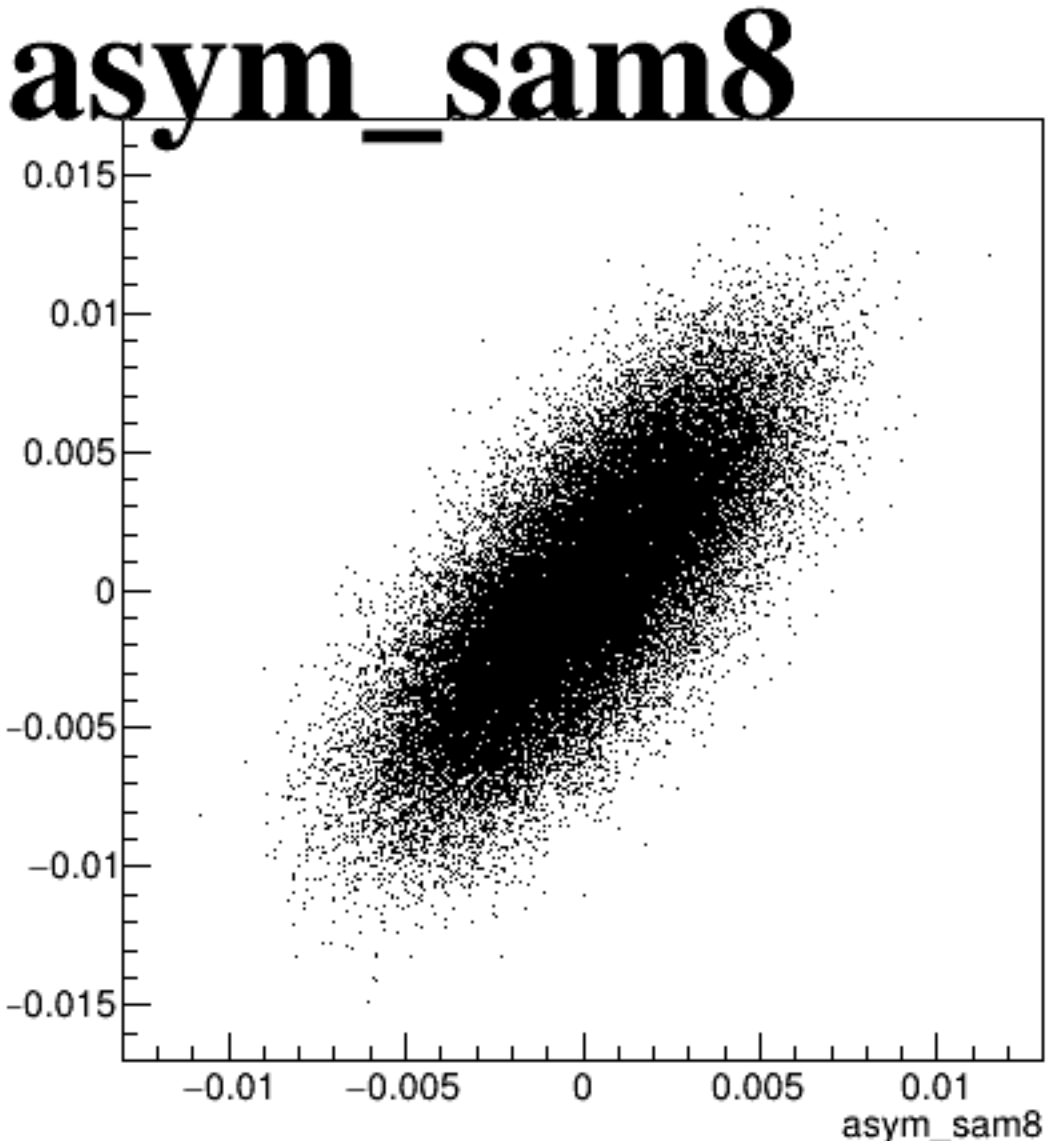
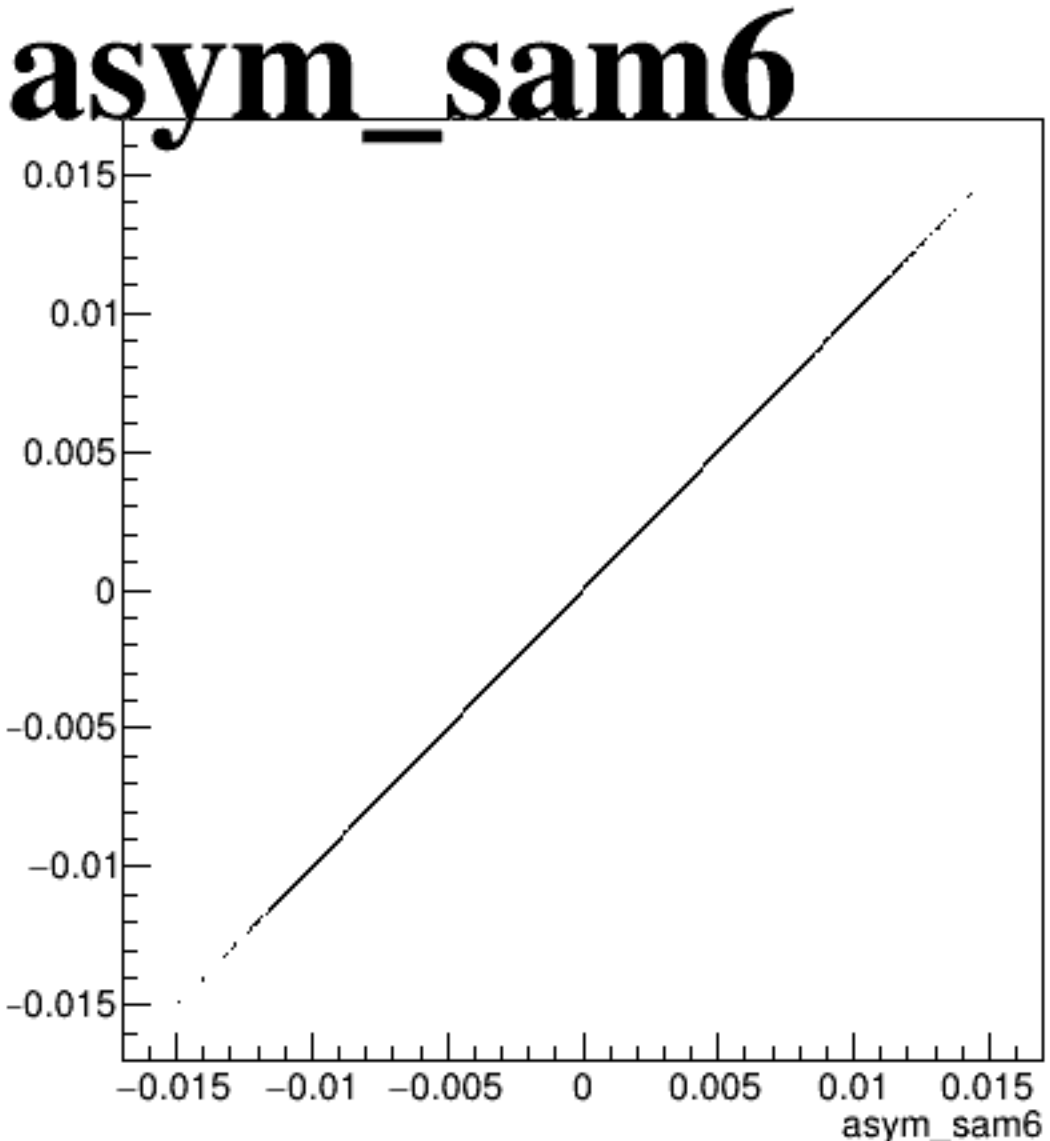
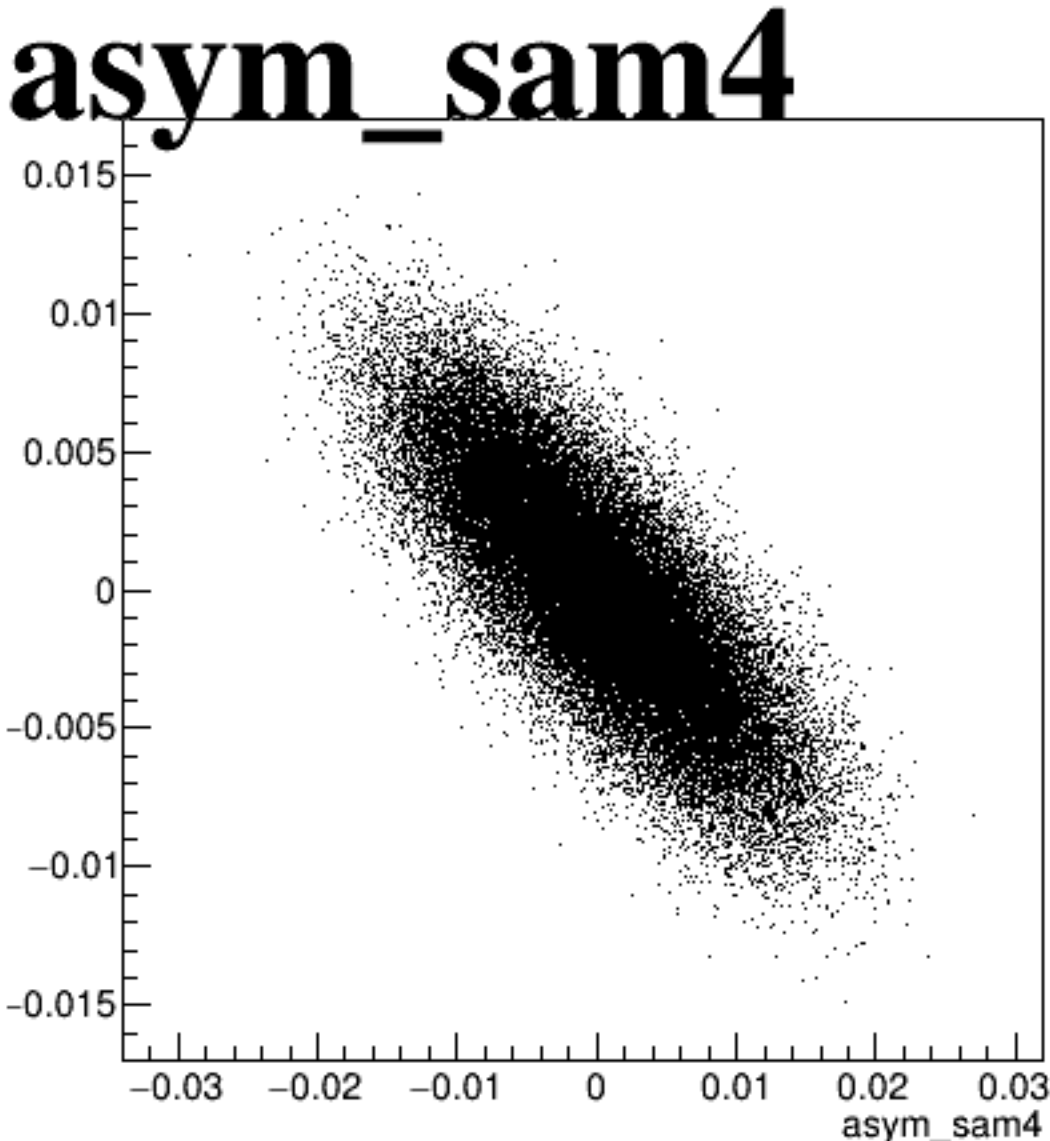
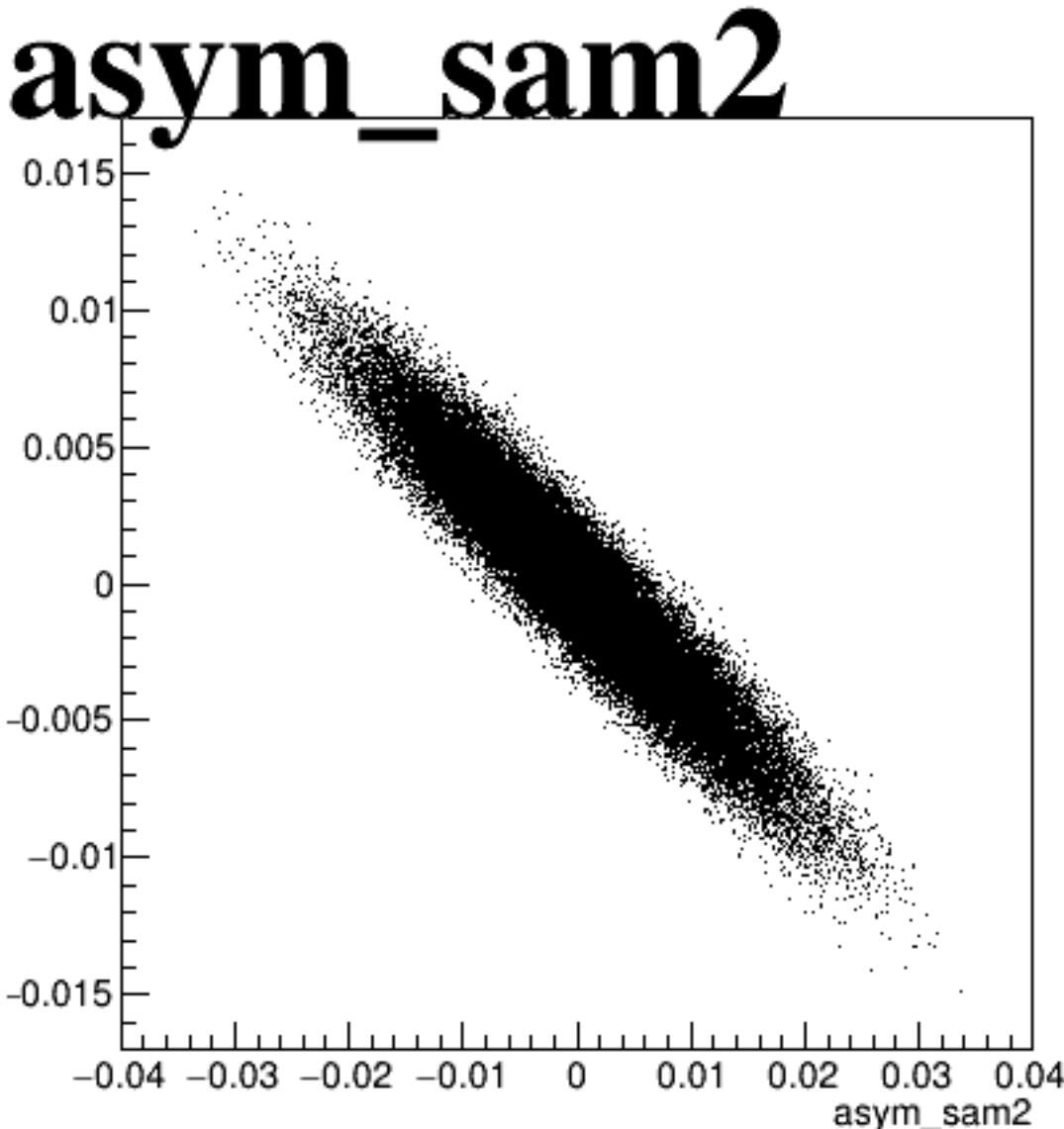
asym_sam2



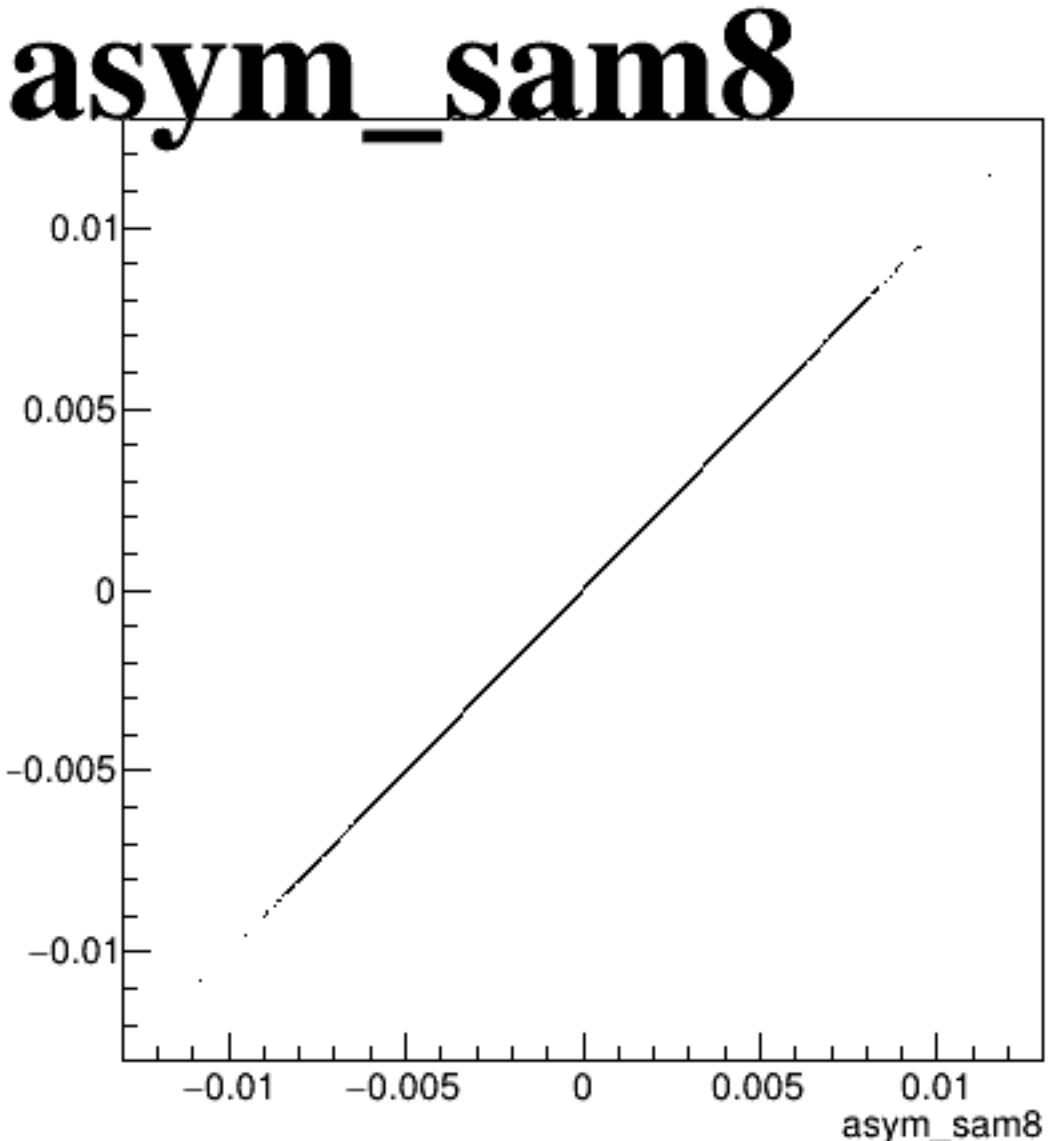
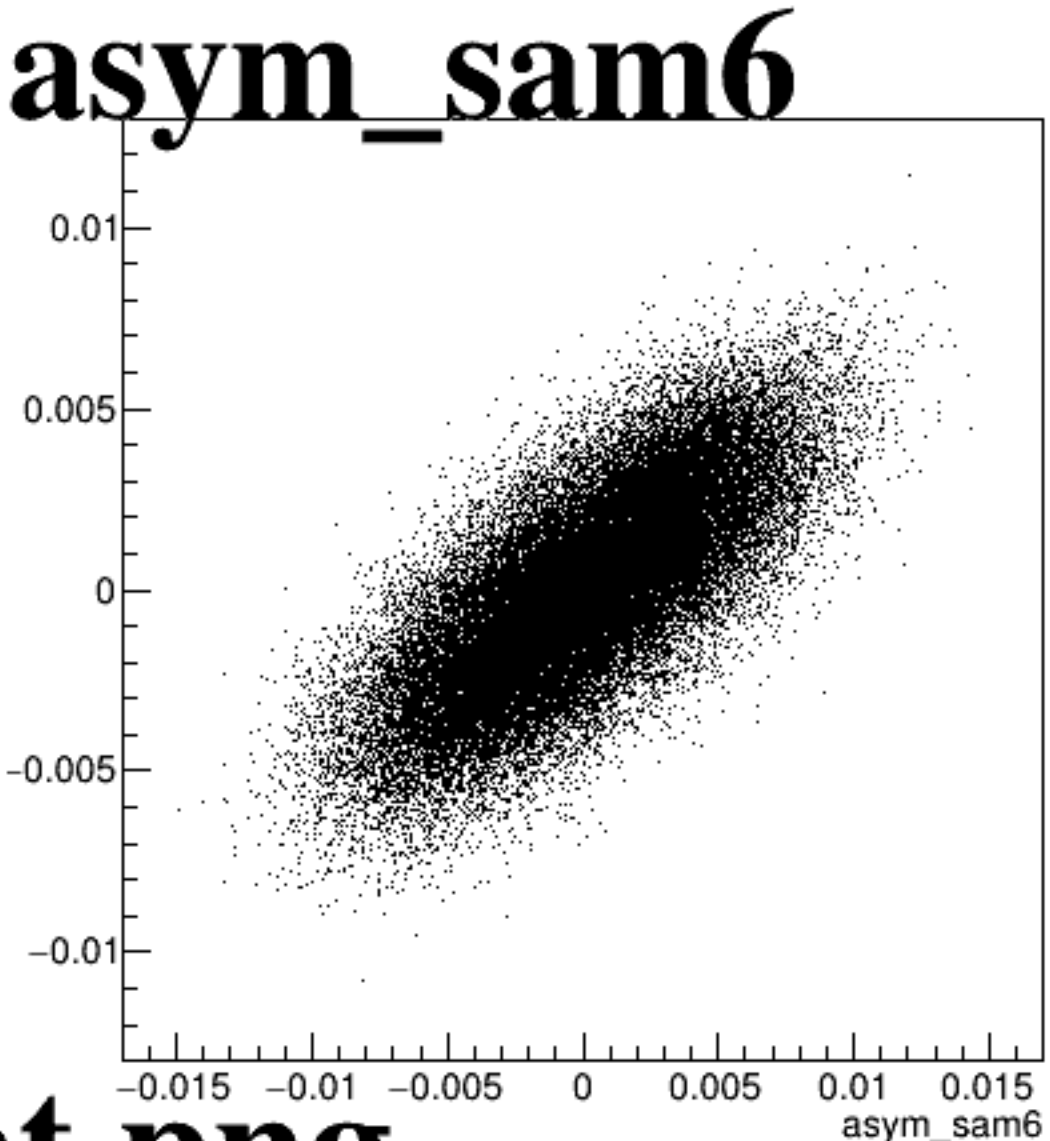
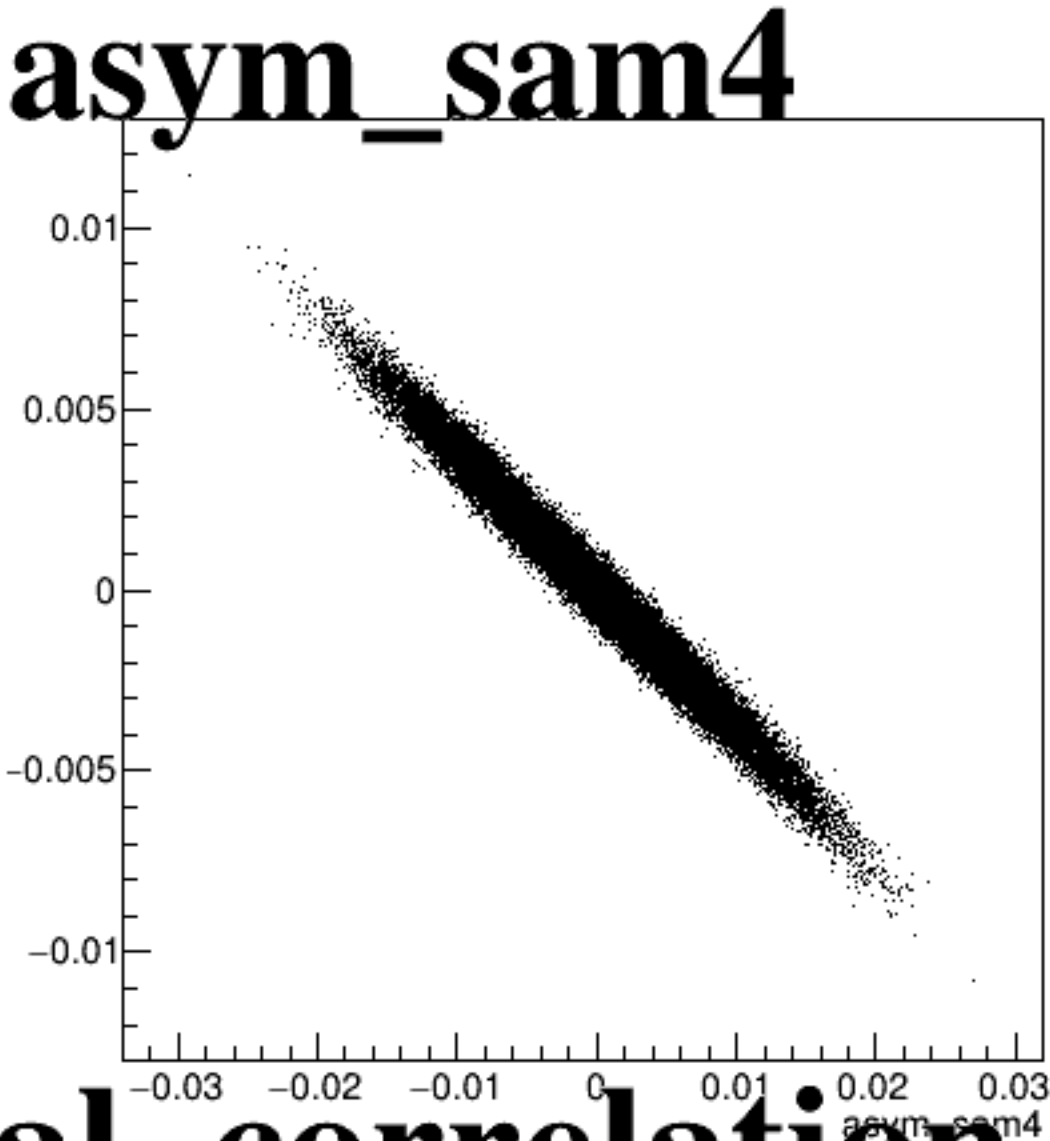
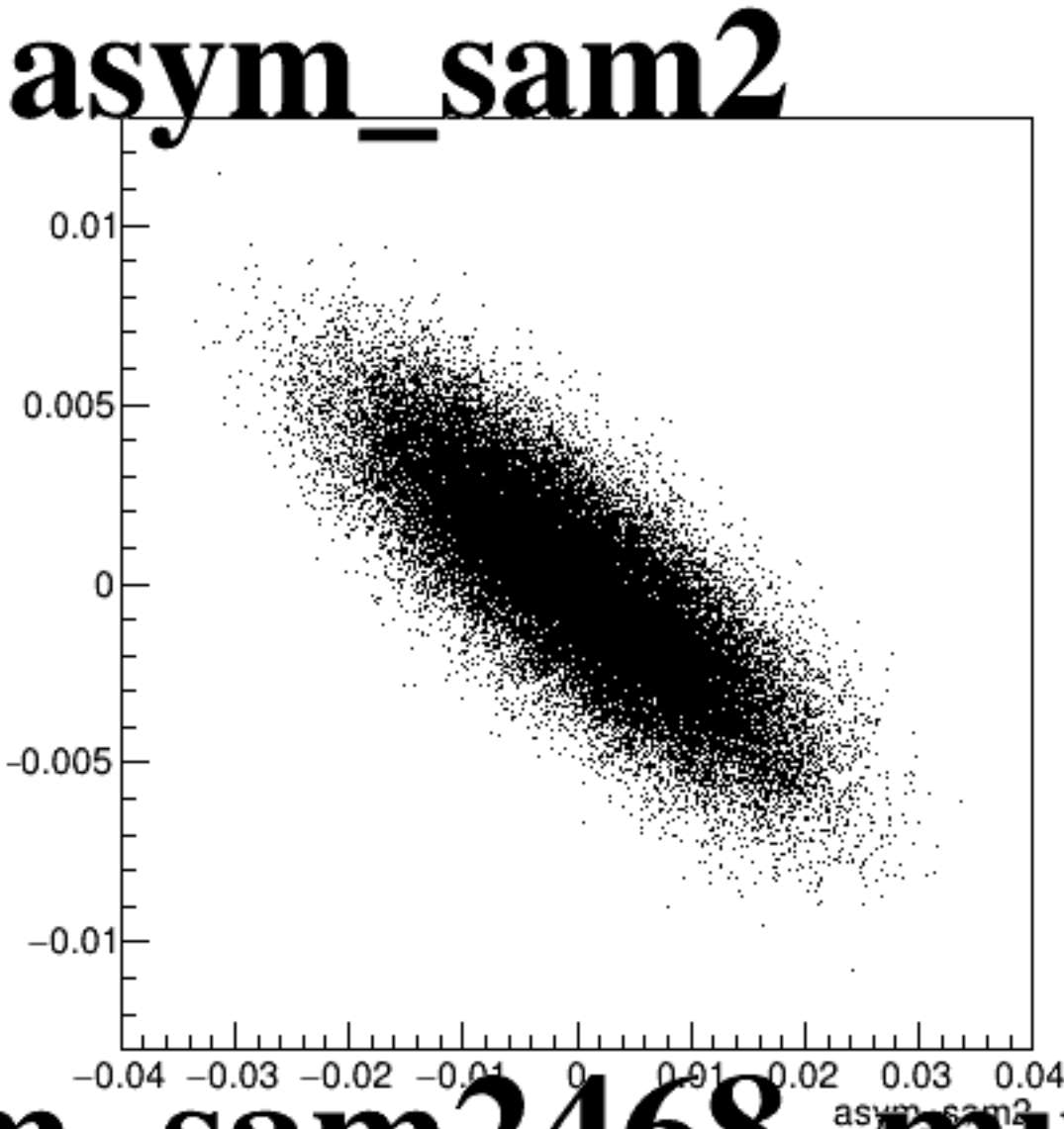
asym_sam4



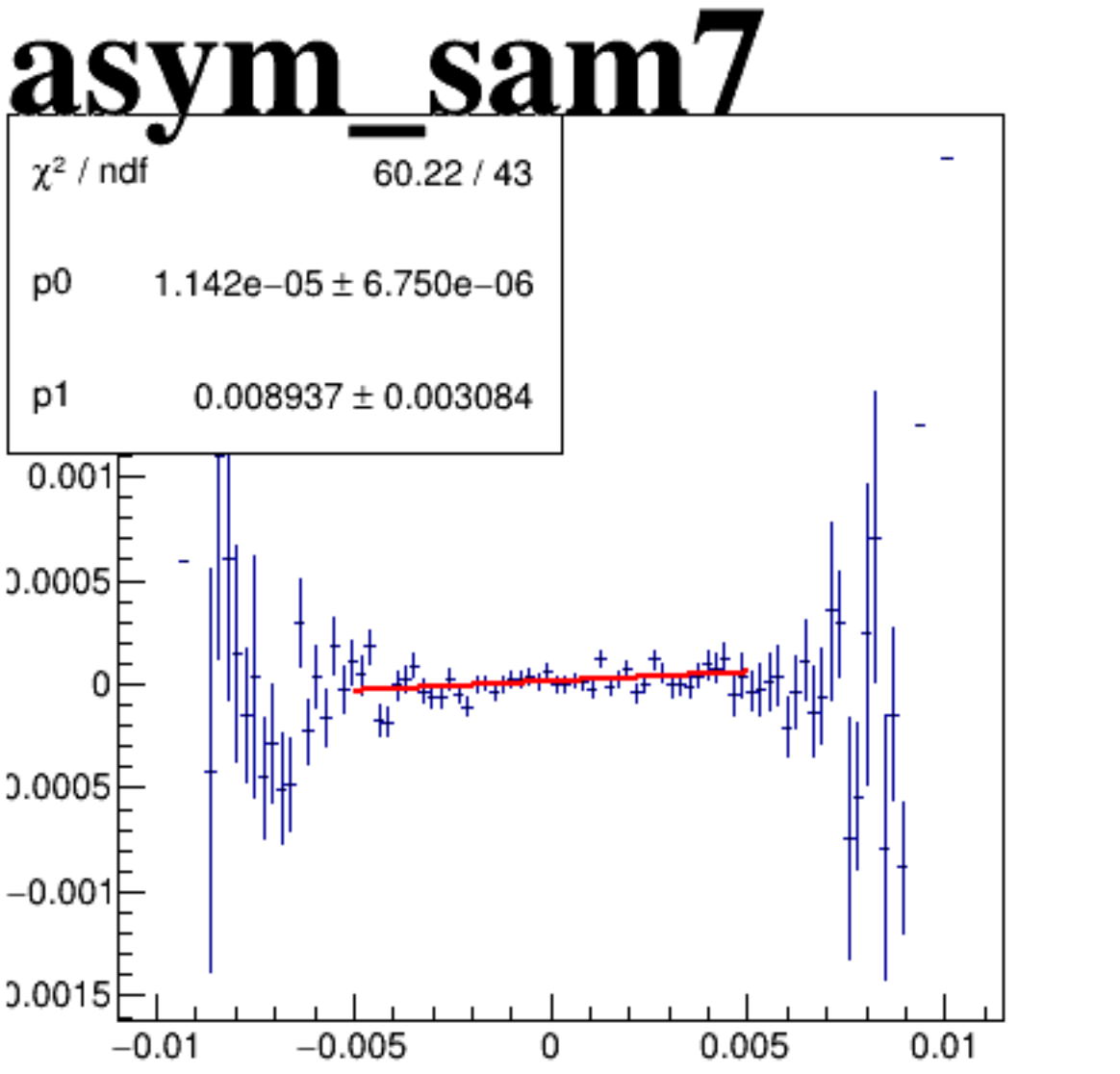
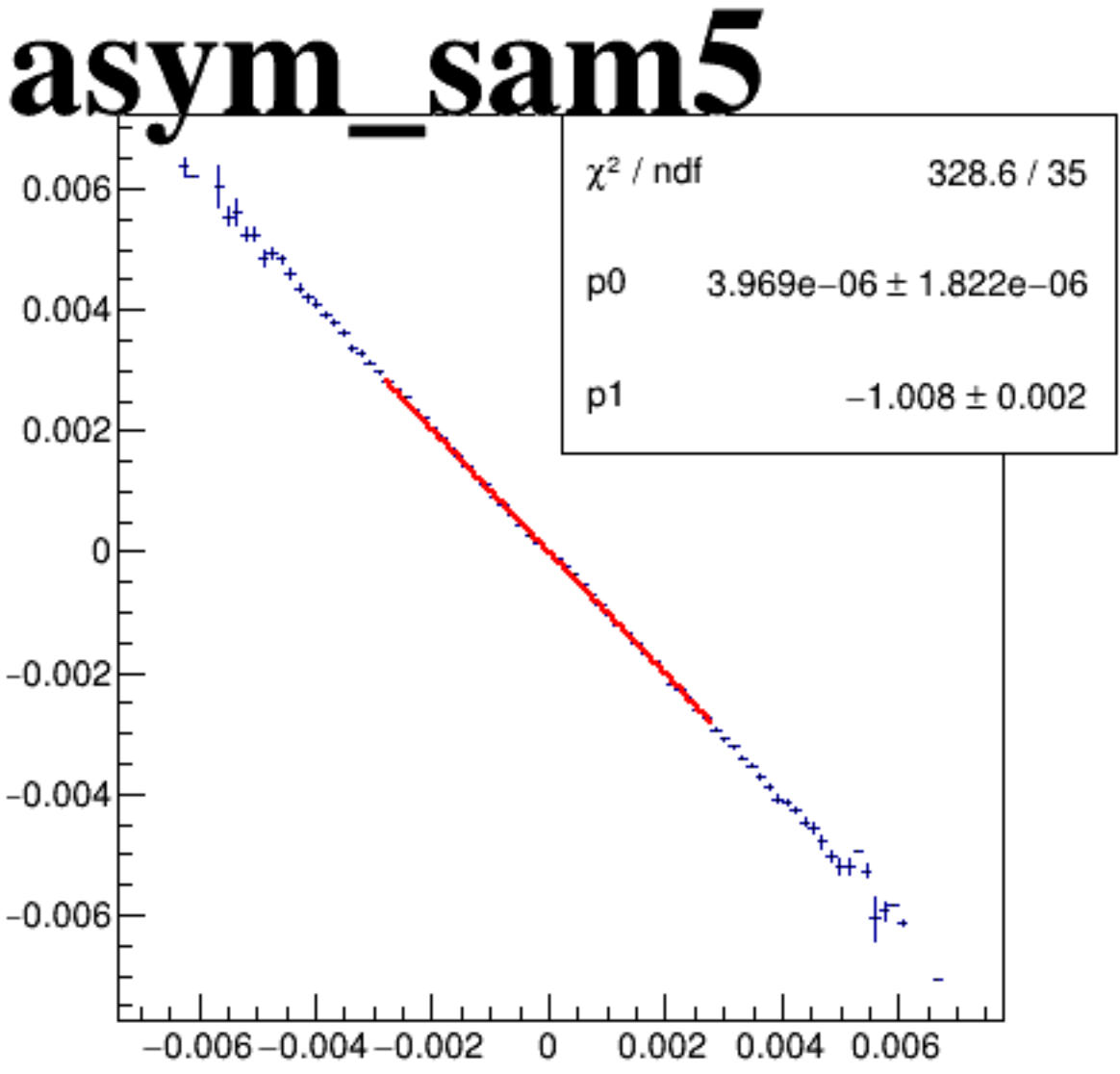
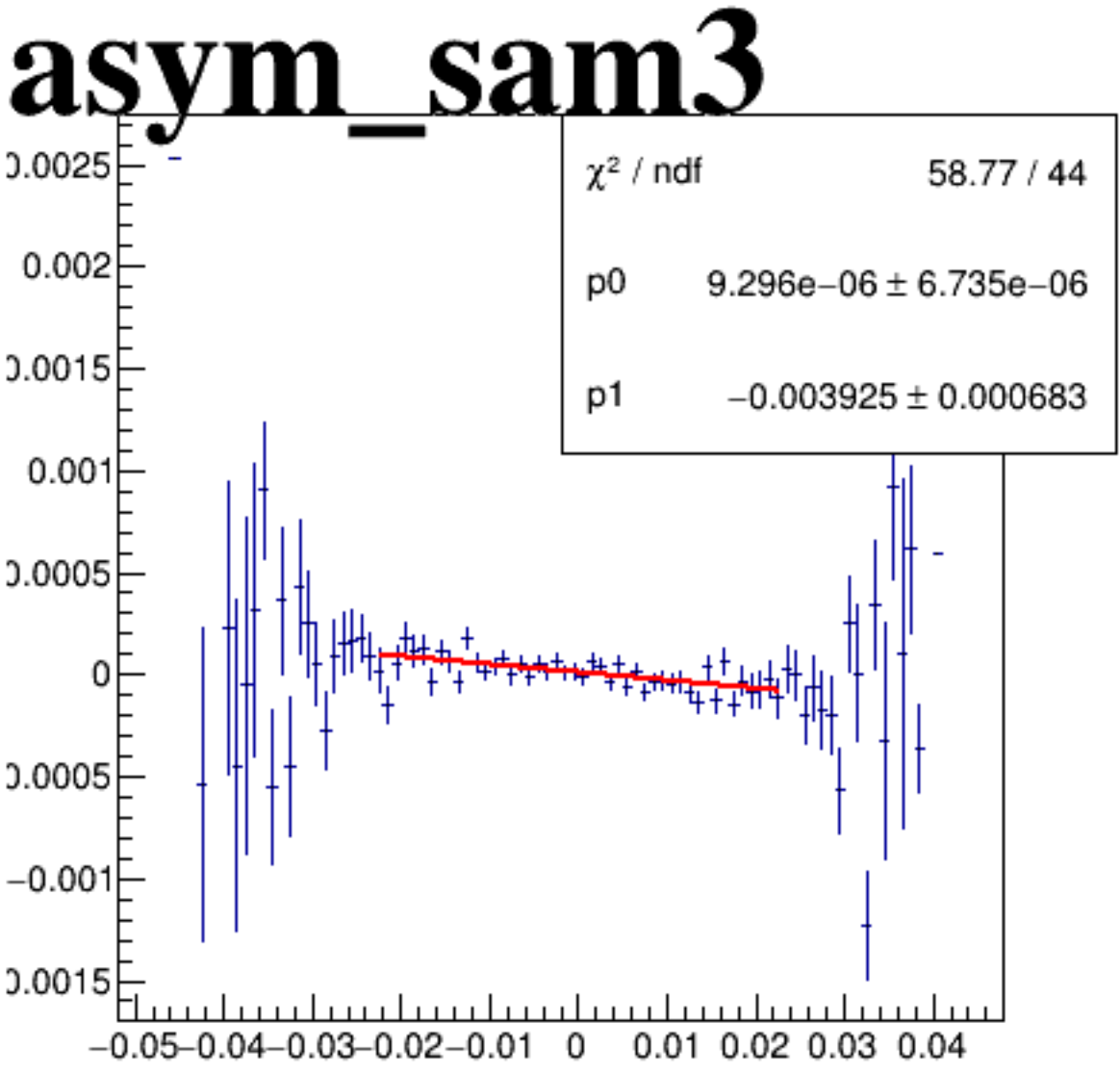
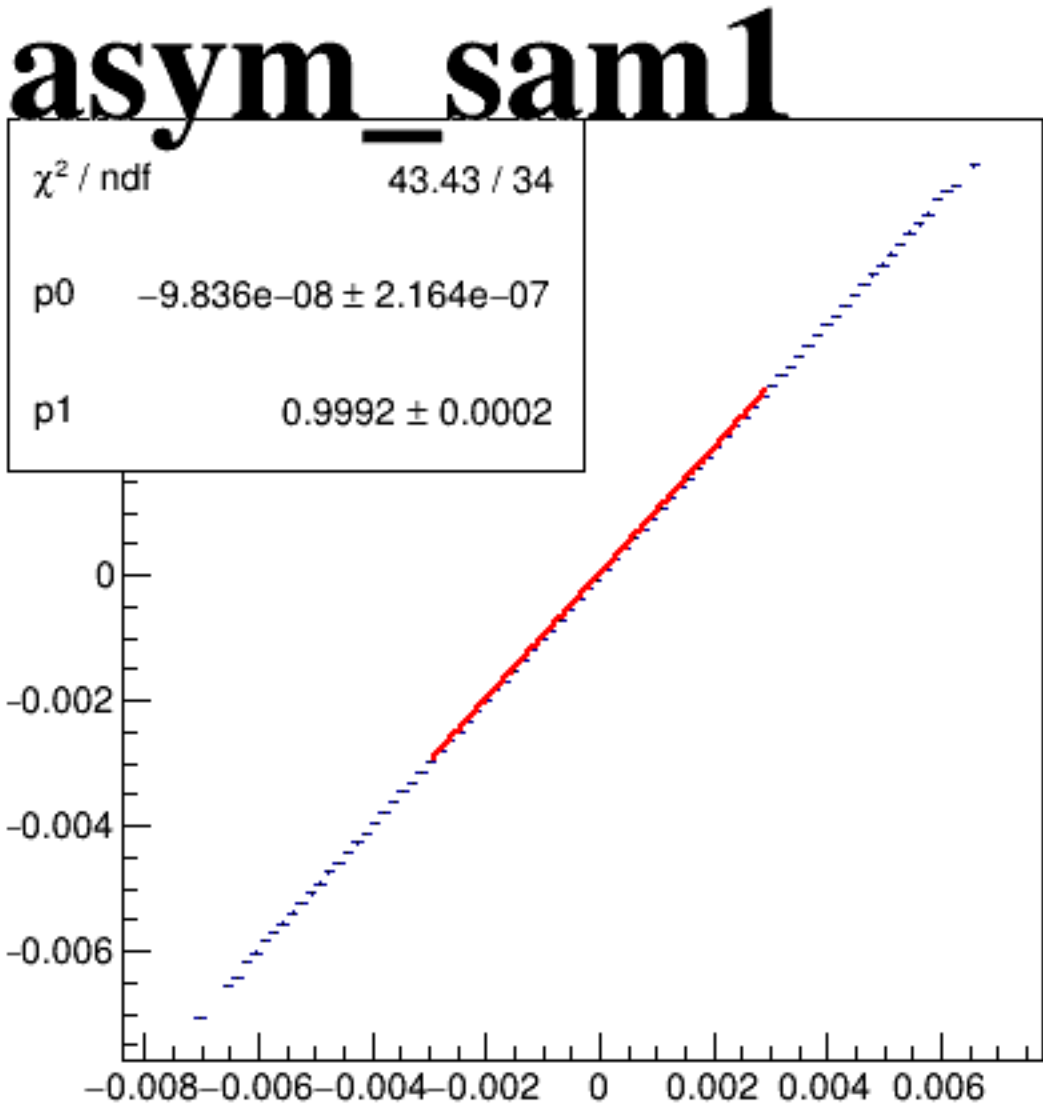
asym_sam6



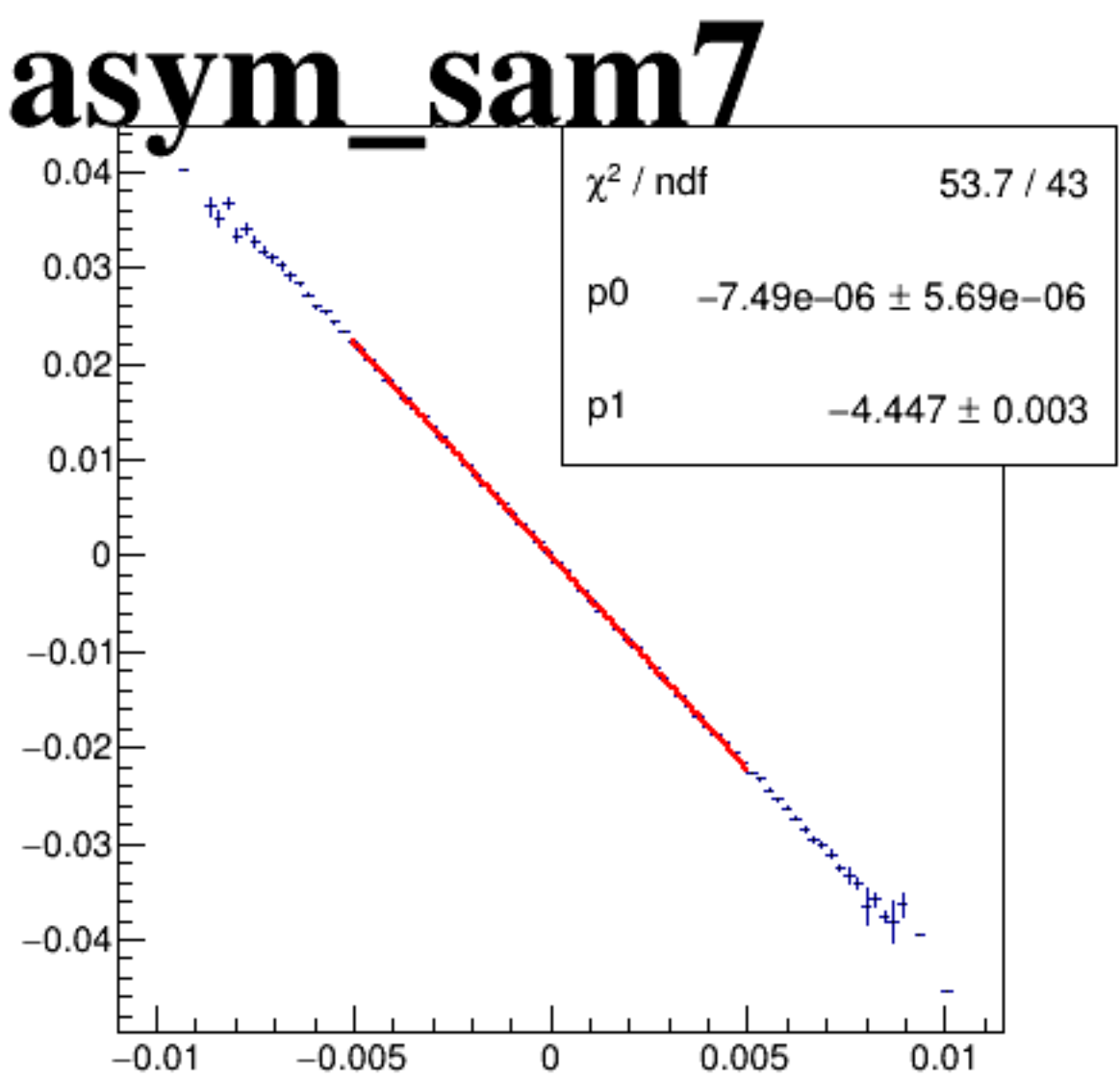
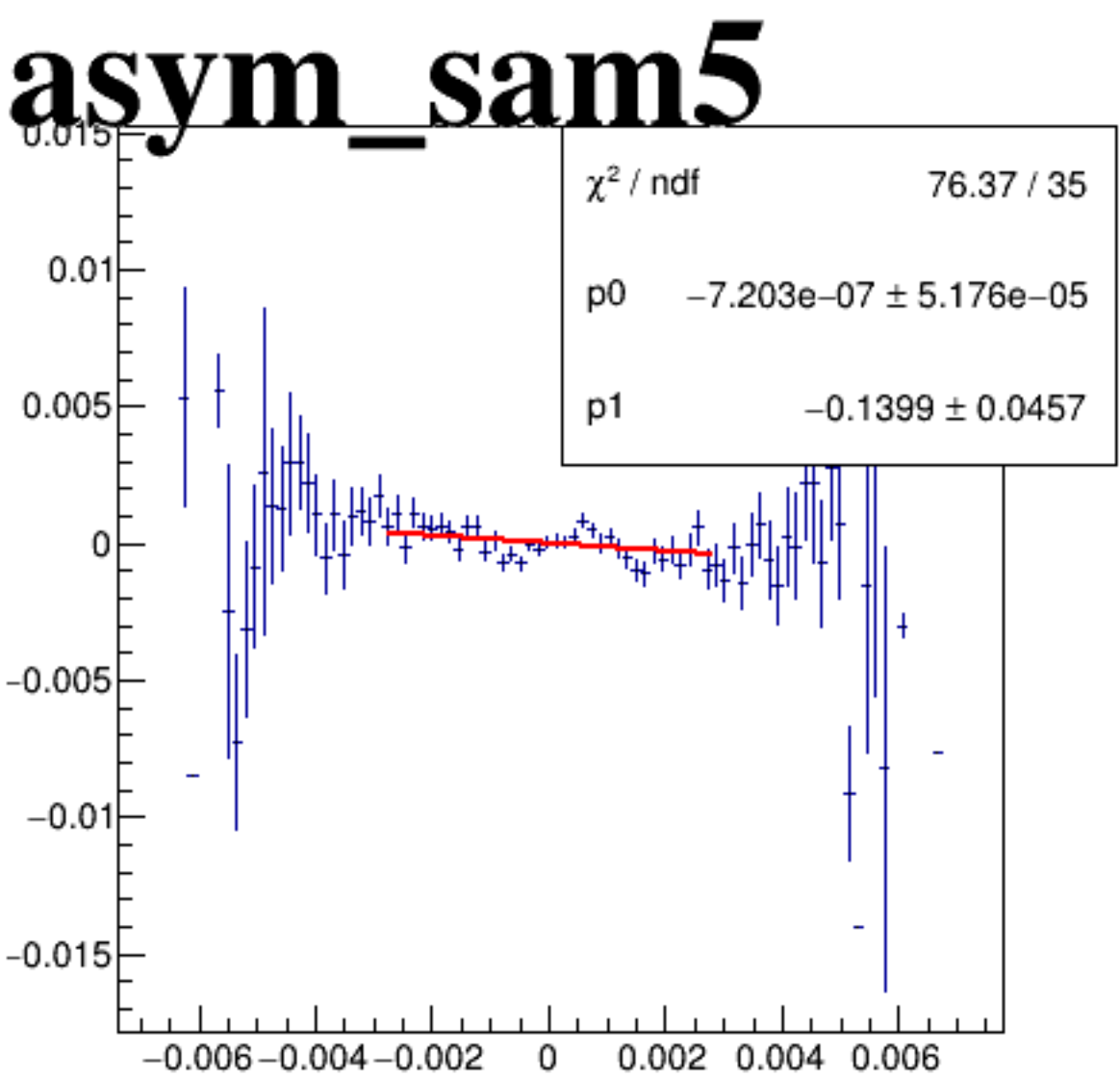
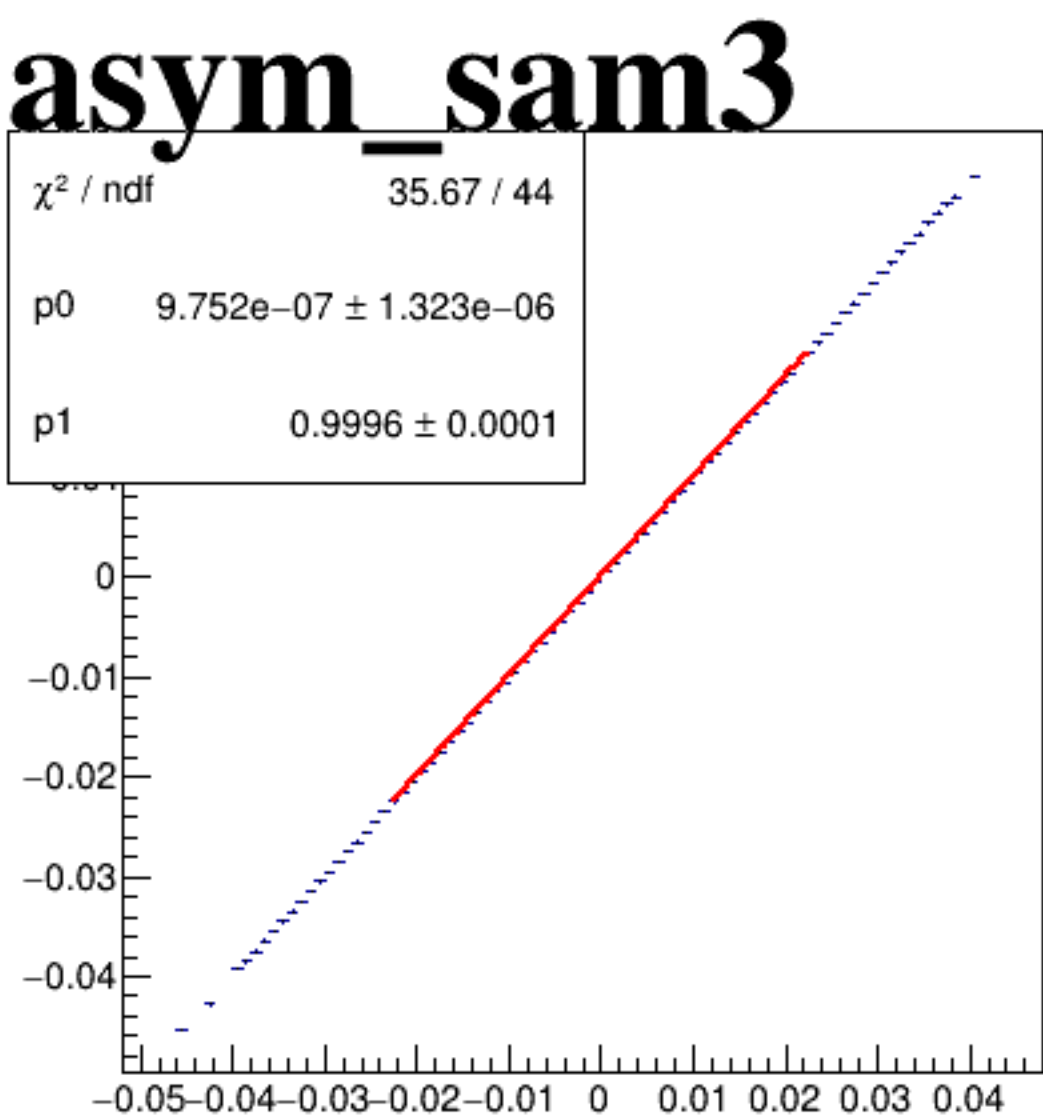
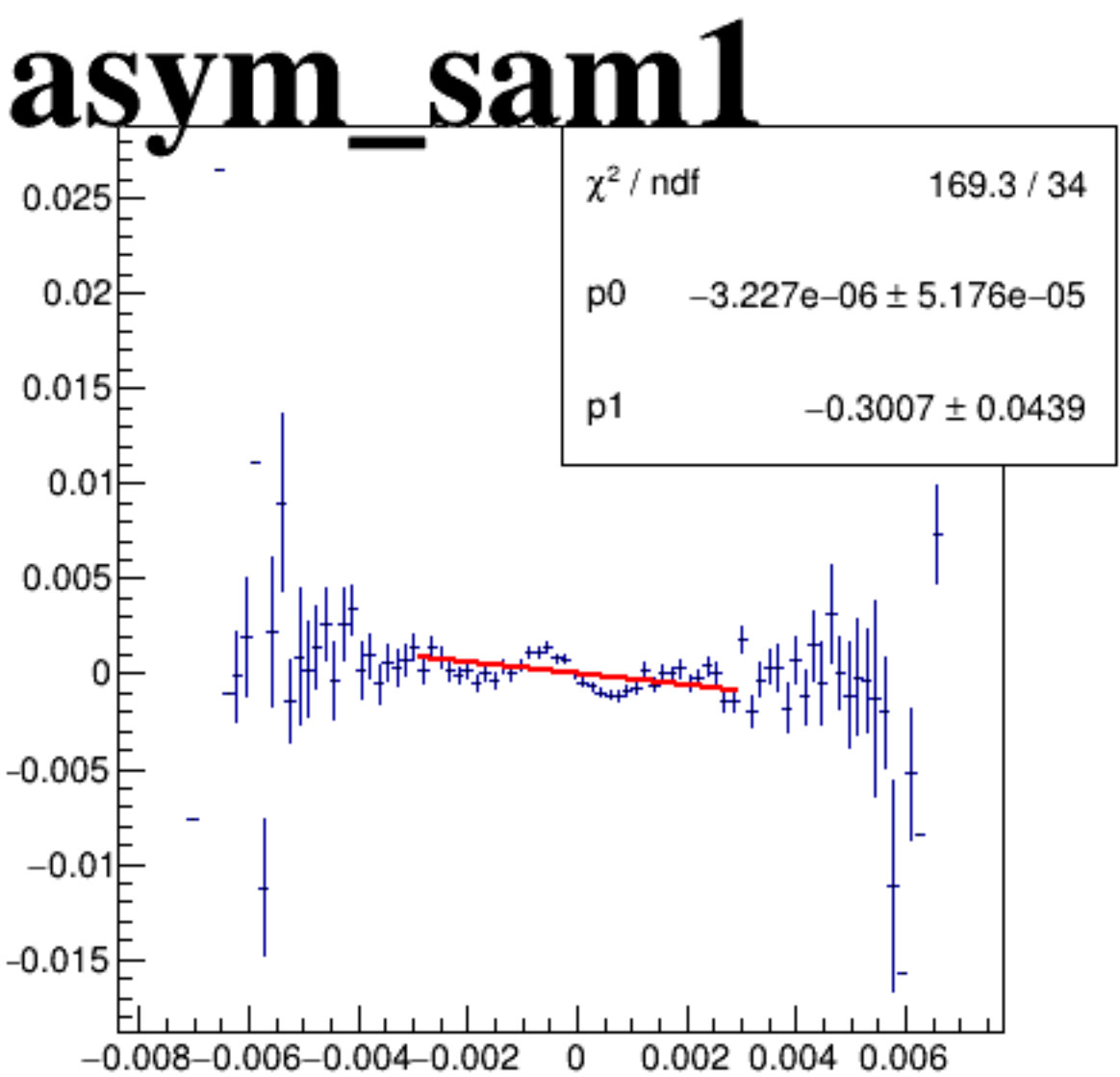
asym_sam8



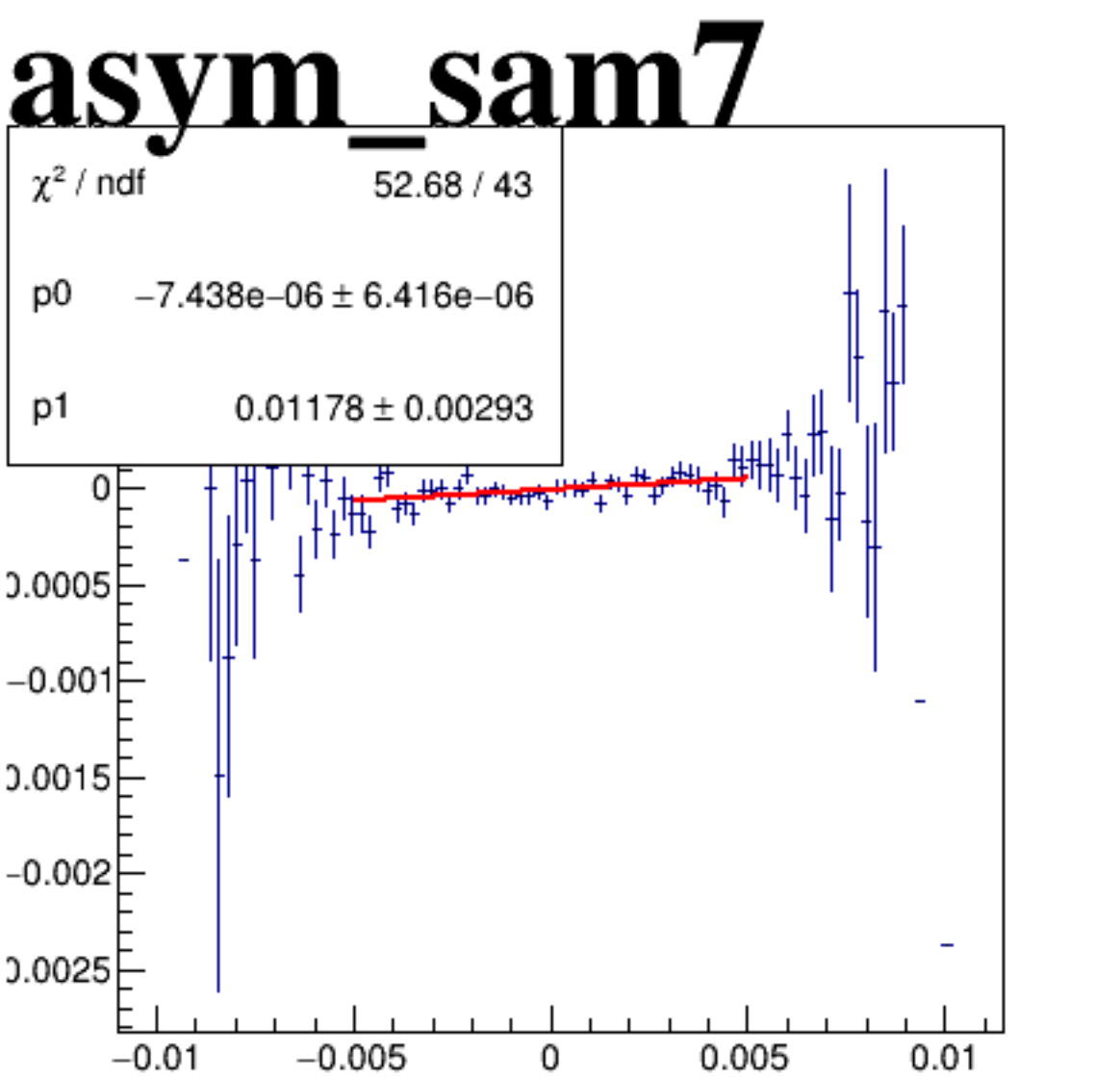
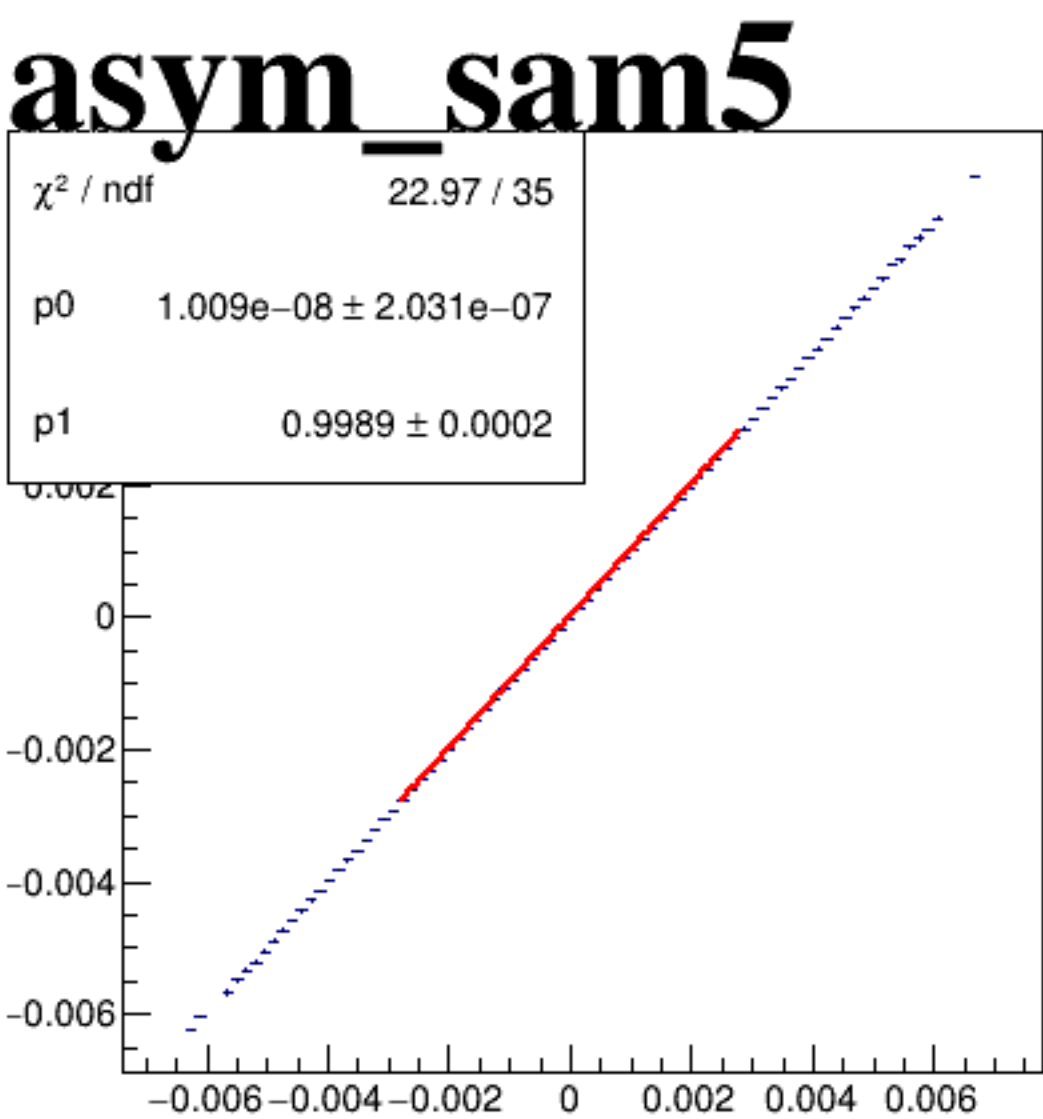
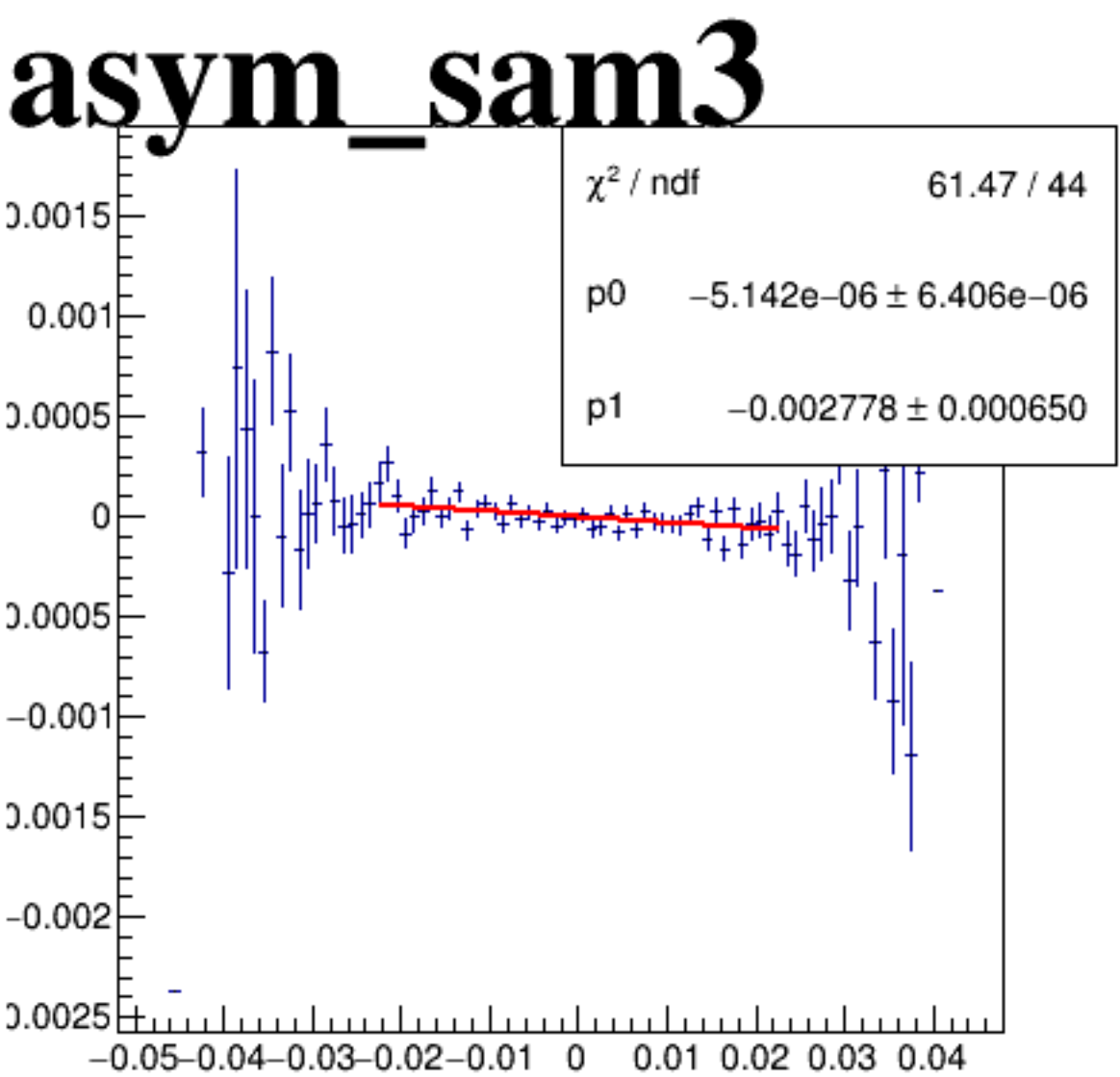
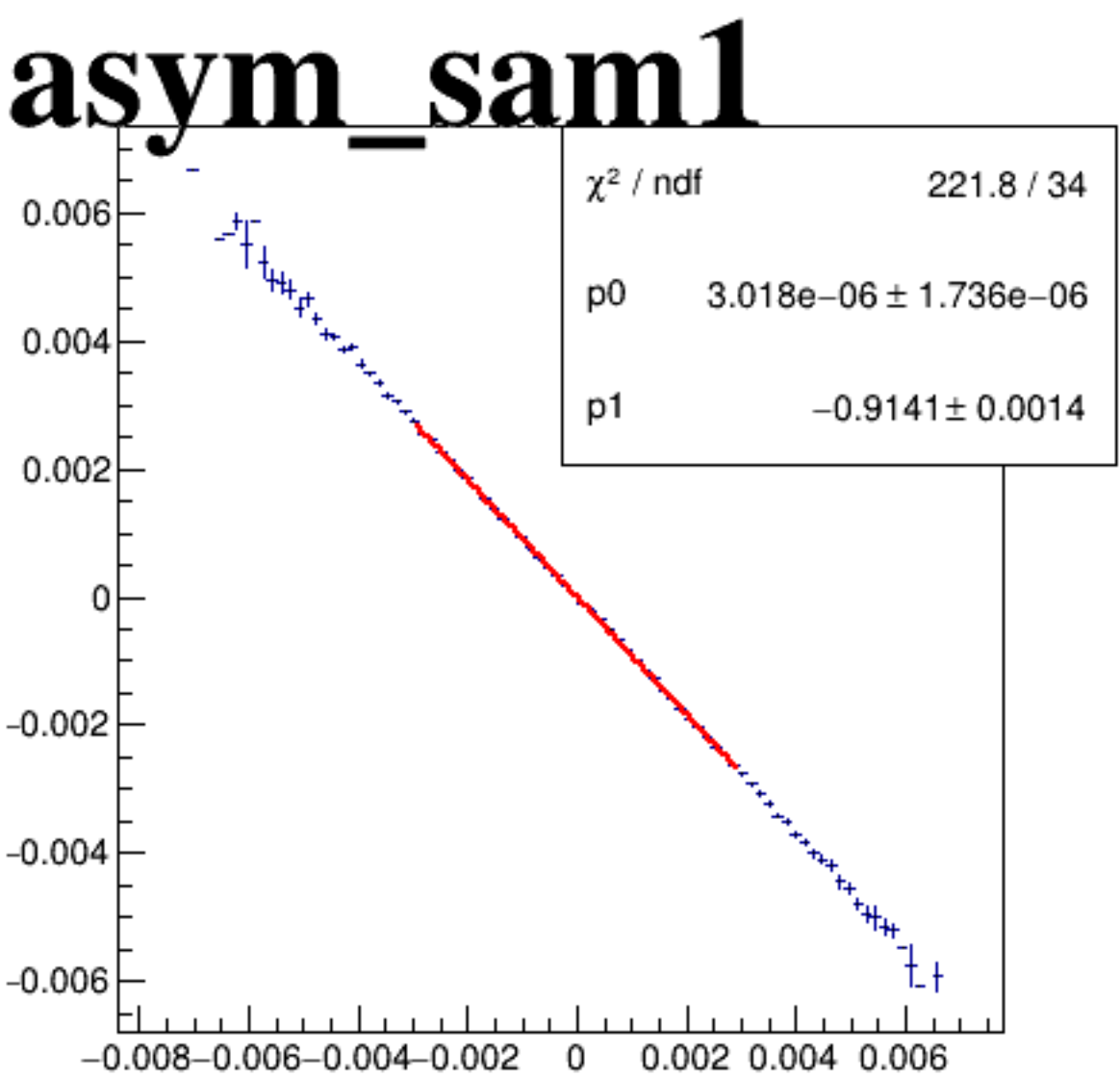
asym_sam1



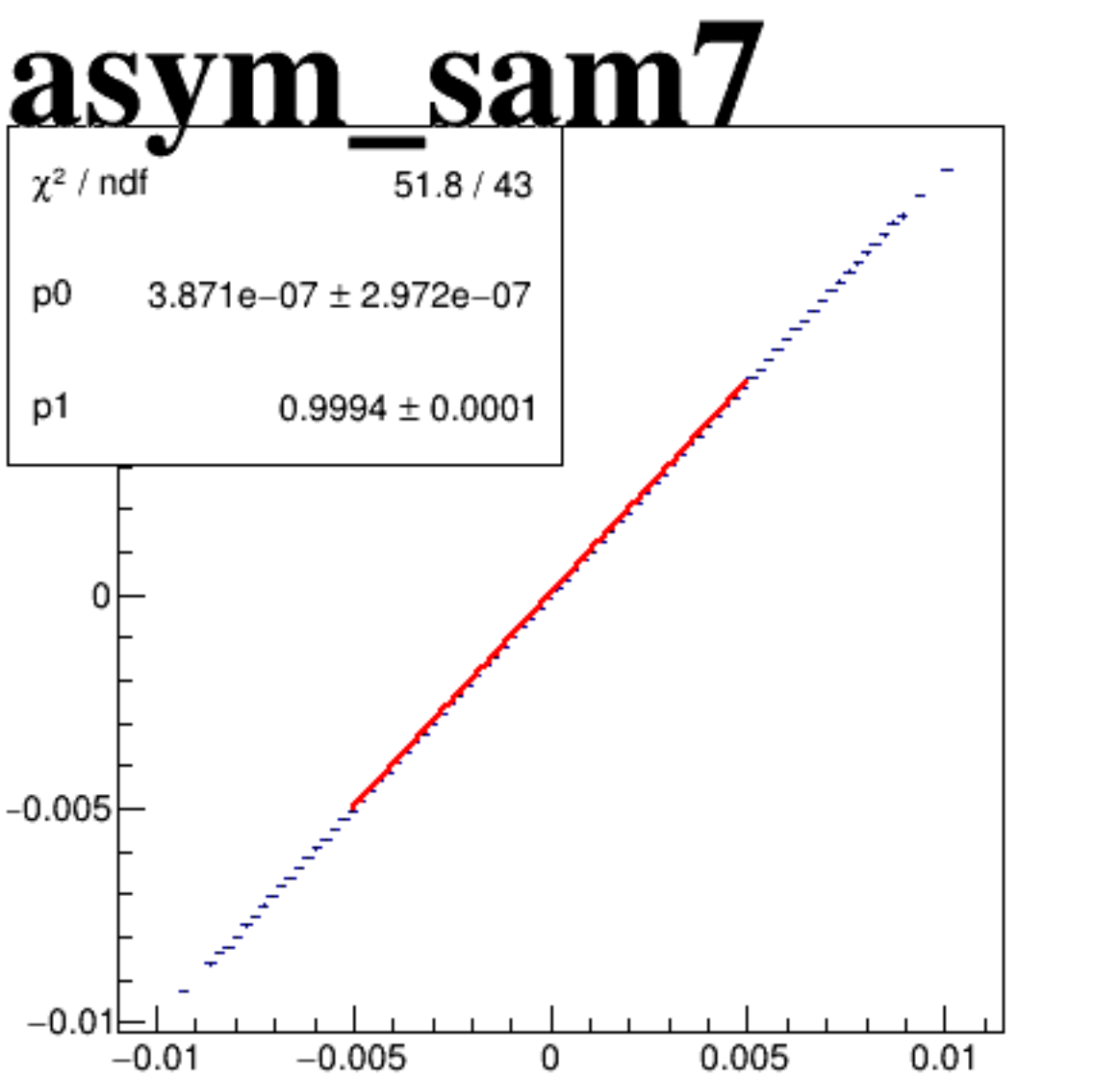
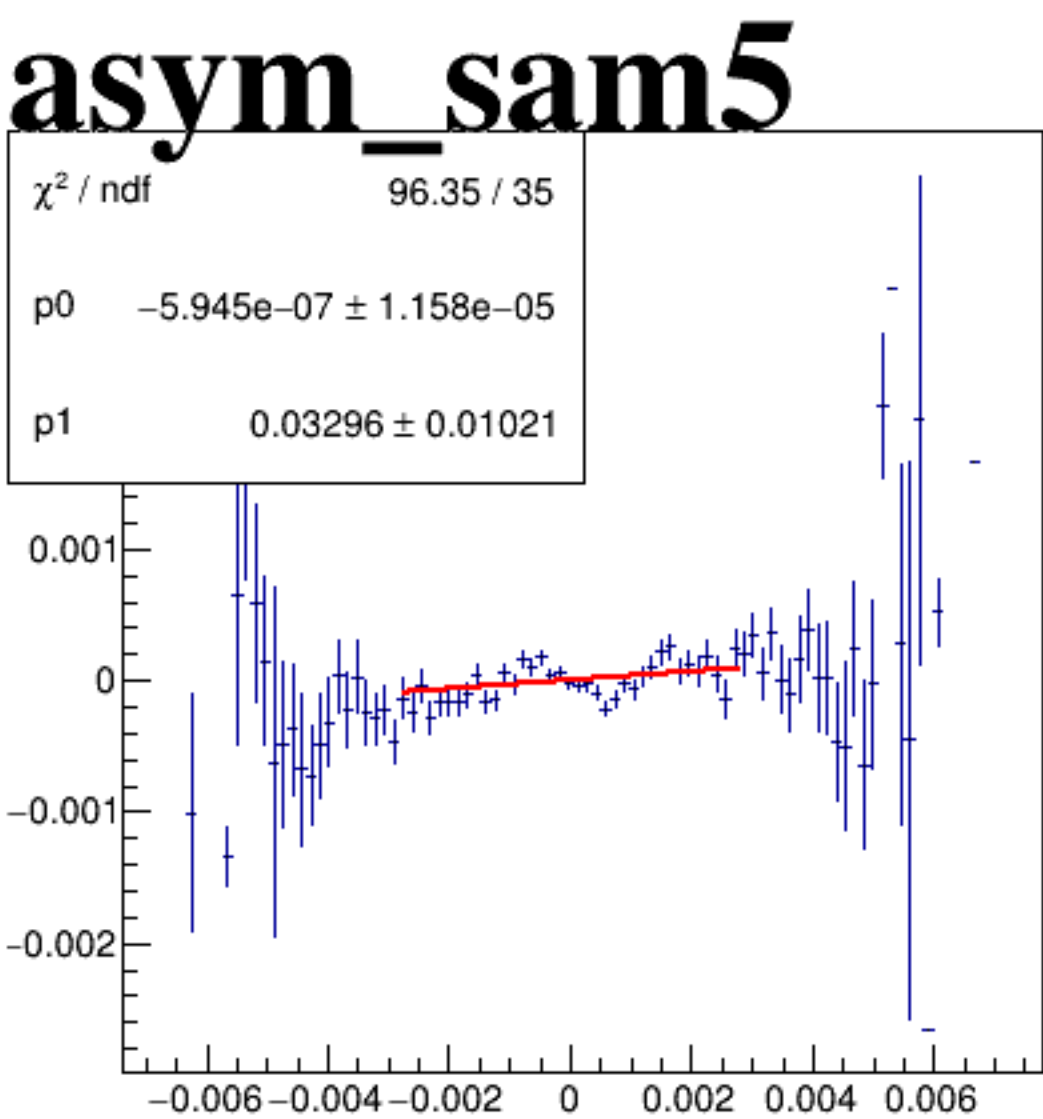
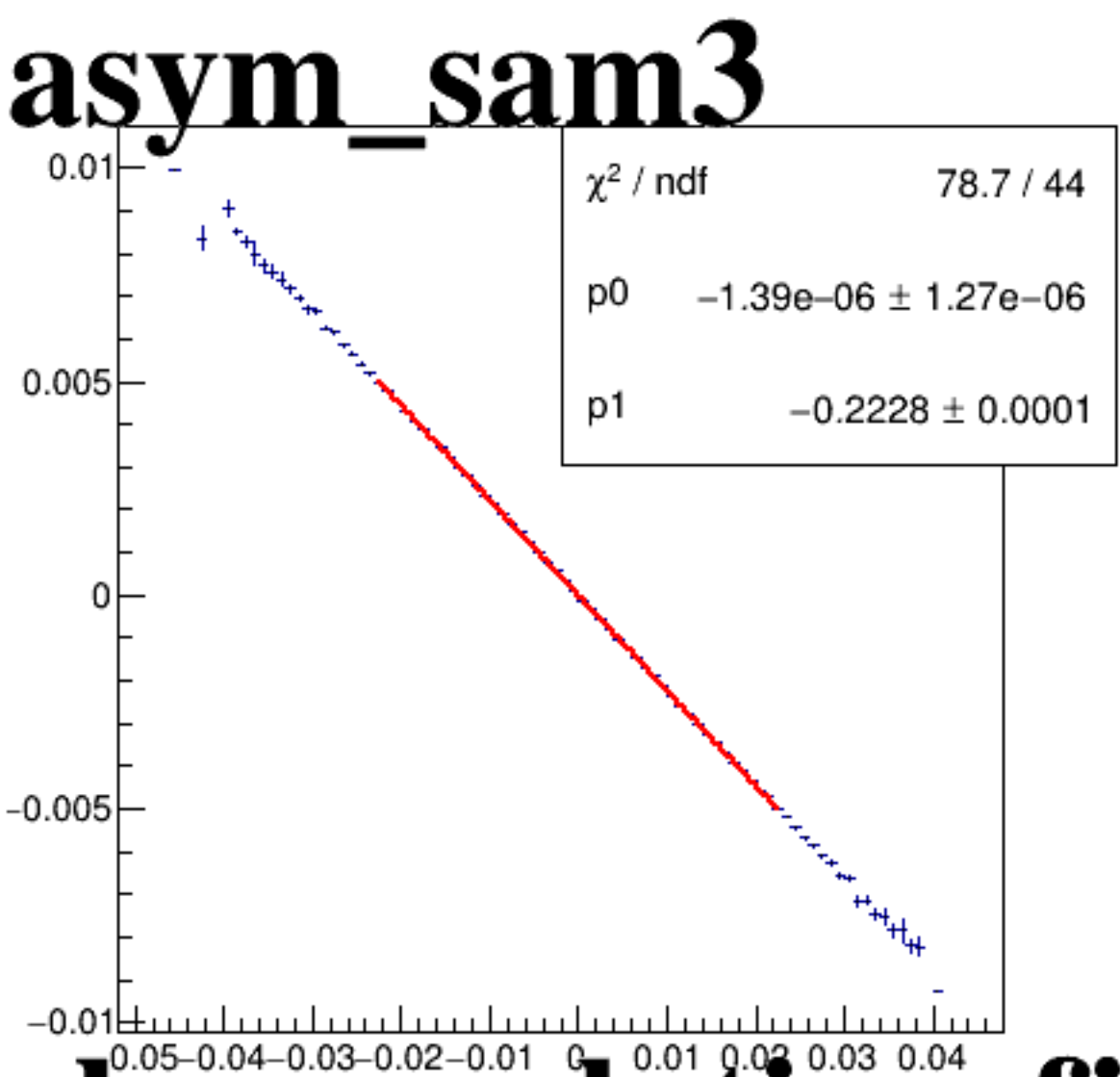
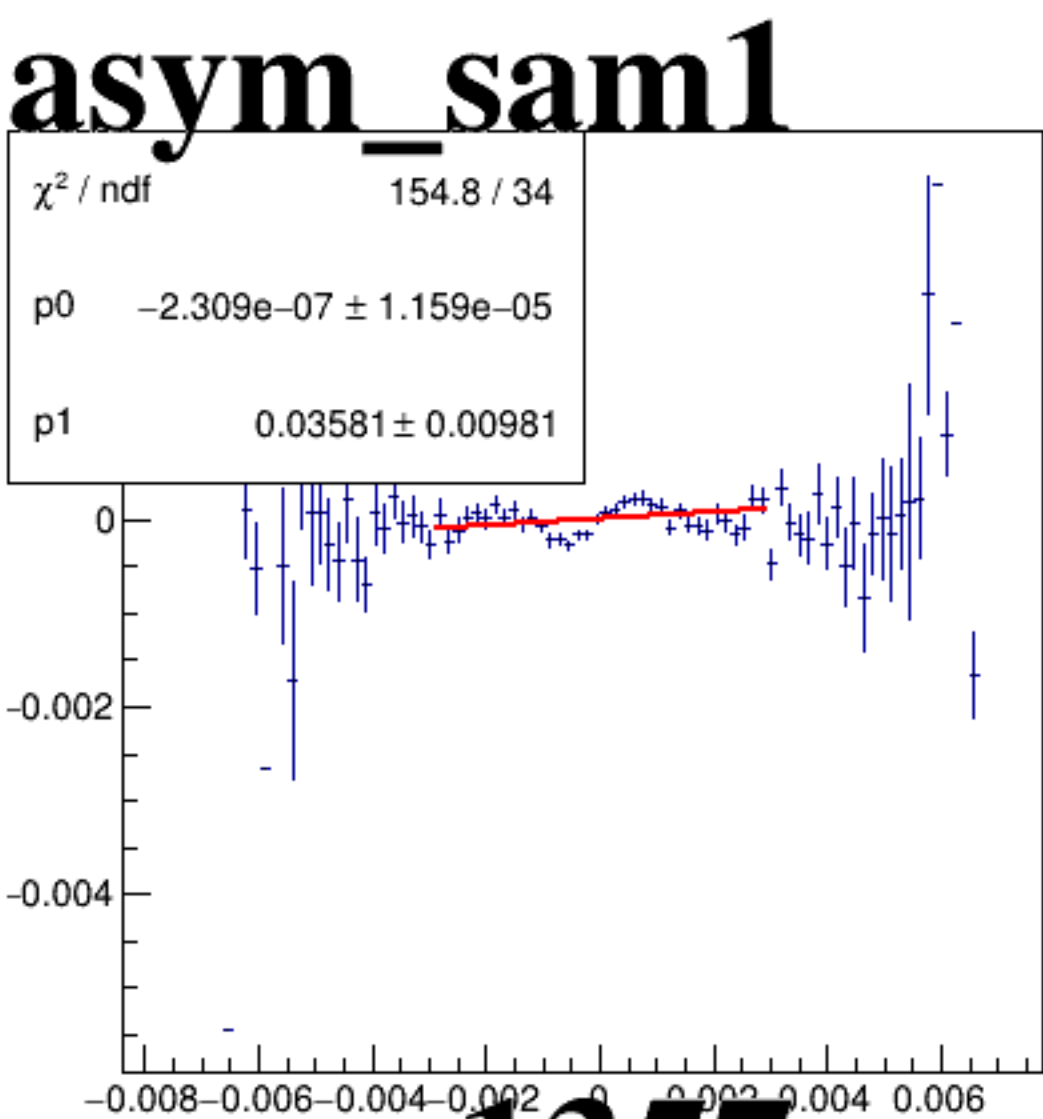
asym_sam3



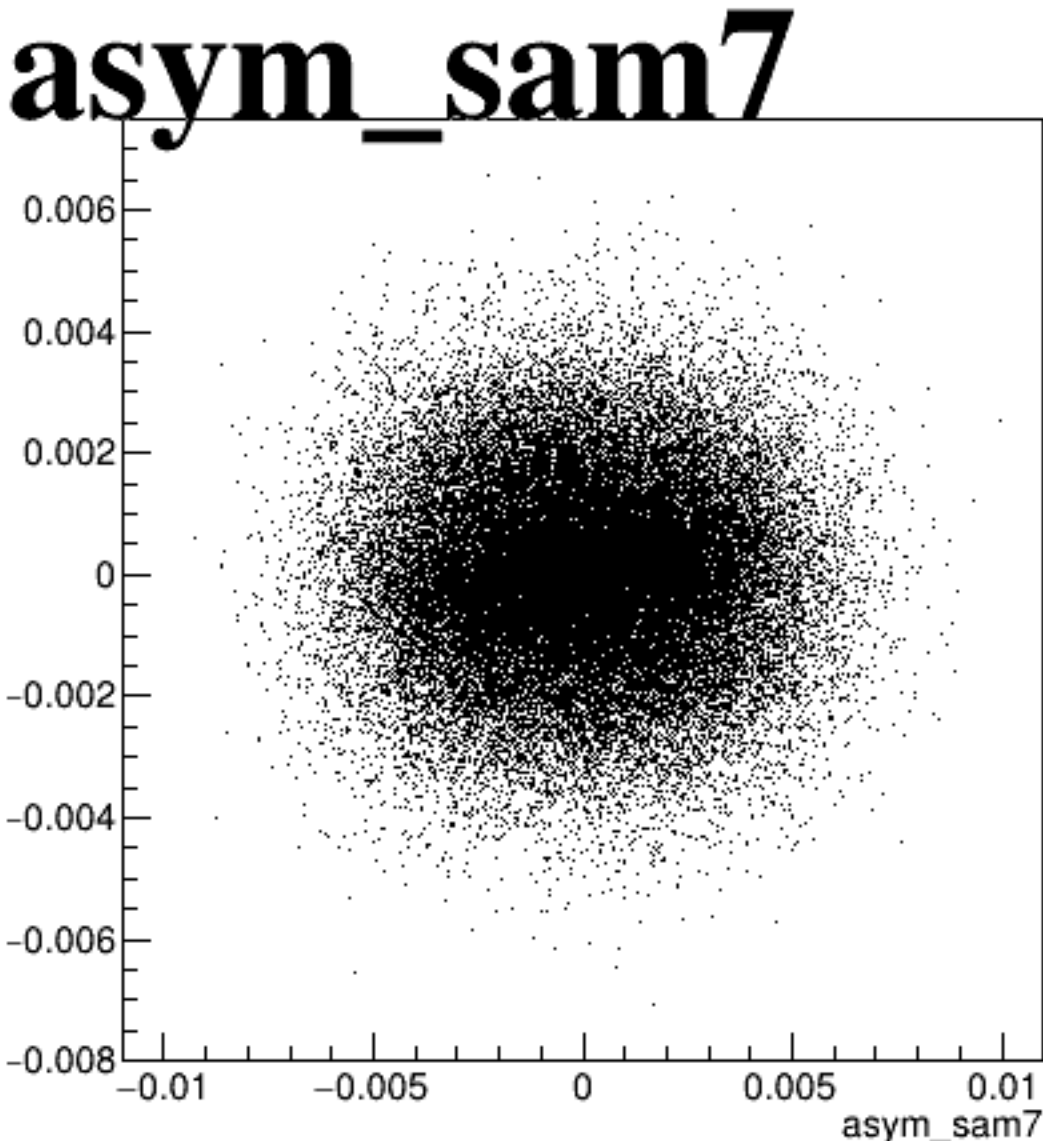
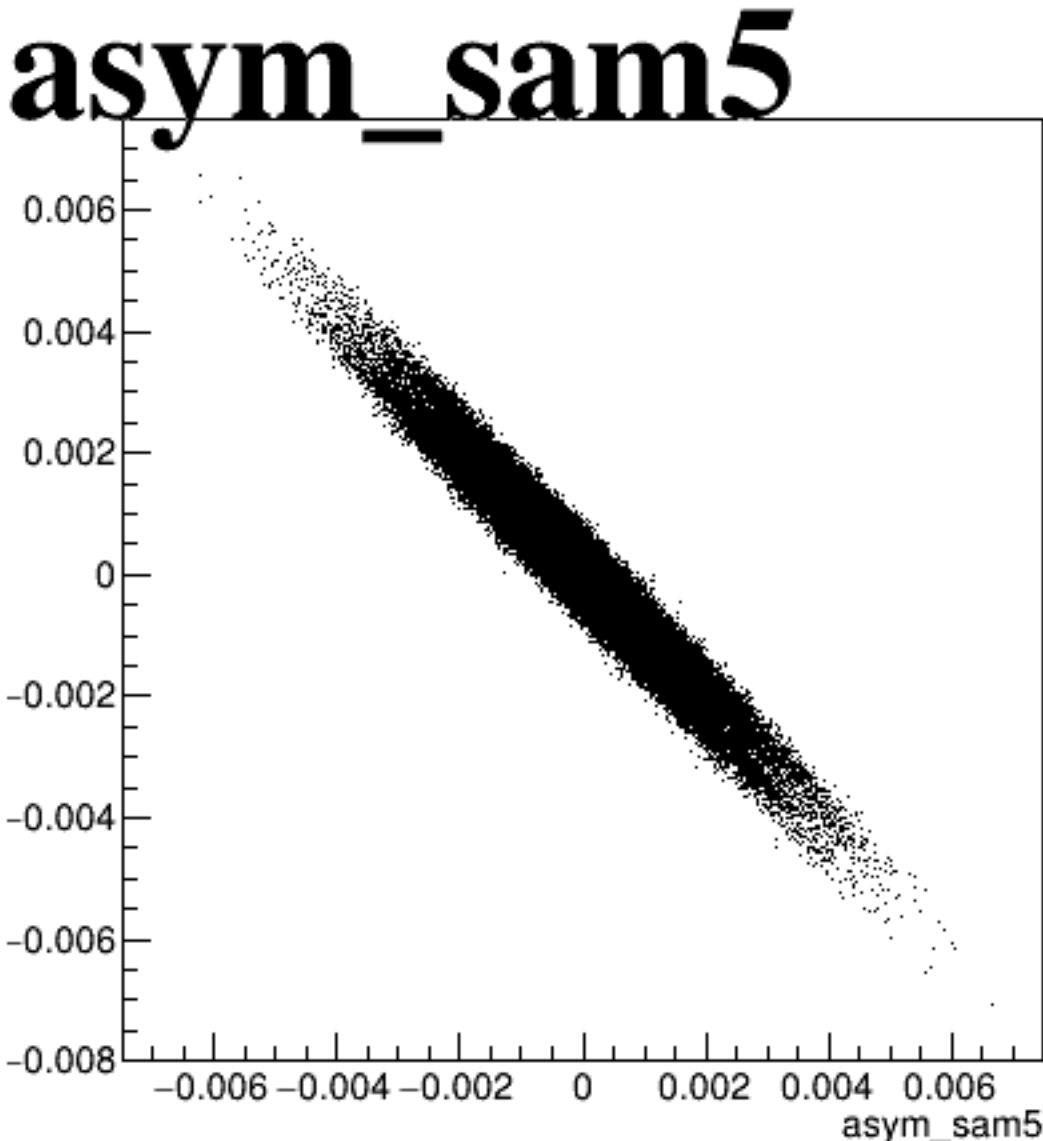
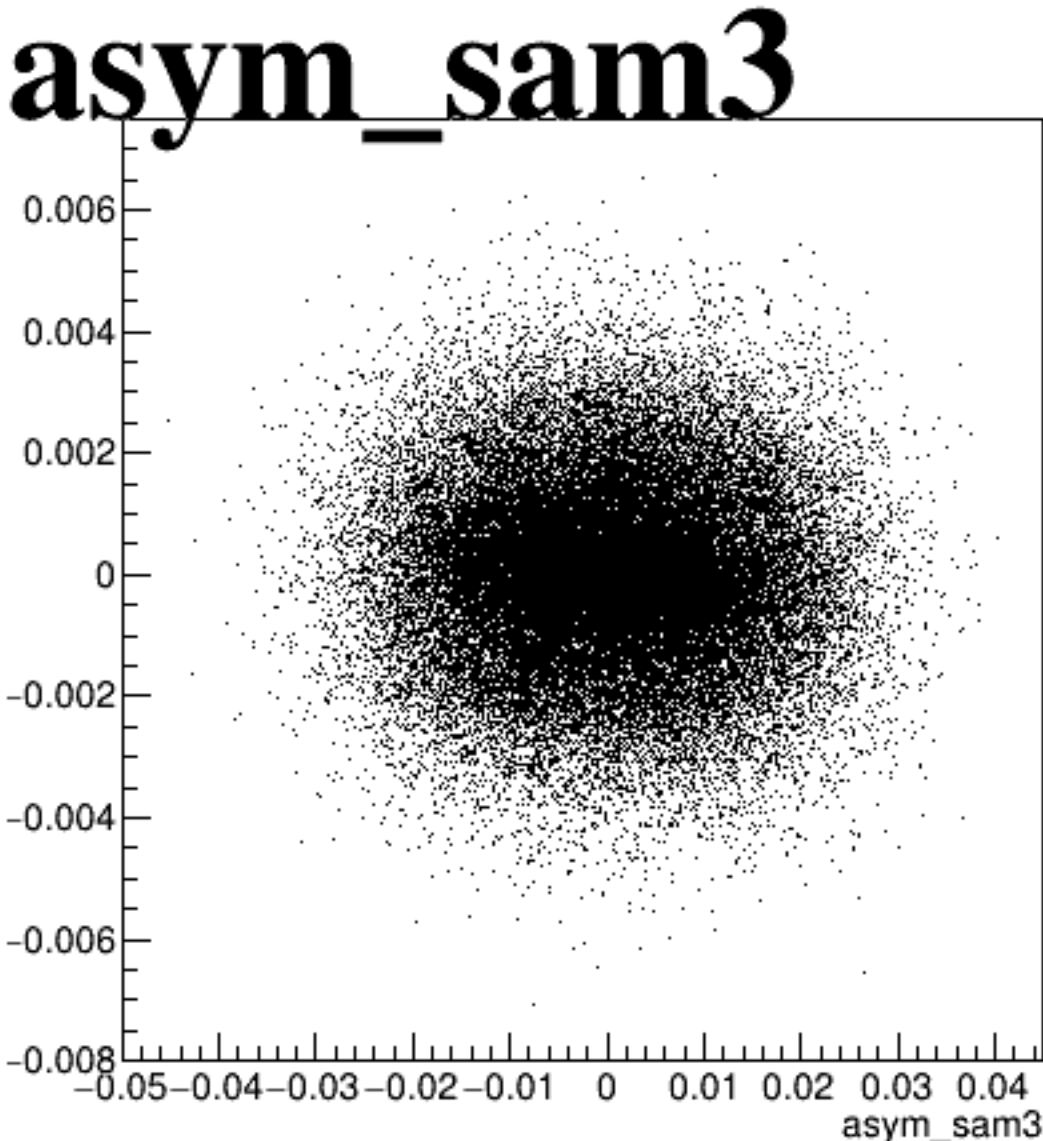
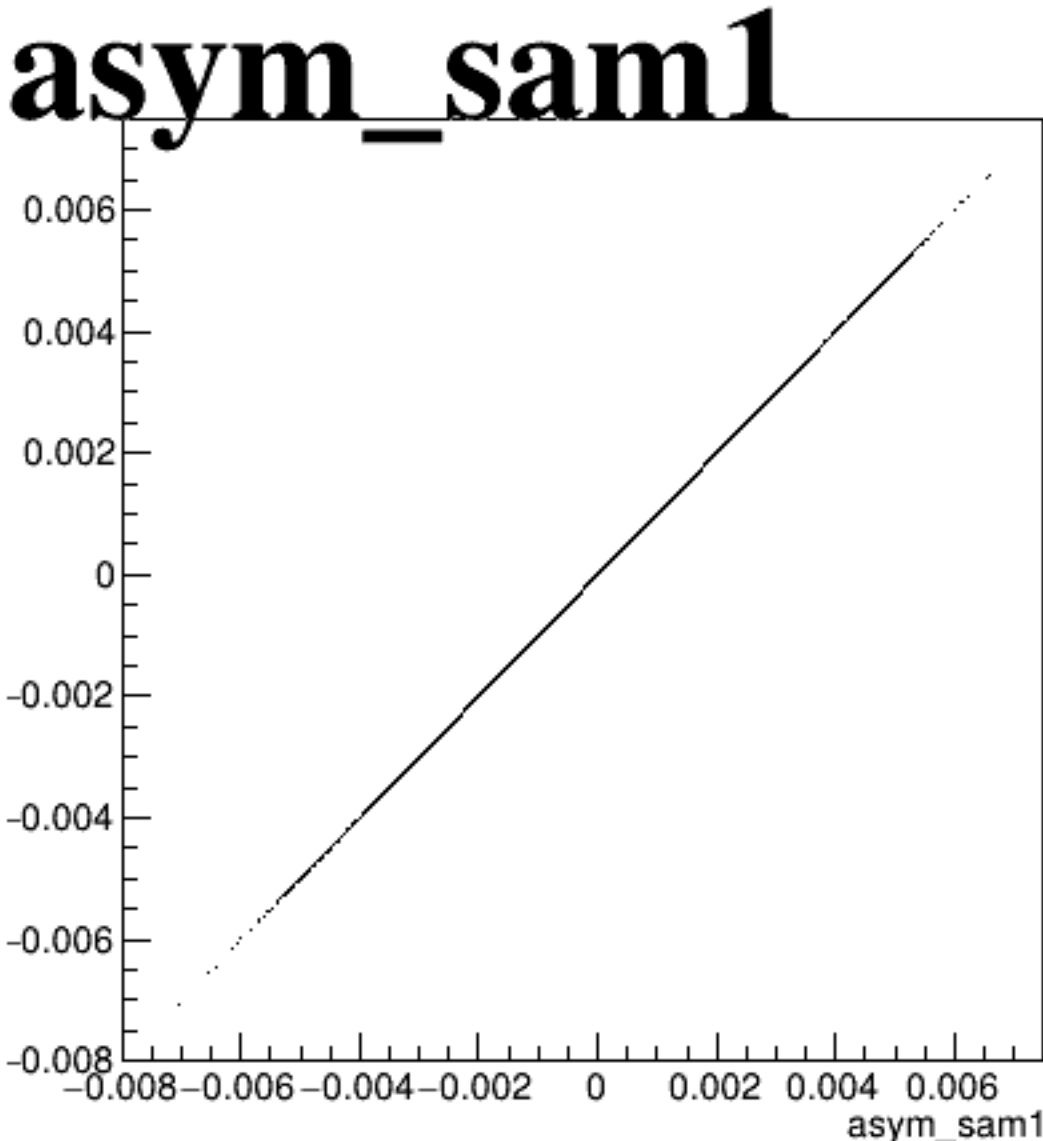
asym_sam5



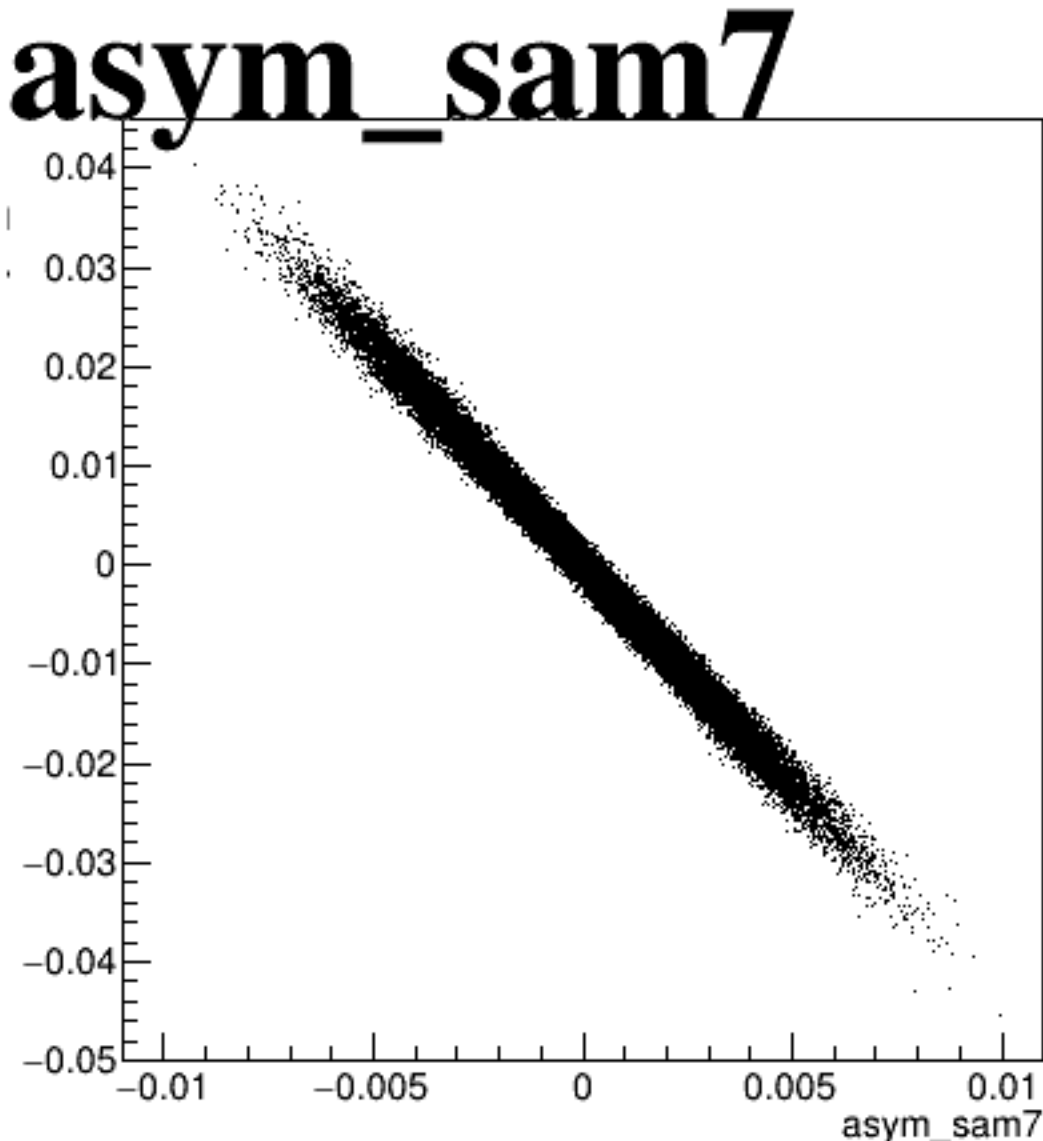
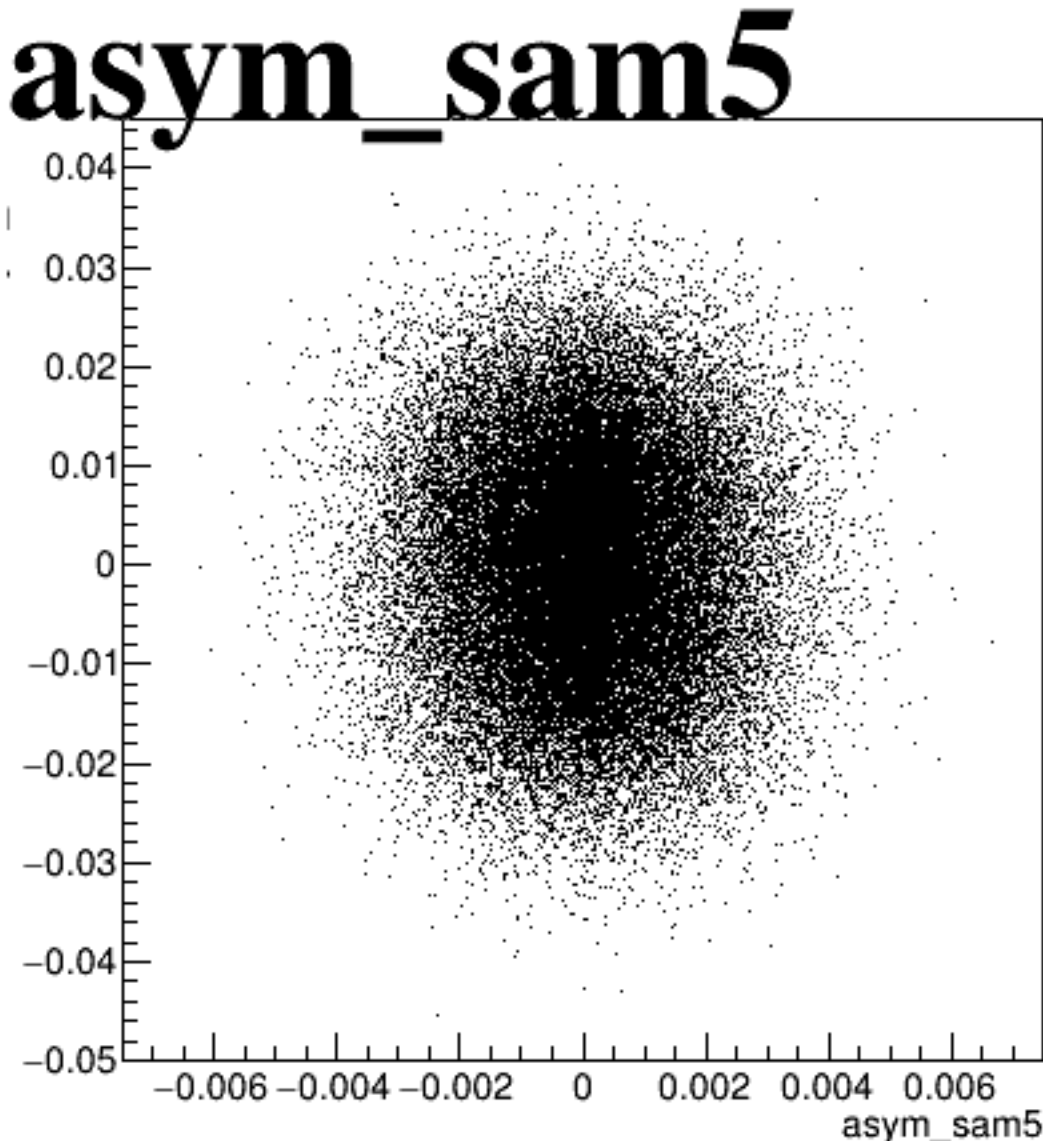
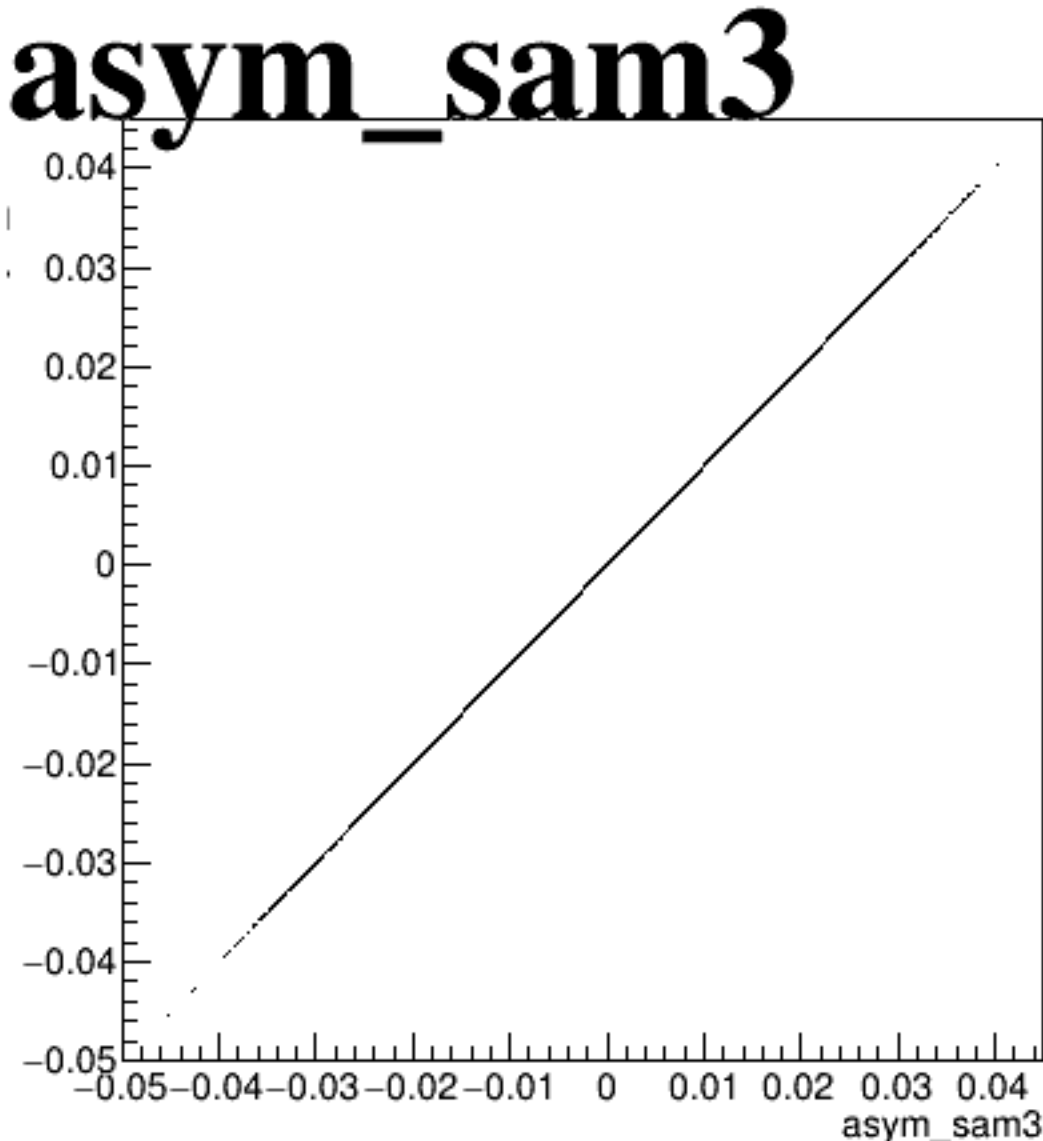
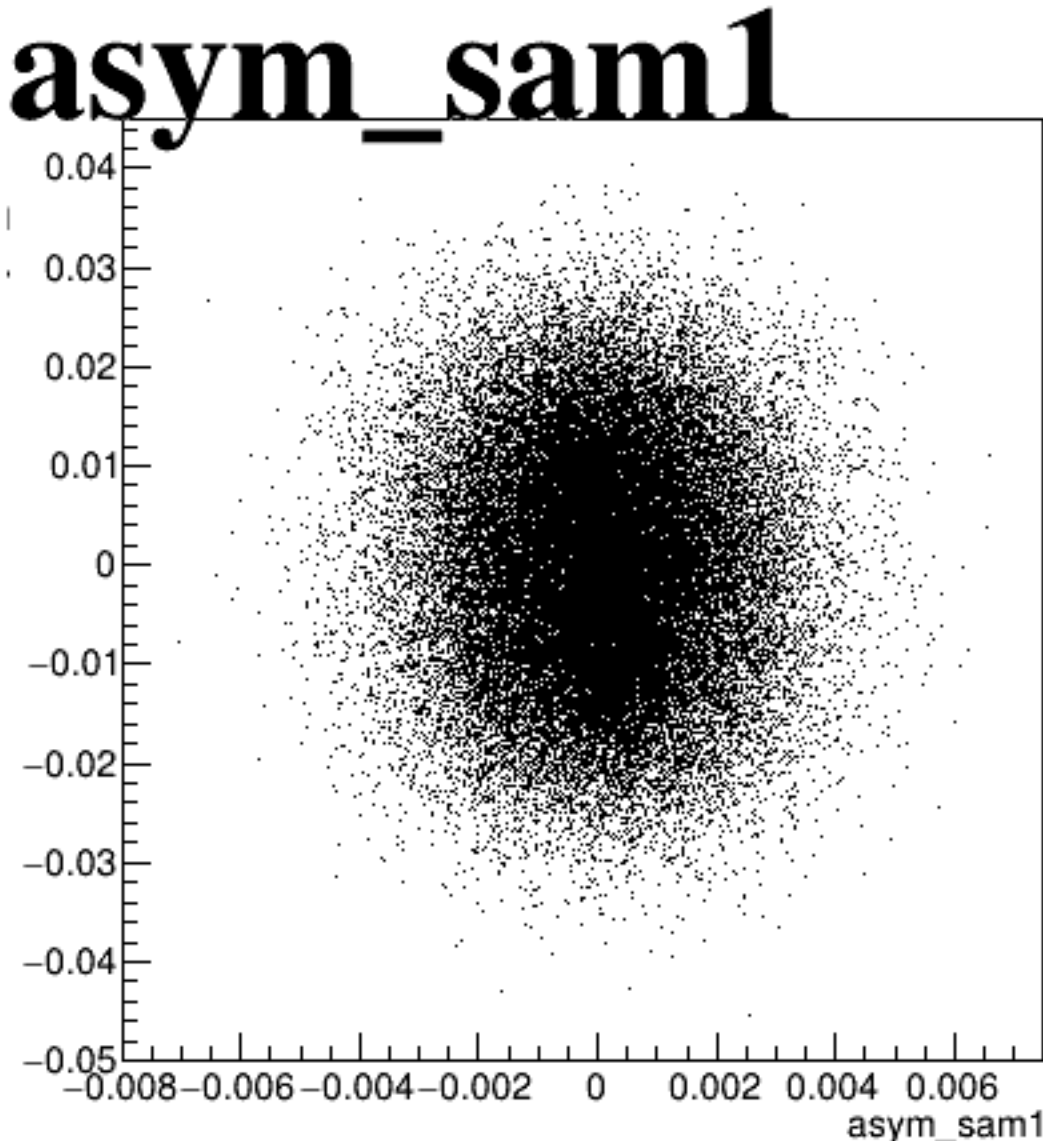
asym_sam7



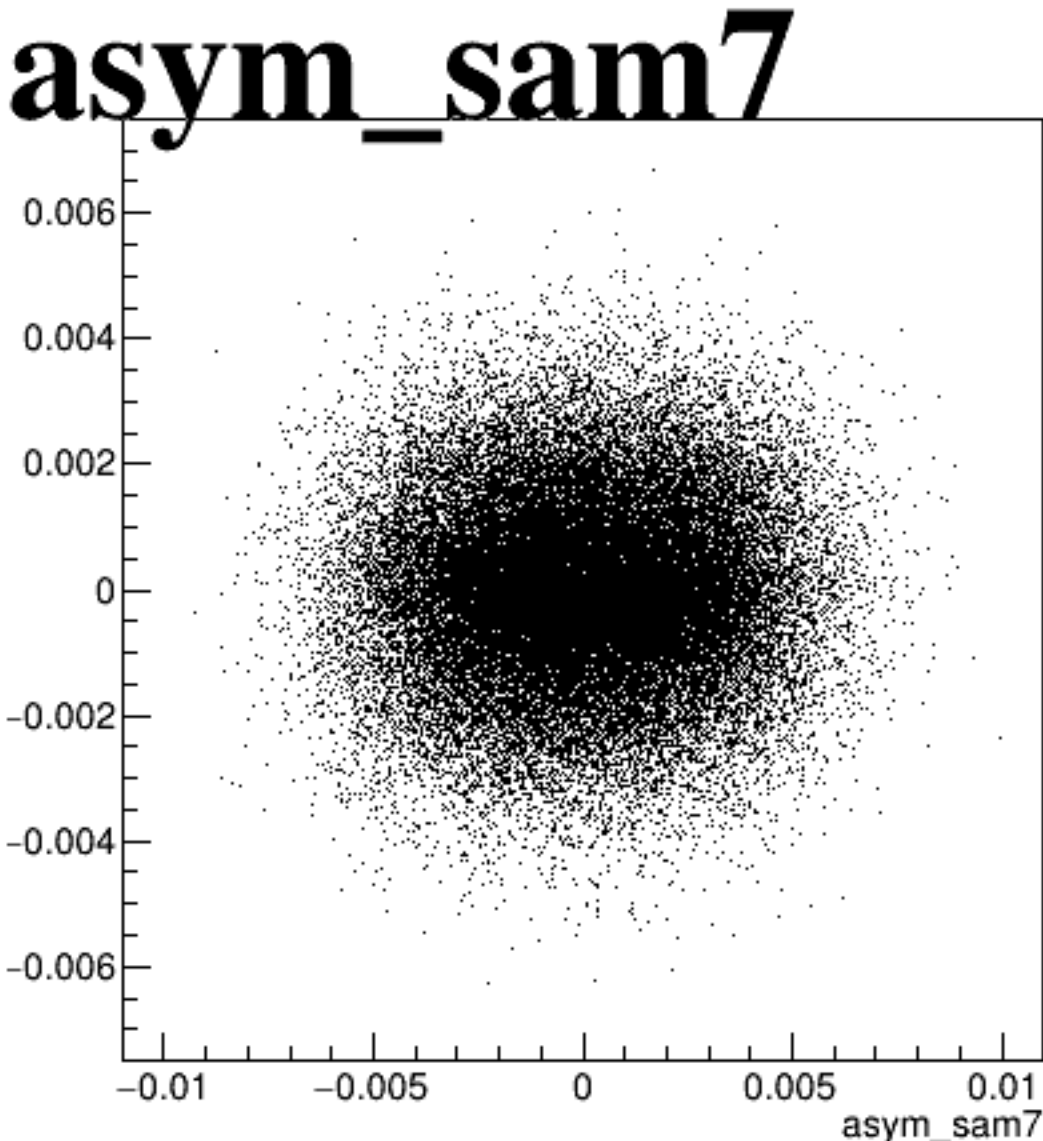
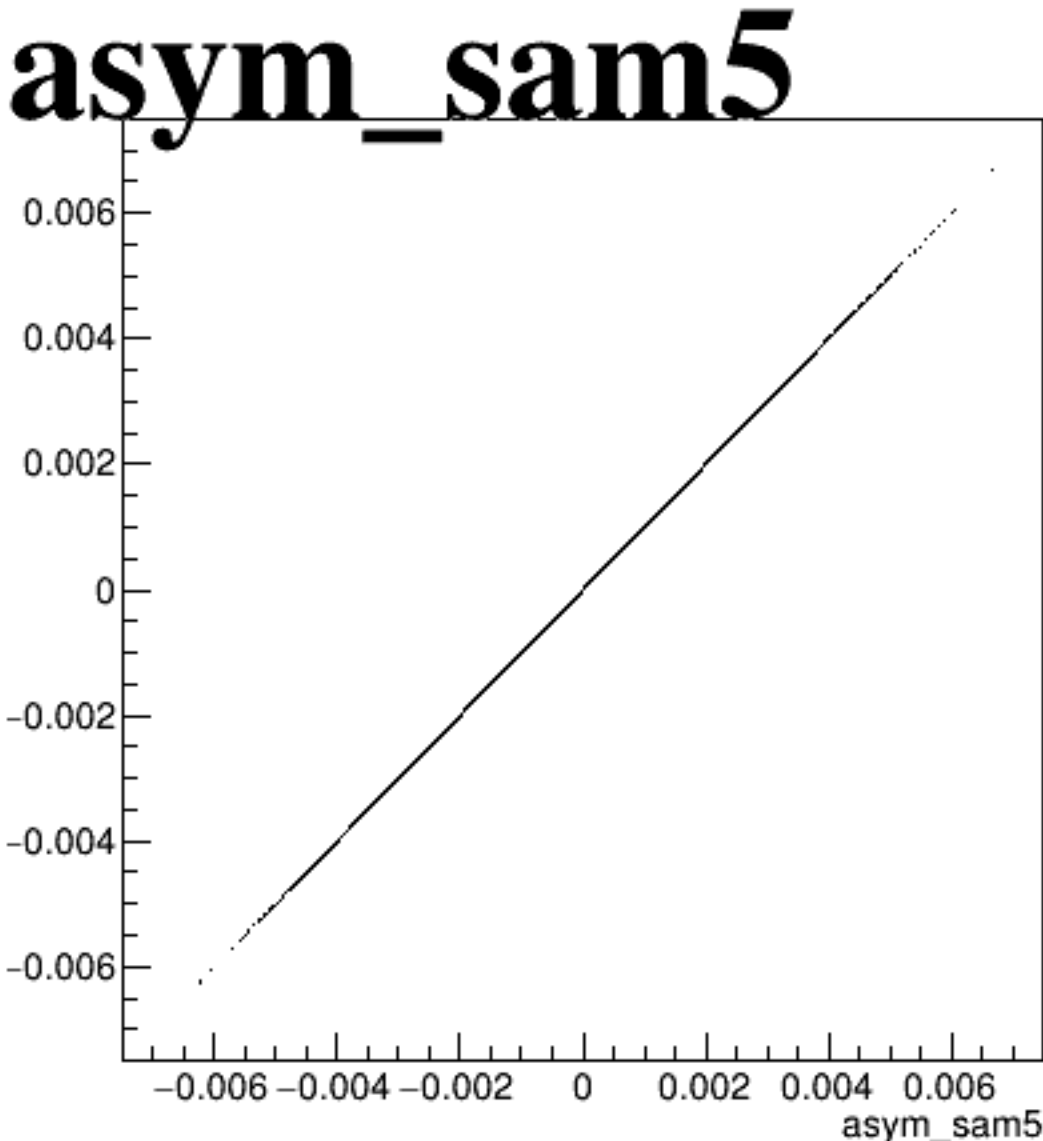
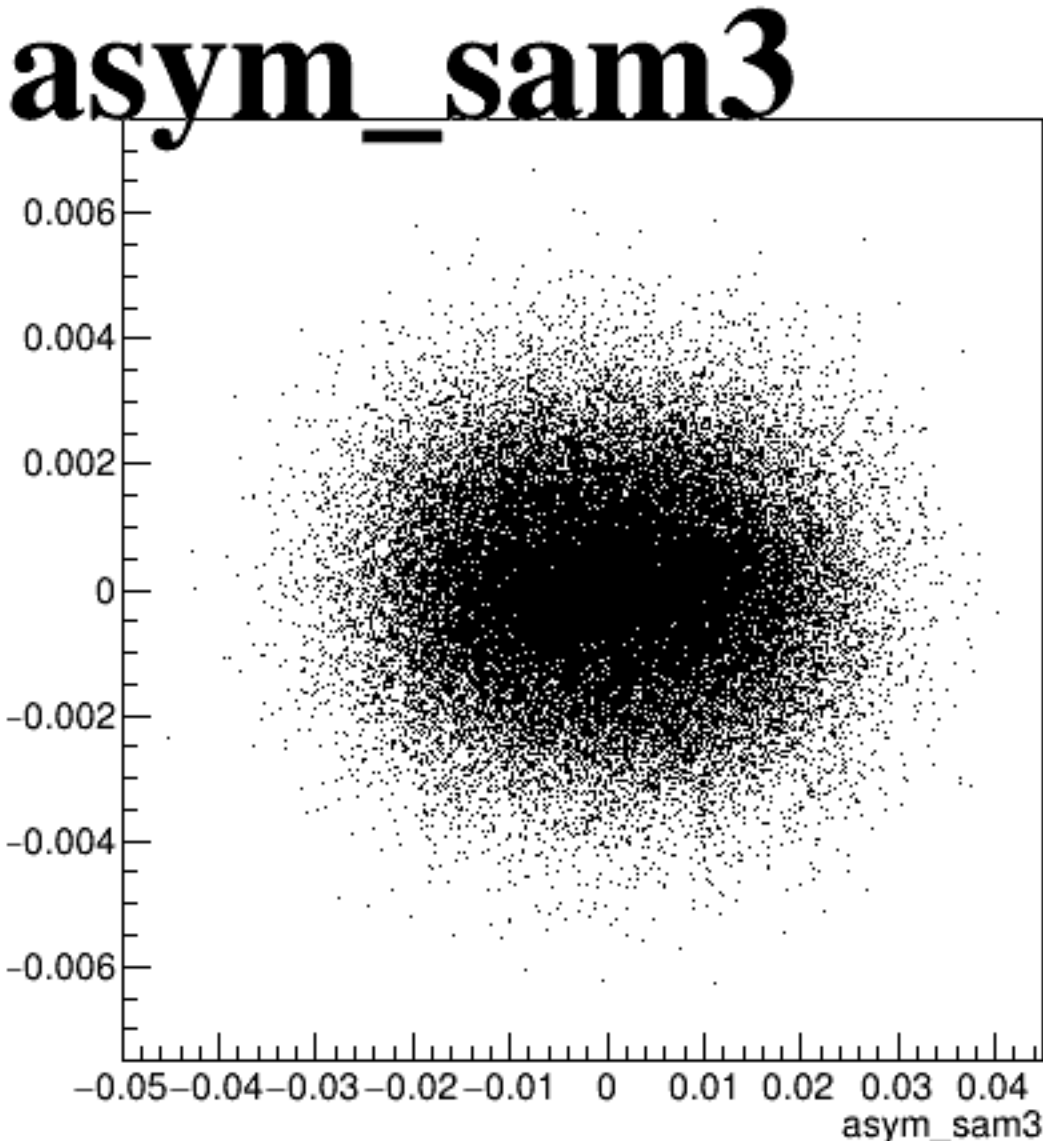
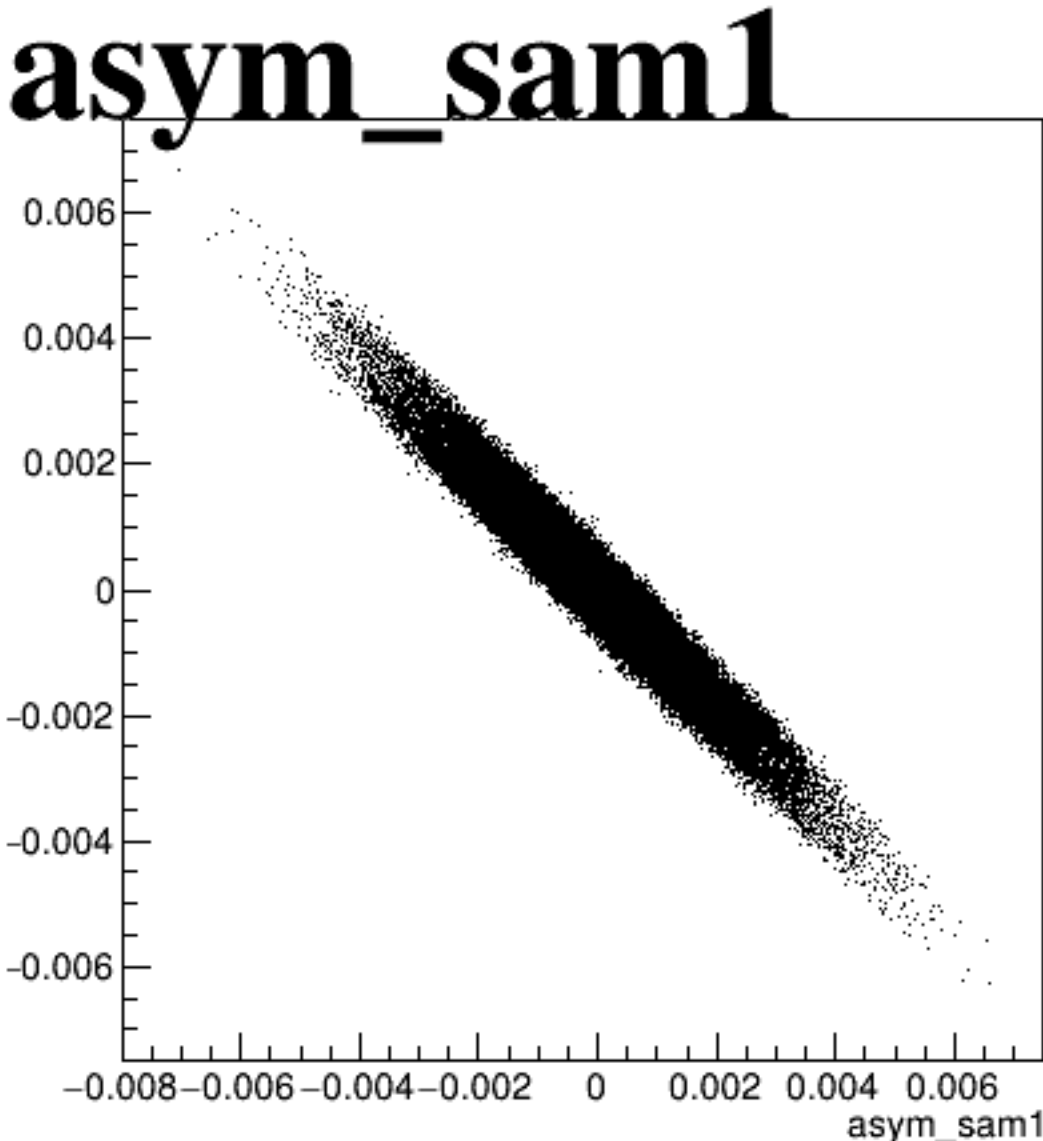
asym_sam1



asym_sam3



asym_sam5



asym_sam7

