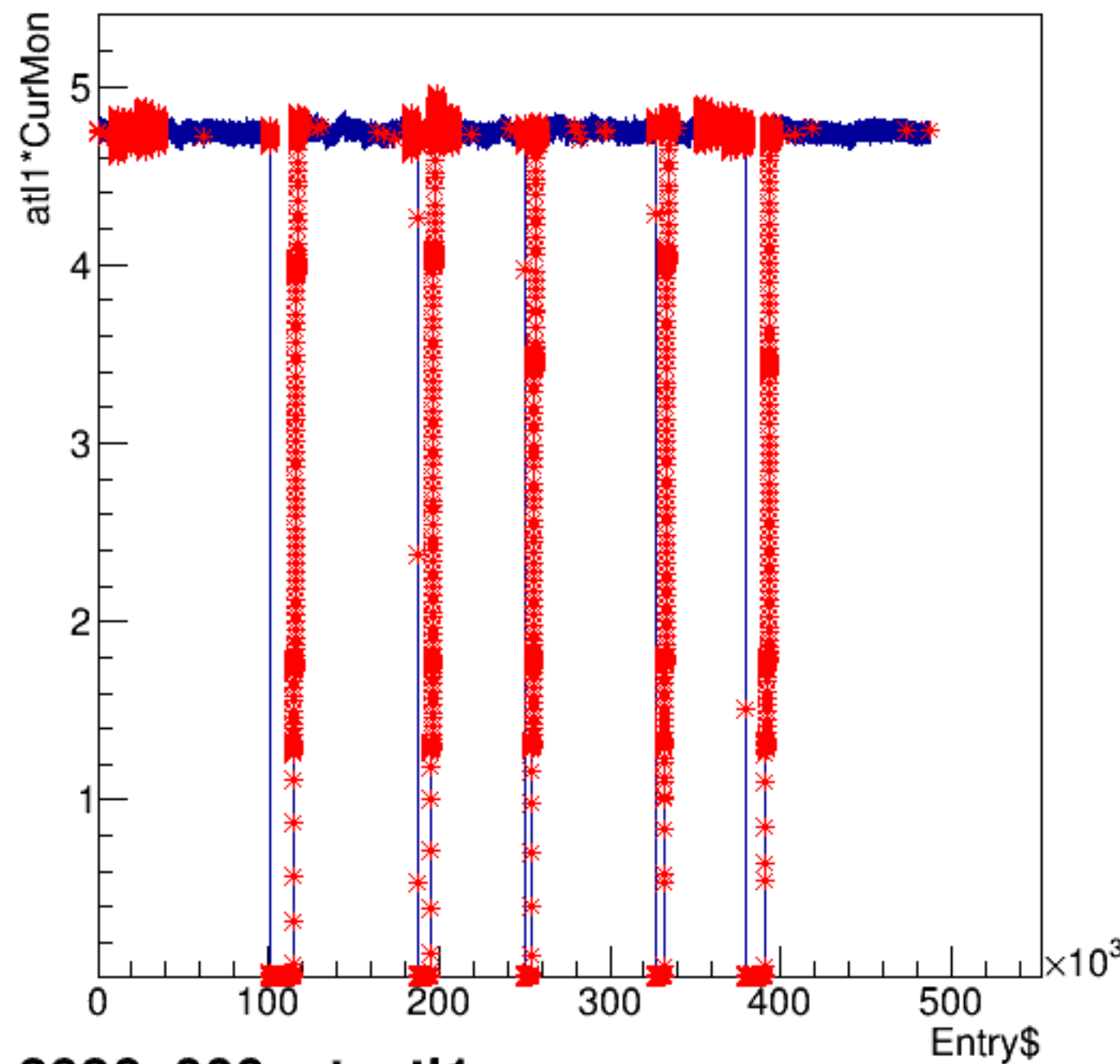
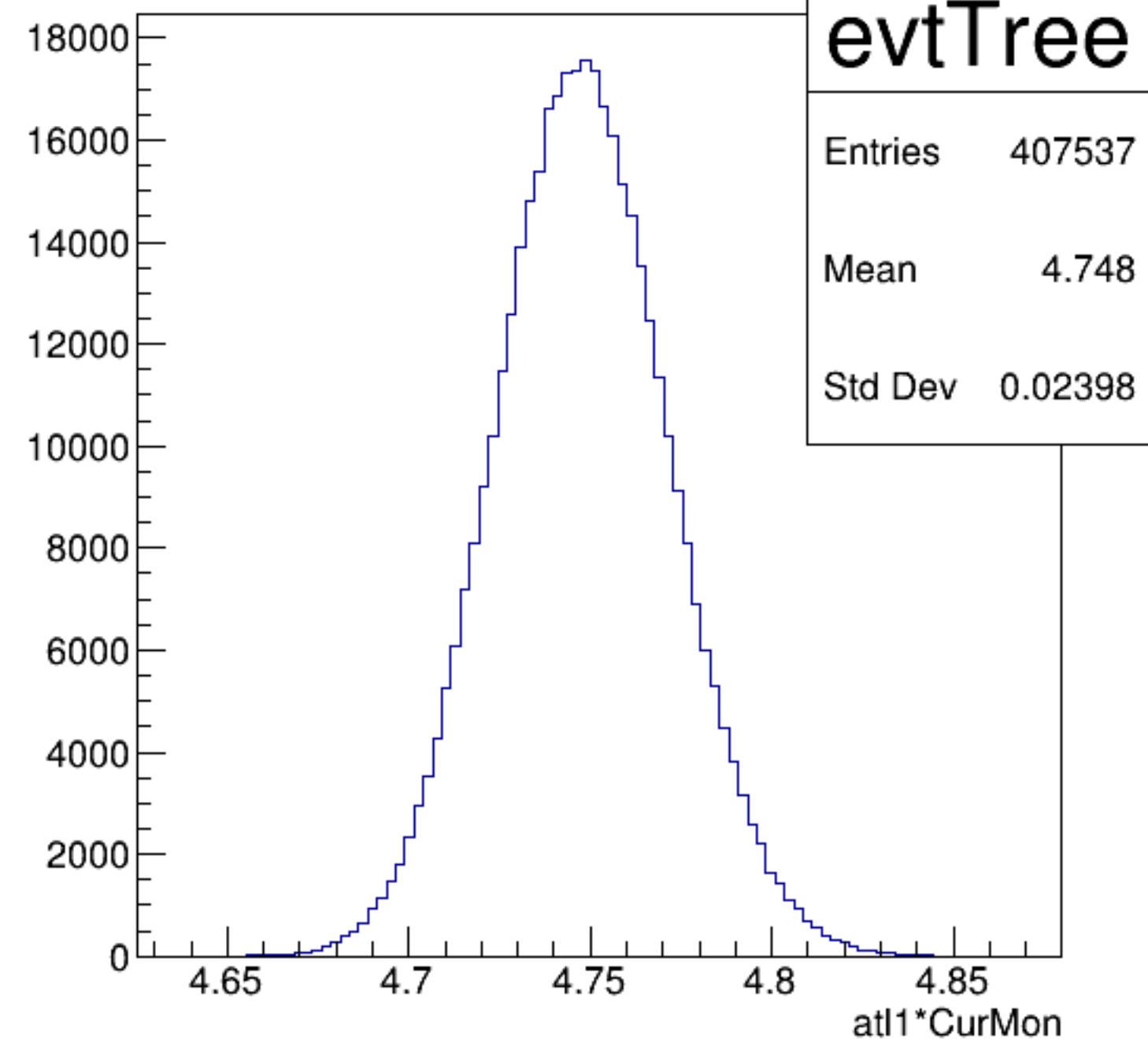


atl1*CurMon:Entry\$

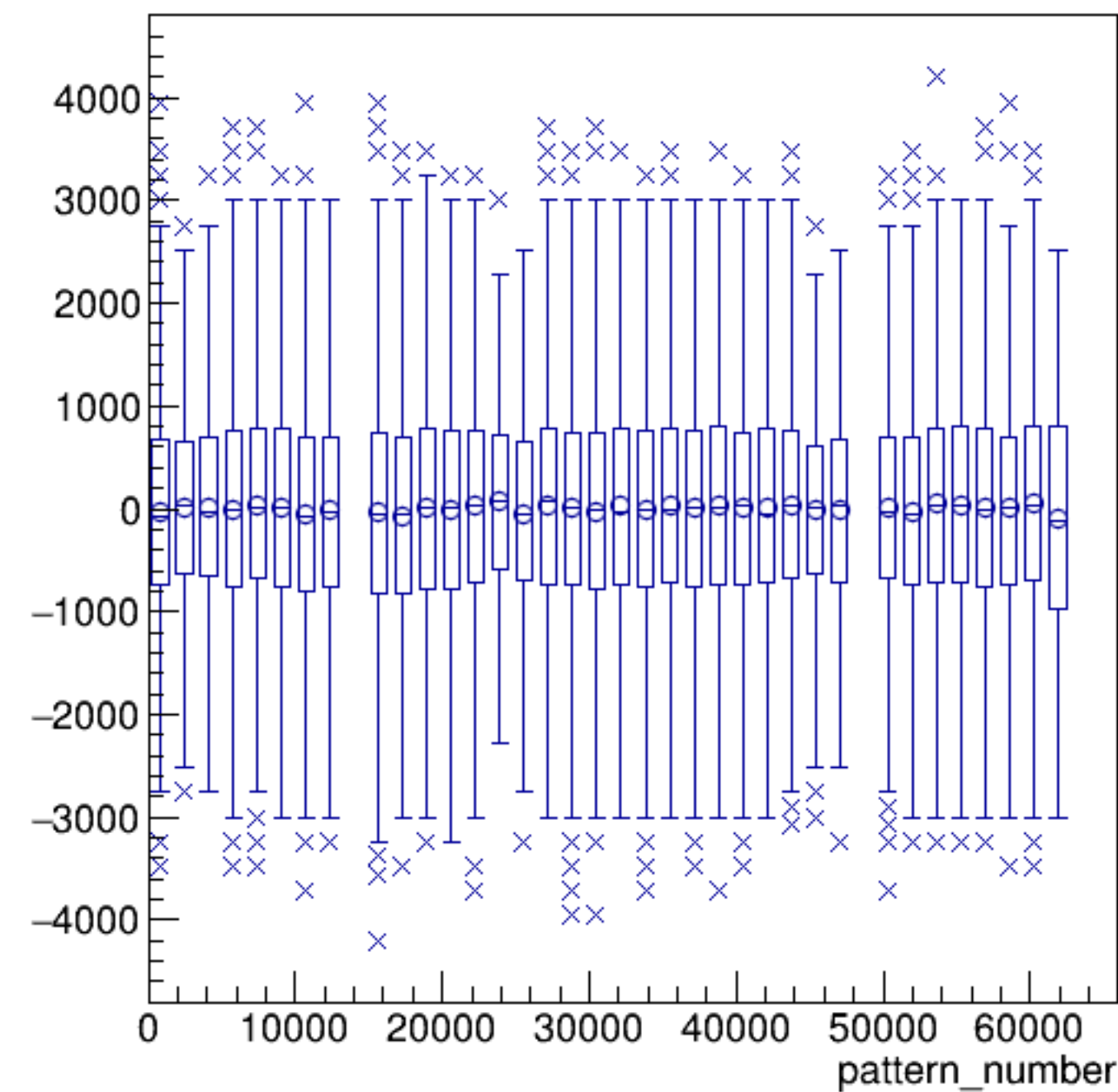


run3936_000_at_atl1.png

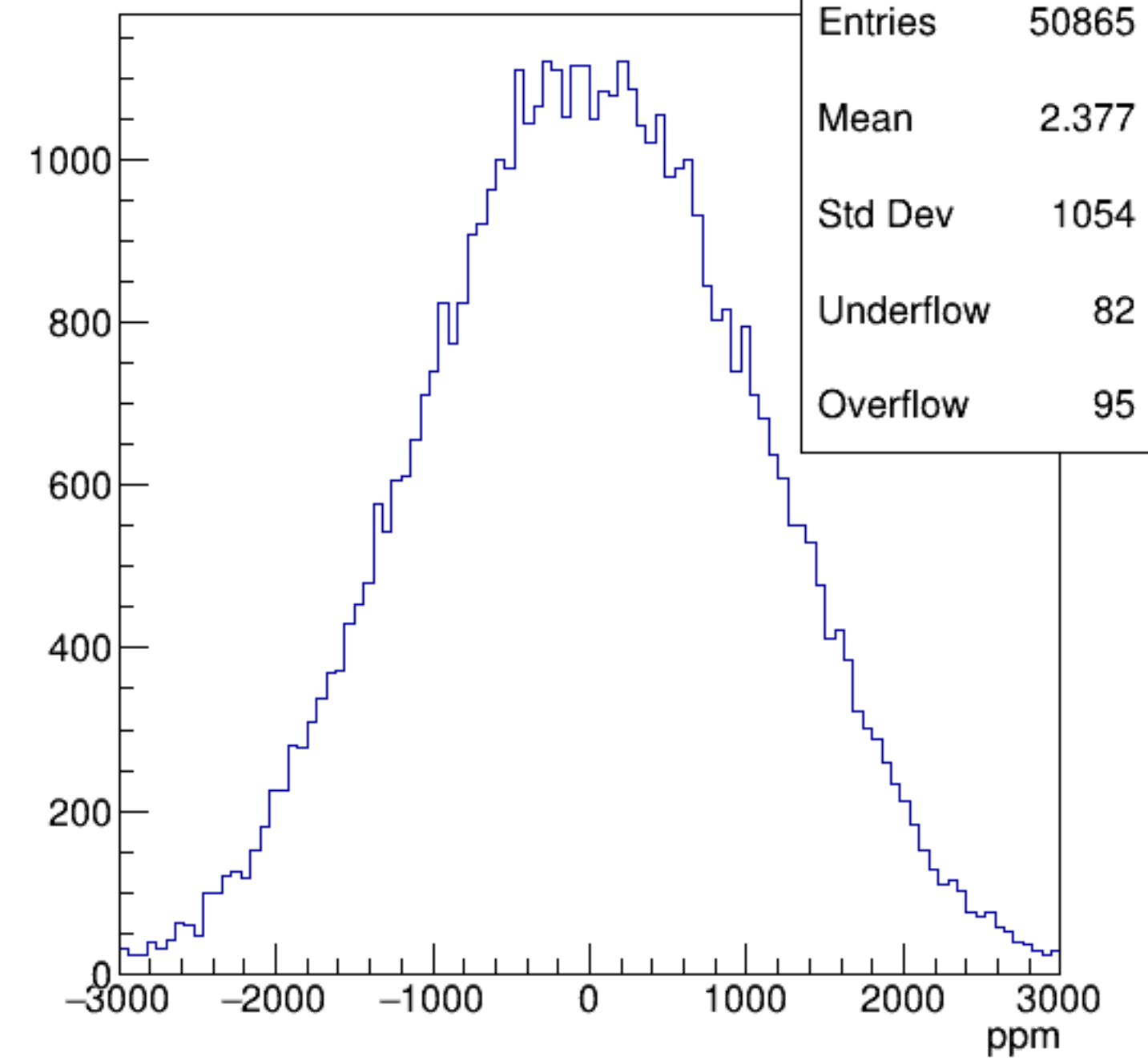
atl1*CurMon {ErrorFlag==0}



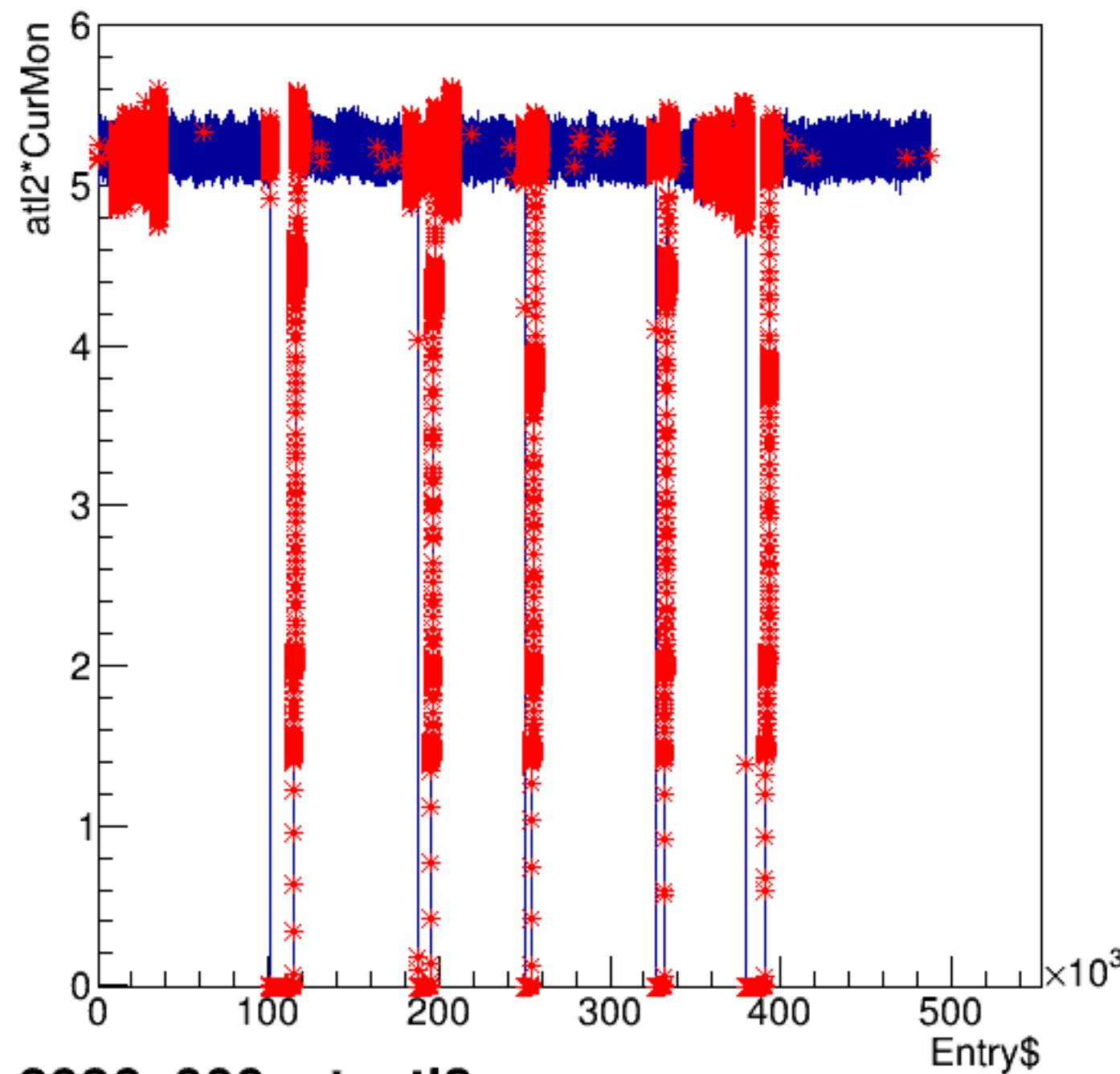
asym_atl1/ppm:pattern_number {ErrorFlag==0}



asym_atl1

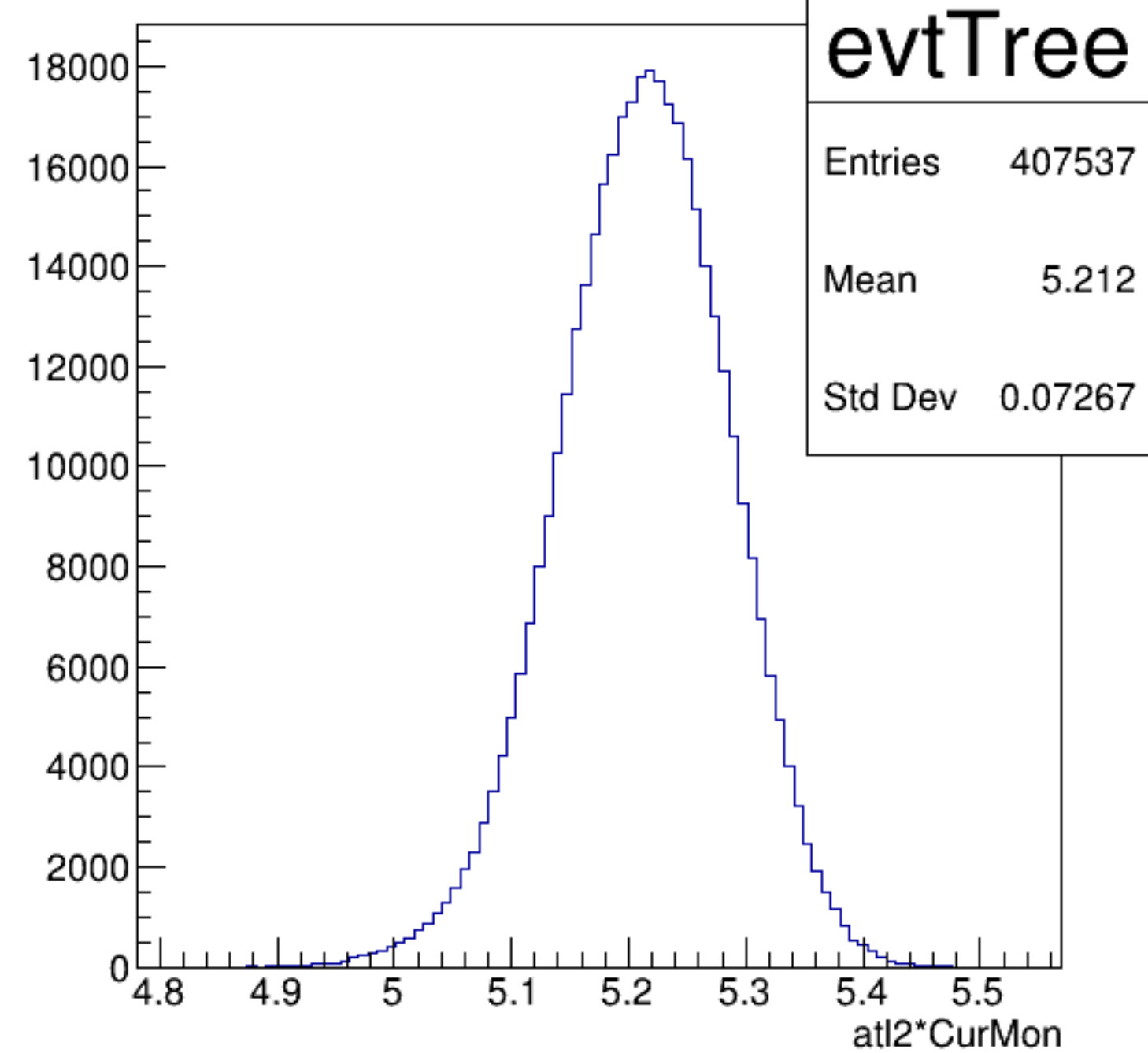


atl2*CurMon:Entry\$



run3936_000_at_atl2.png

atl2*CurMon {ErrorFlag==0}



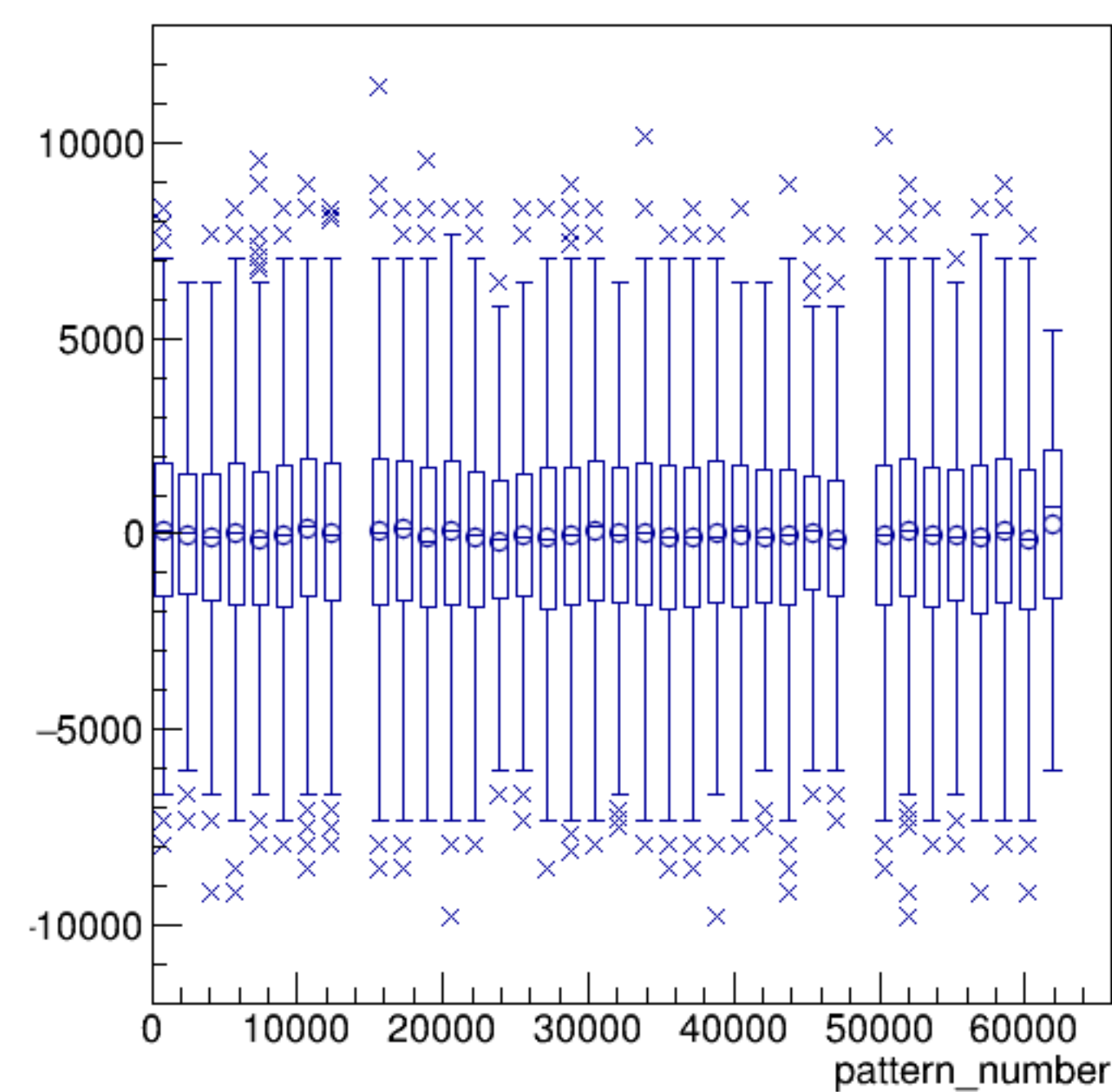
evtTree

Entries 407537

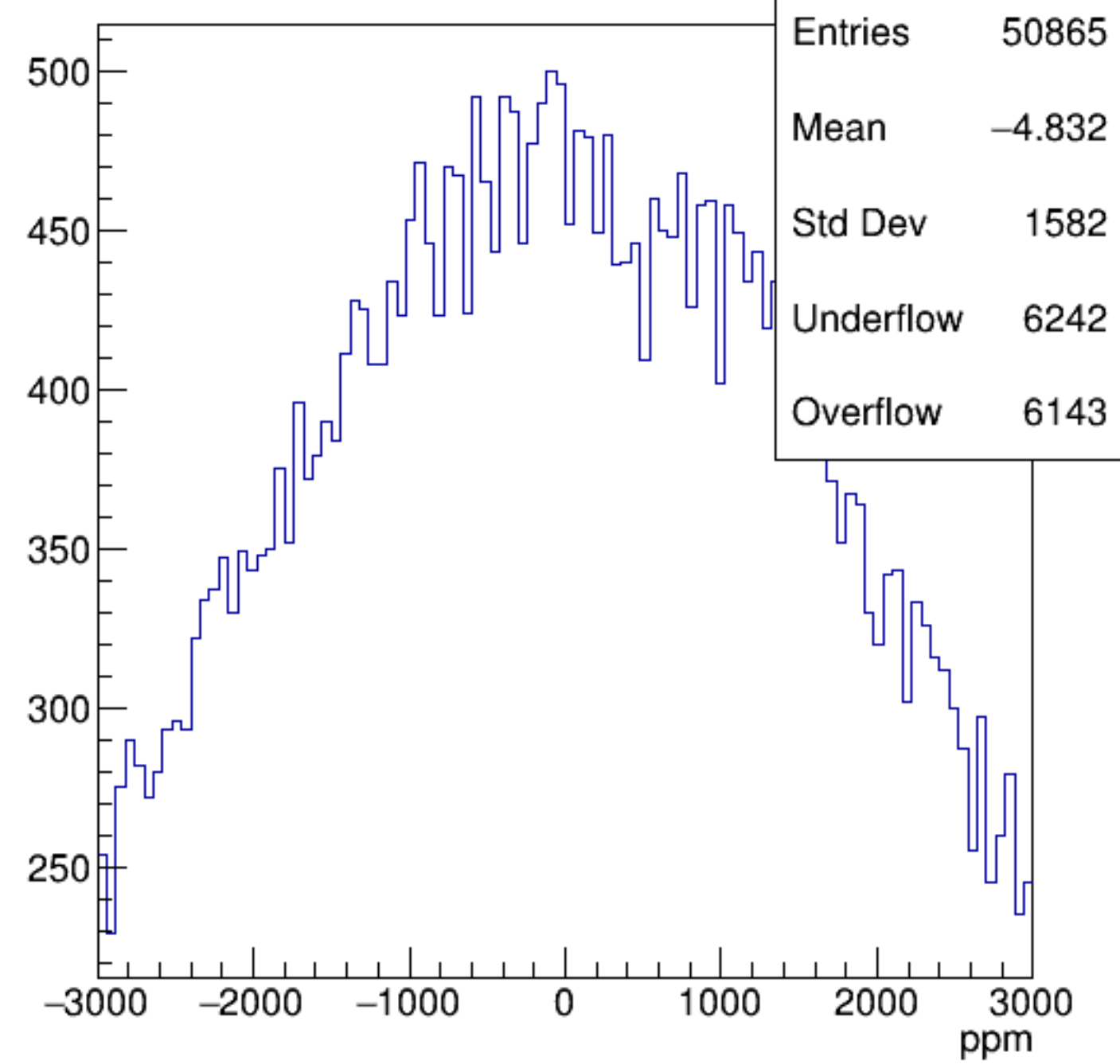
Mean 5.212

Std Dev 0.07267

asym_atl2/ppm:pattern_number {ErrorFlag==0}

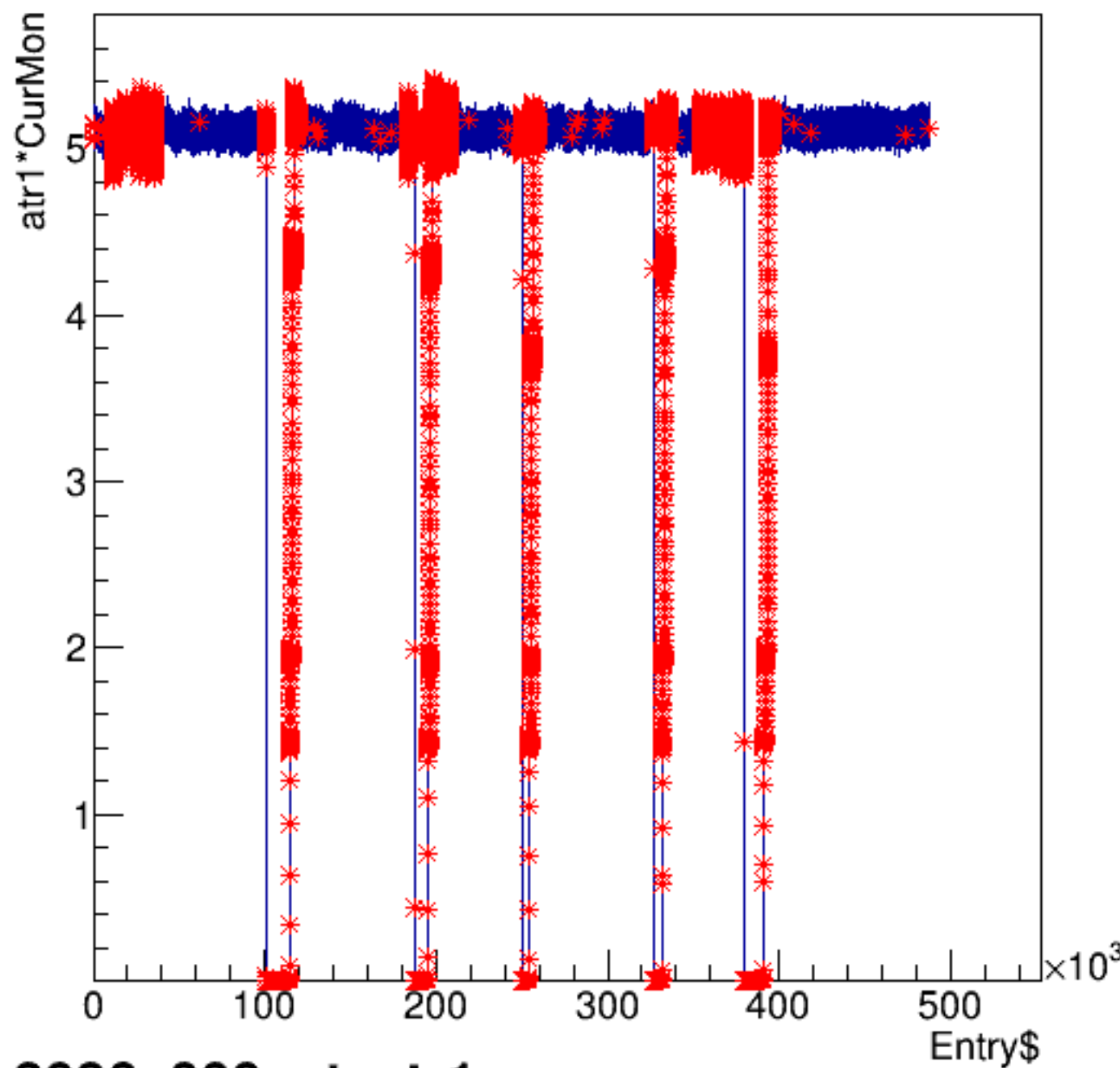


asym_atl2



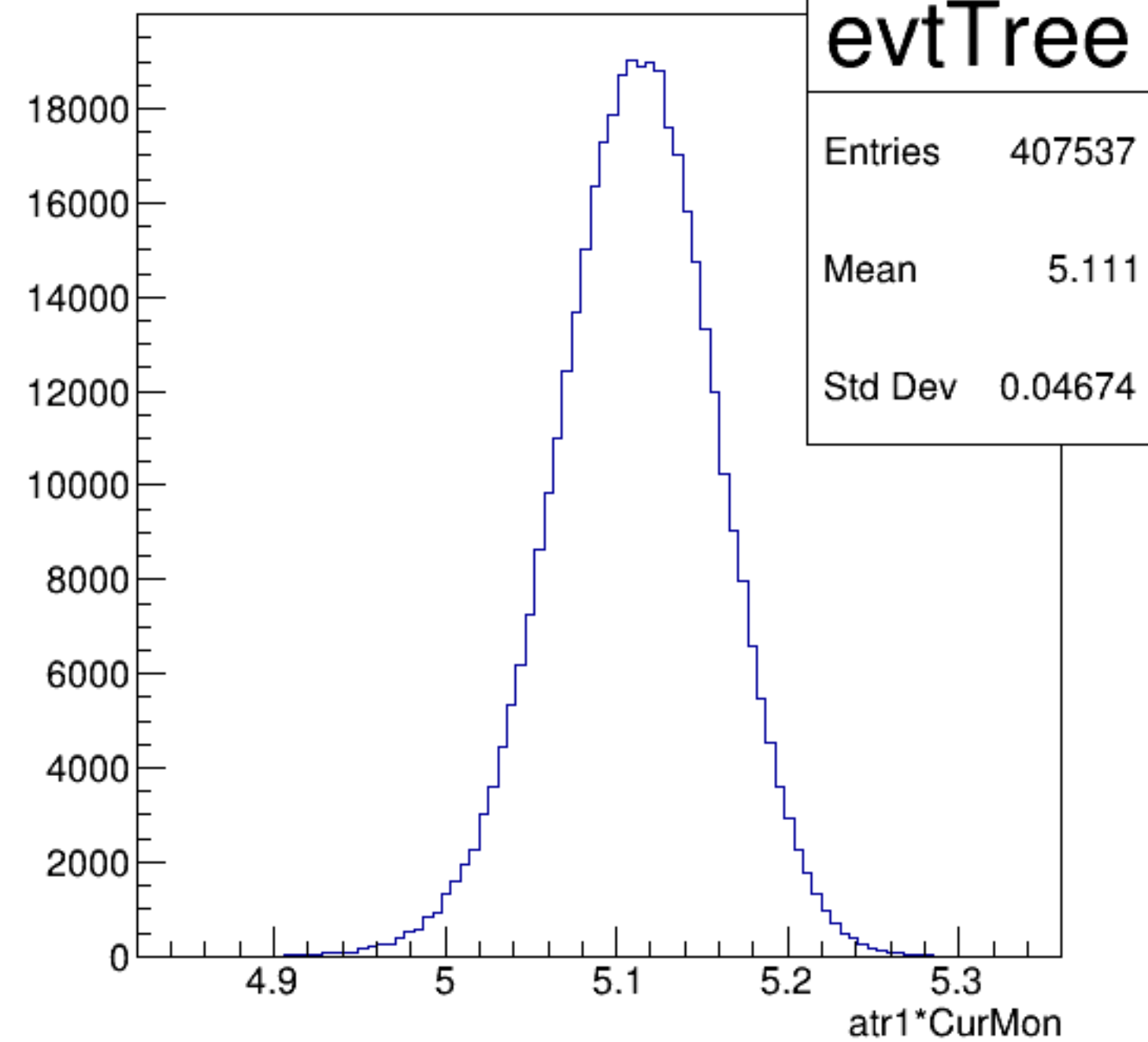
Entries	50865
Mean	-4.832
Std Dev	1582
Underflow	6242
Overflow	6143

atr1*CurMon:Entry\$

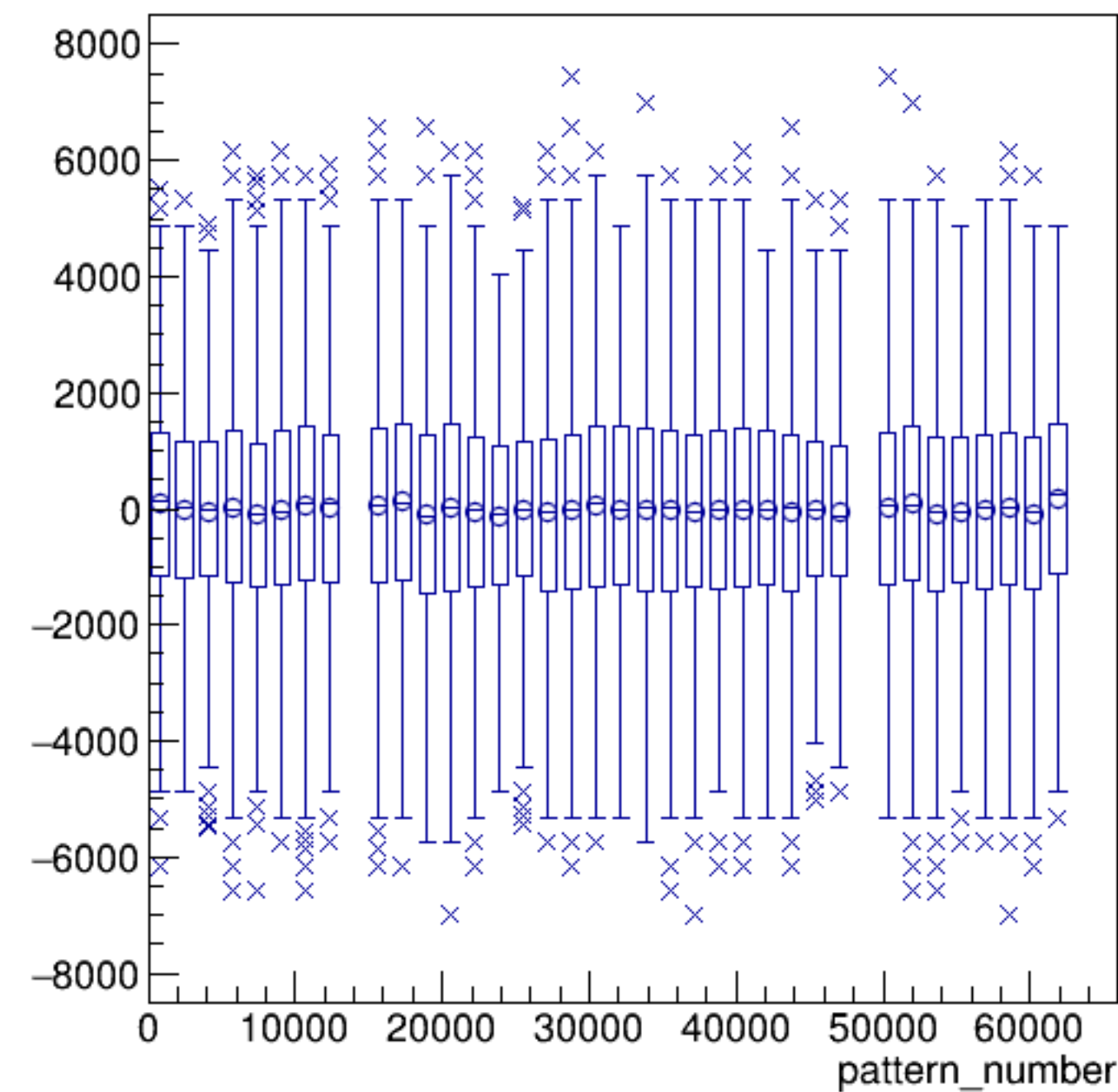


run3936_000_at_atr1.png

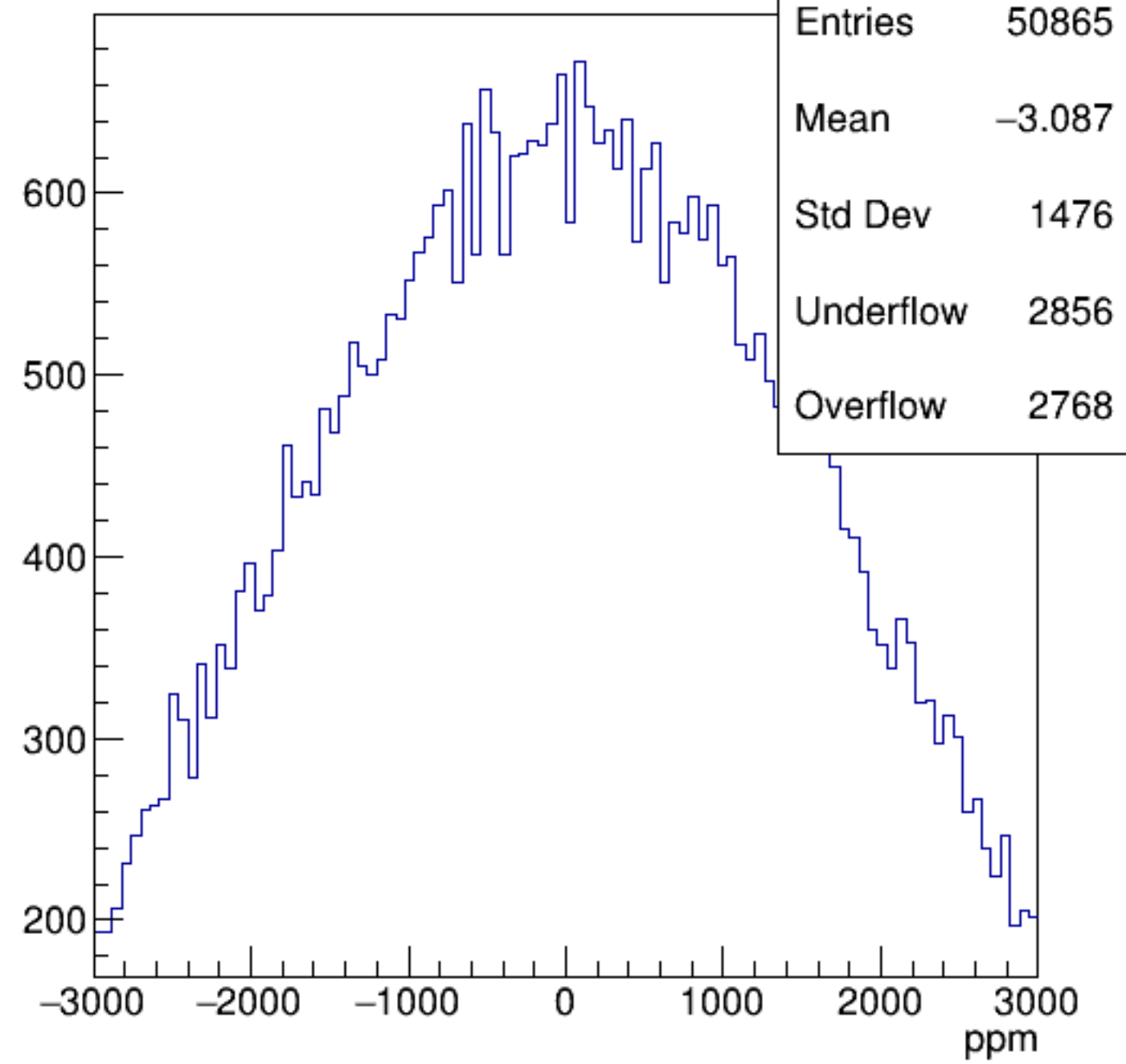
atr1*CurMon {ErrorFlag==0}

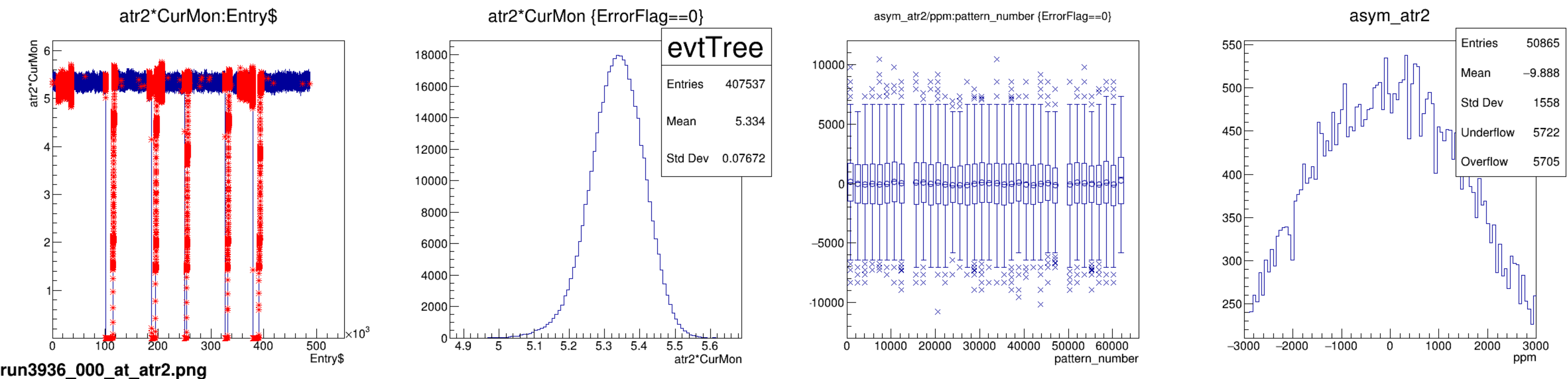


asym_atr1/ppm:pattern_number {ErrorFlag==0}

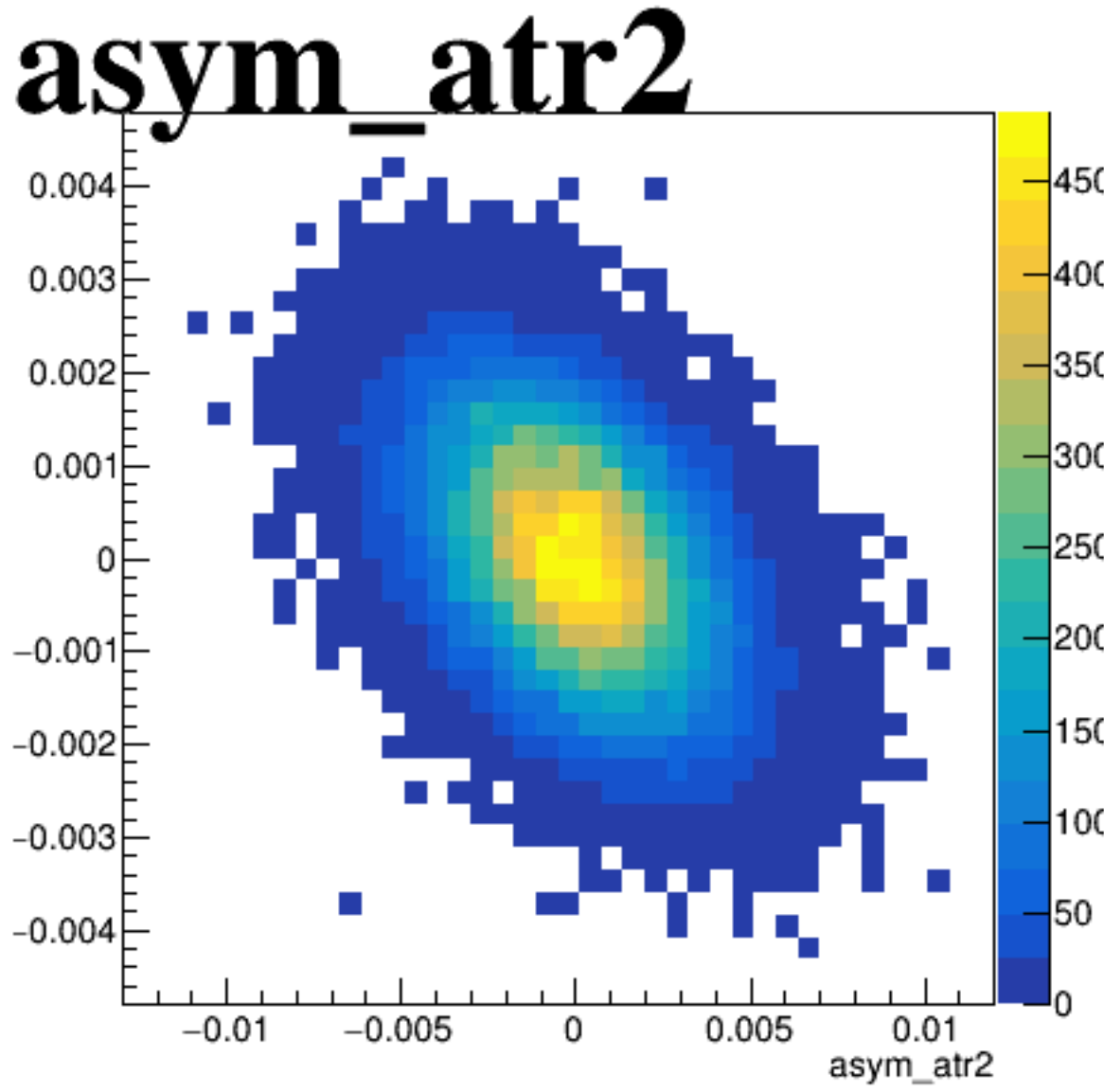
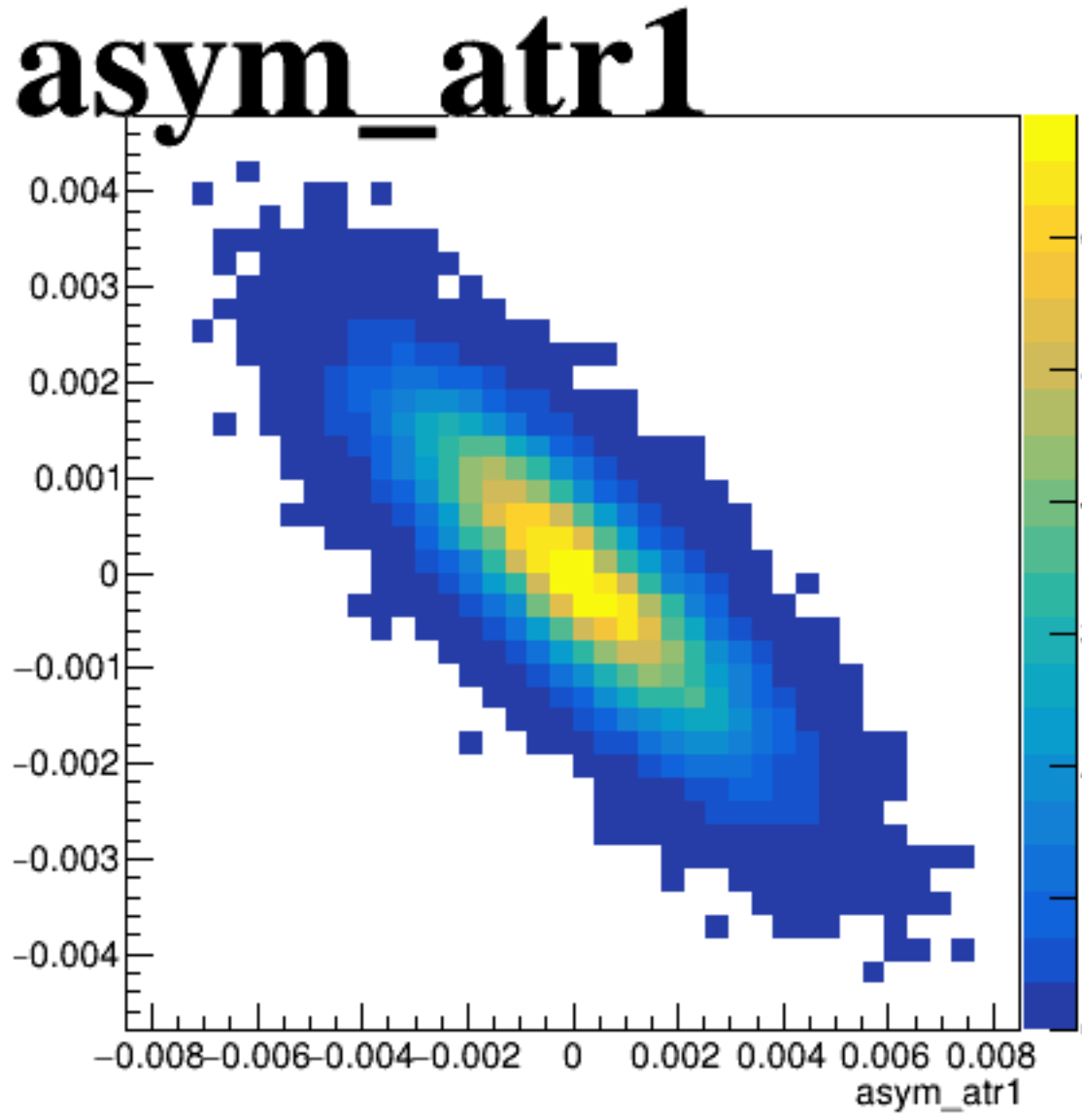
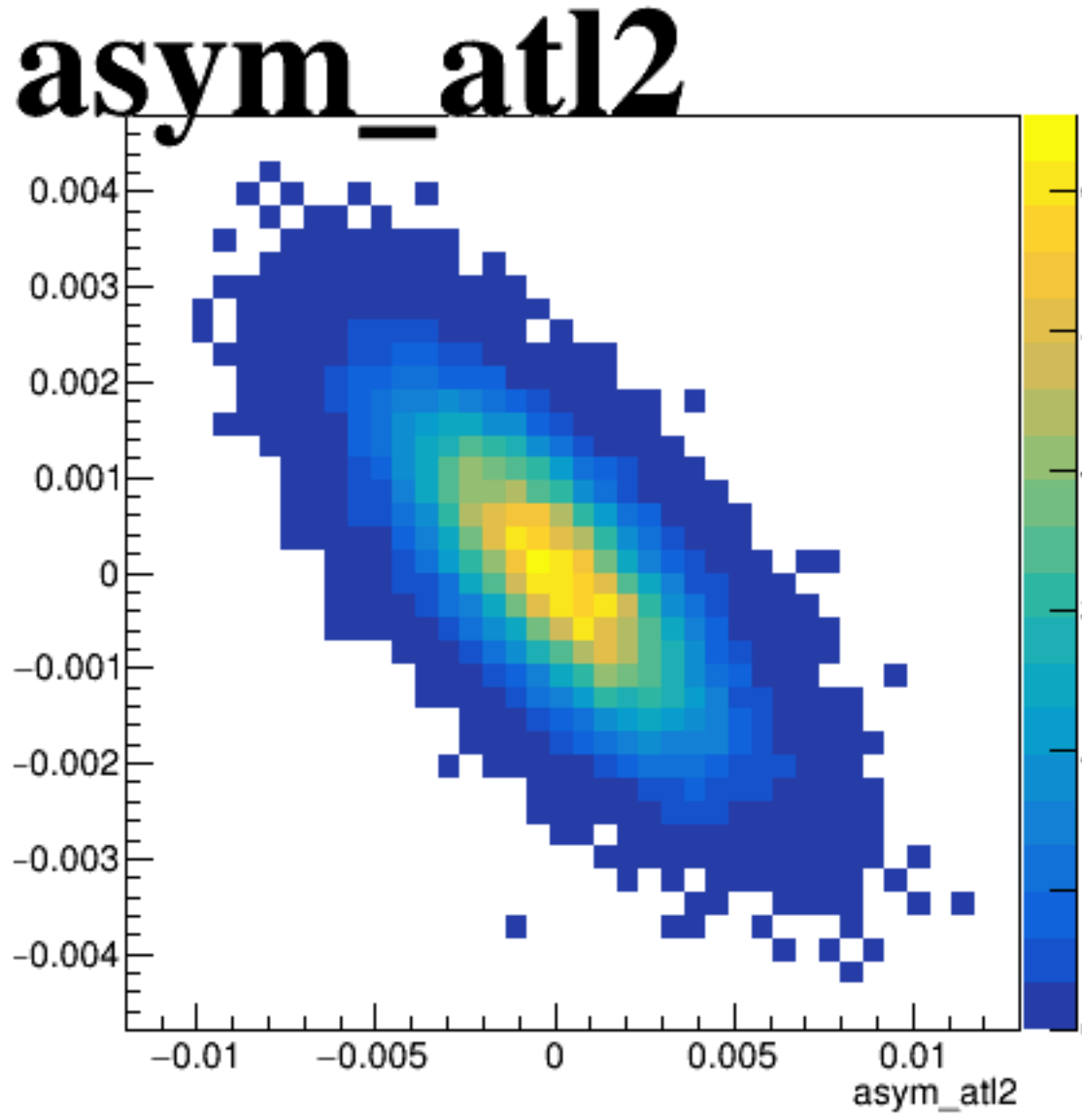
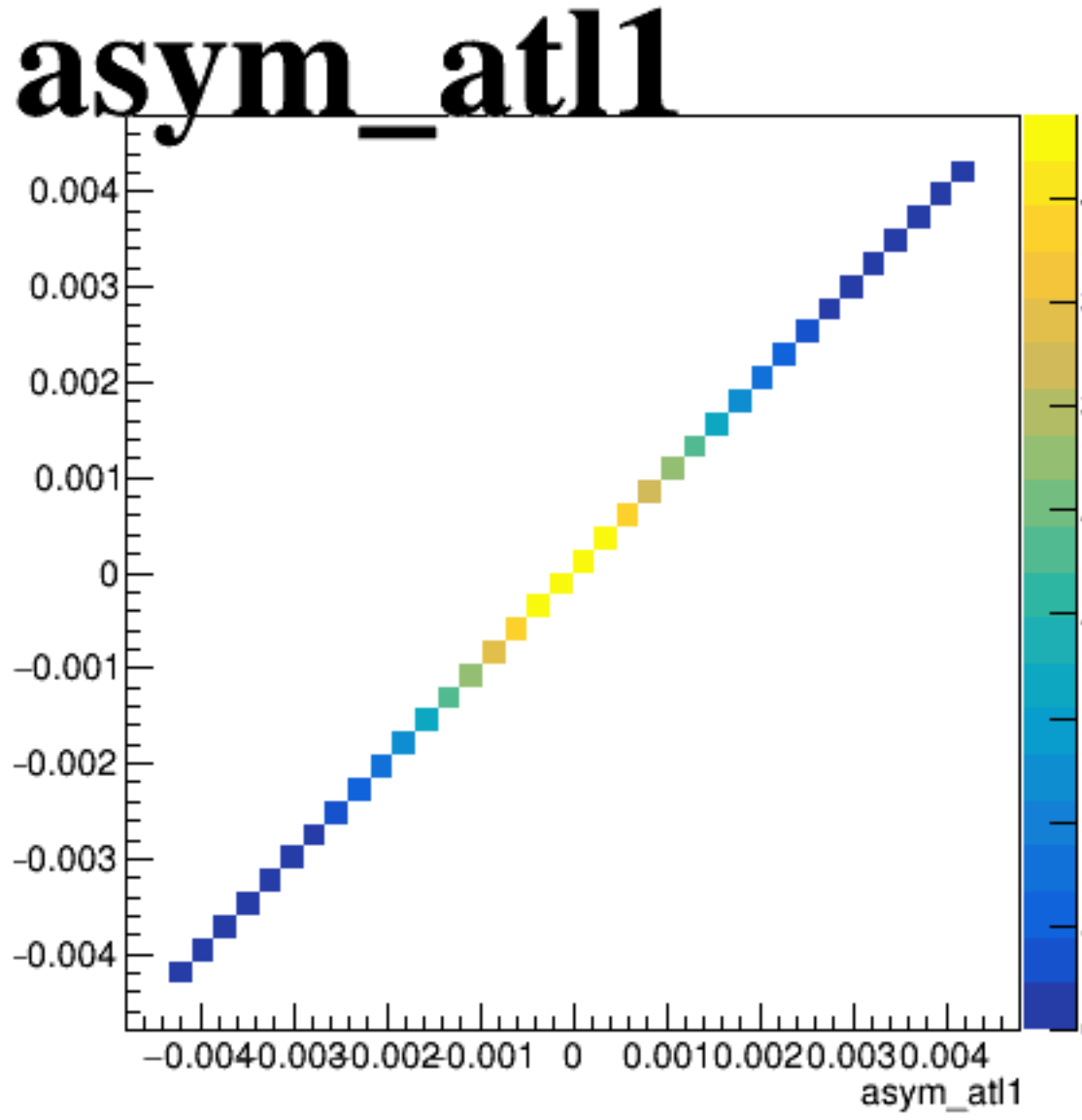


asym_atr1

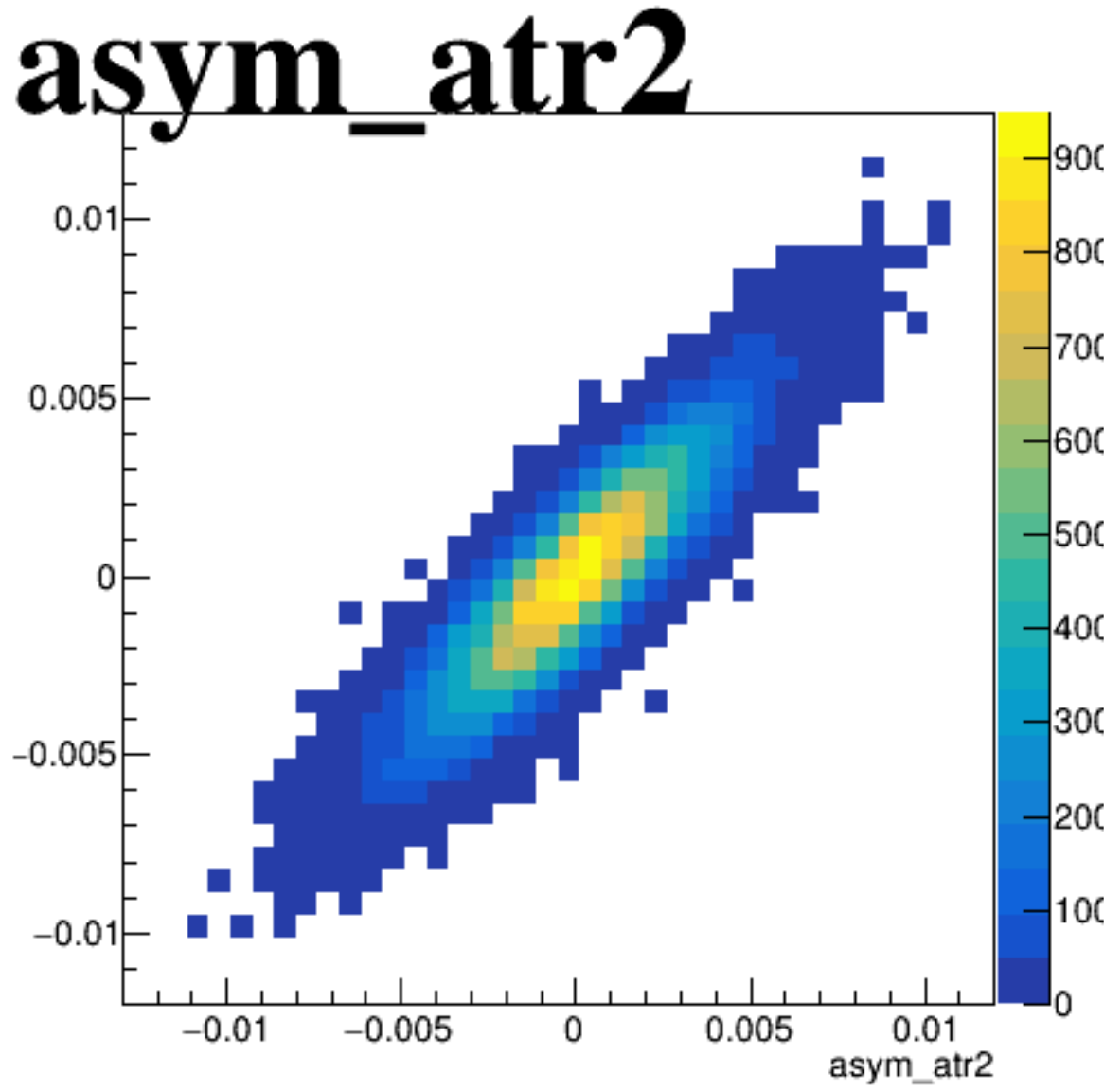
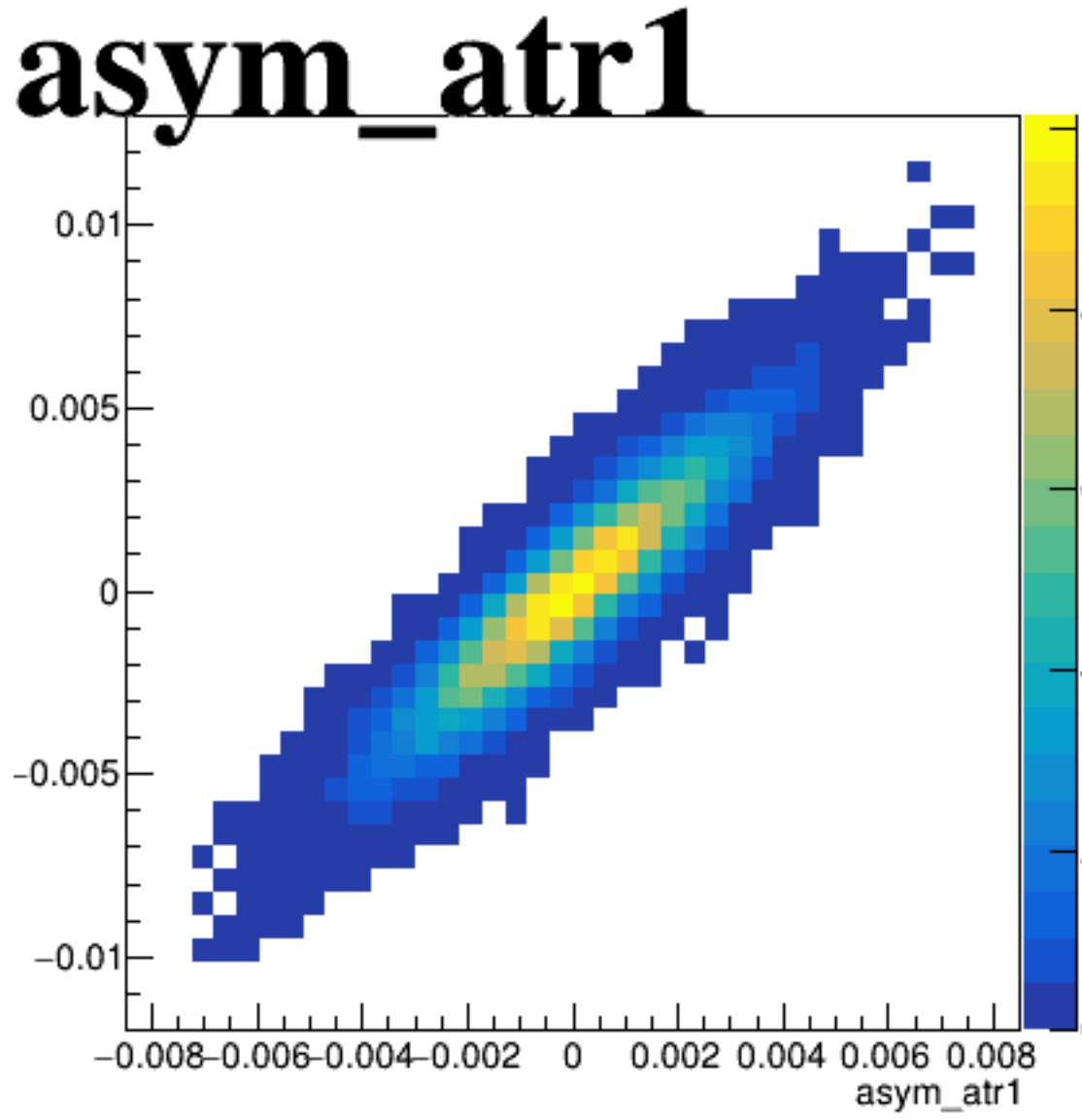
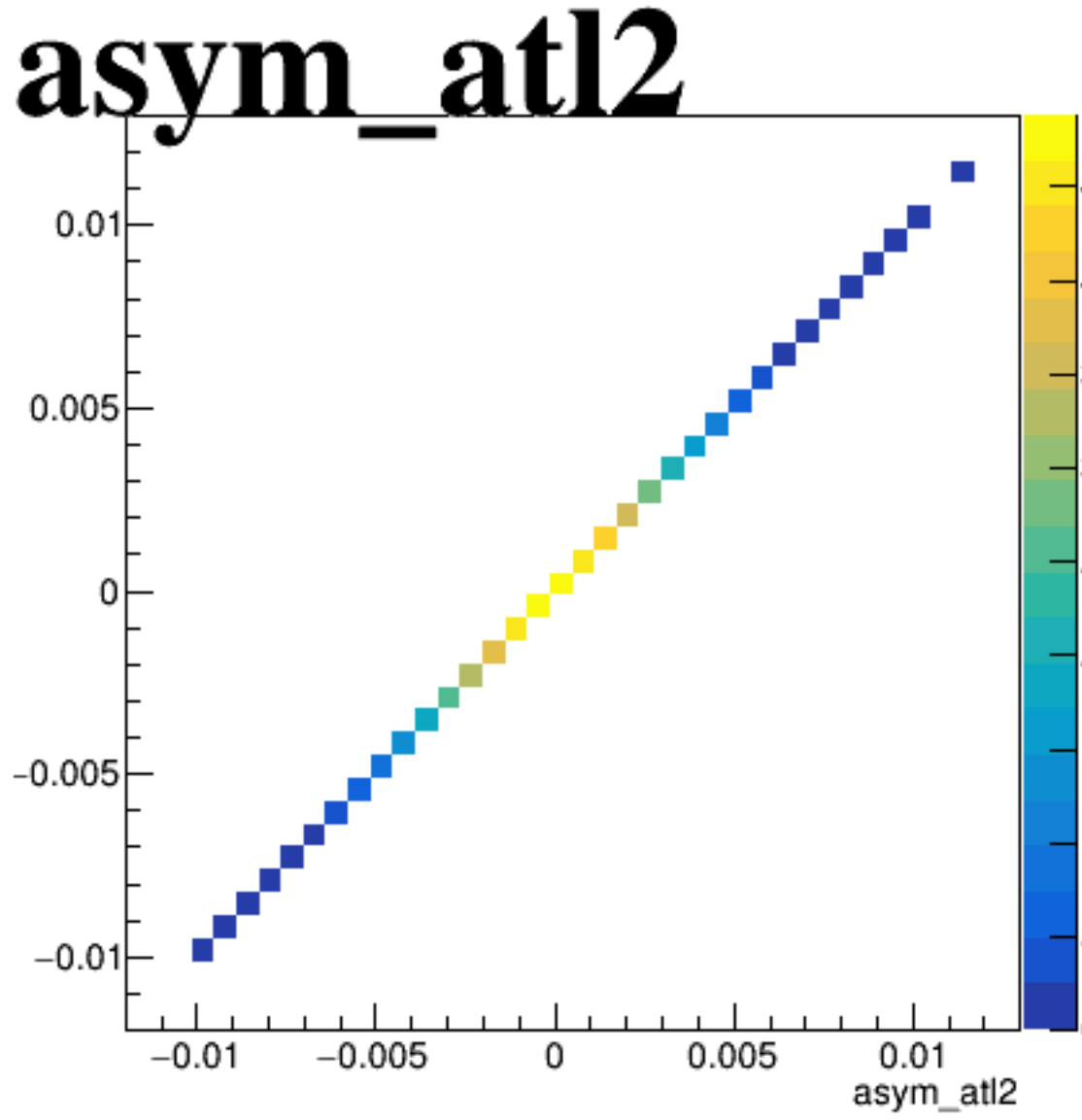
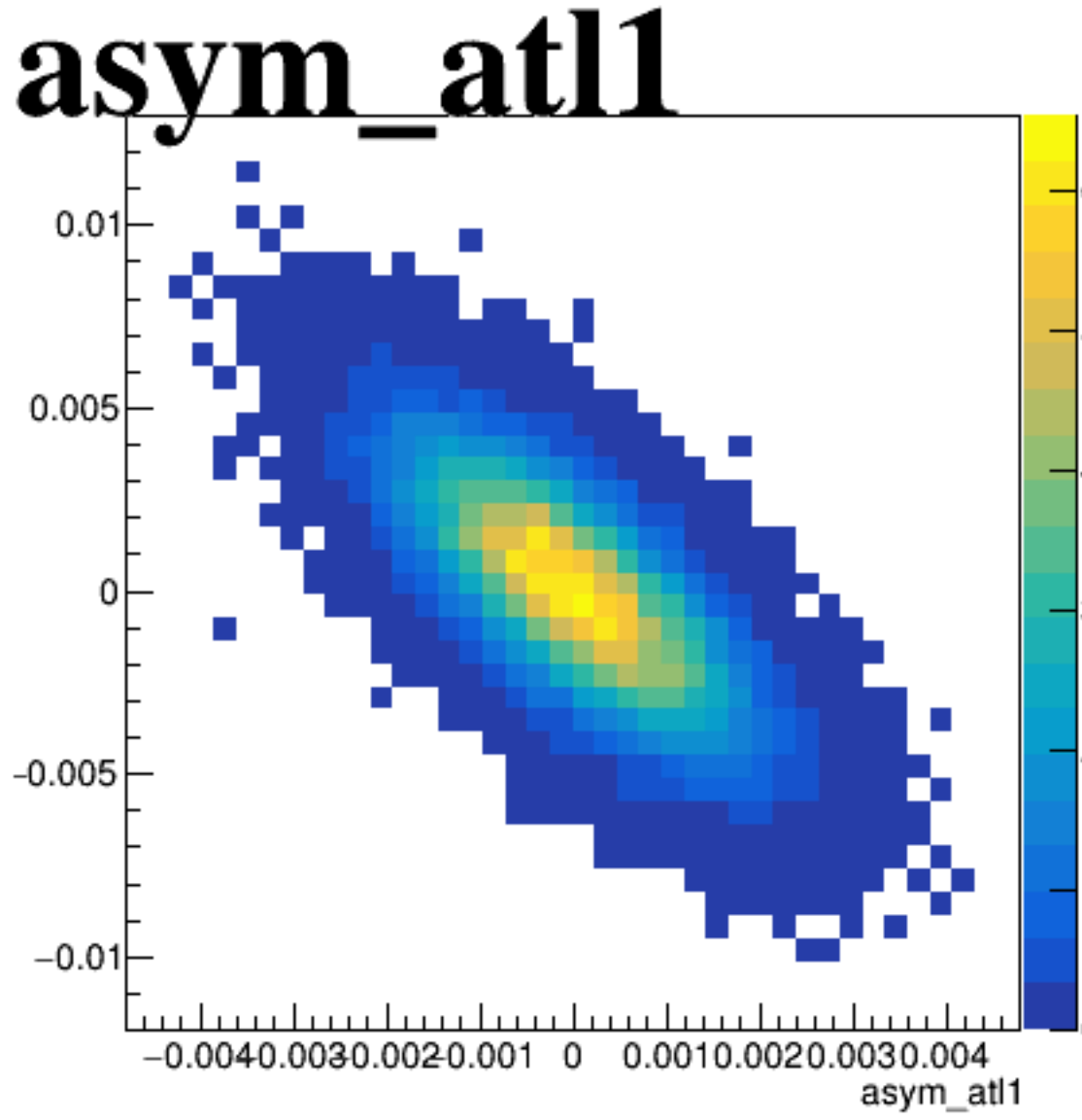




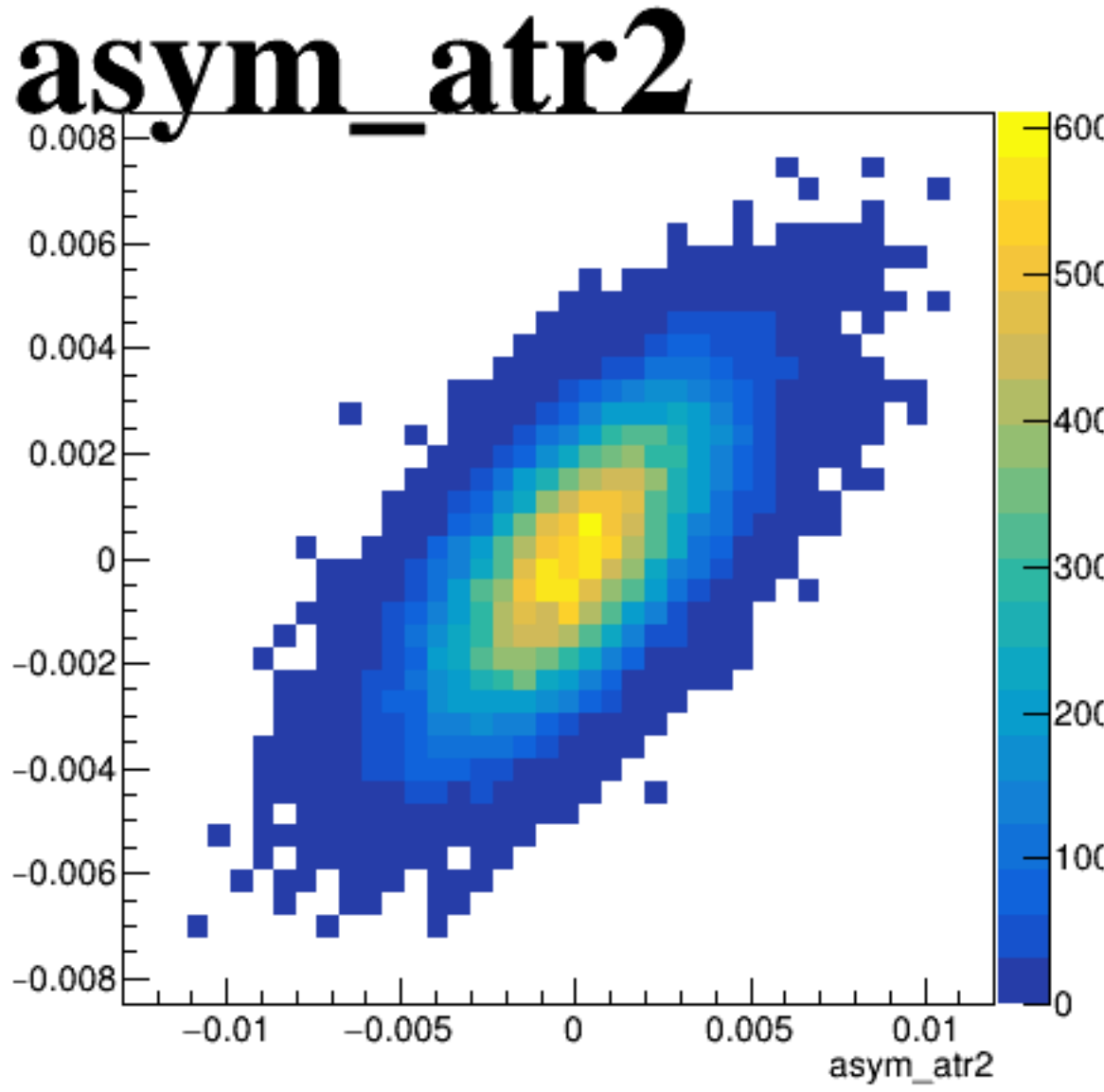
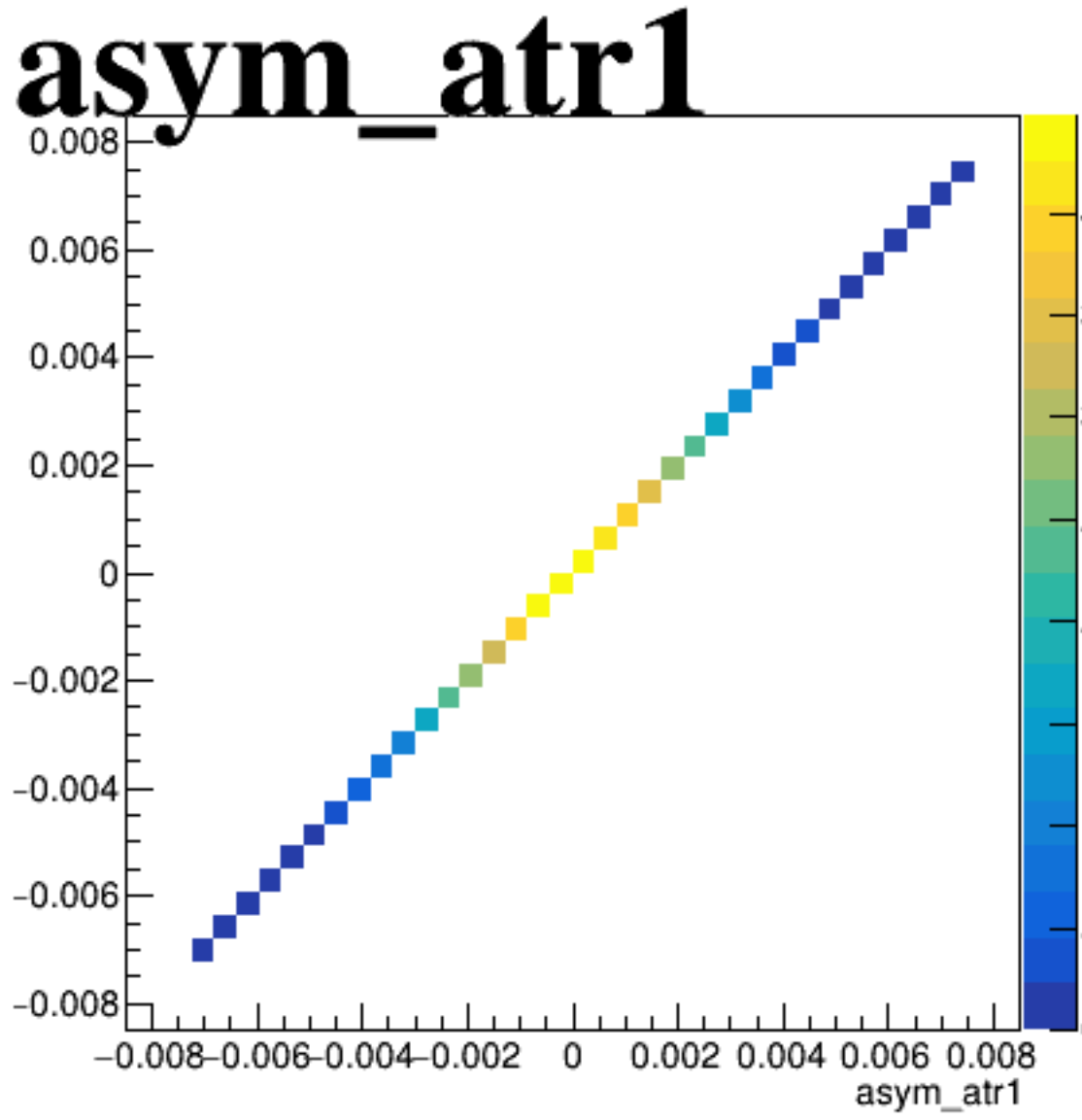
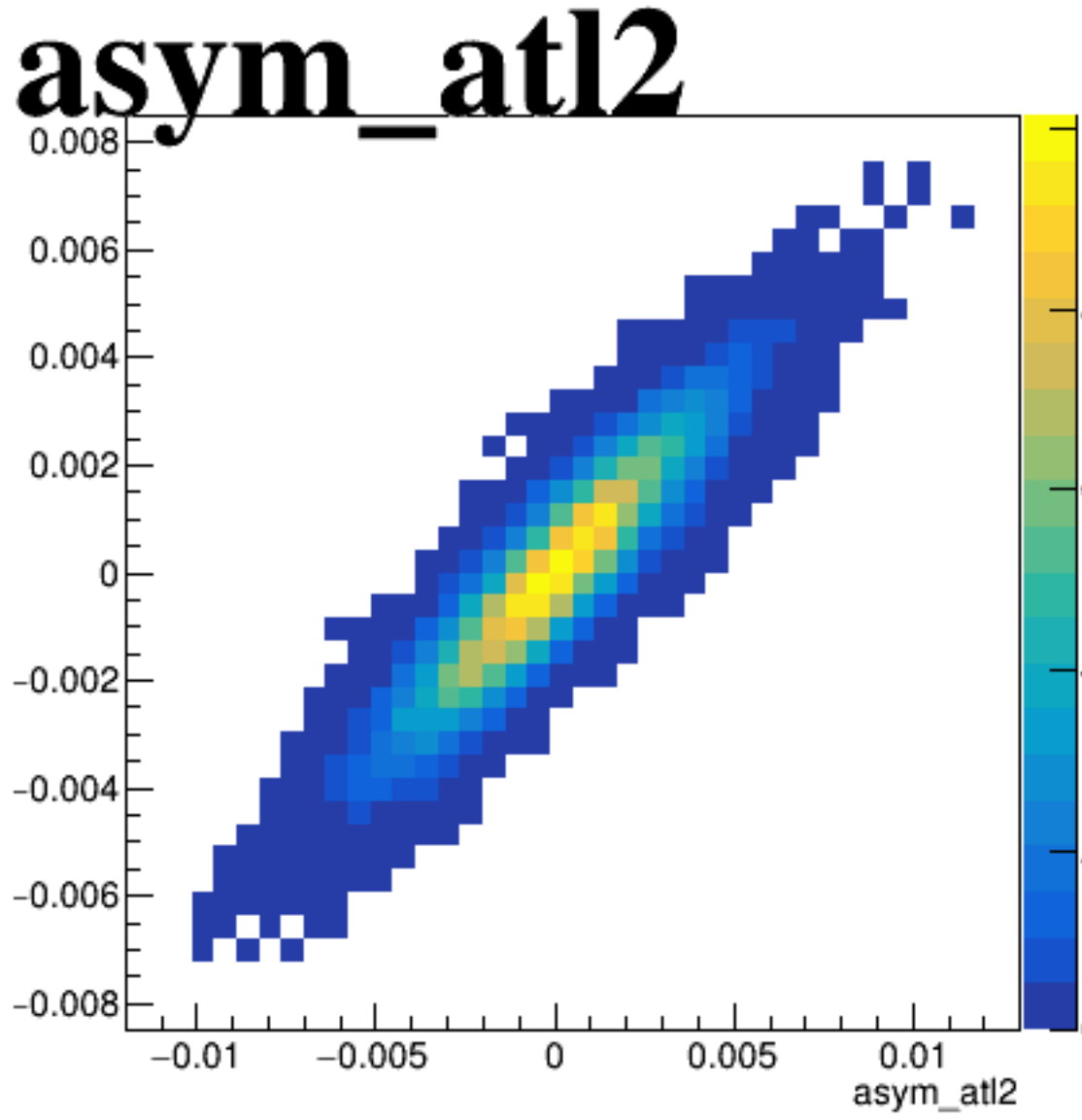
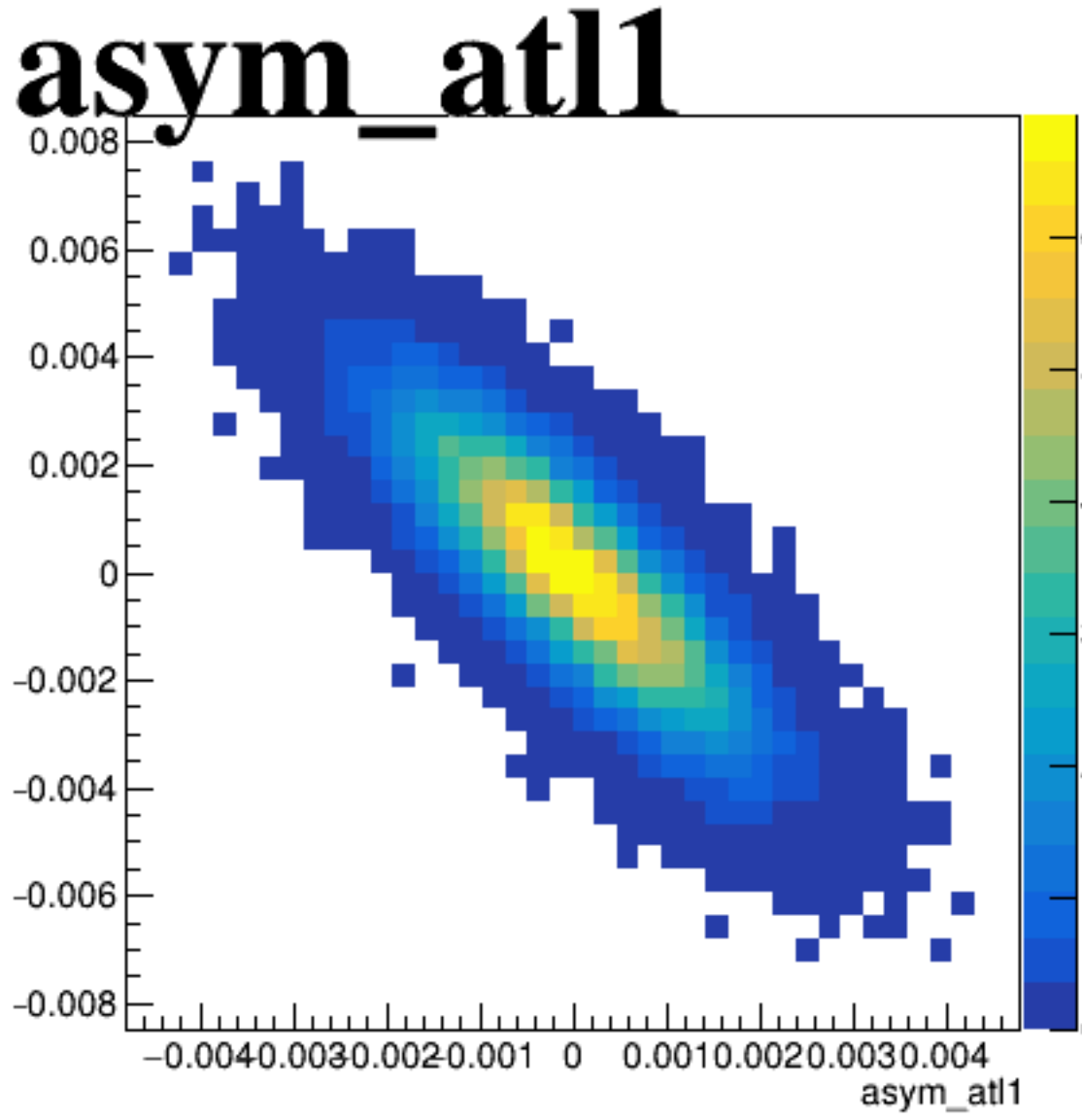
asym_atl1



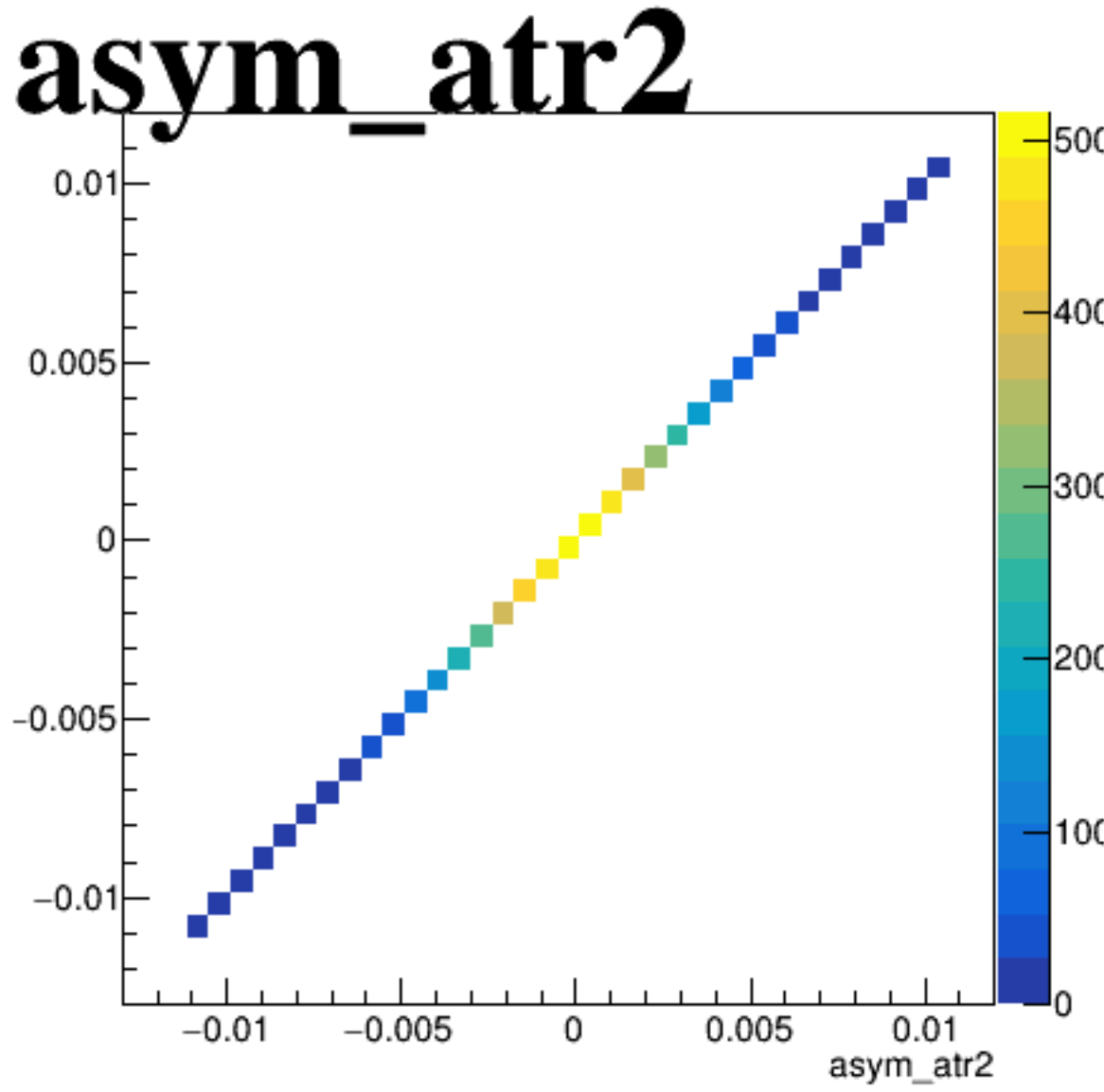
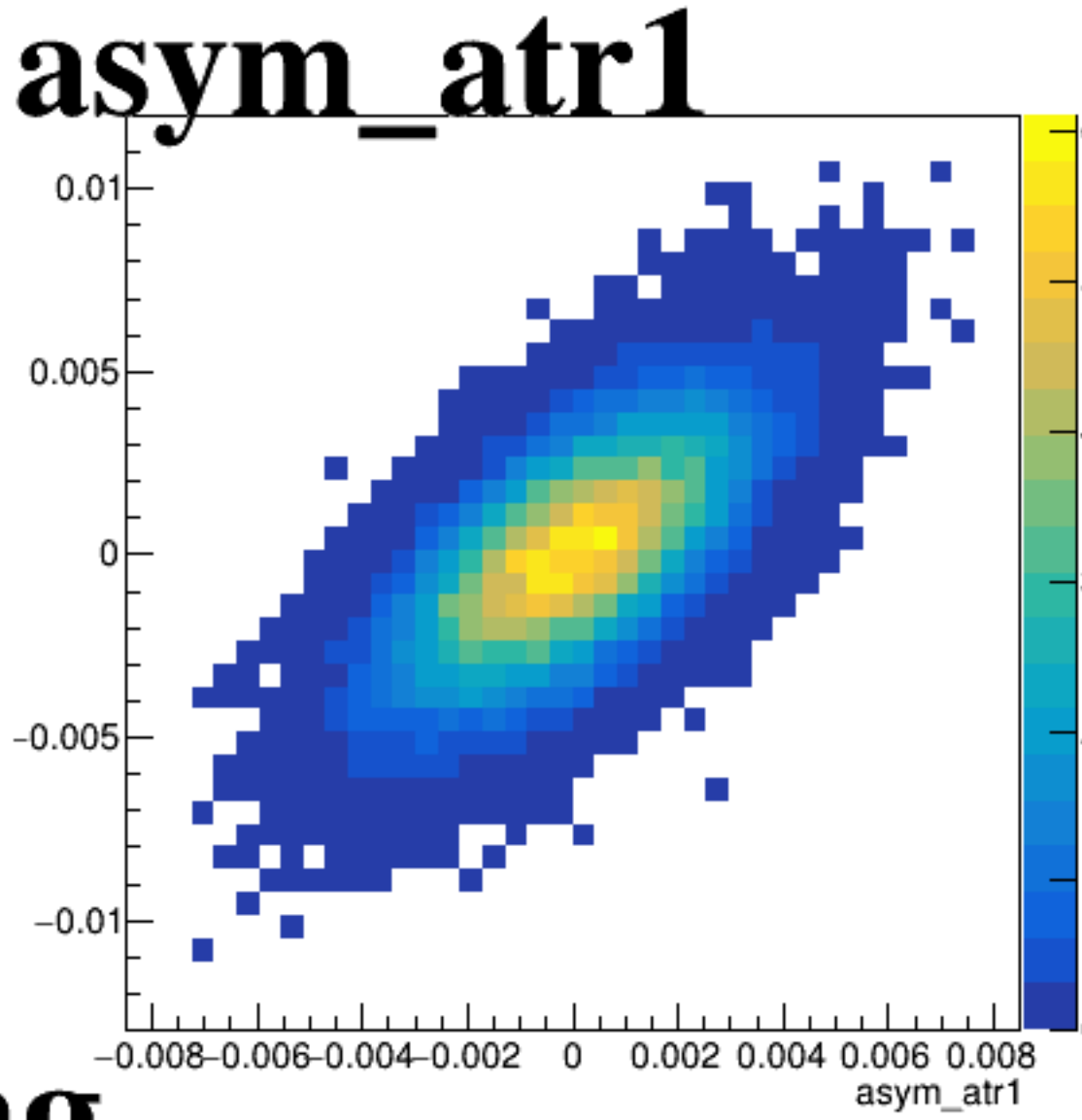
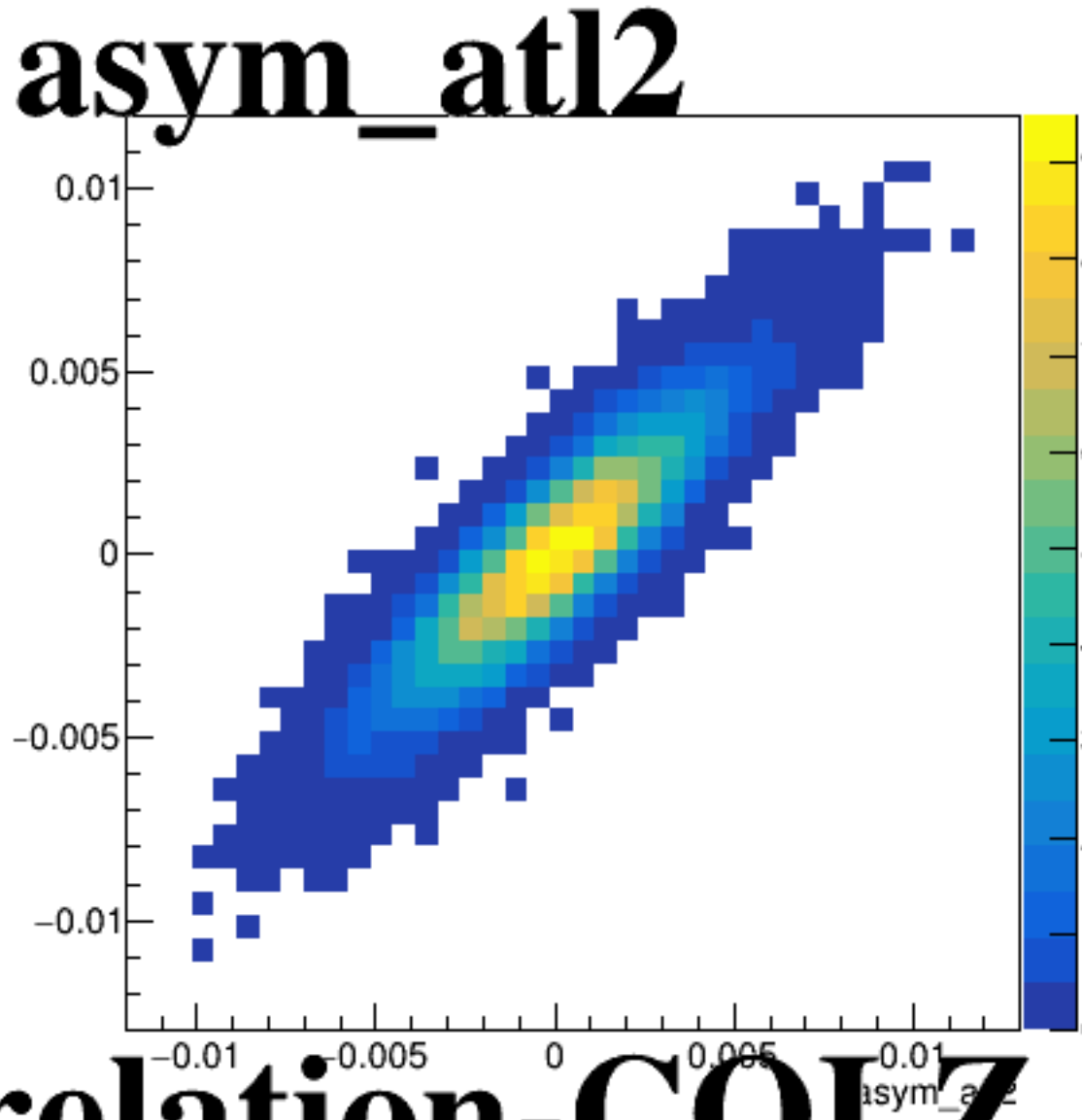
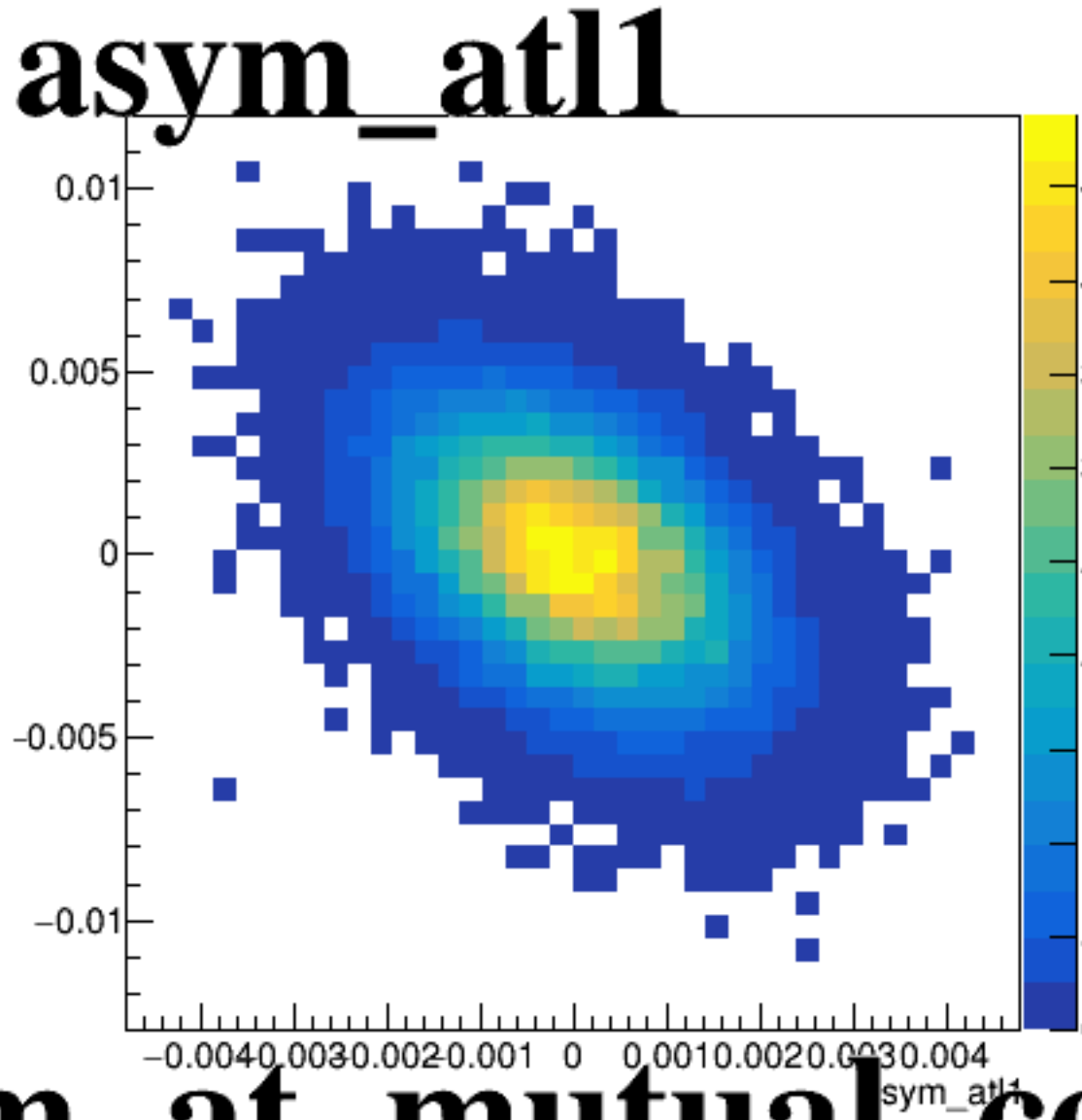
asym_atl2



asym_atr1

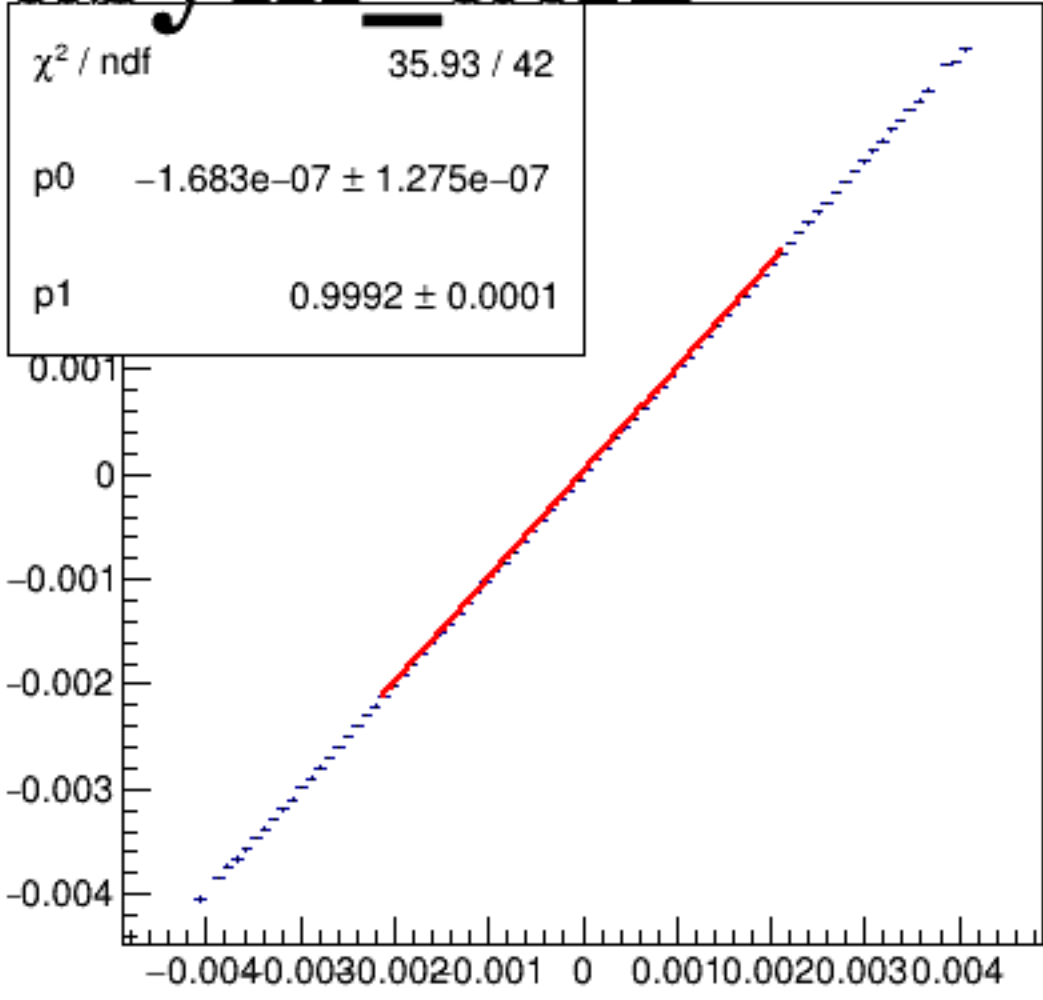


asym_atr2

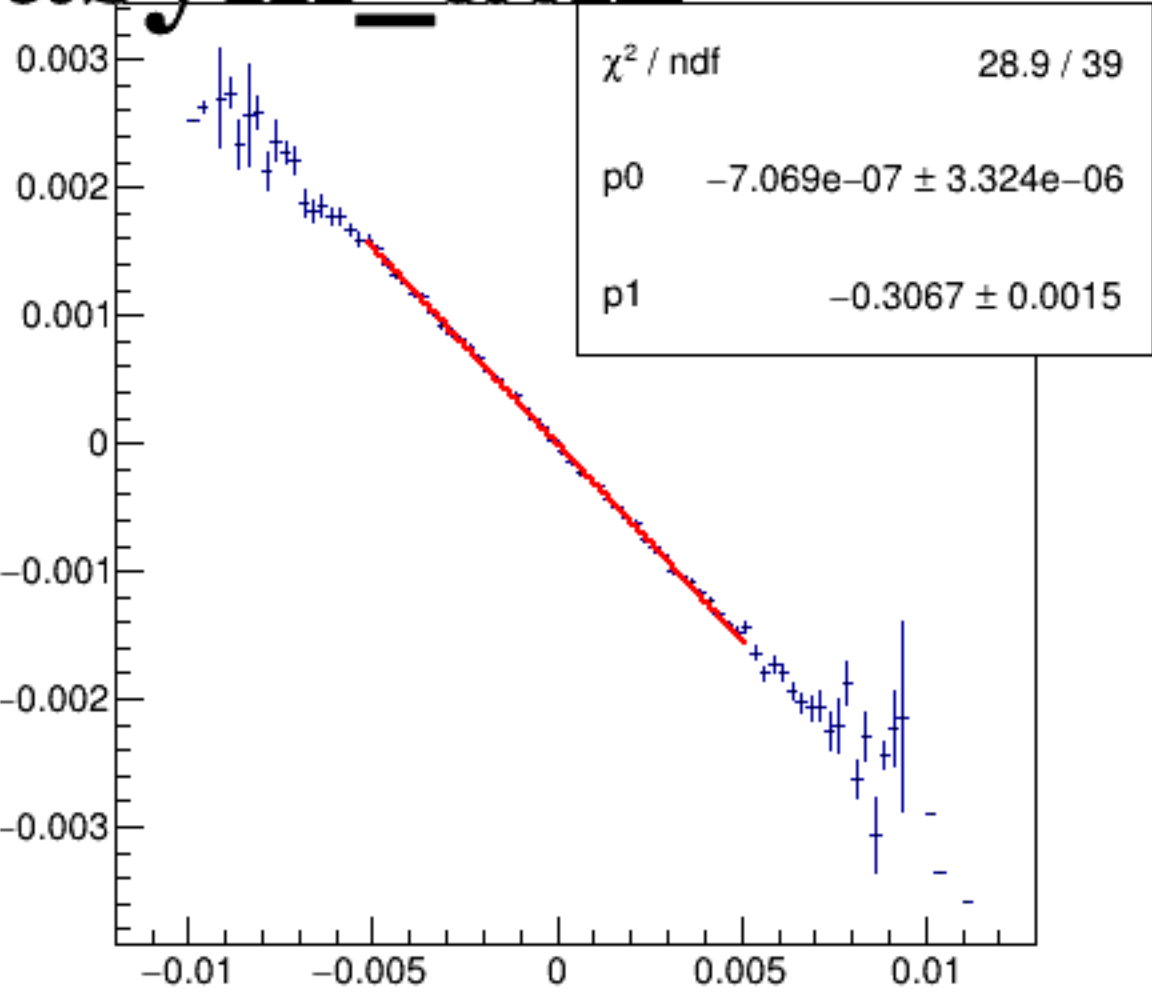


asym_atl1

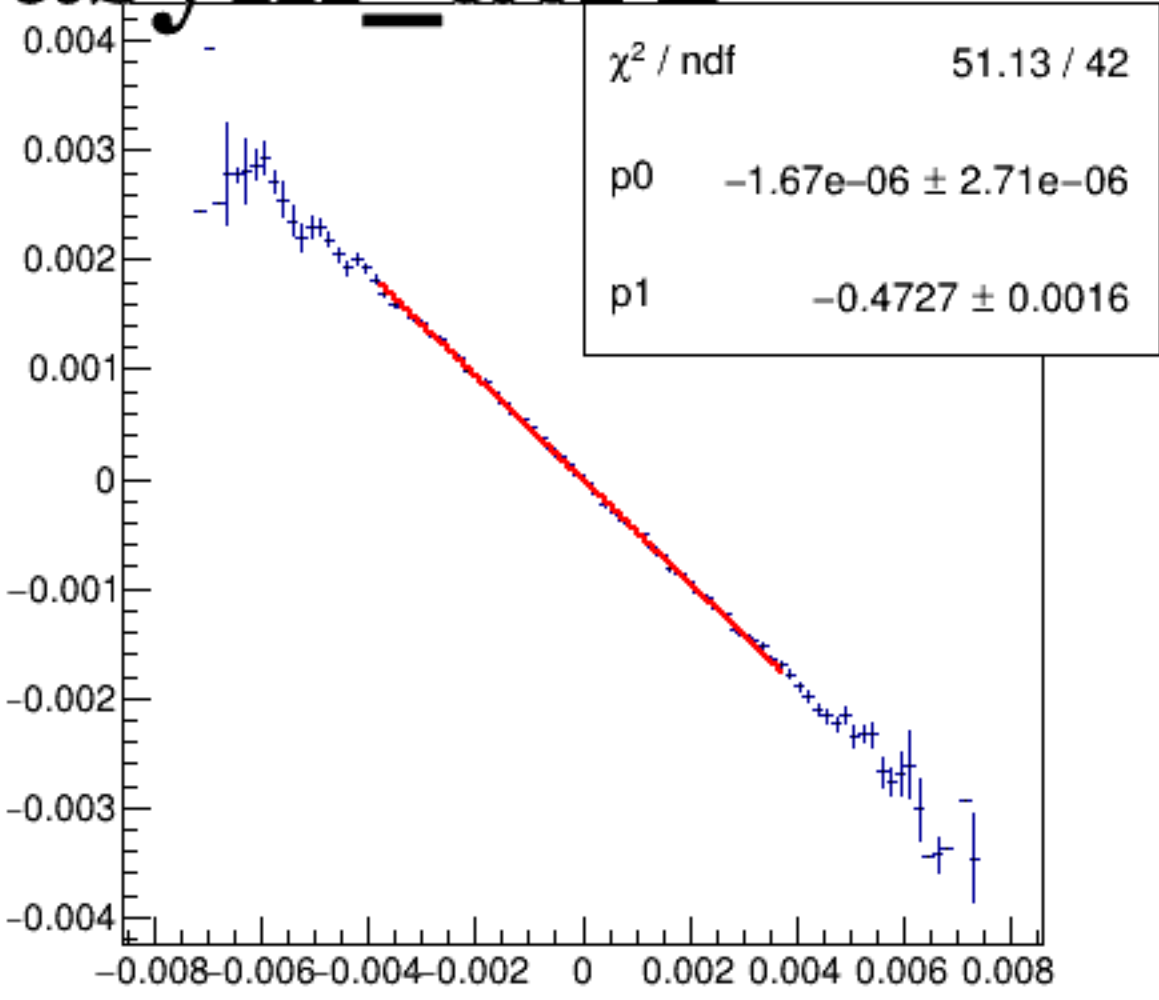
asym_atl1



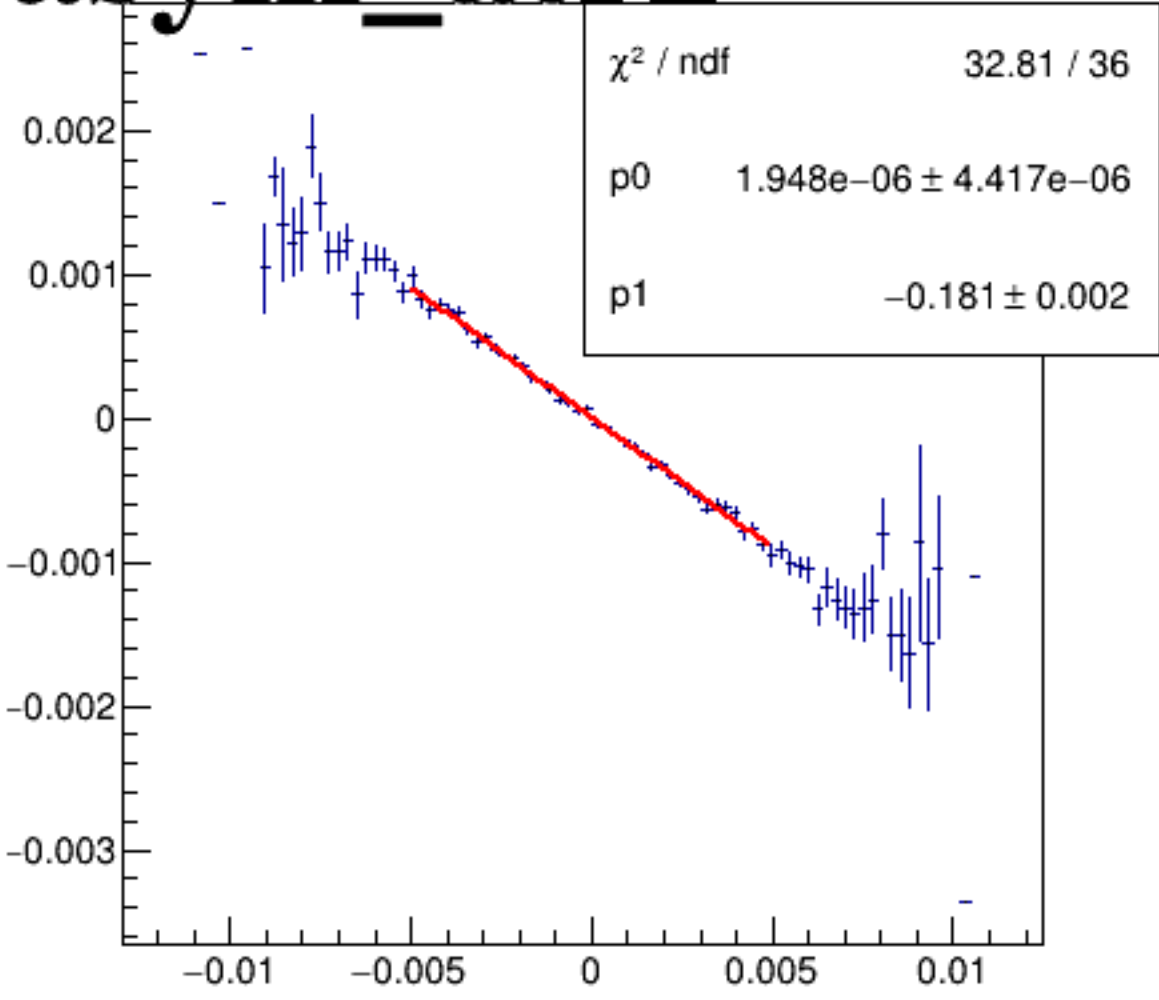
asym_atl2



asym_atr1

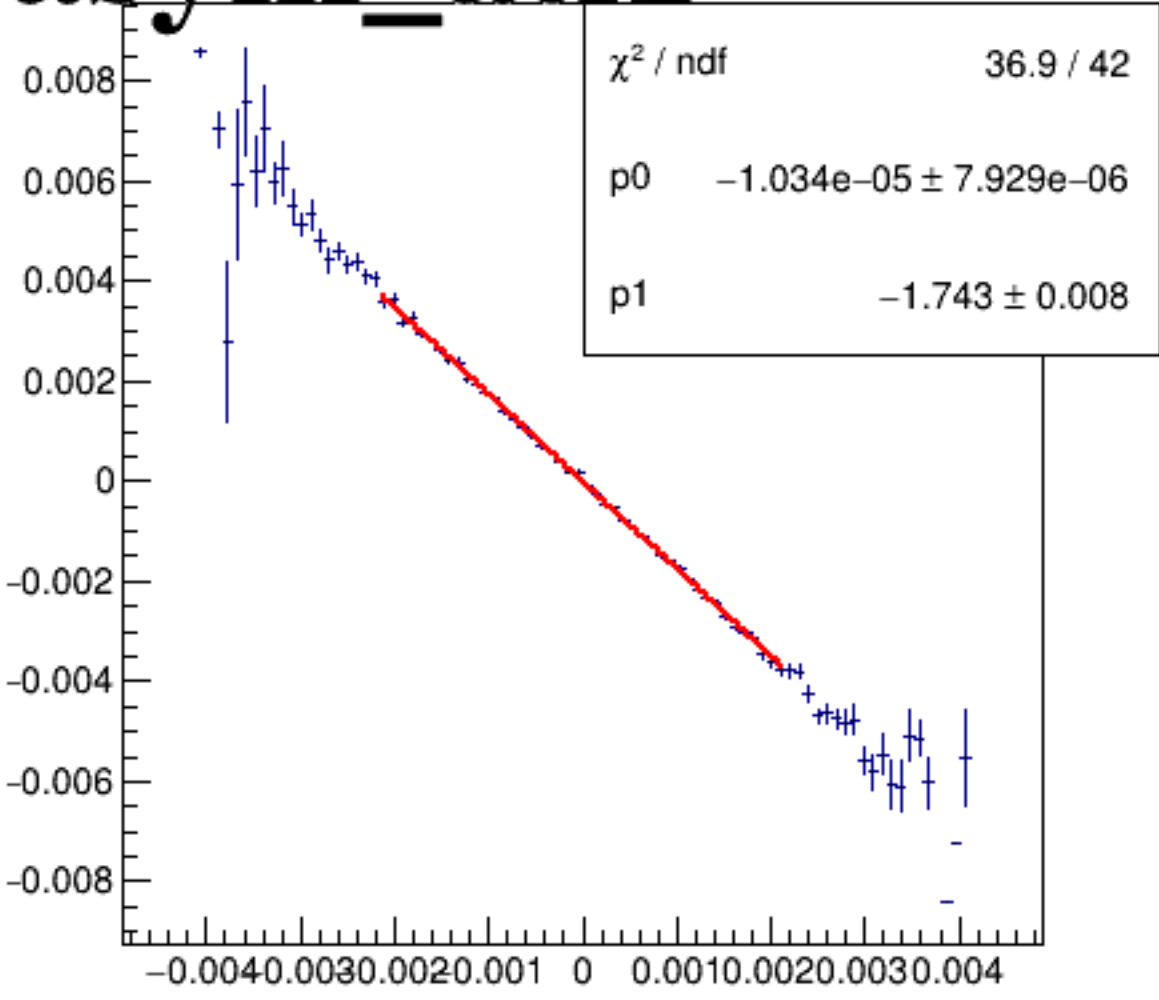


asym_atr2

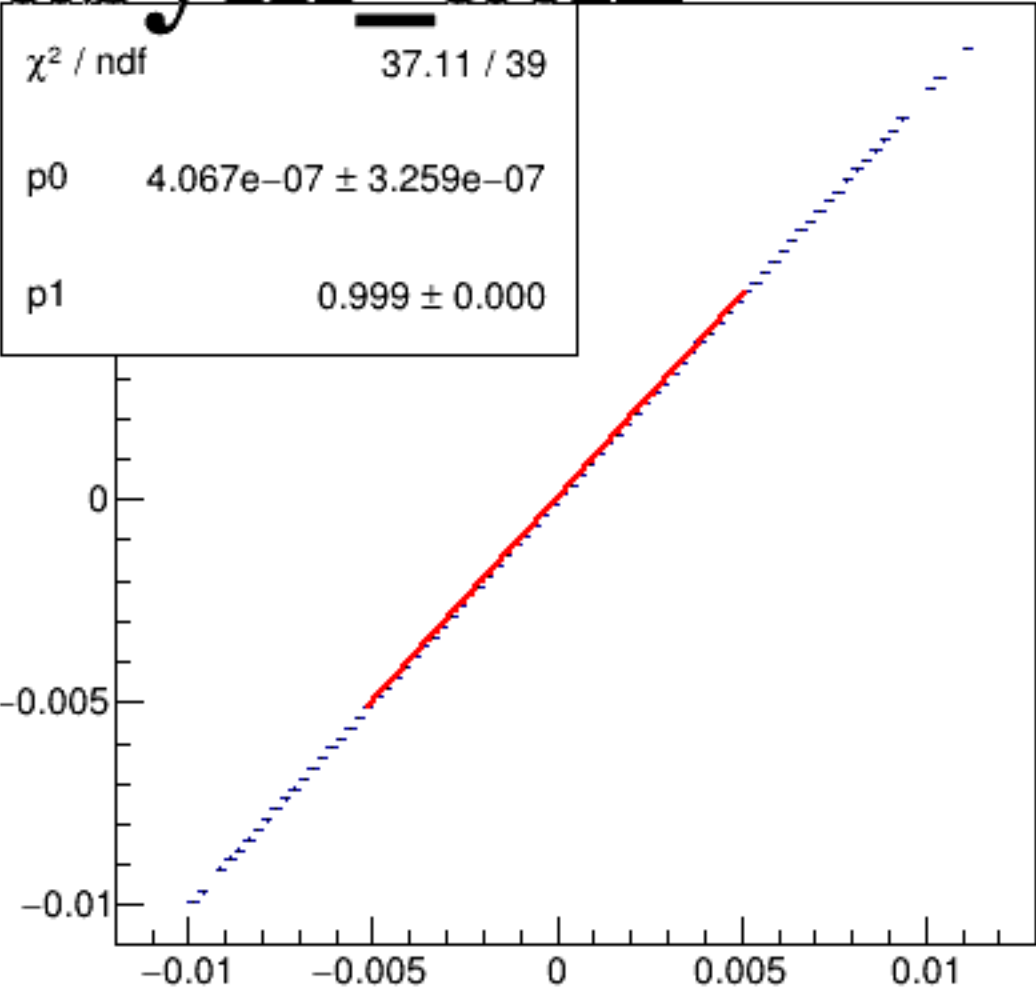


asym_atl2

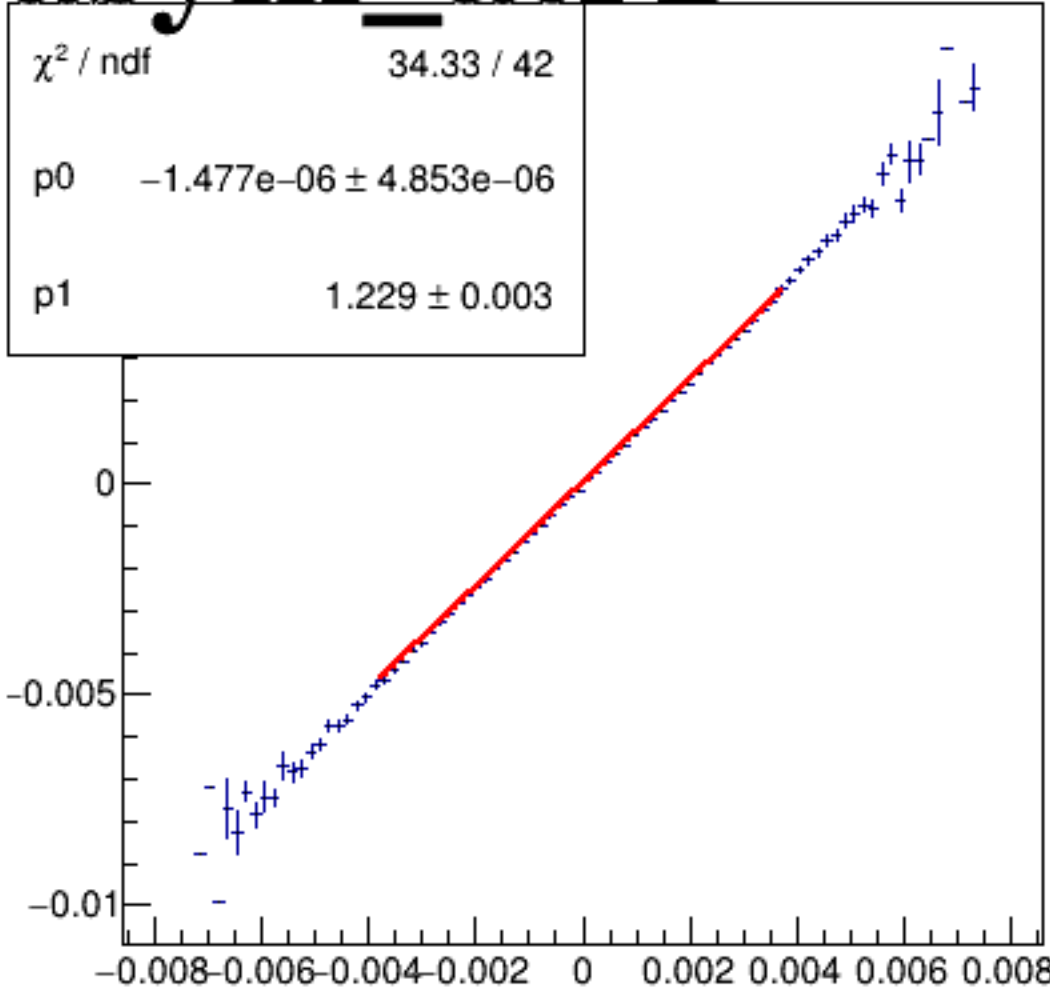
asym_atl1



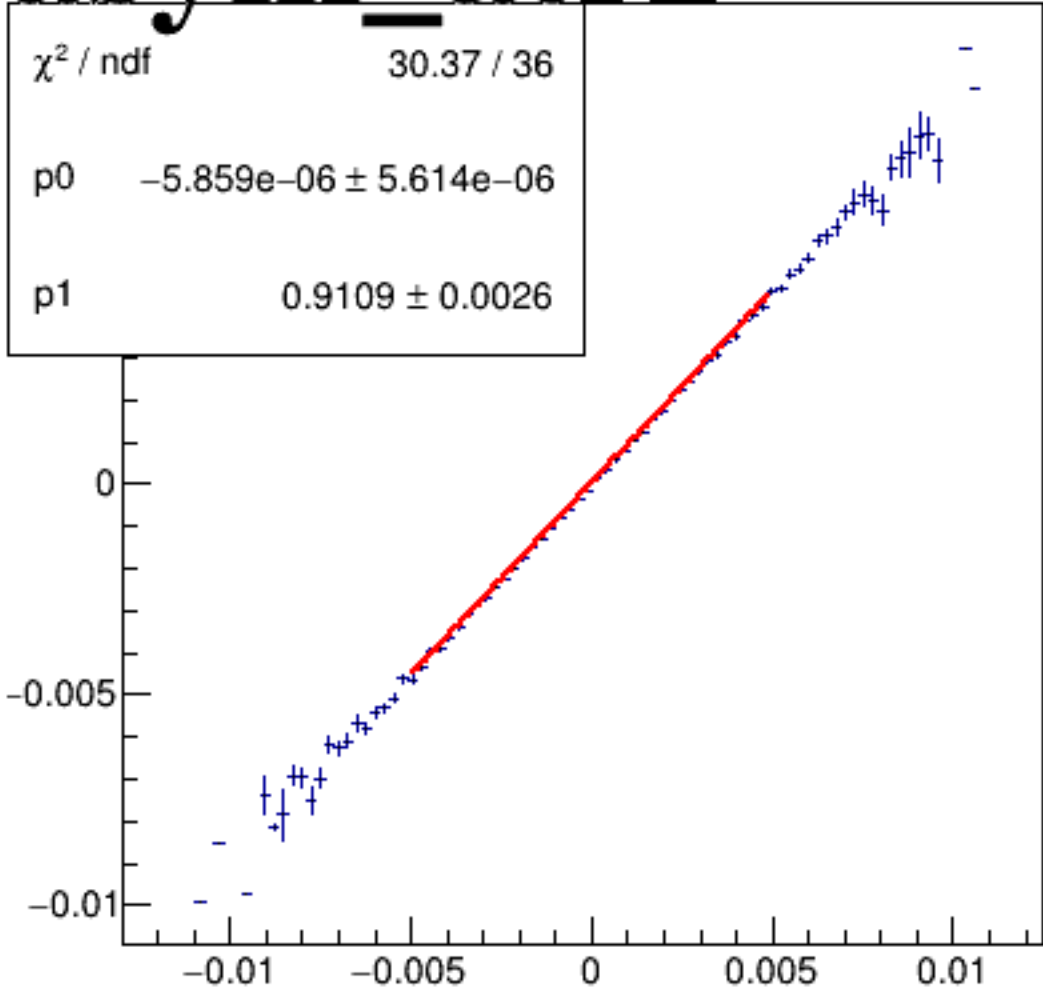
asym_atl2



asym_atr1

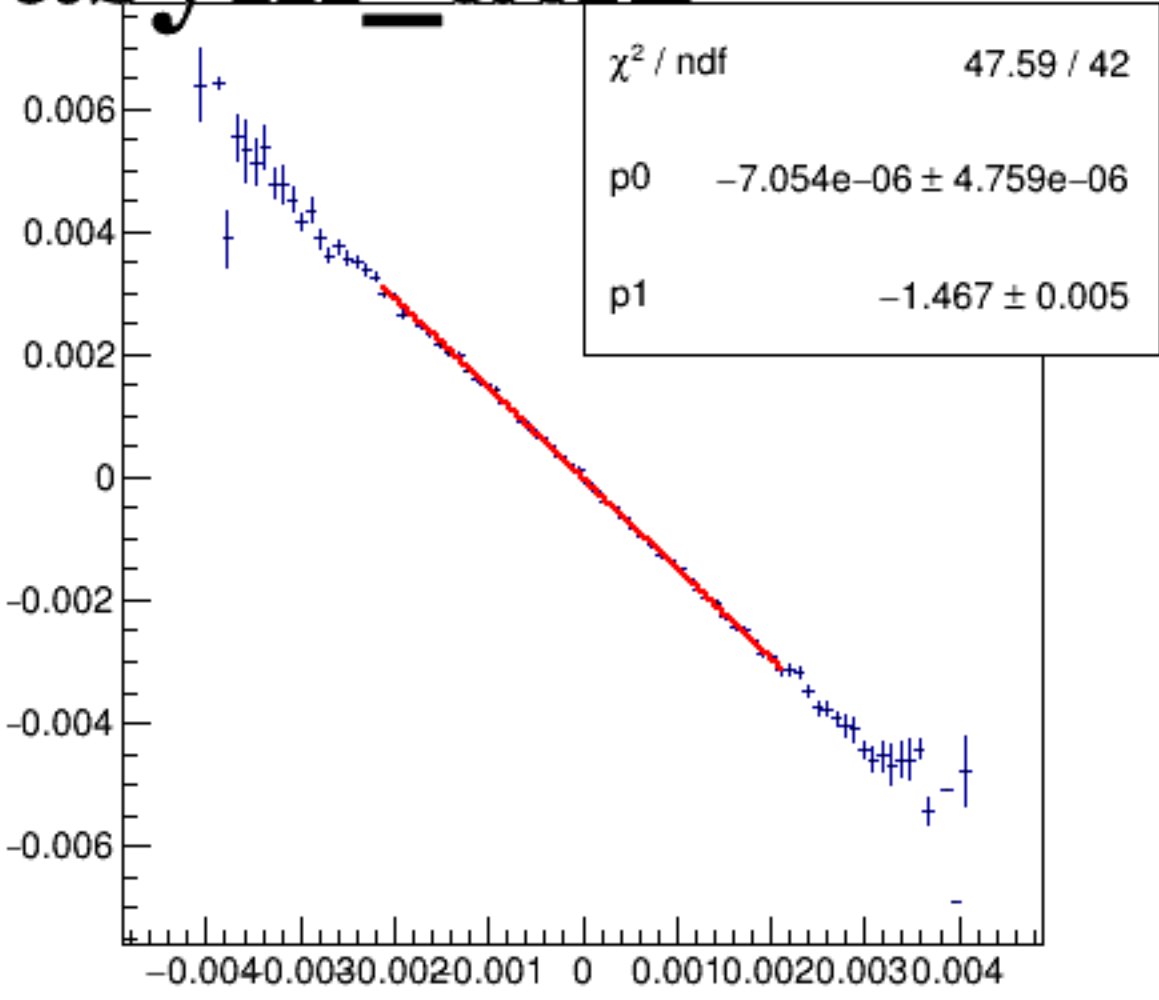


asym_atr2

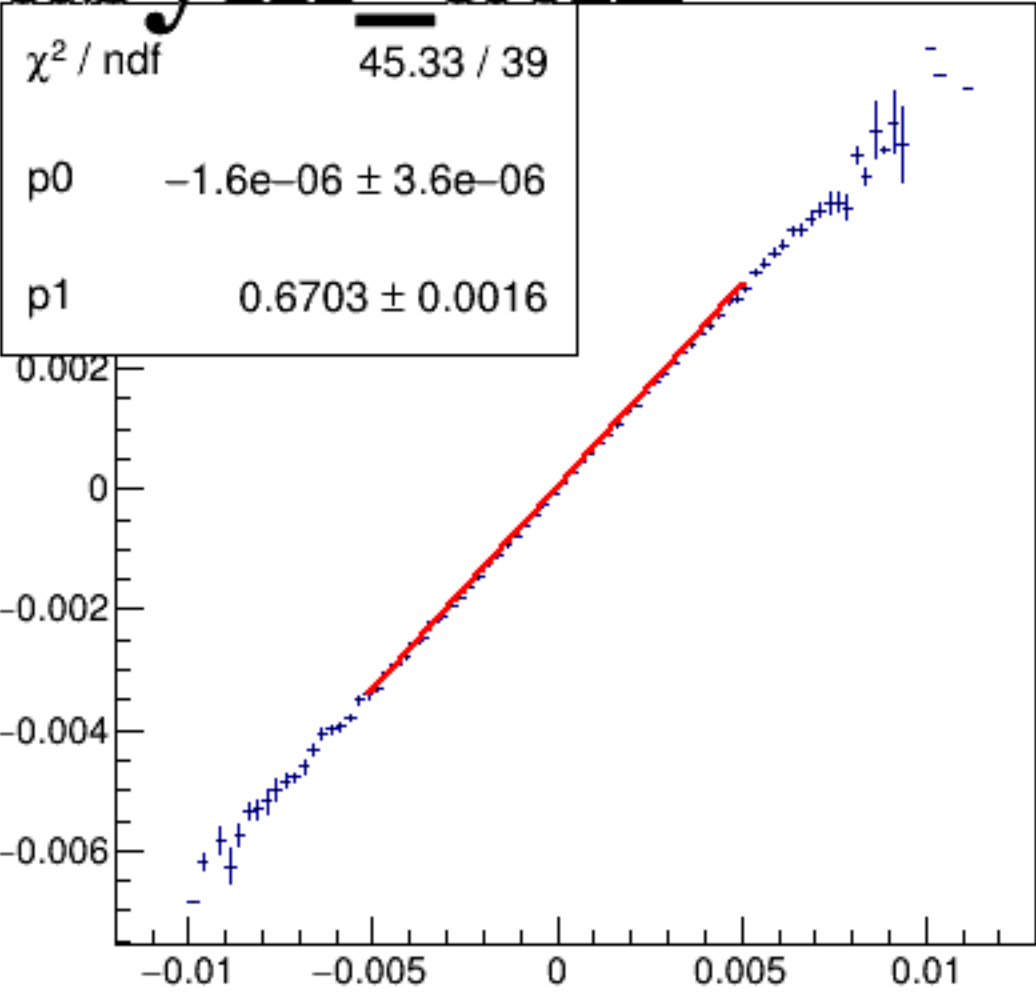


asym_atr1

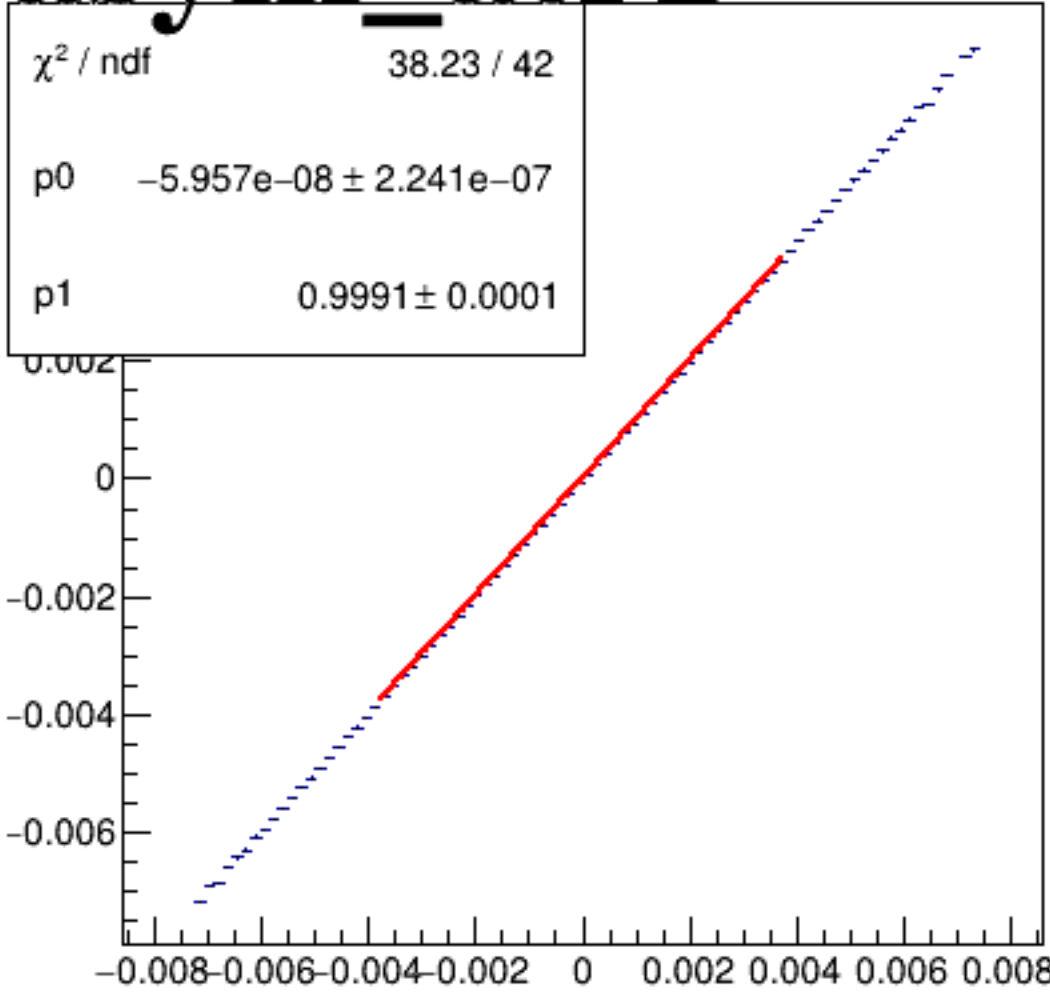
asym_atl1



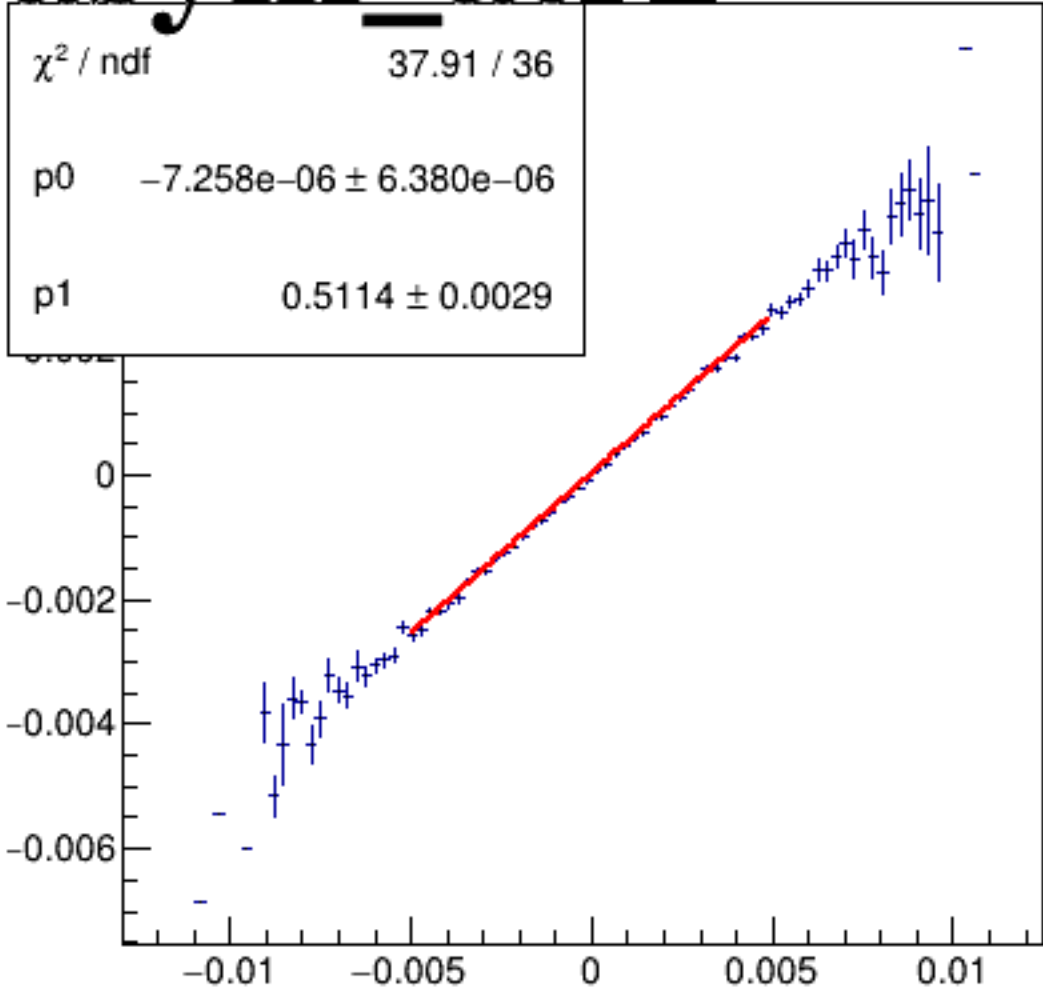
asym_atl2



asym_atr1

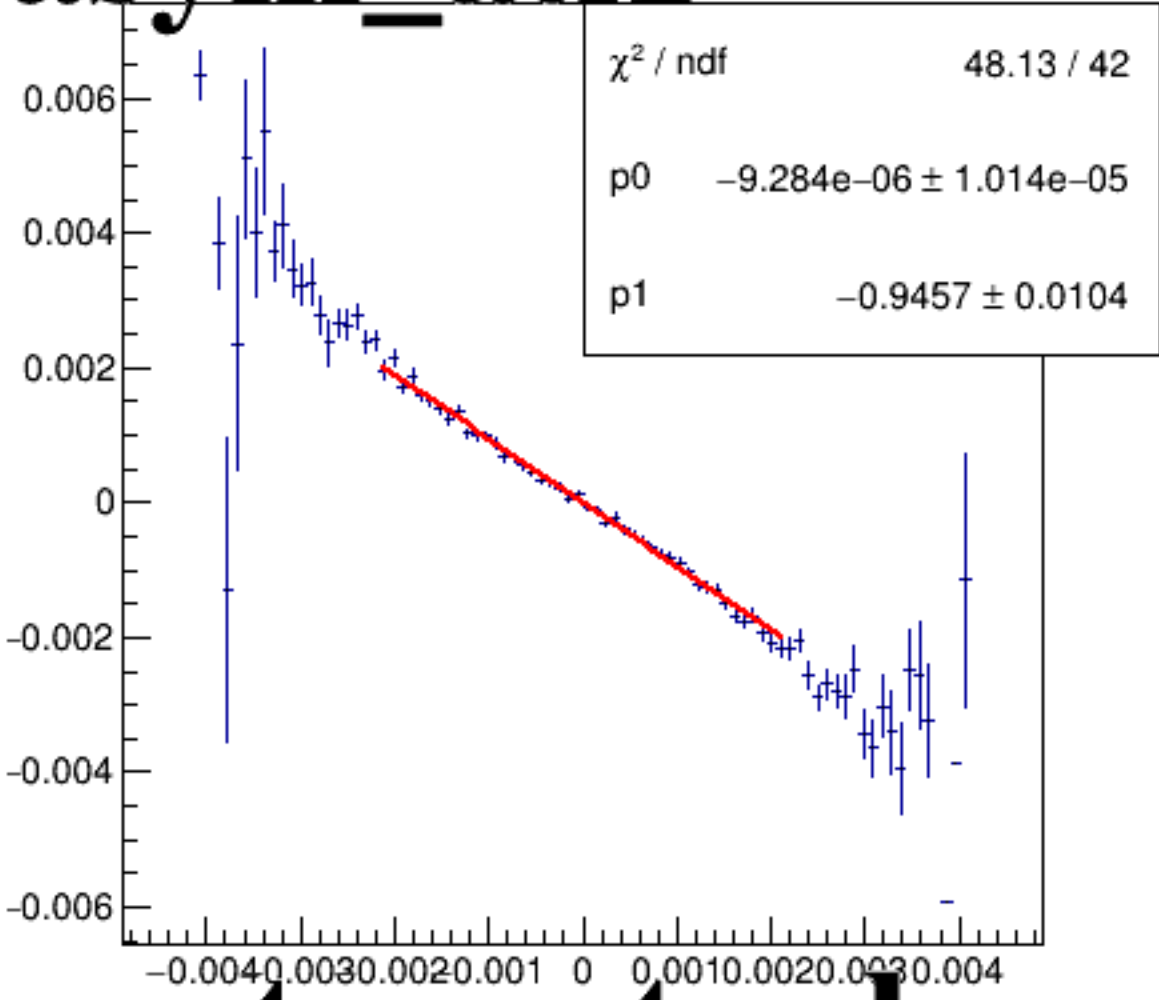


asym_atr2

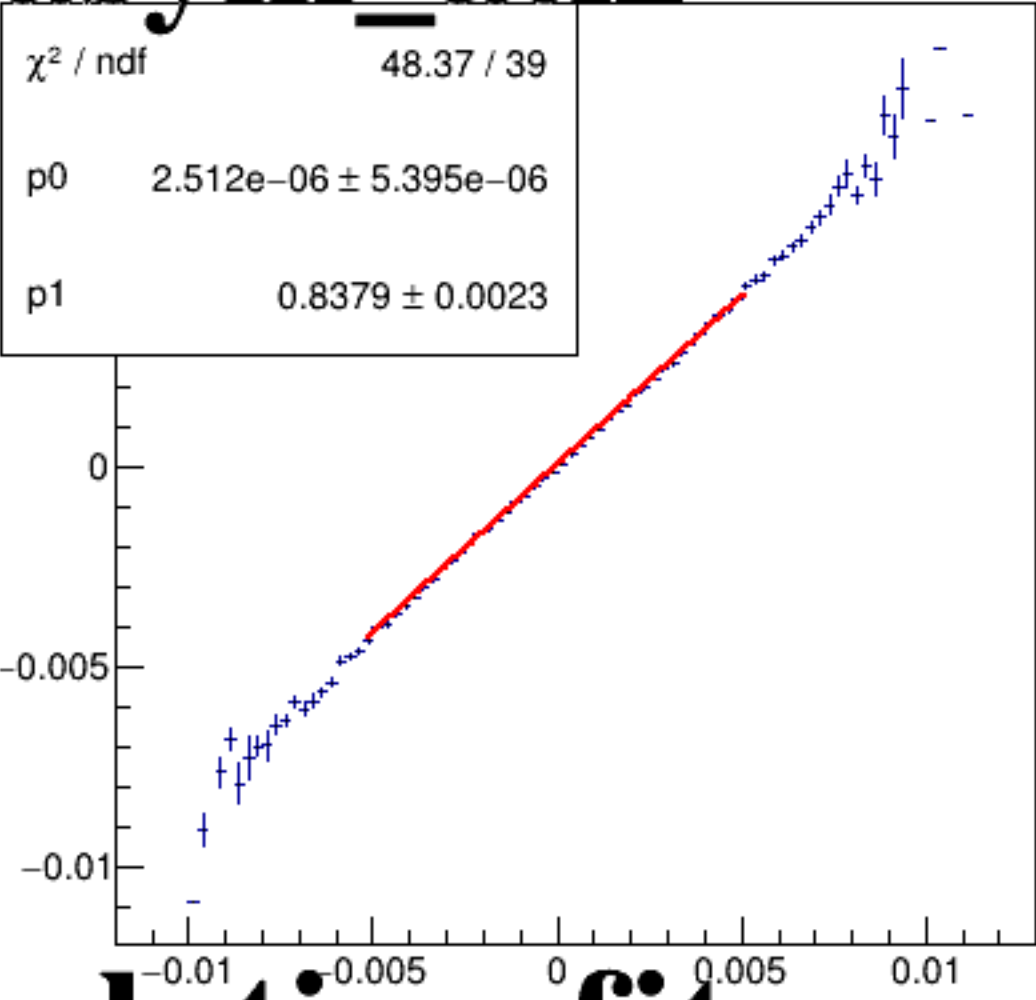


asym_atr2

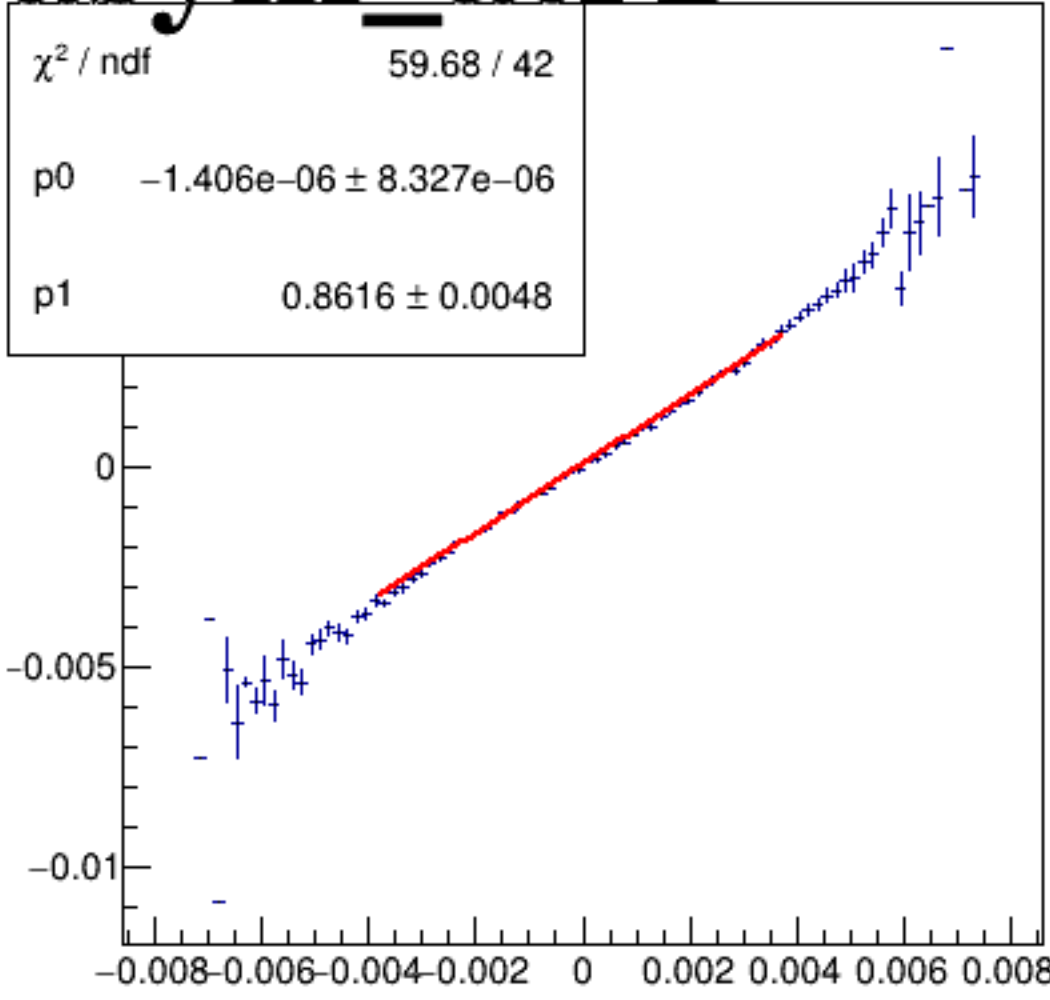
asym_atl1



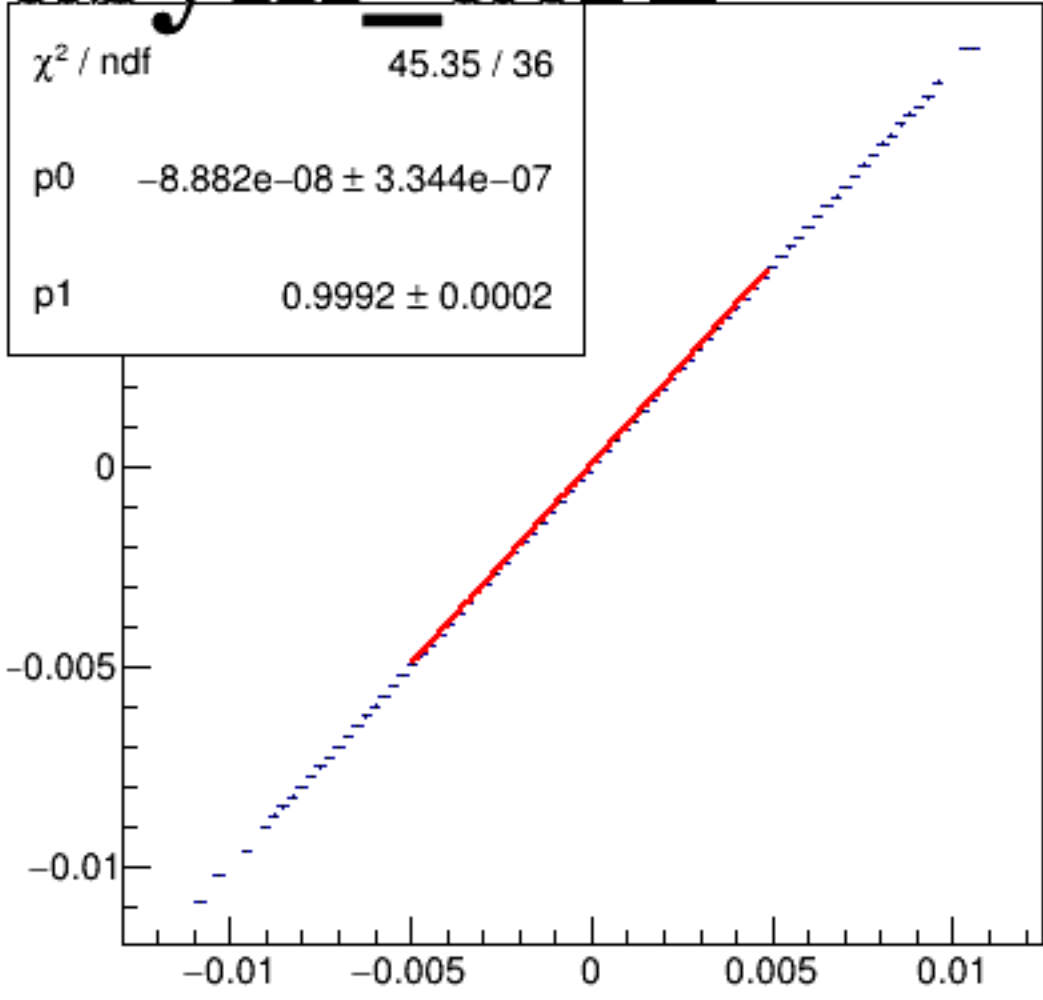
asym_atl2



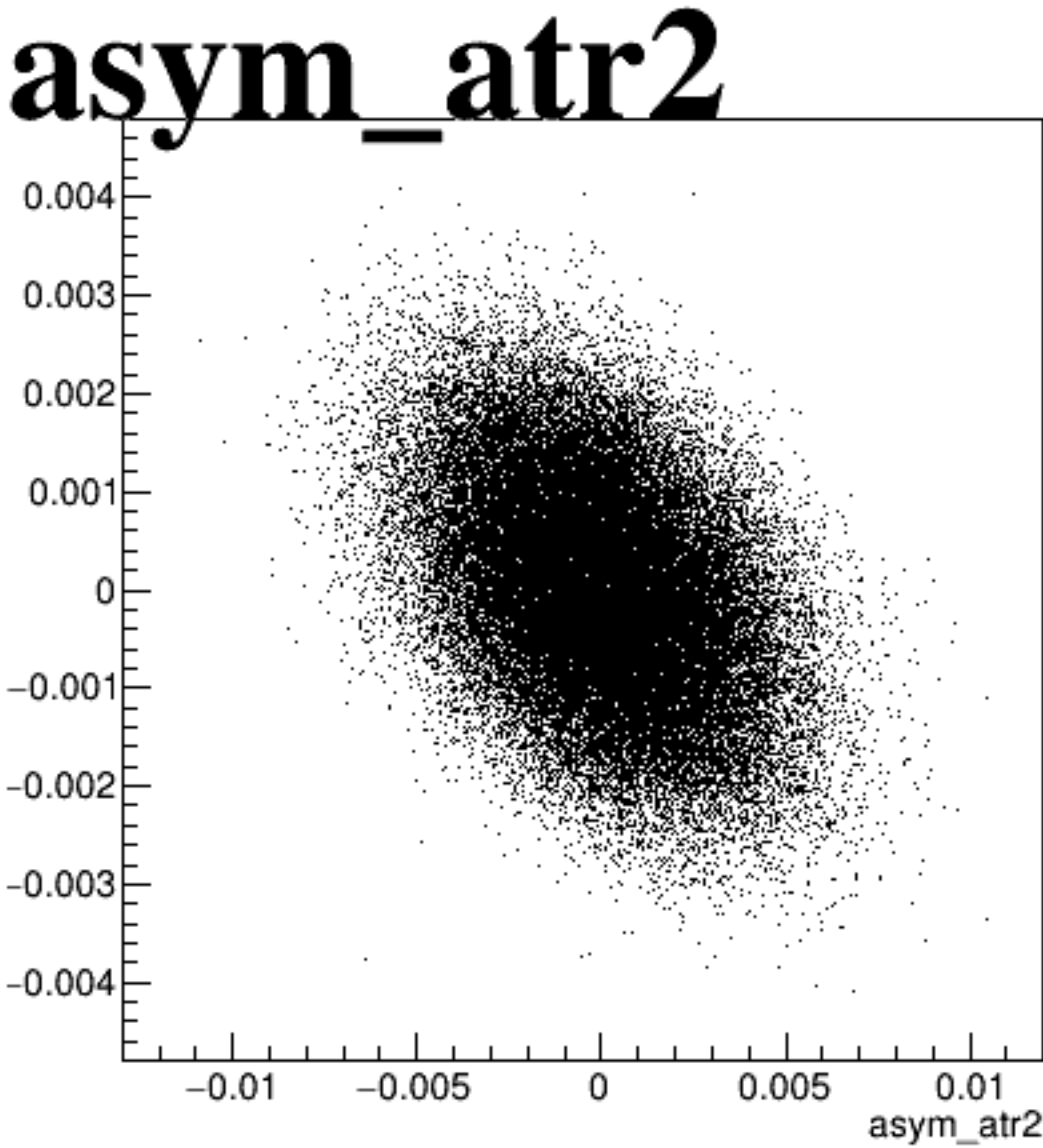
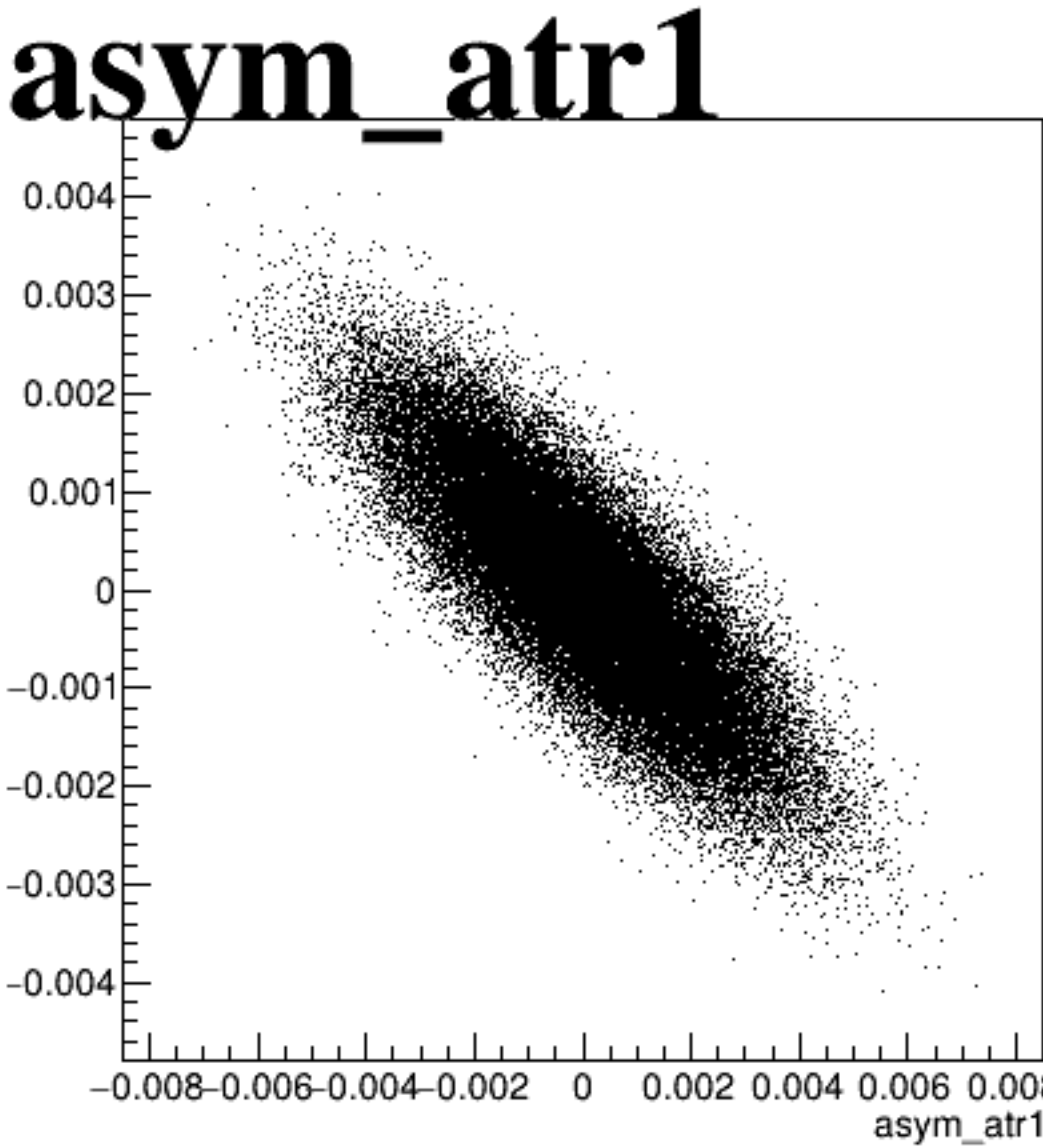
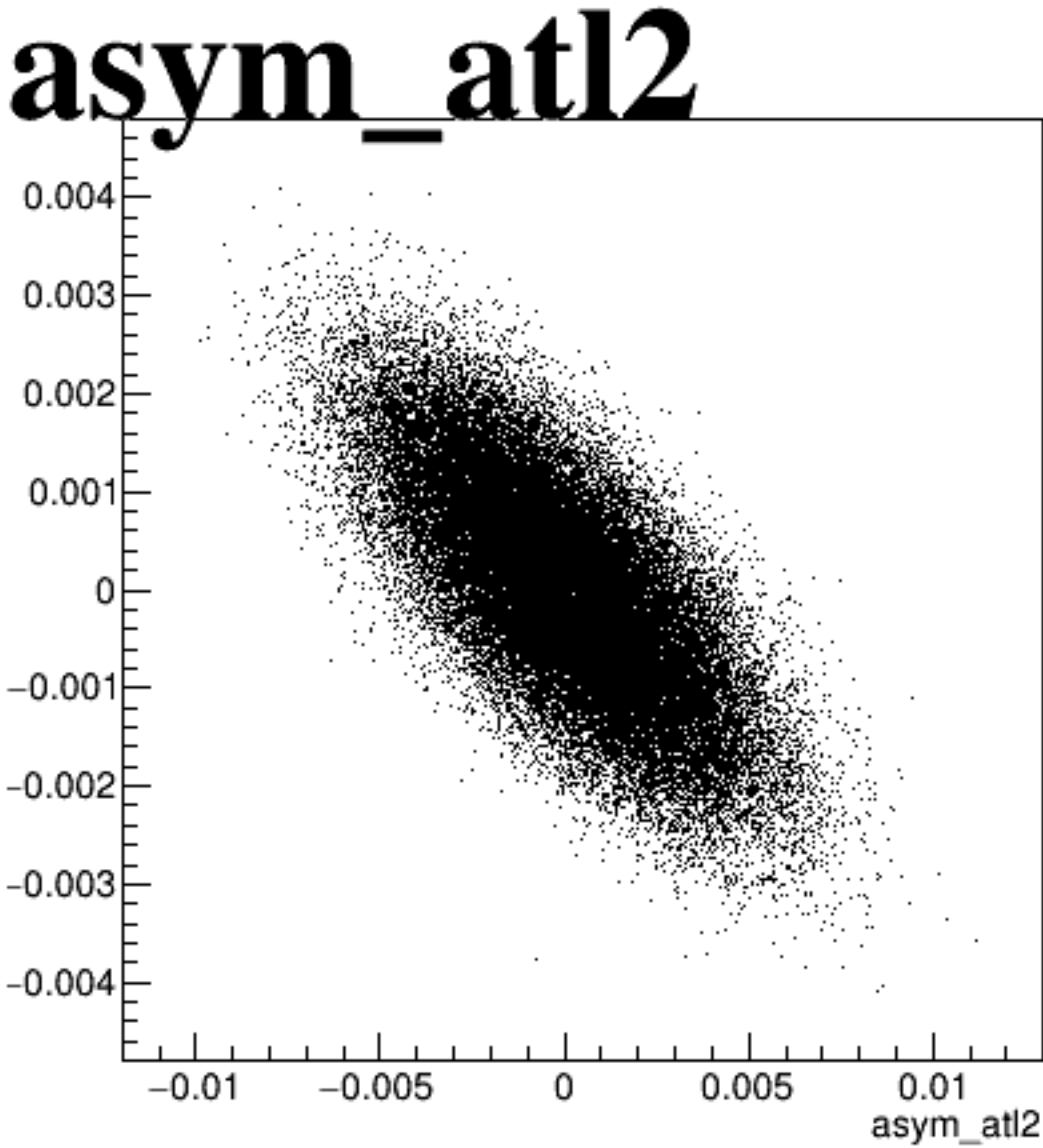
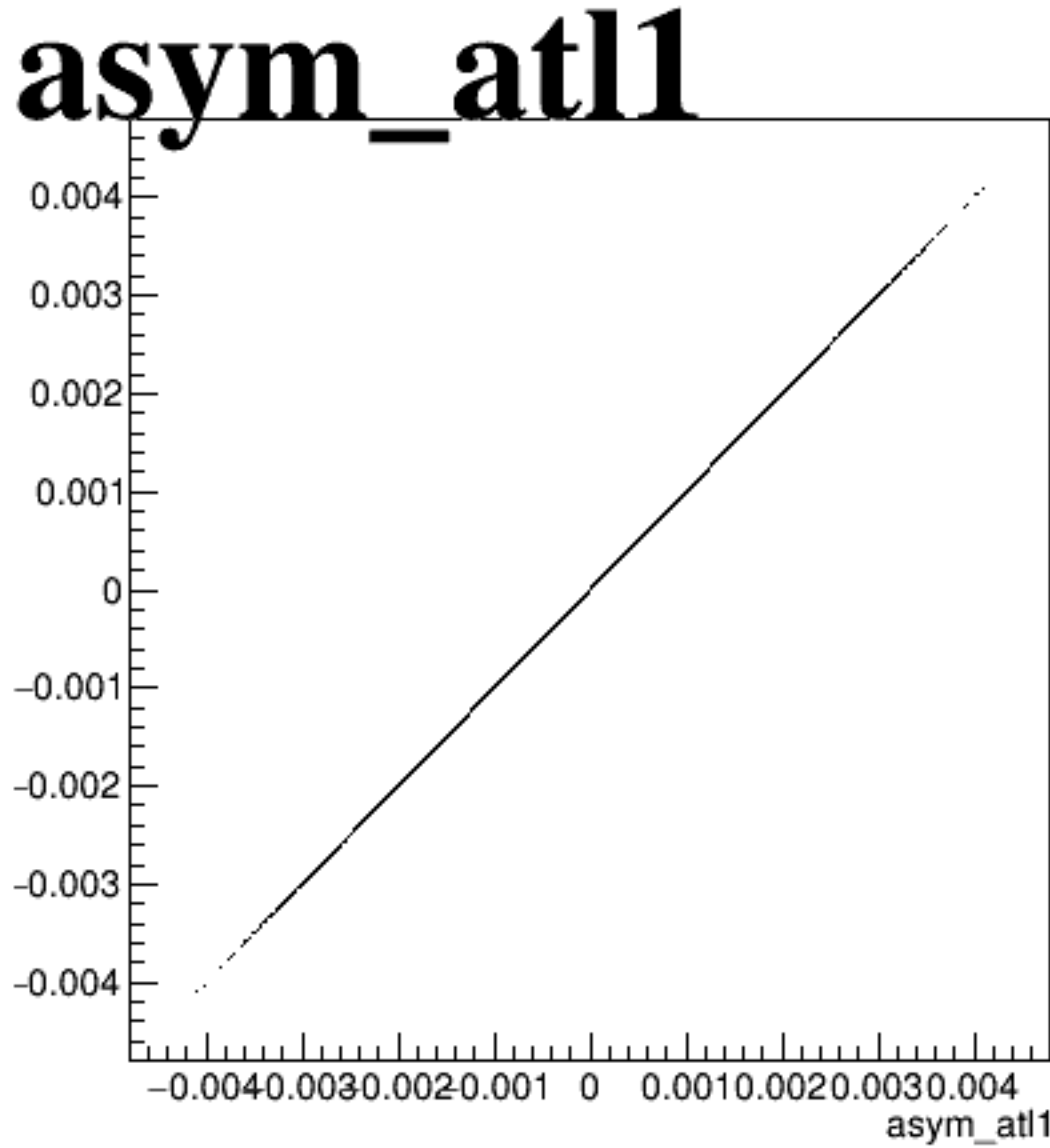
asym_atr1



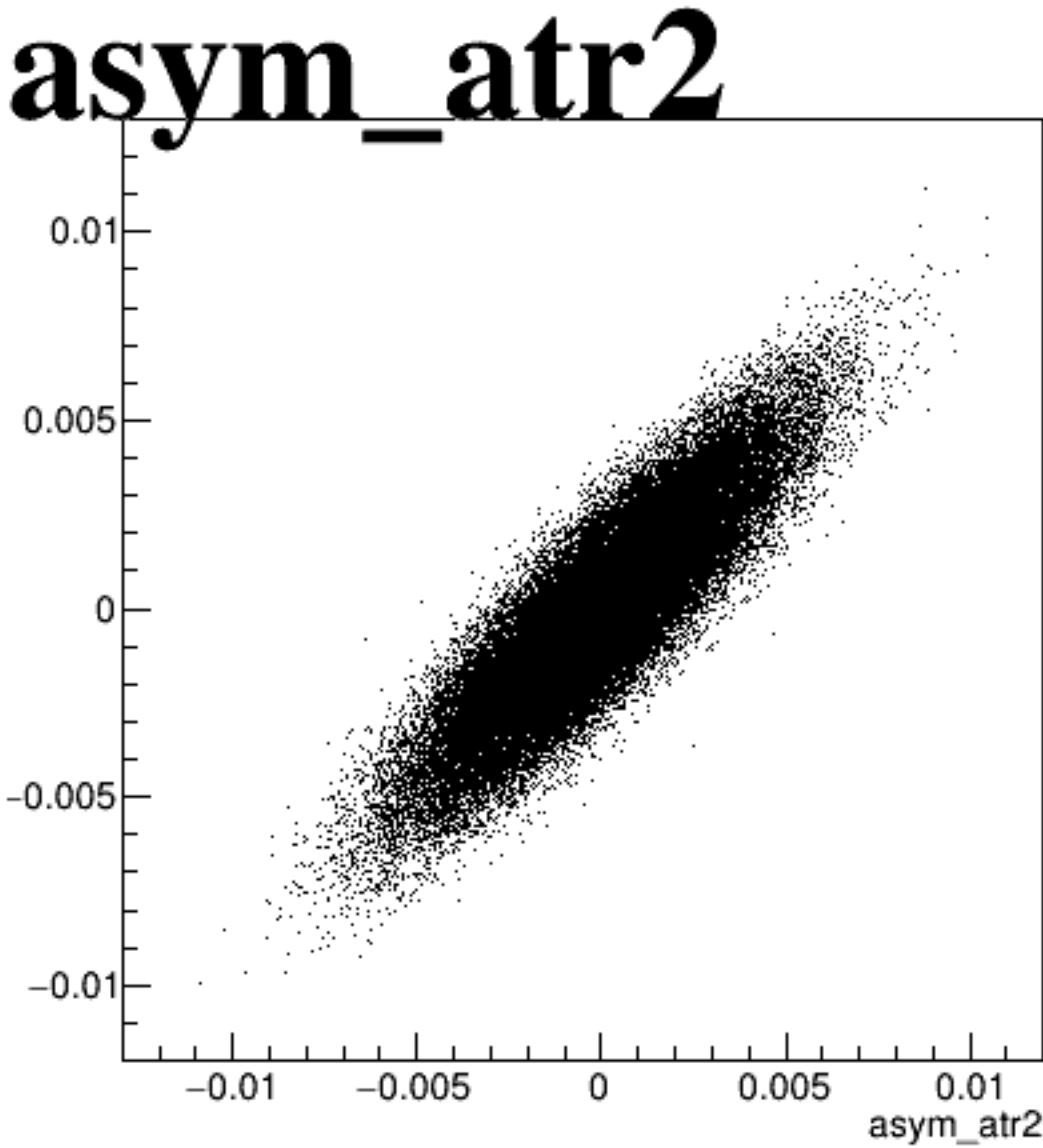
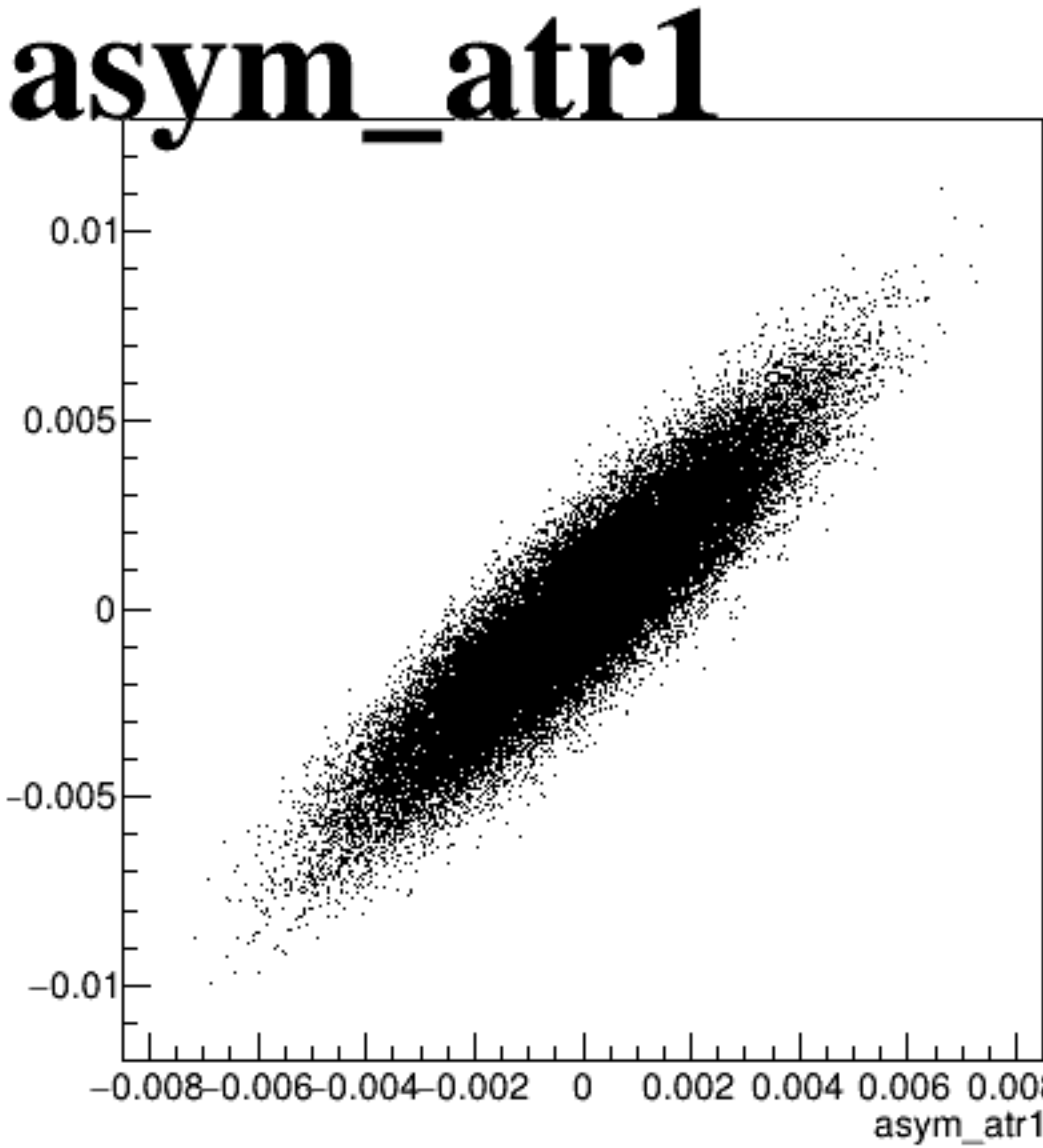
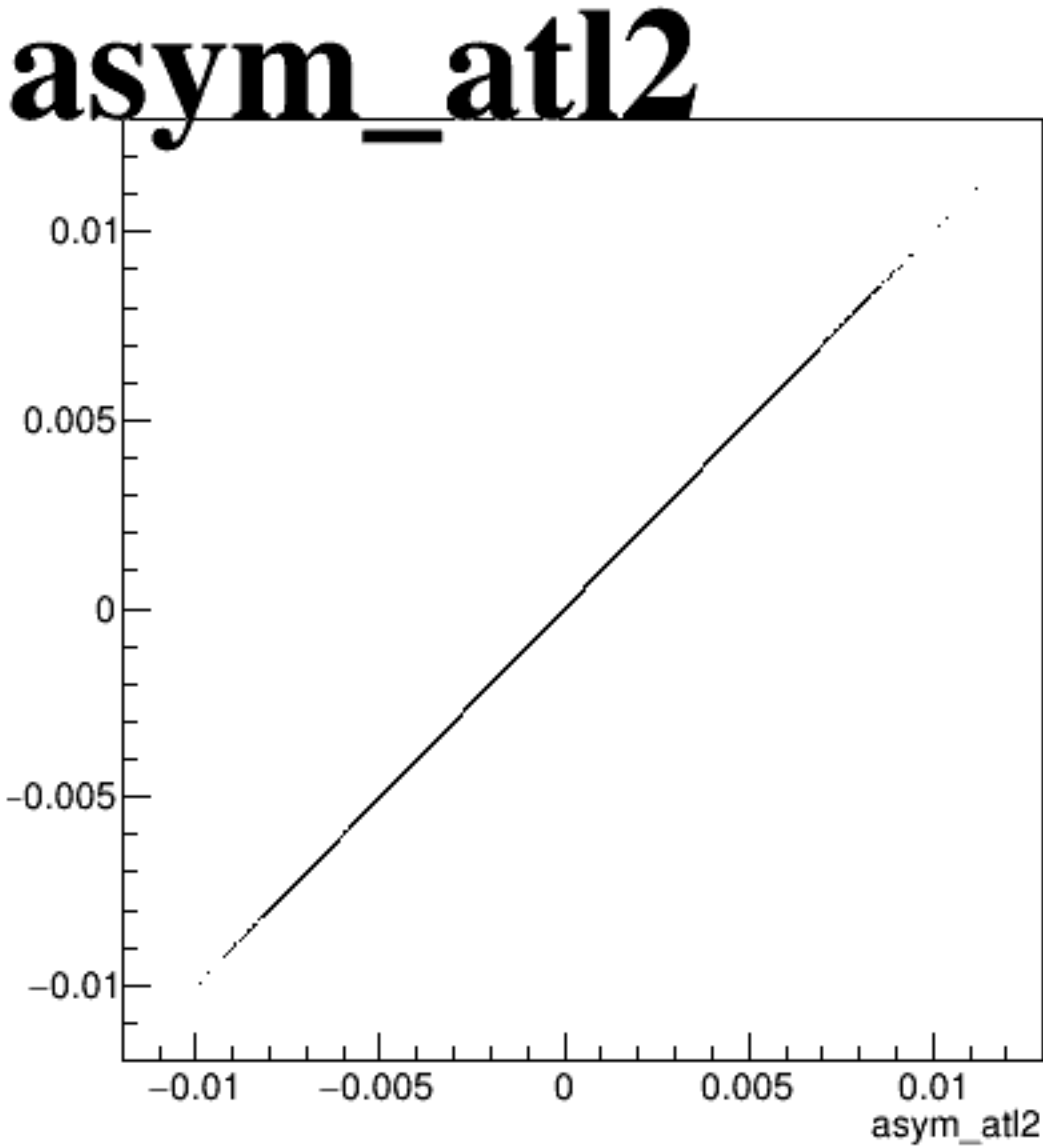
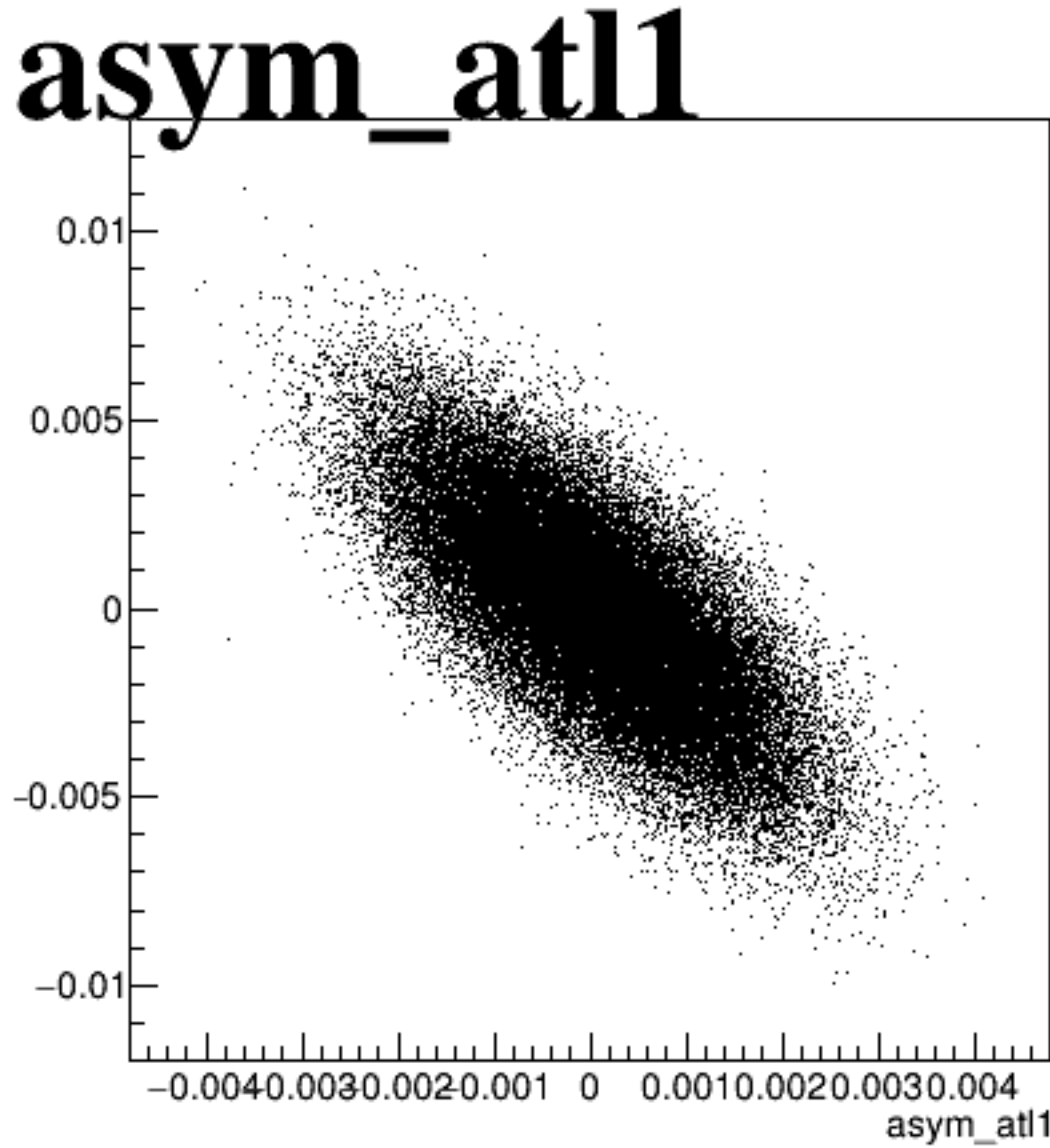
asym_atr2



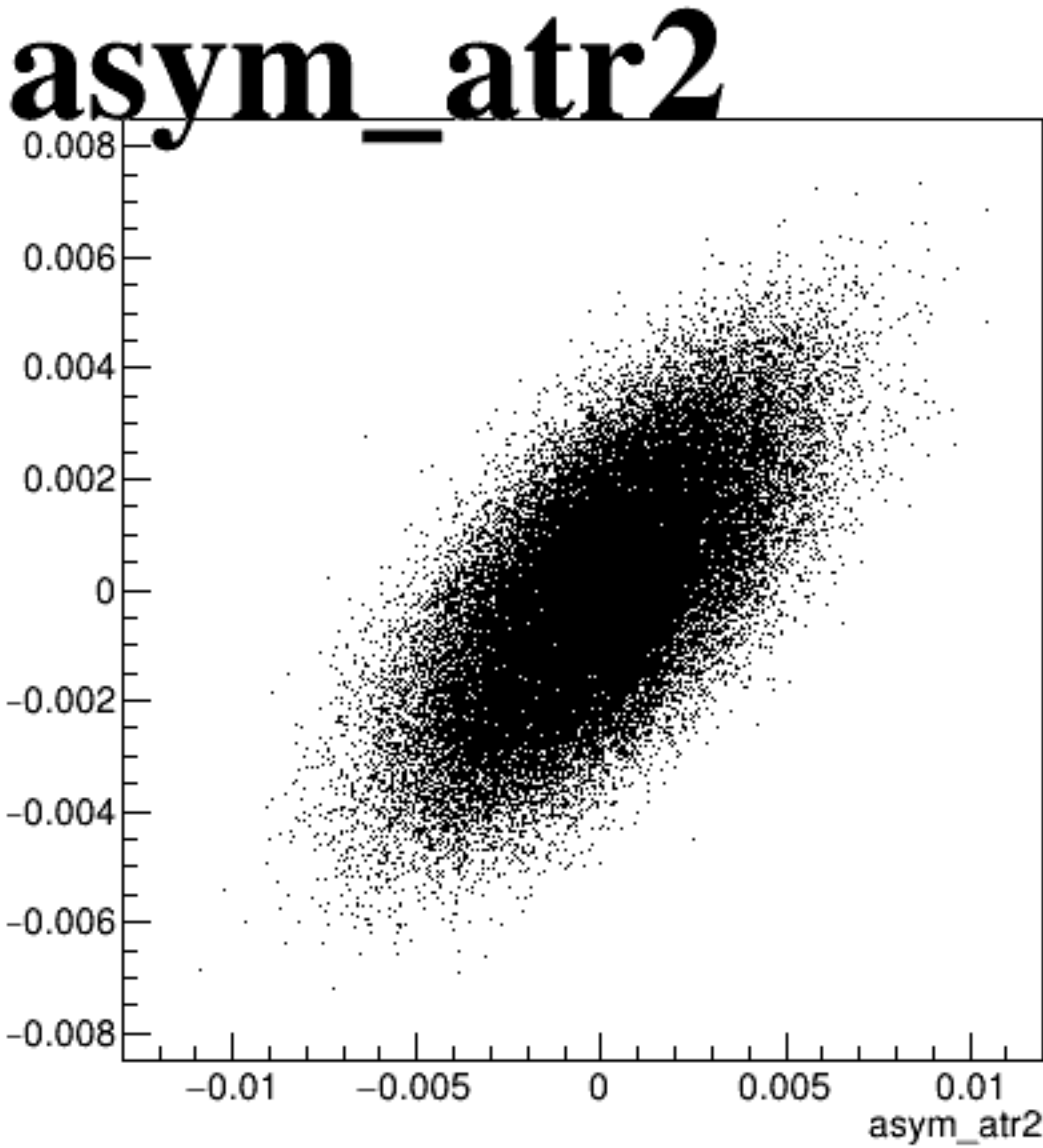
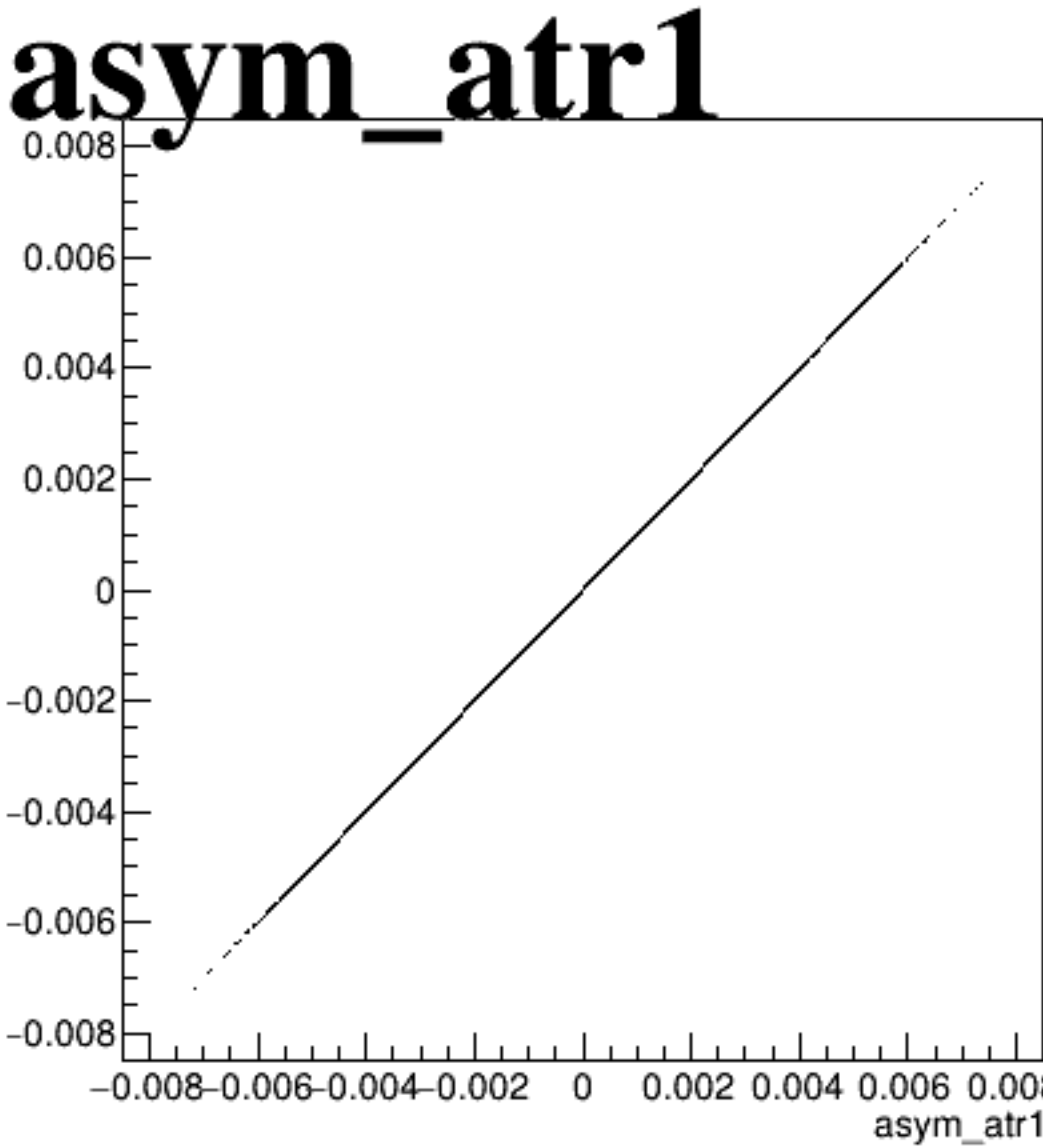
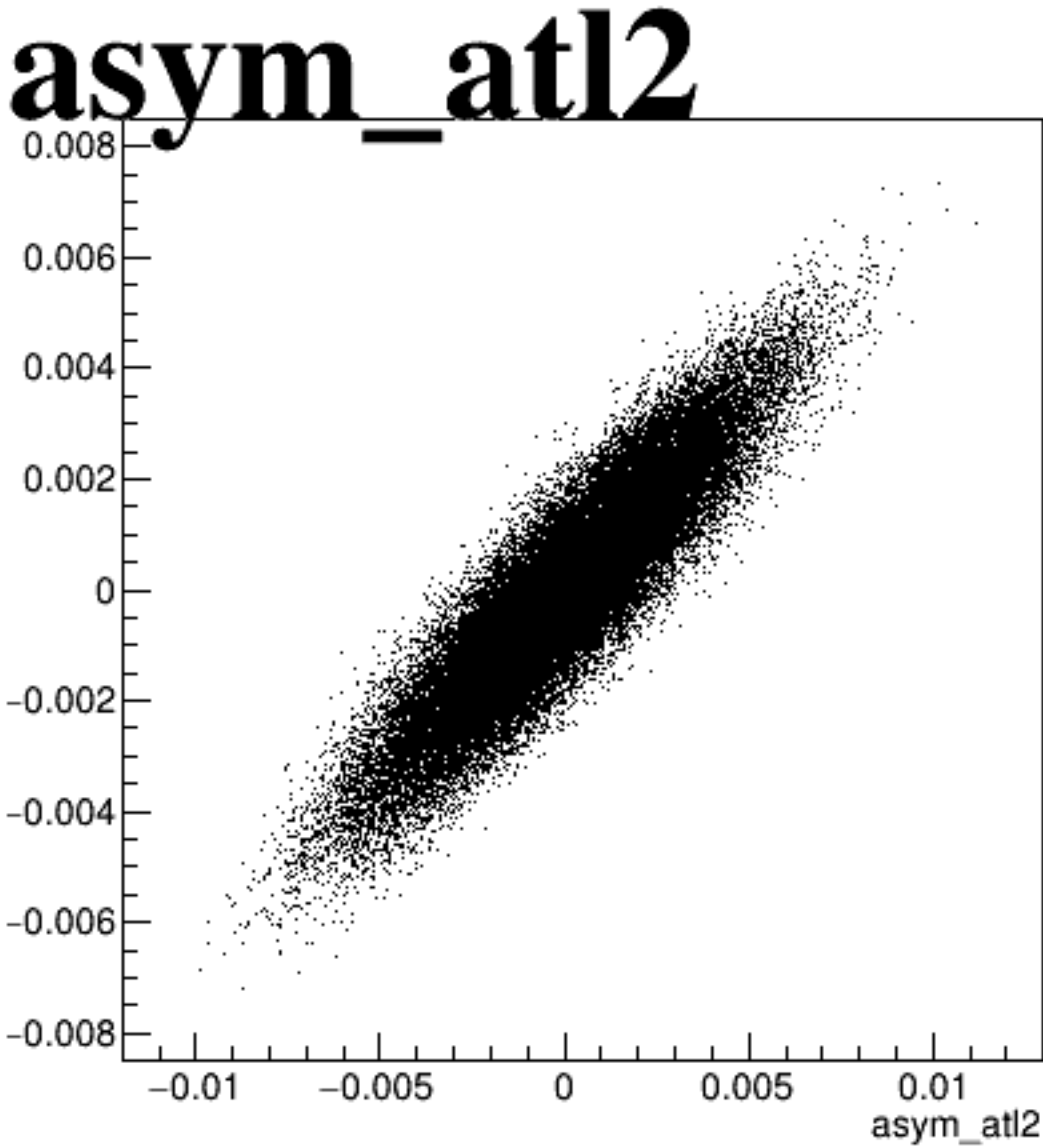
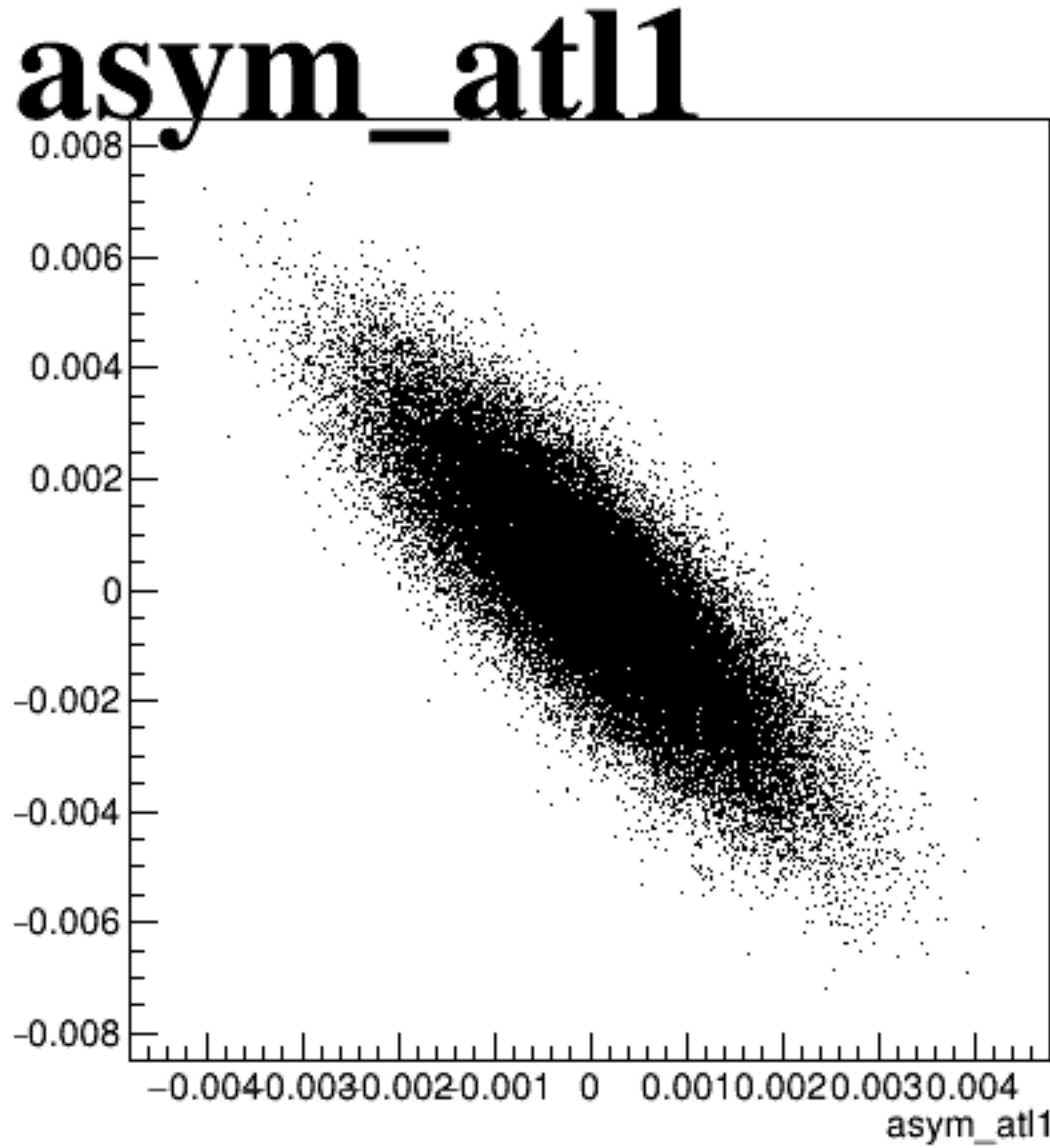
asym_atl1



asym_atl2



asym_atr1



asym_atr2

