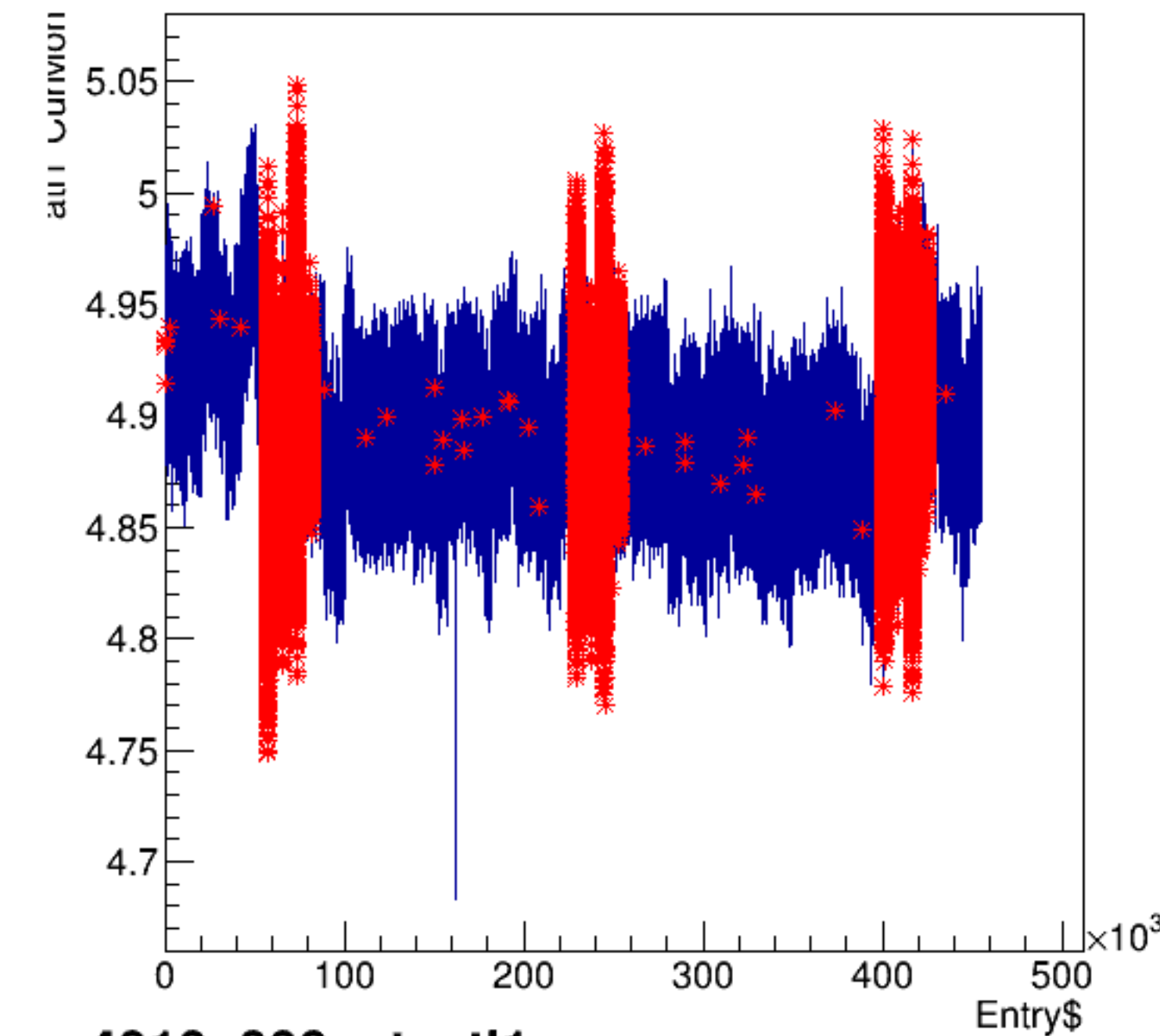
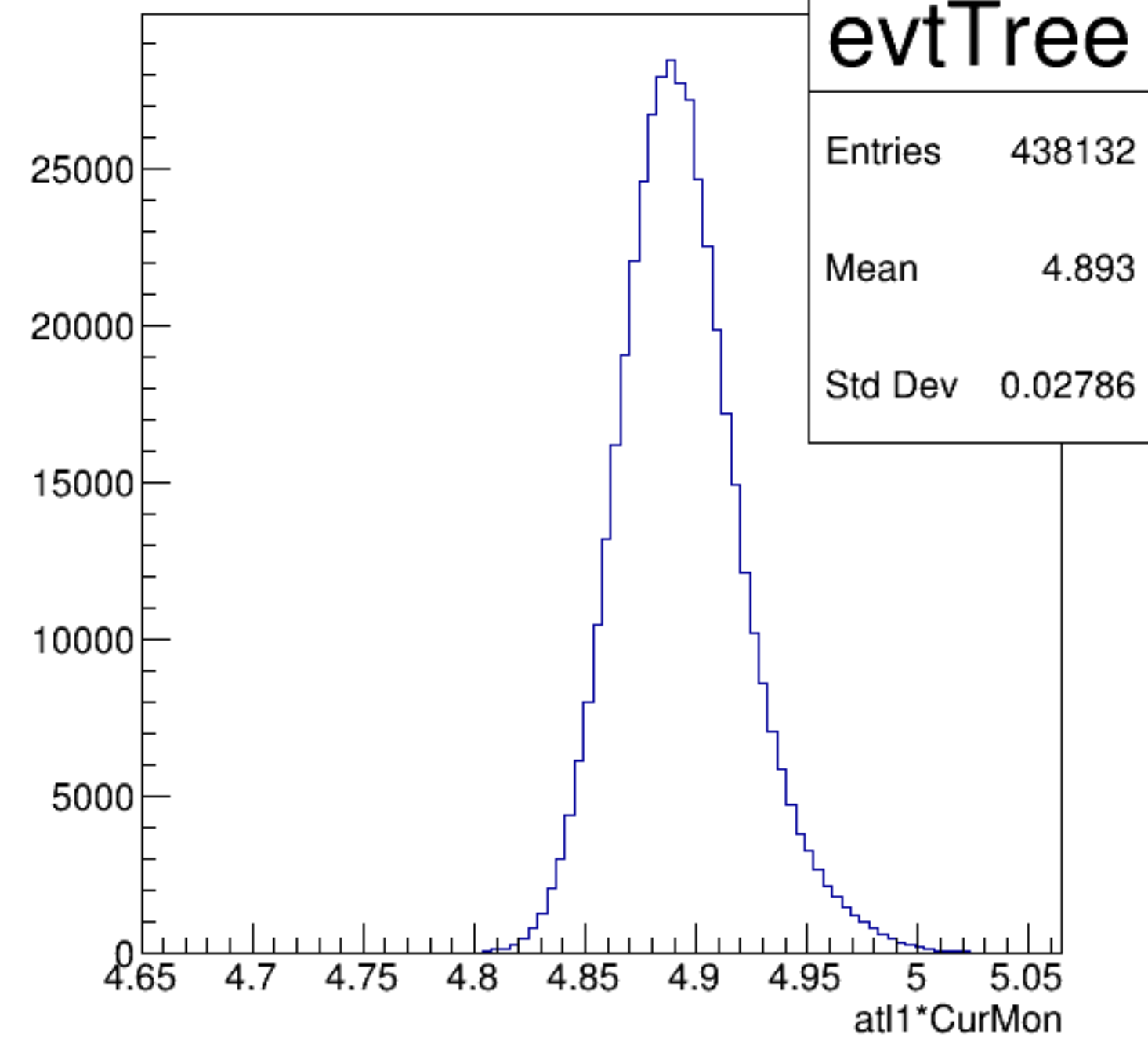


atl1*CurMon:Entry\$

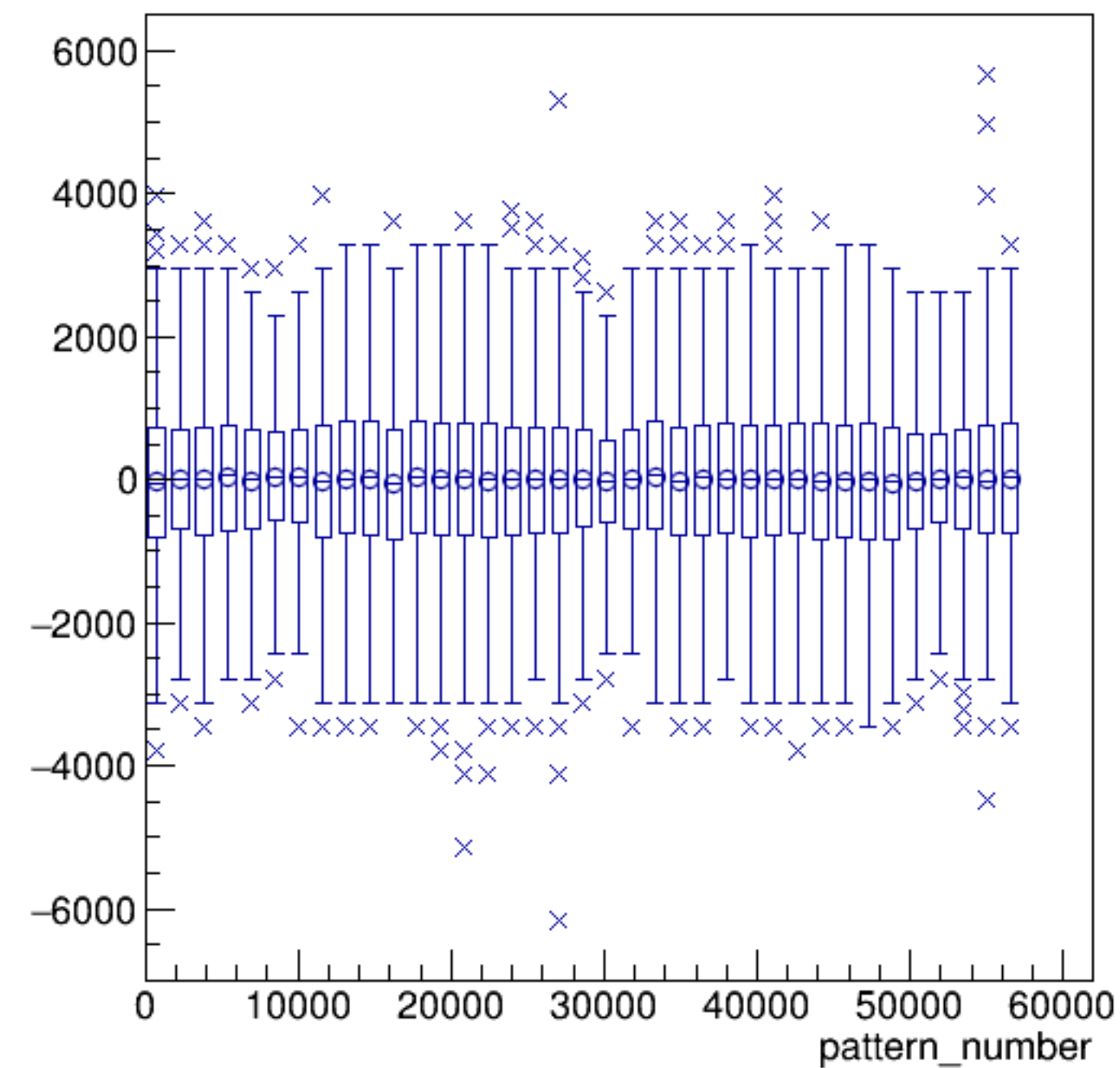


run4010_000_at_atl1.png

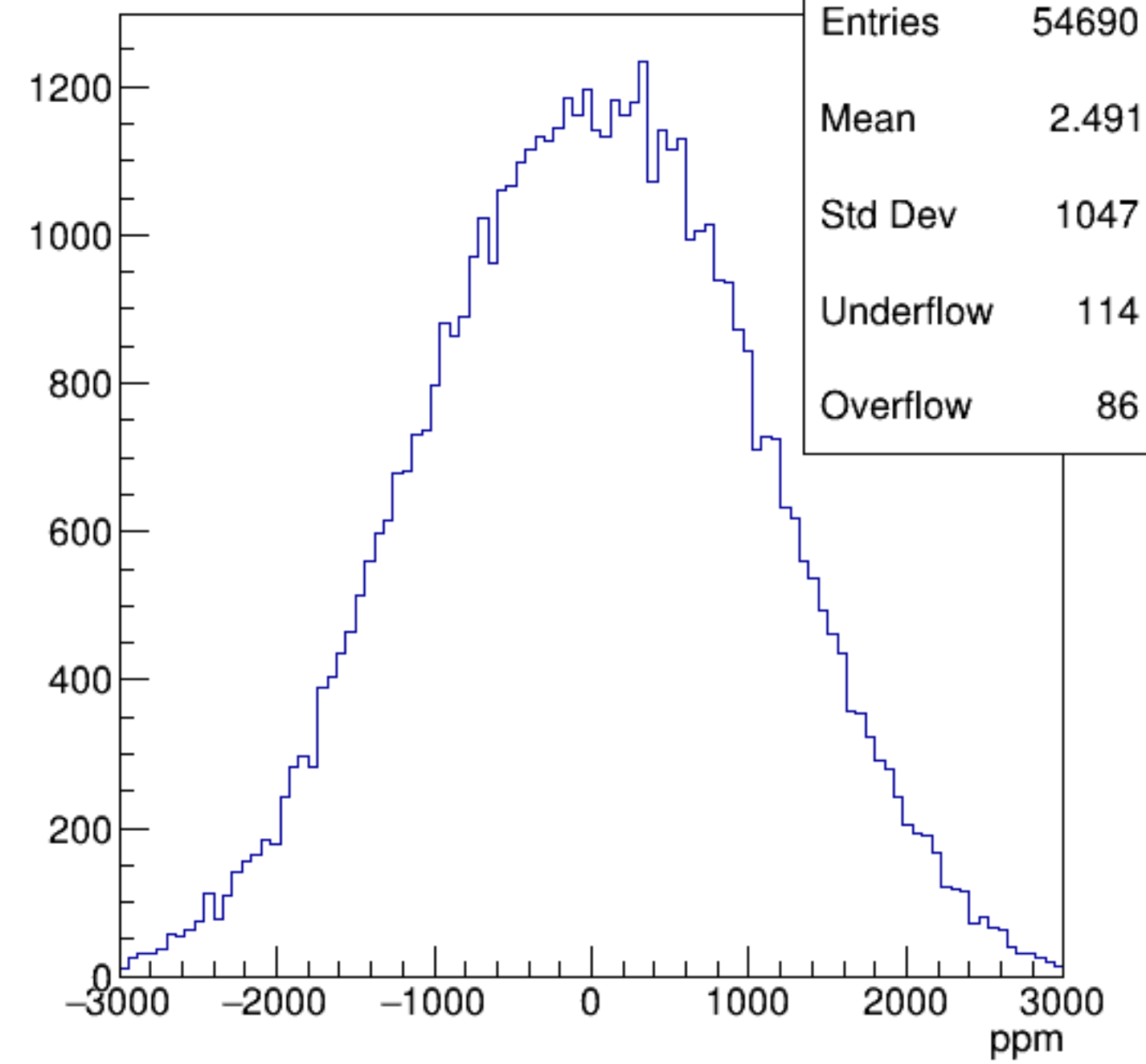
atl1*CurMon {ErrorFlag==0}



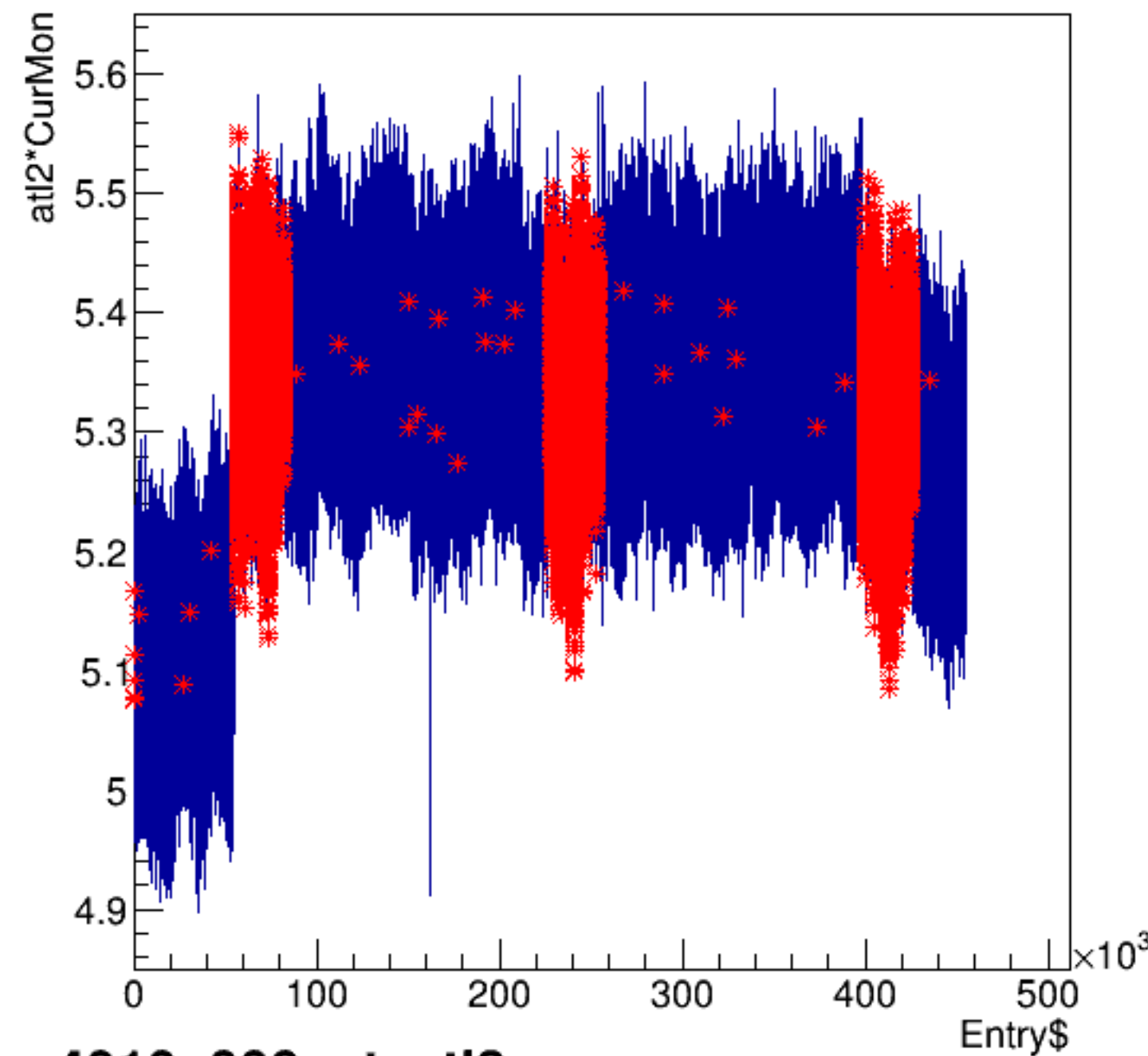
asym_atl1/ppm:pattern_number {ErrorFlag==0}



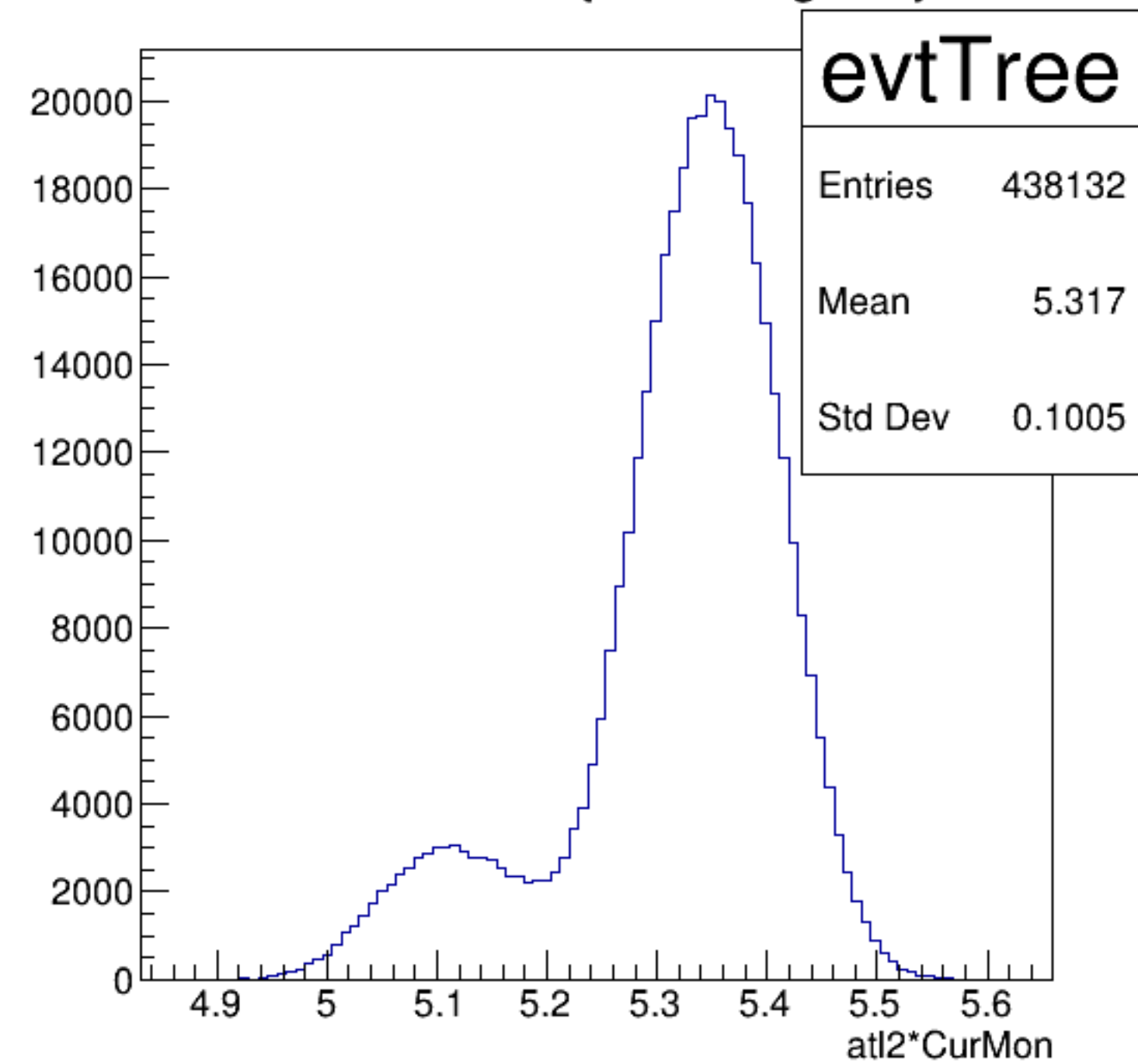
asym_atl1



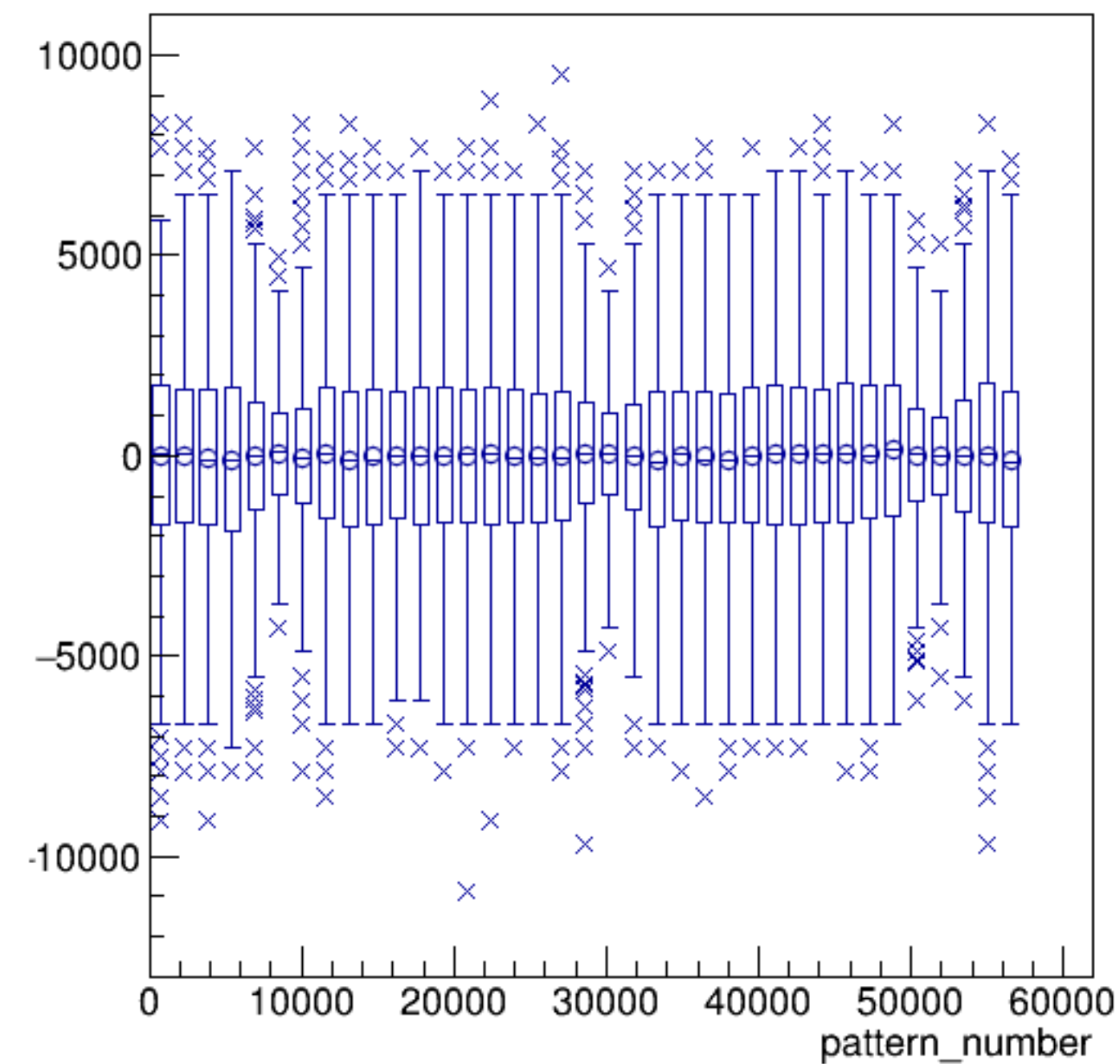
atl2*CurMon:Entry\$



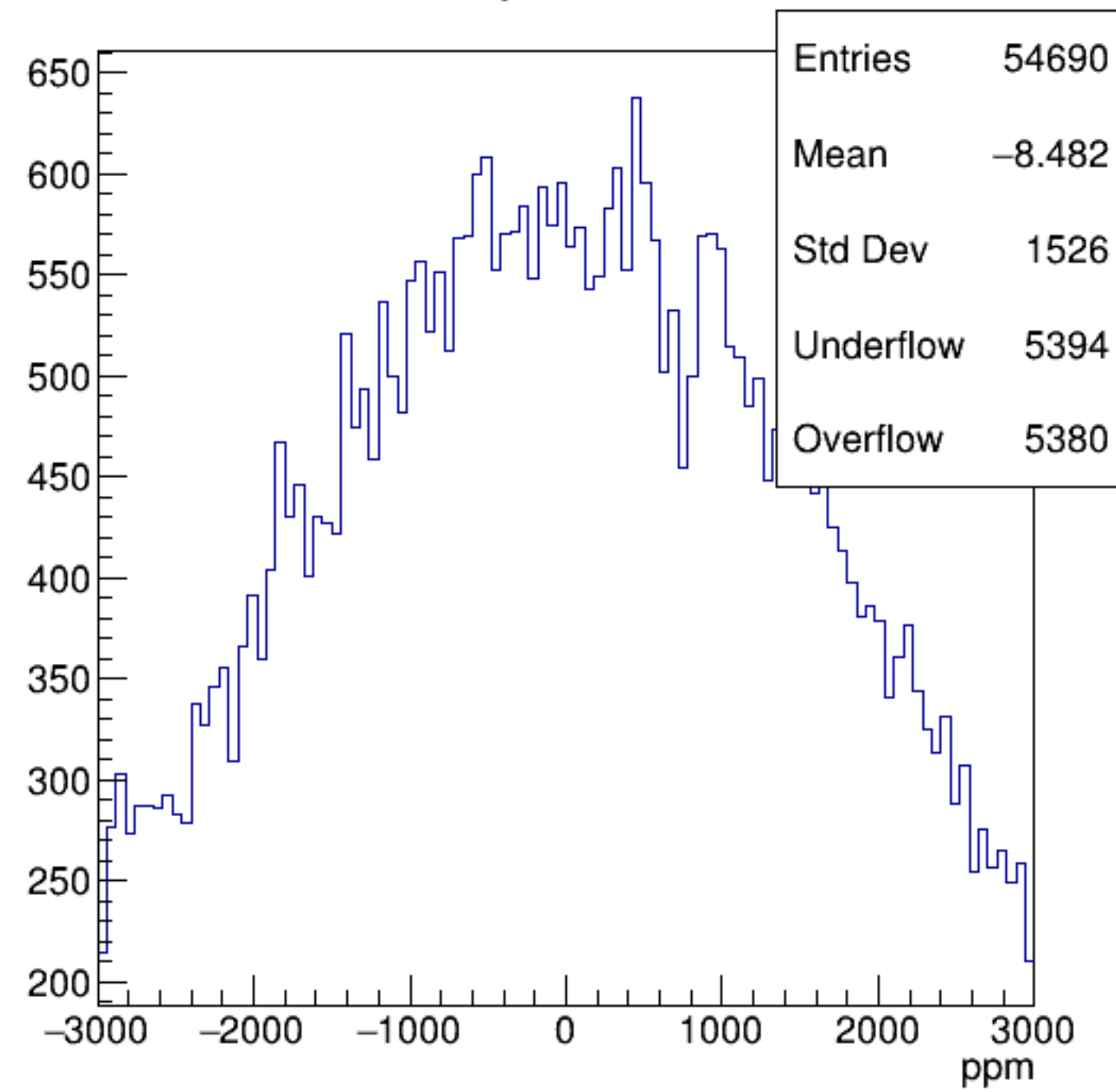
atl2*CurMon {ErrorFlag==0}



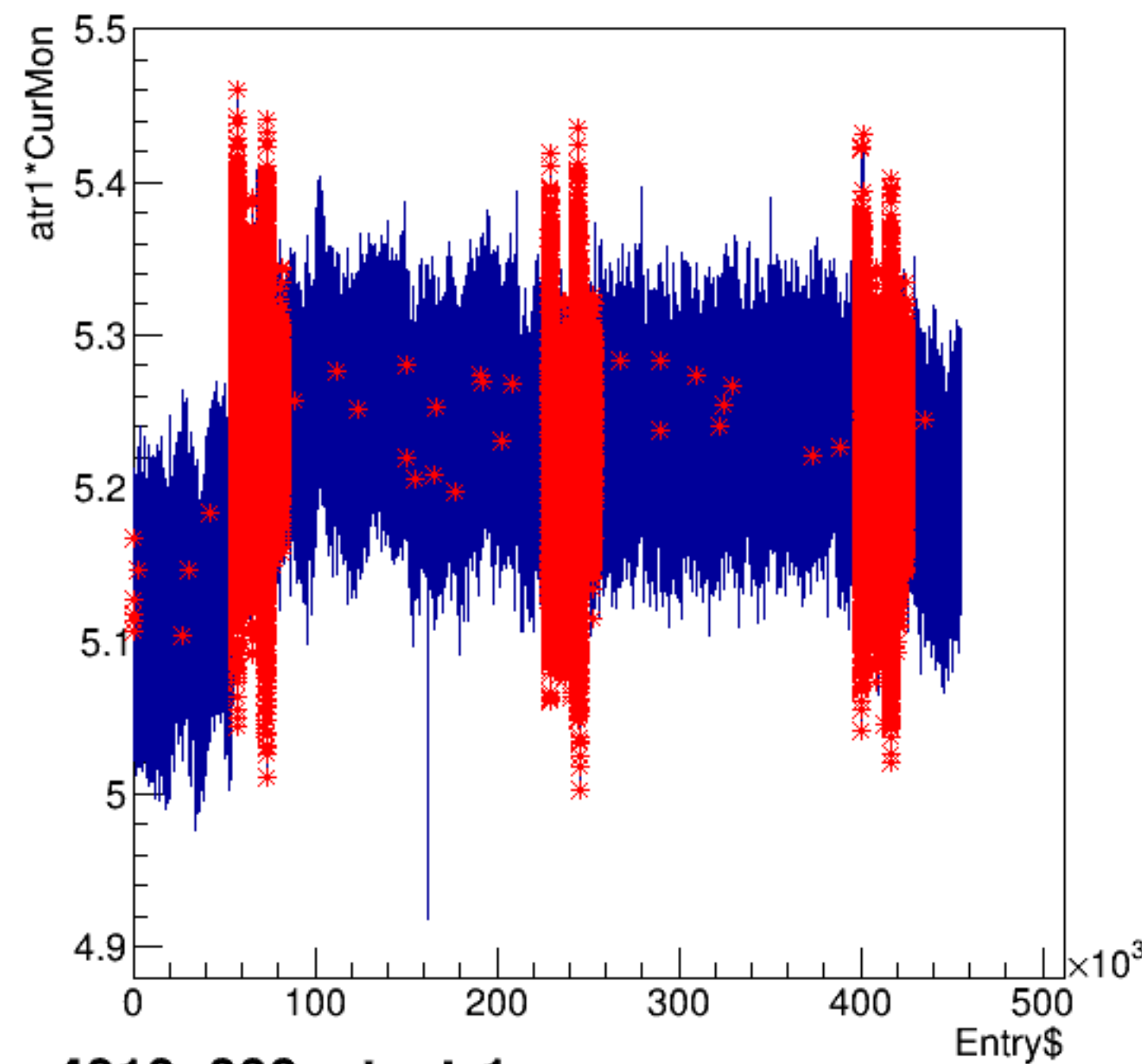
asym_atl2/ppm:pattern_number {ErrorFlag==0}



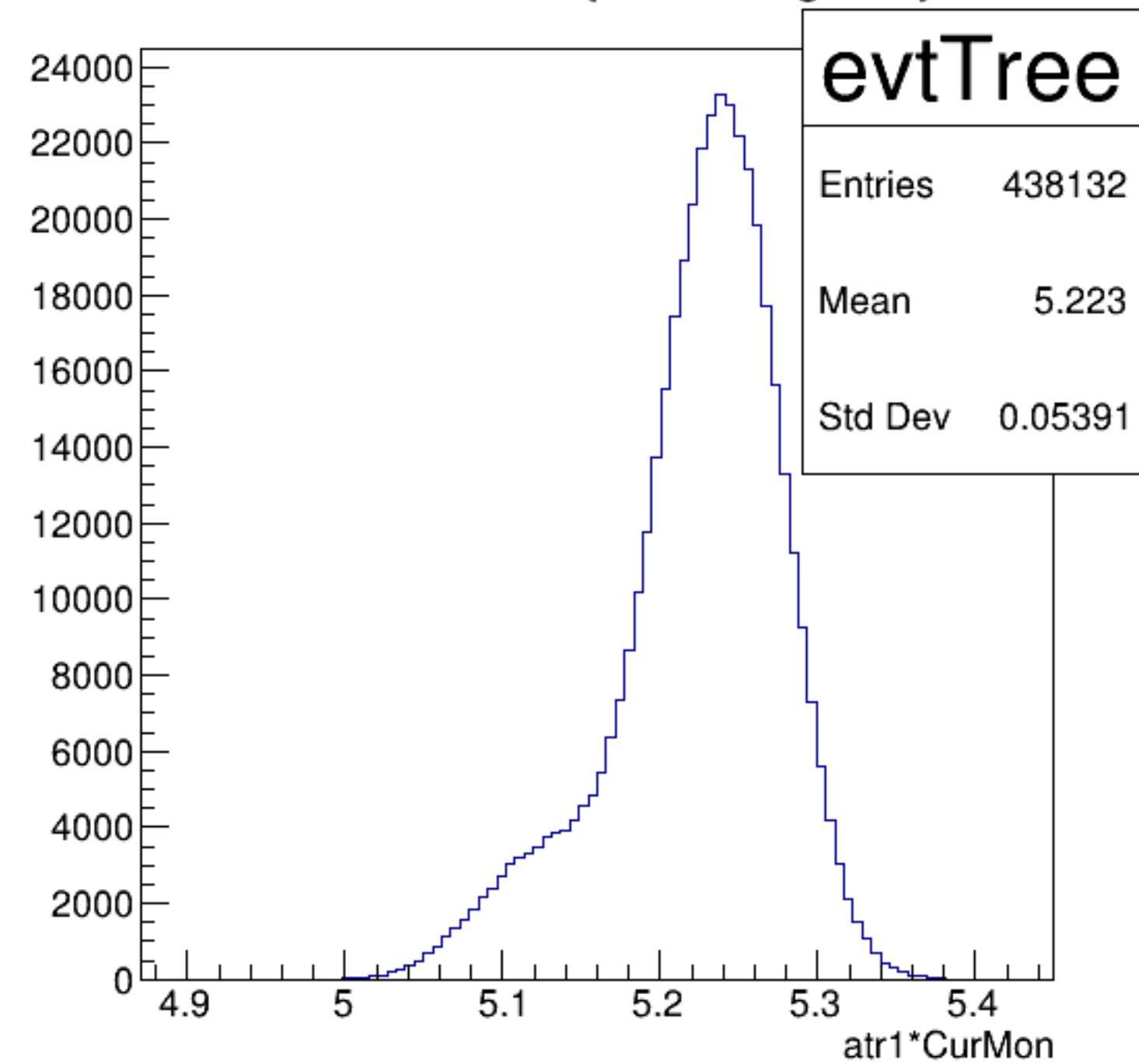
asym_atl2



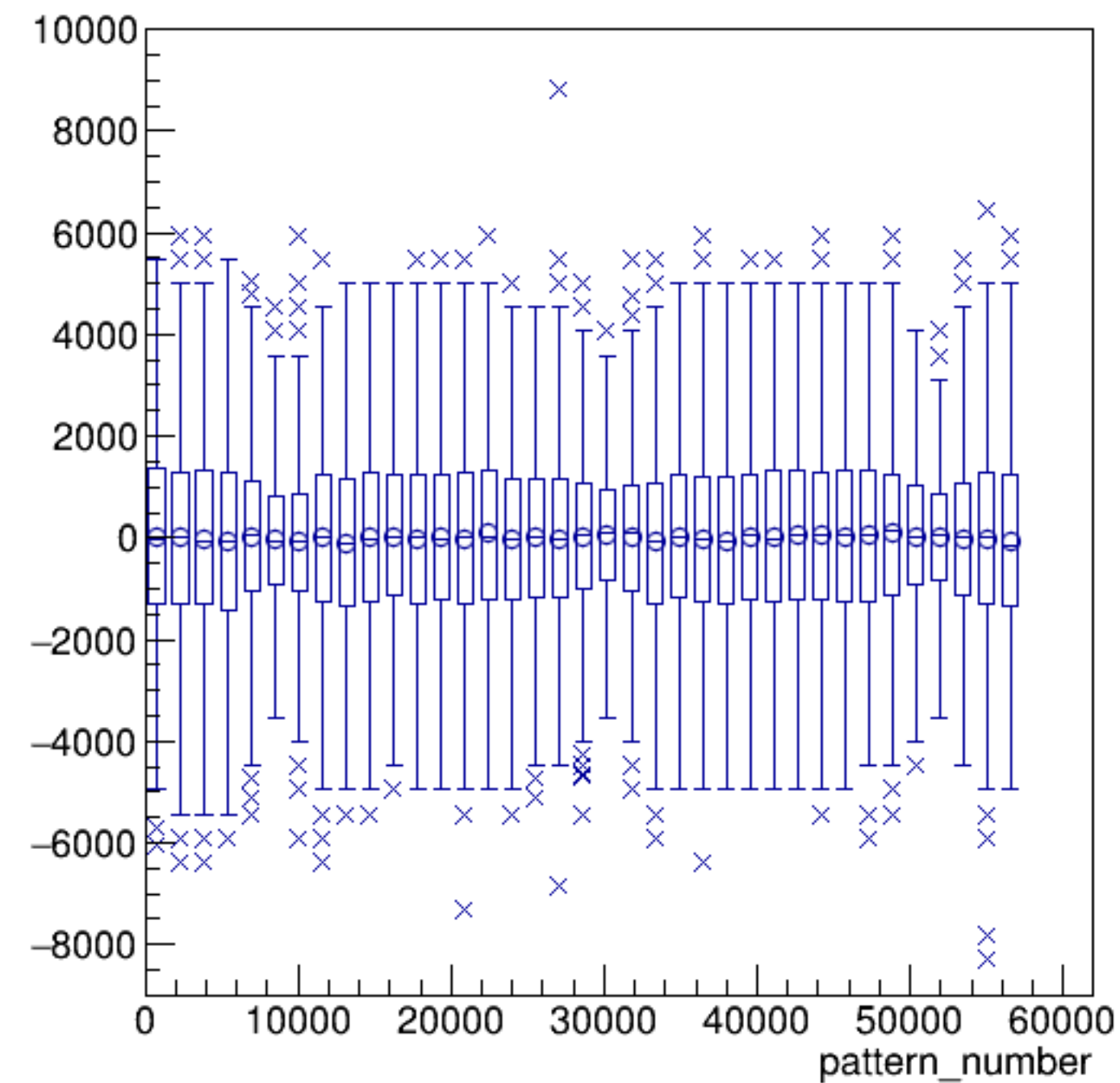
atr1*CurMon:Entry\$



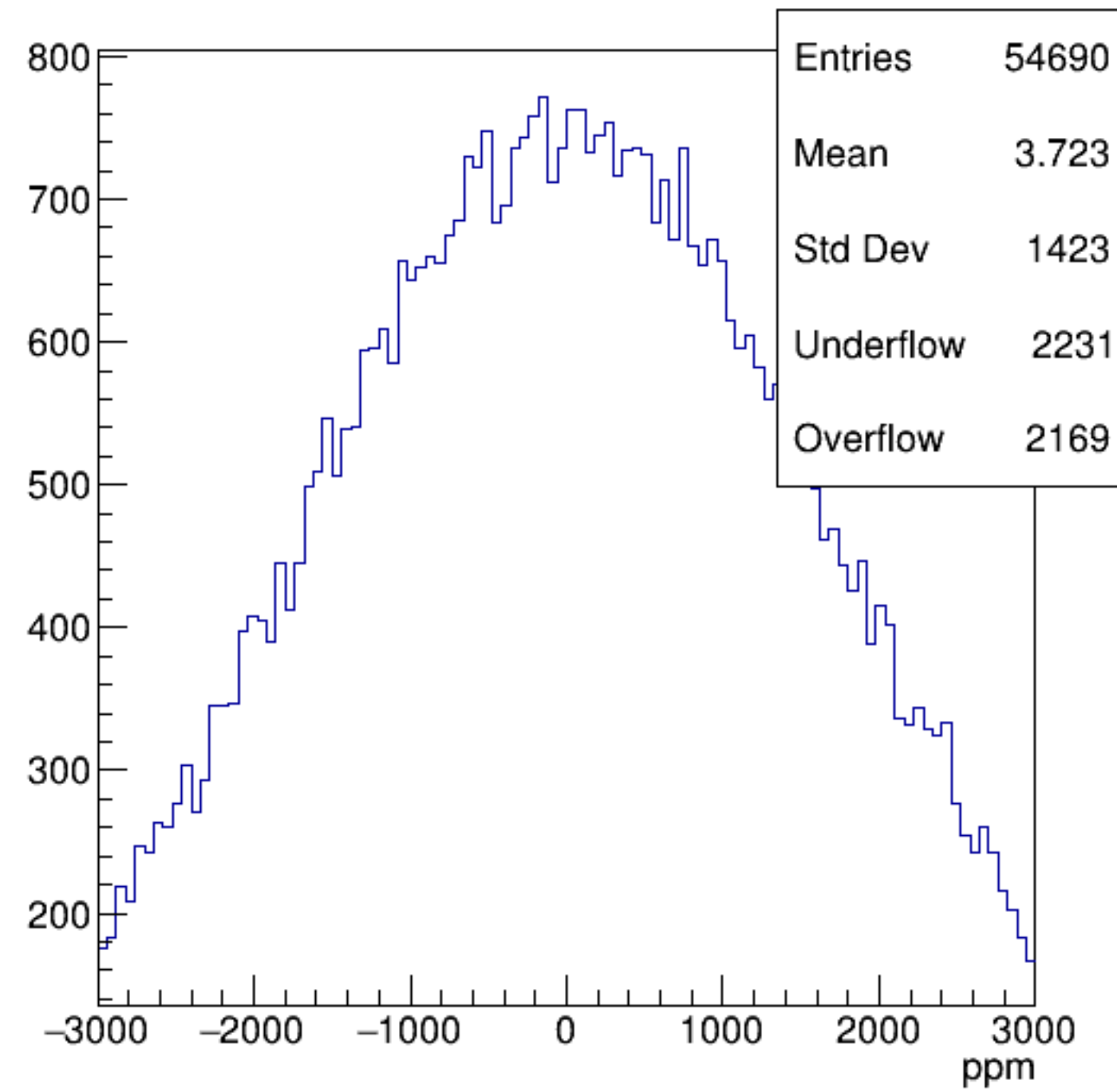
atr1*CurMon {ErrorFlag==0}



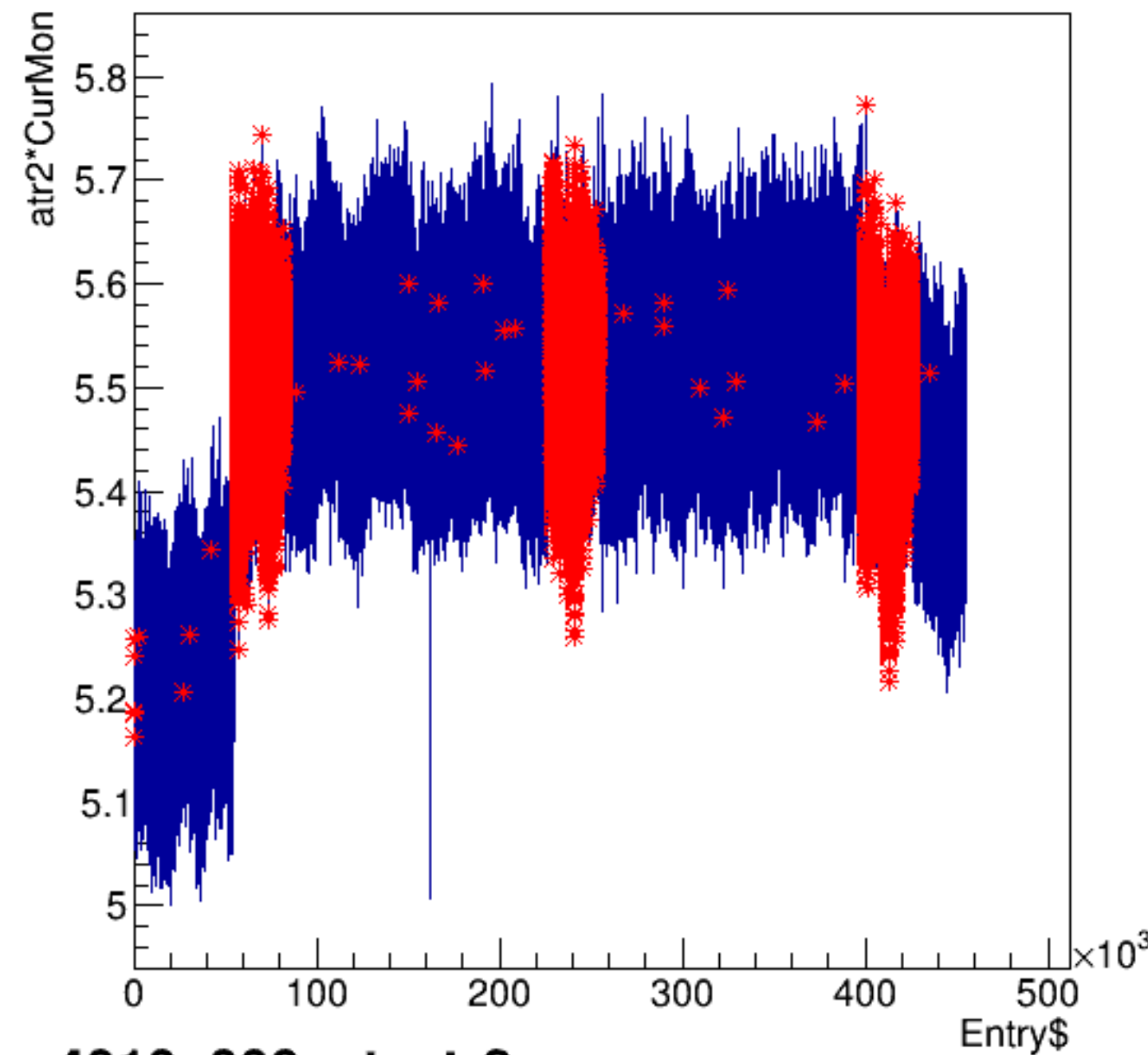
asym_atr1/ppm:pattern_number {ErrorFlag==0}



asym_atr1

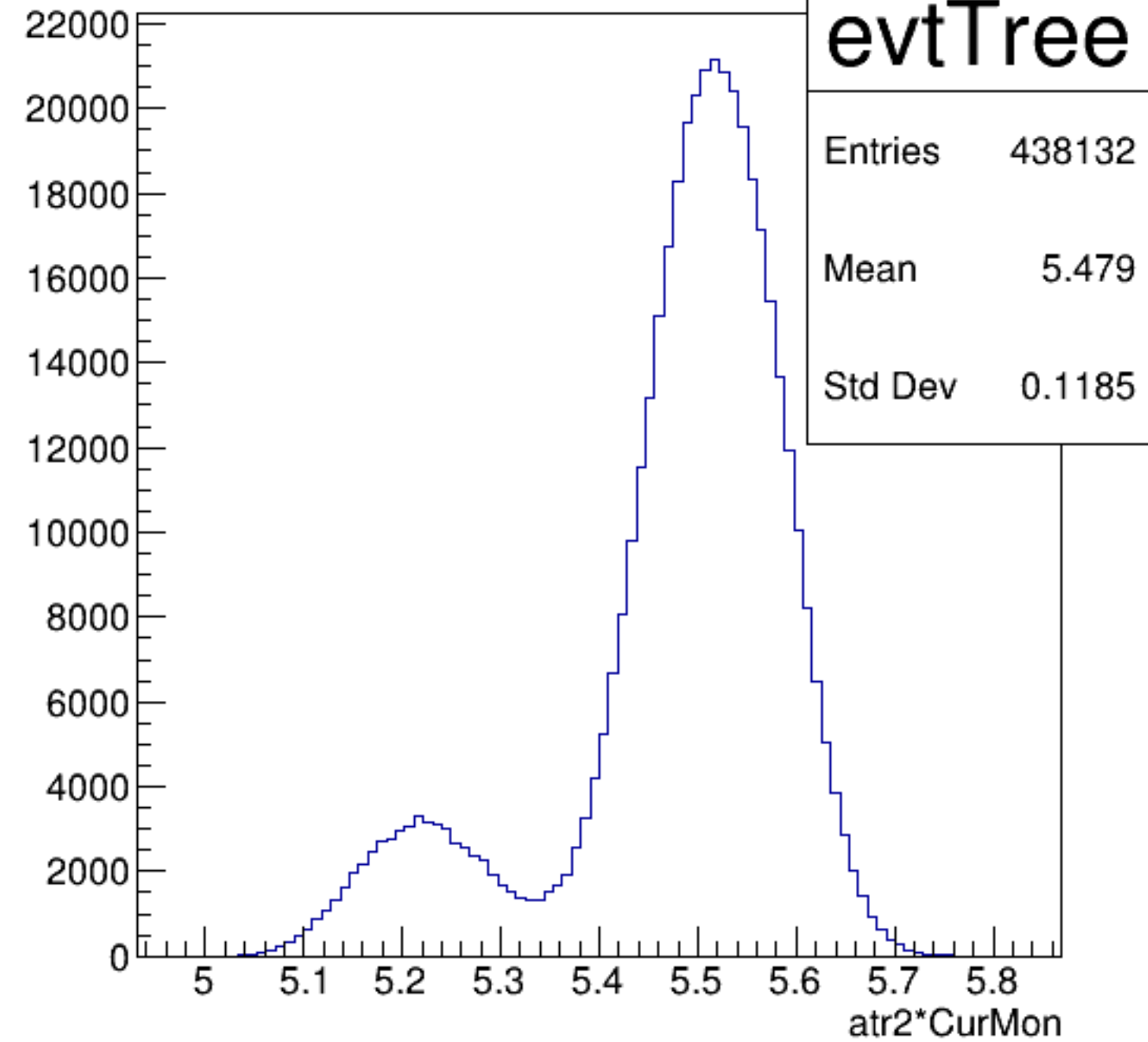


atr2*CurMon:Entry\$

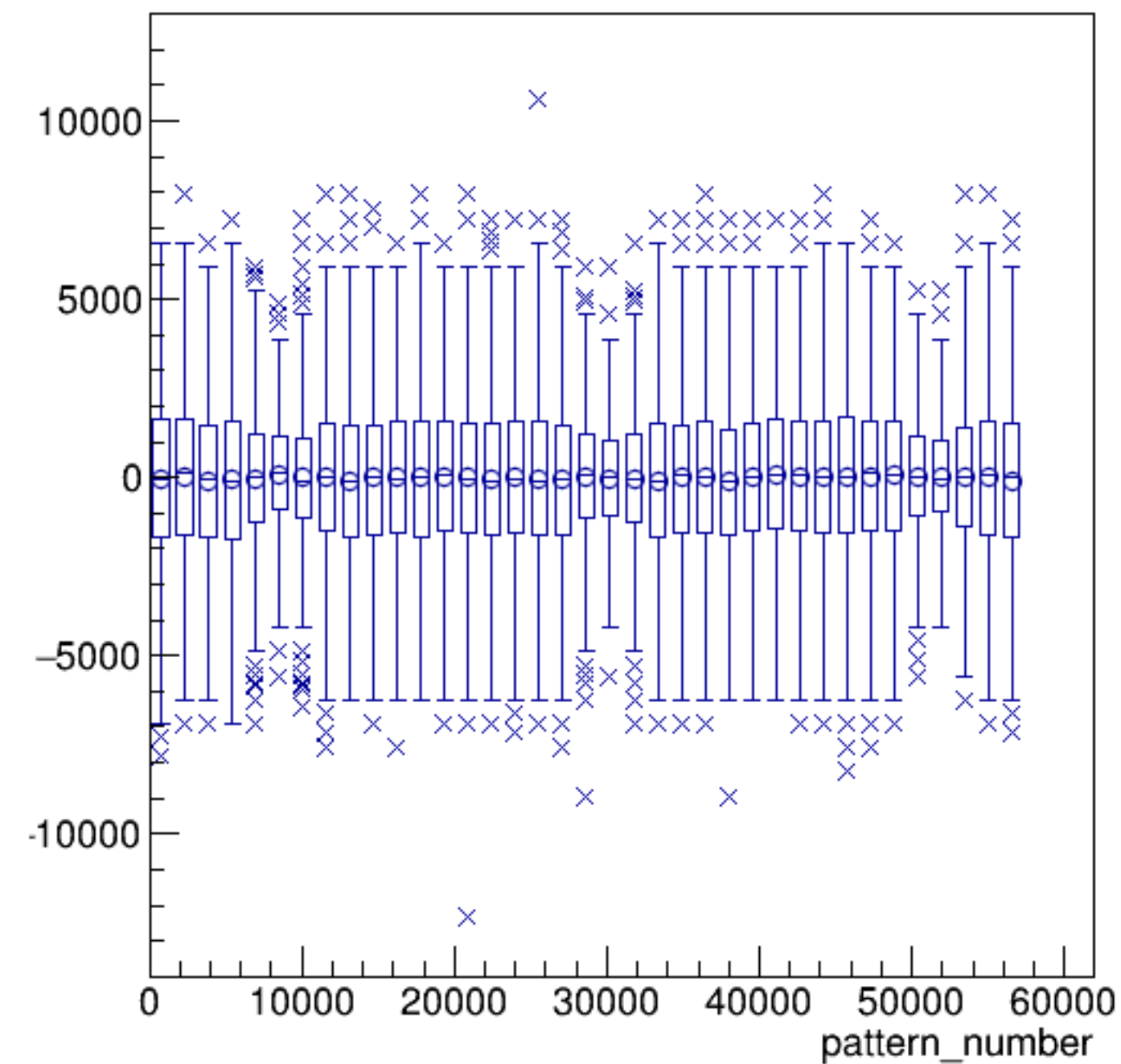


run4010_000_at_atr2.png

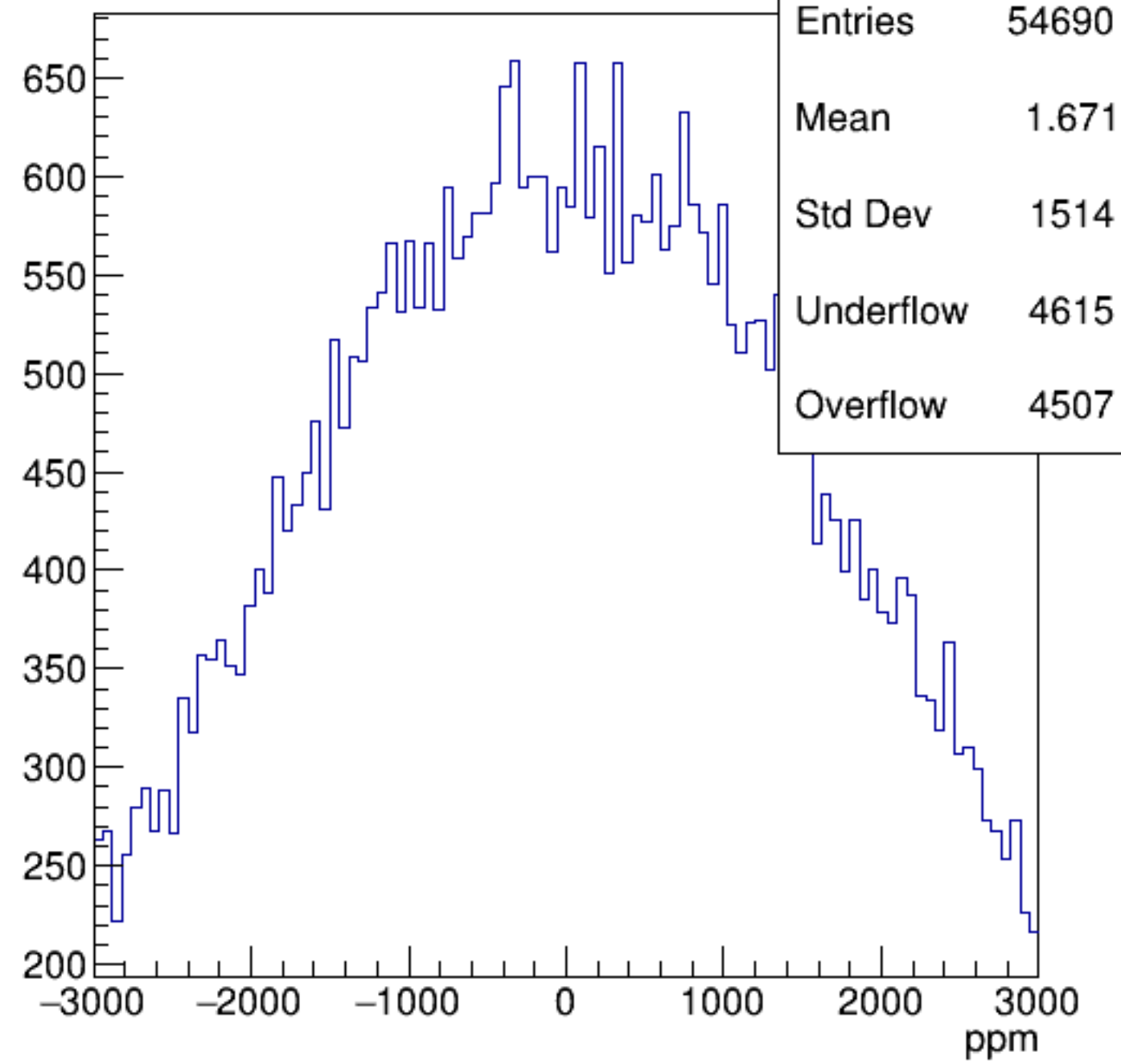
atr2*CurMon {ErrorFlag==0}



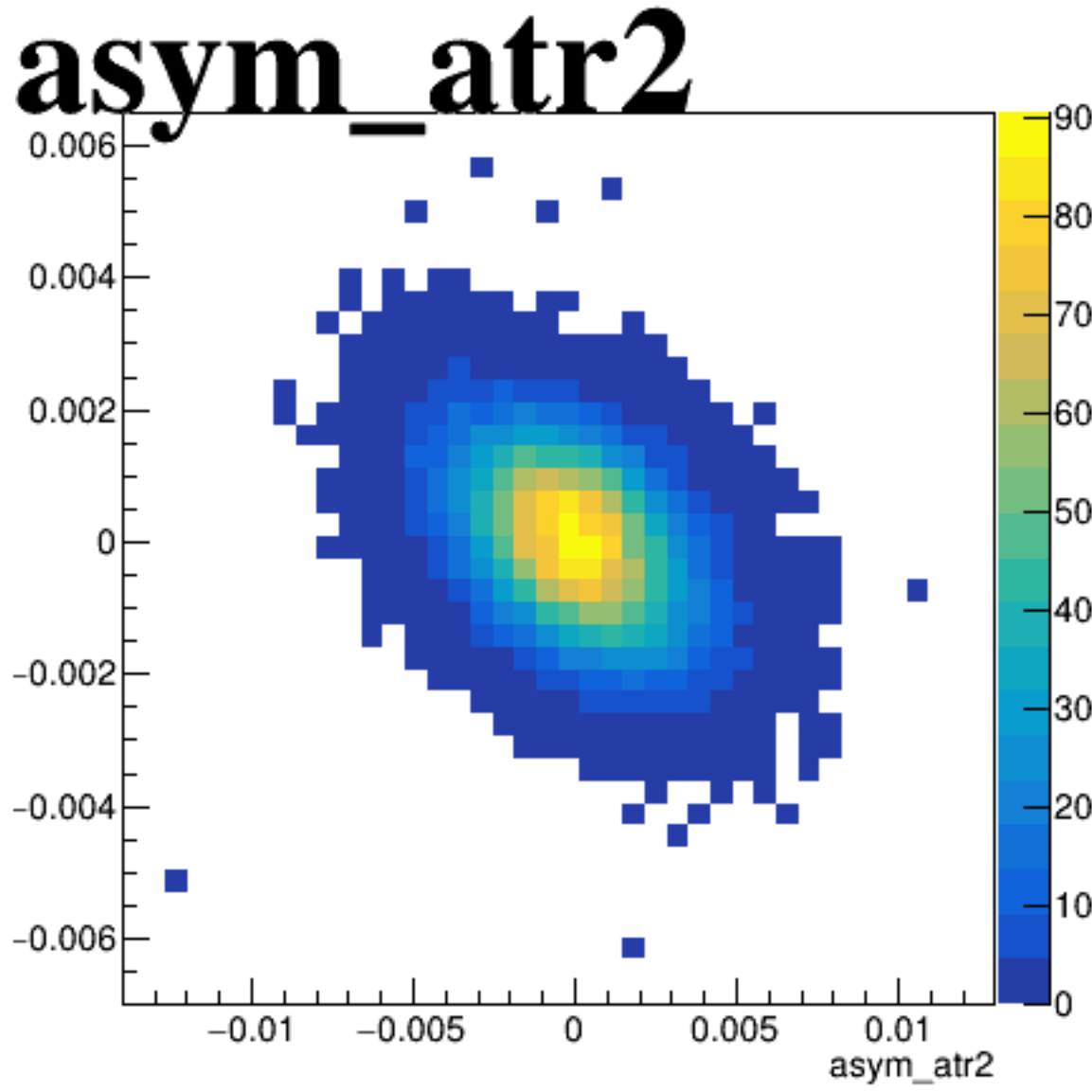
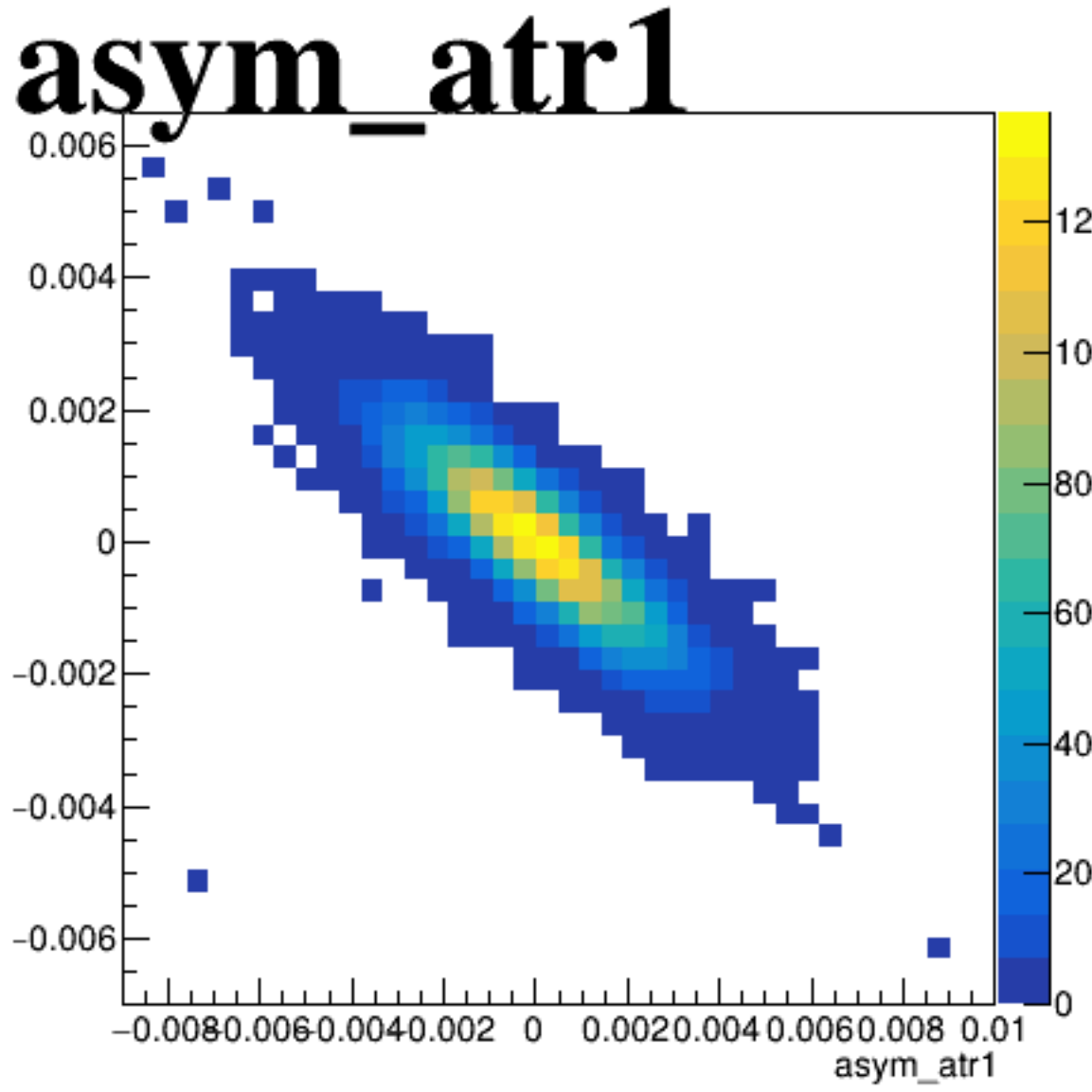
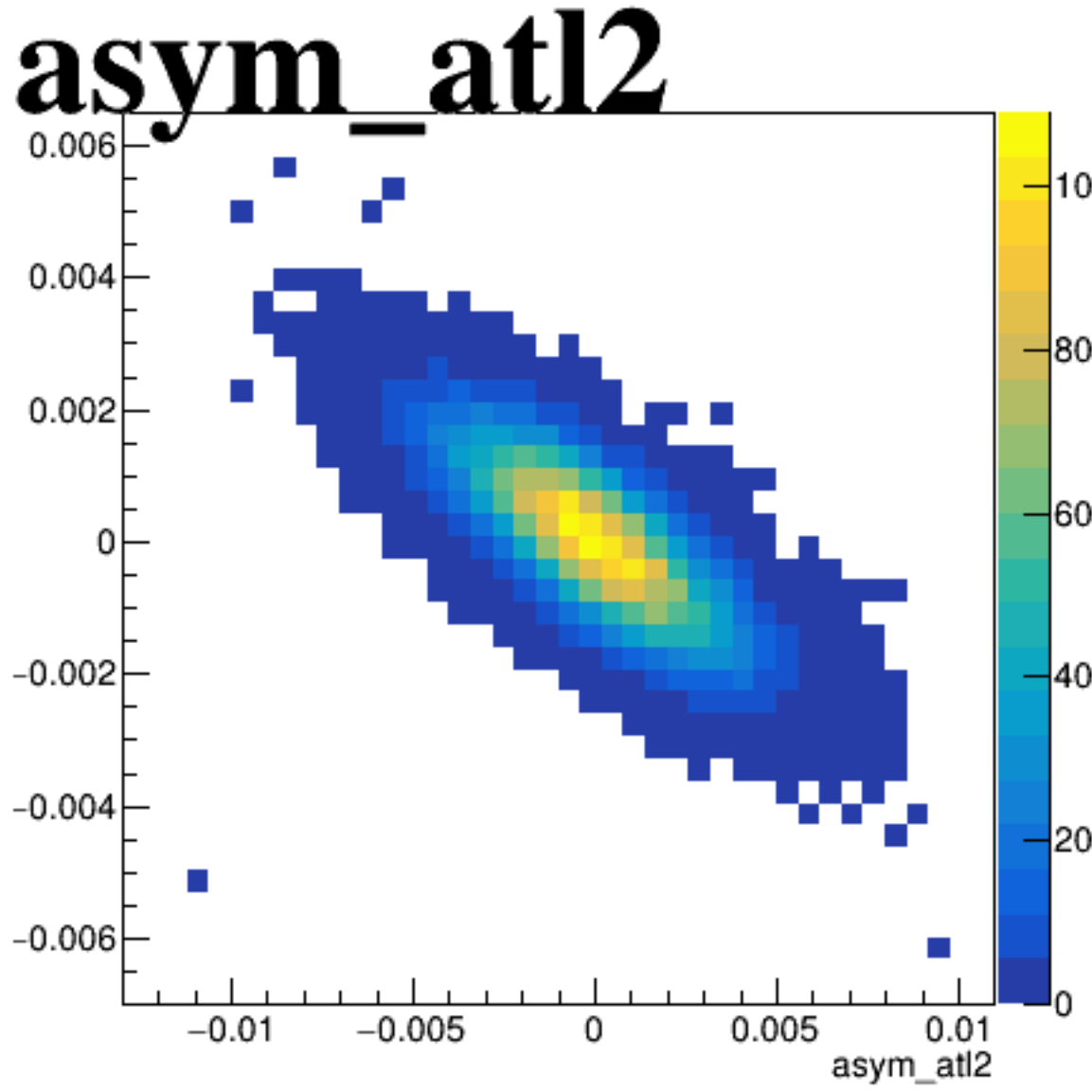
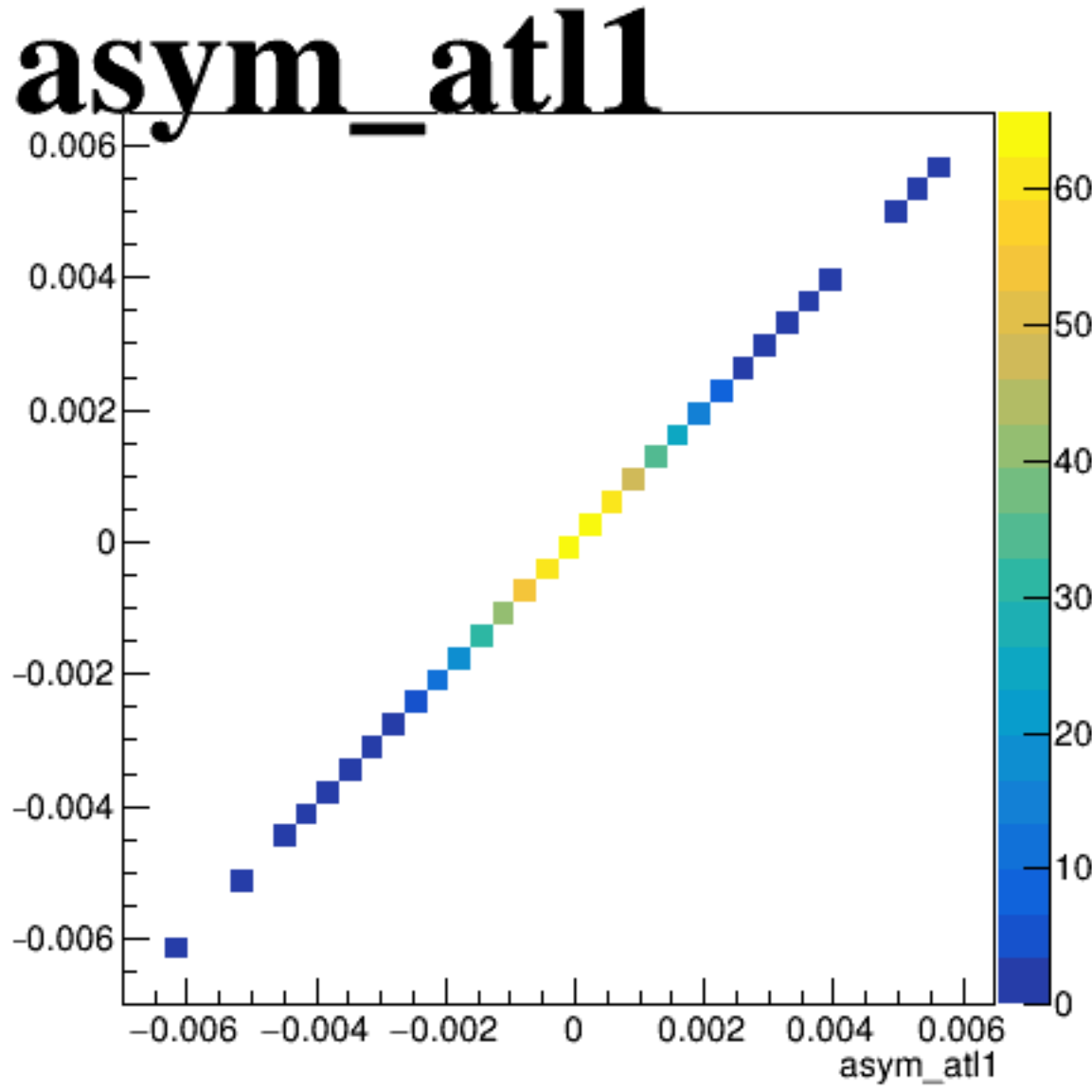
asym_atr2/ppm:pattern_number {ErrorFlag==0}



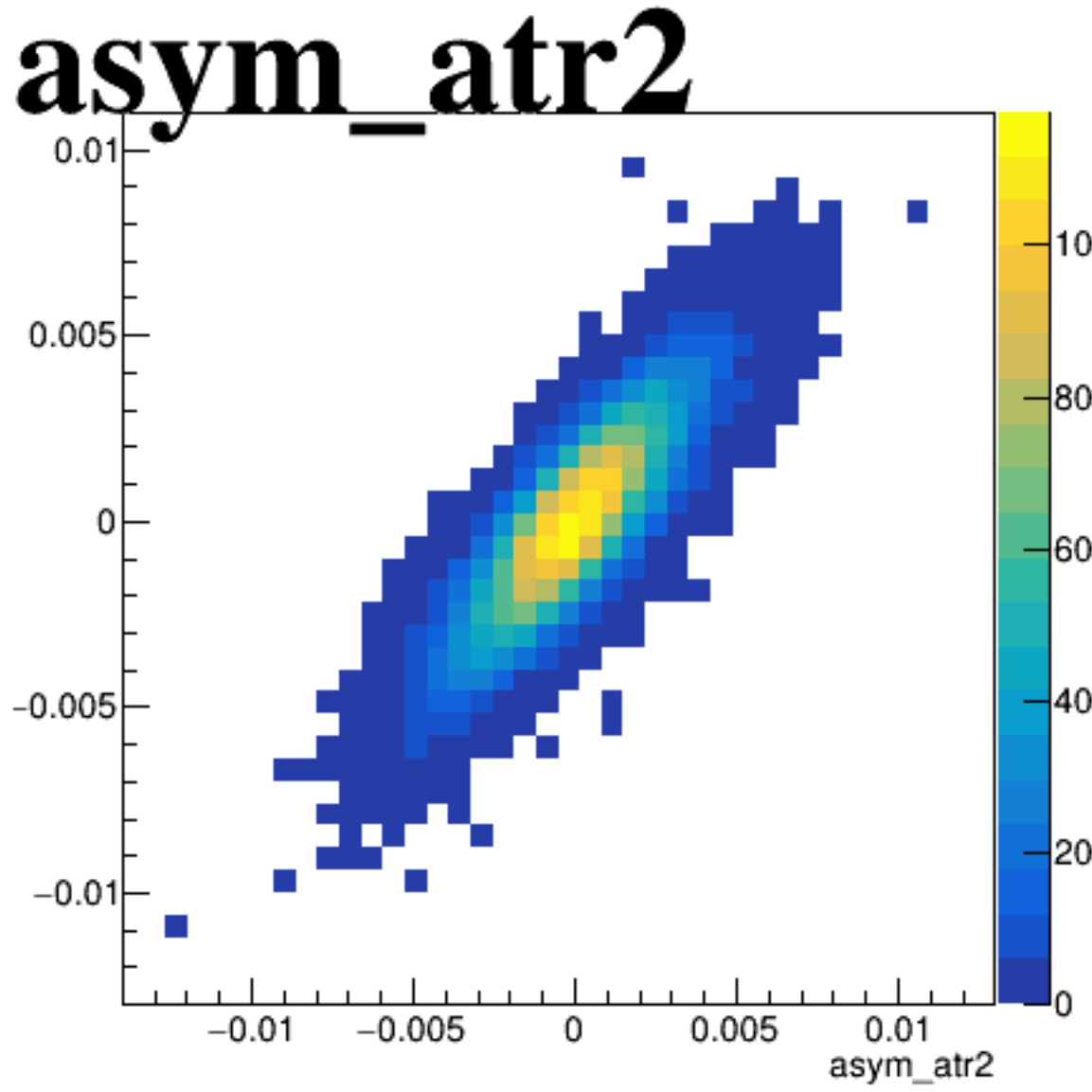
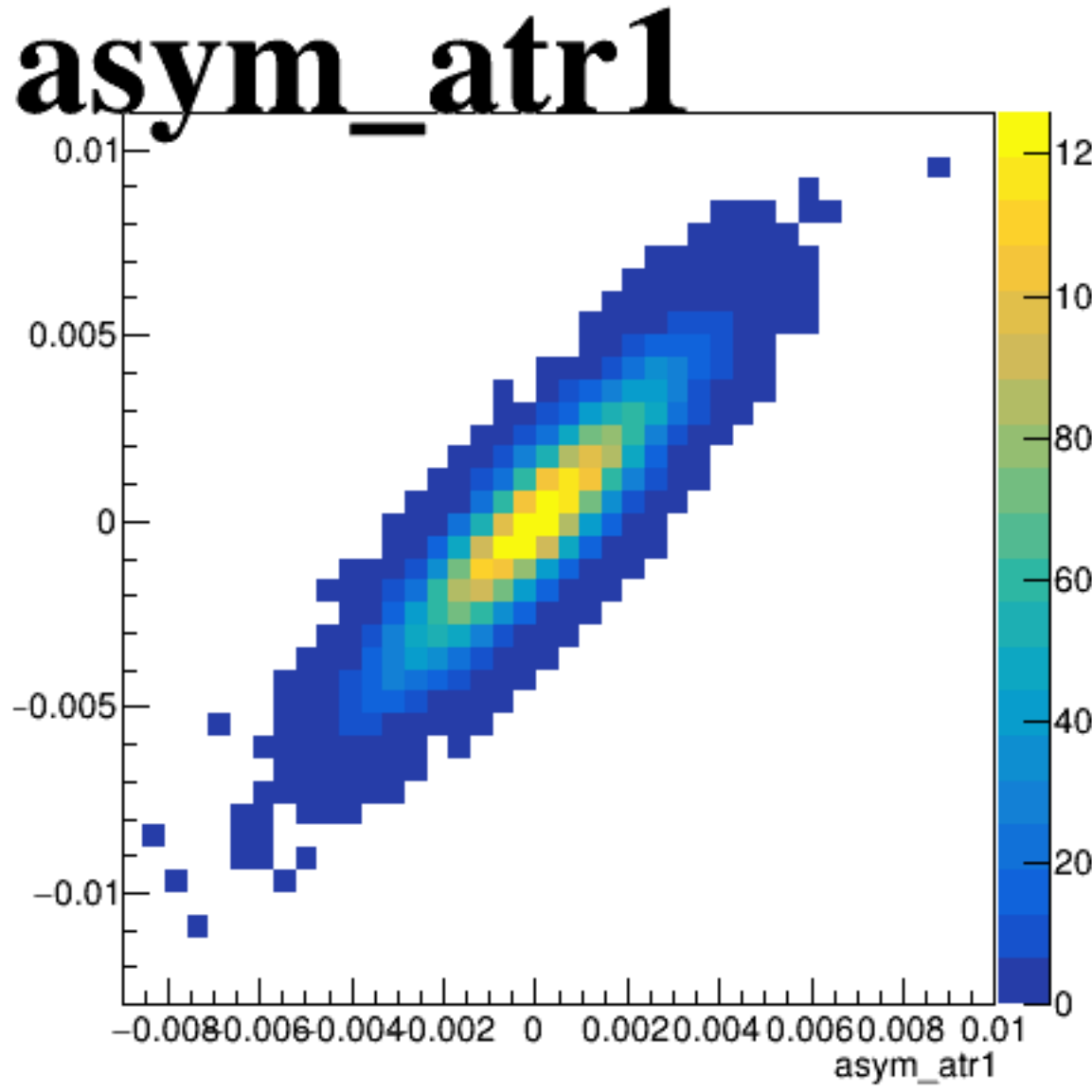
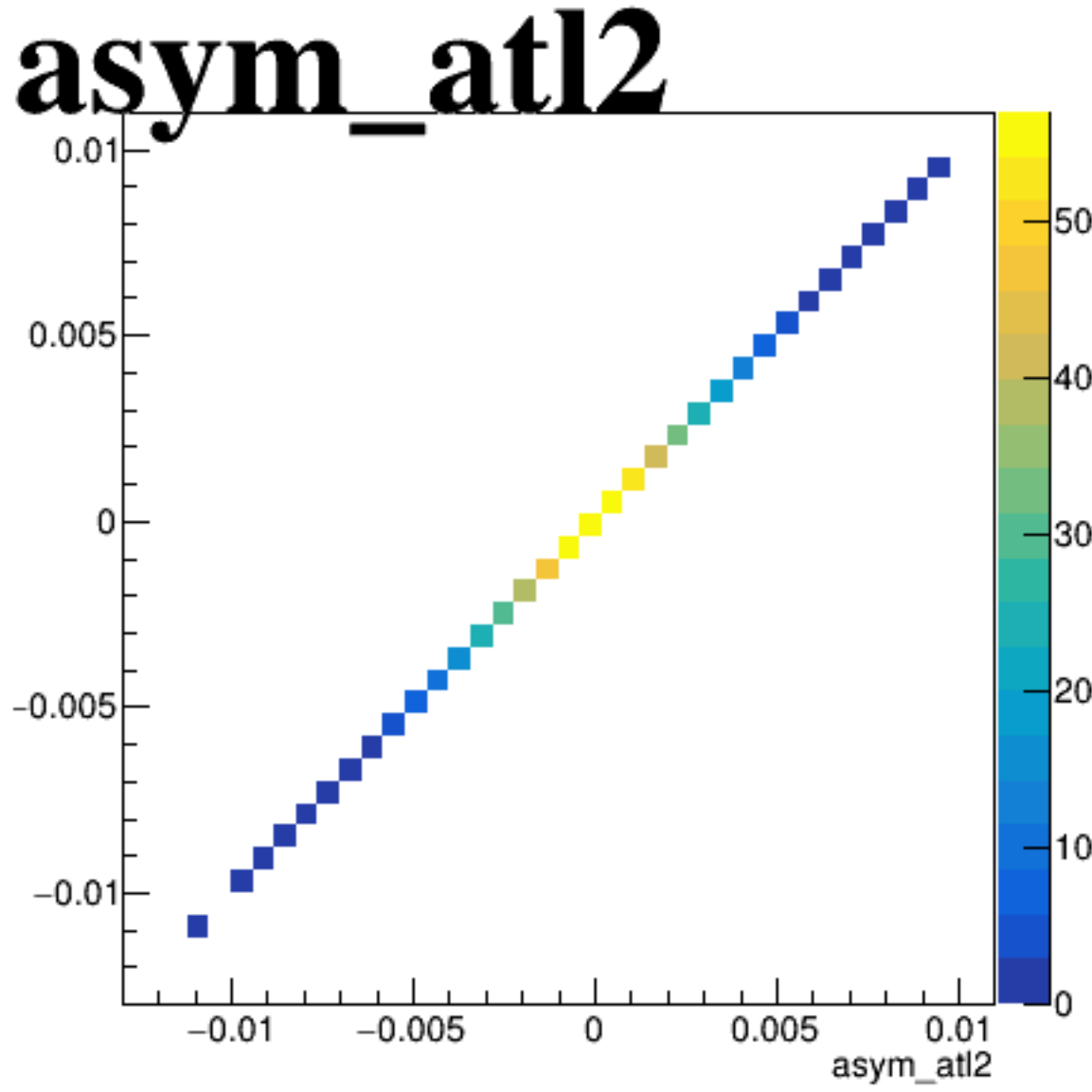
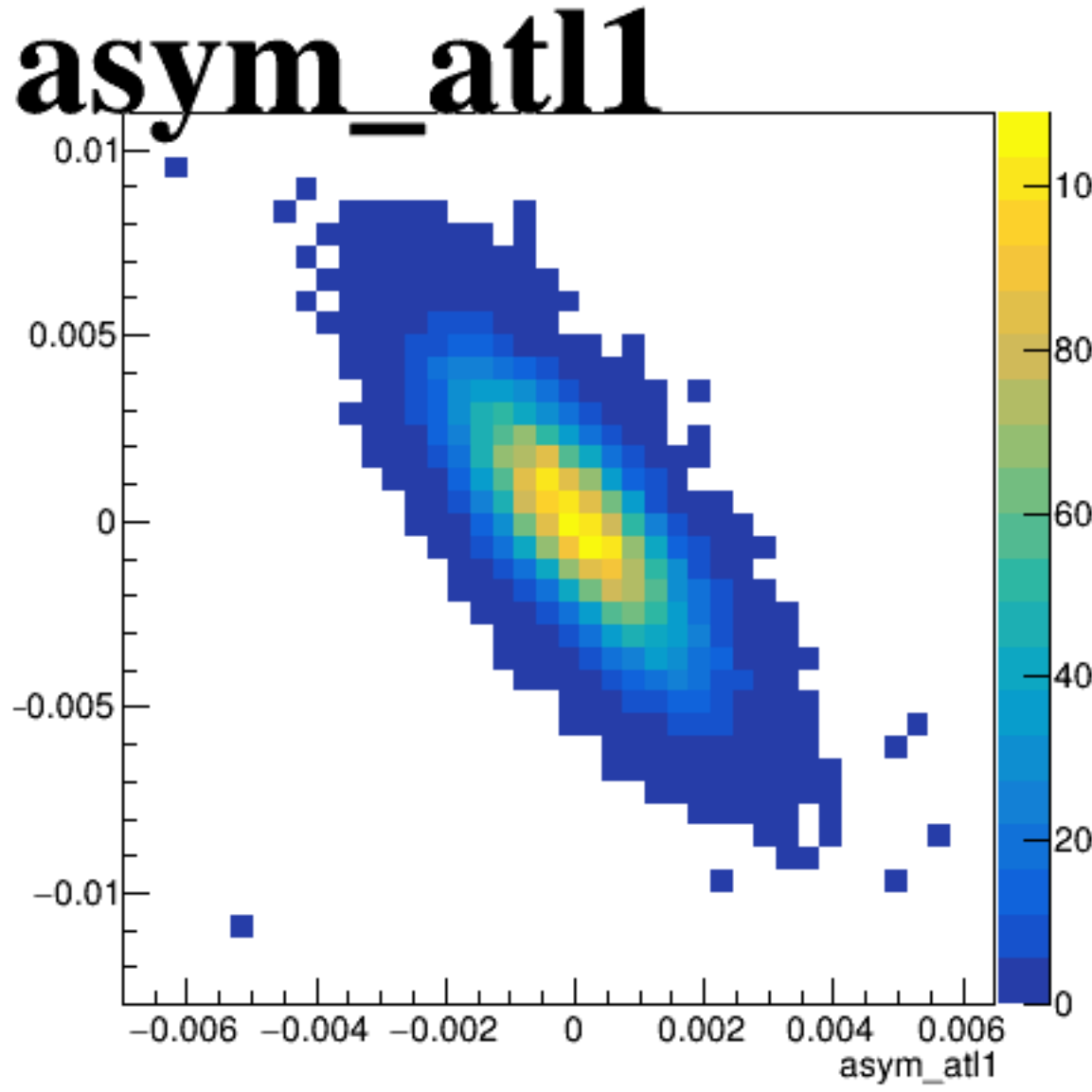
asym_atr2



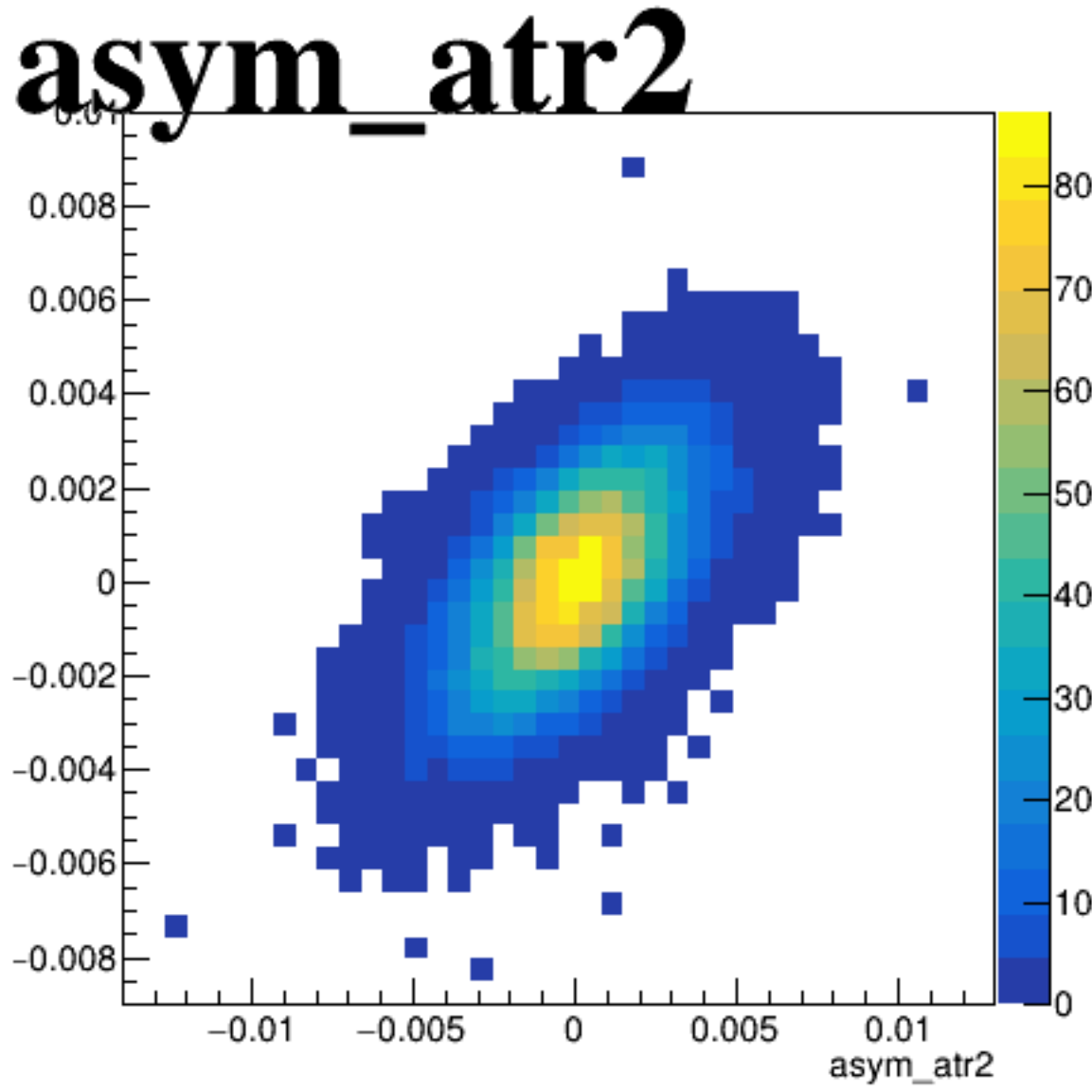
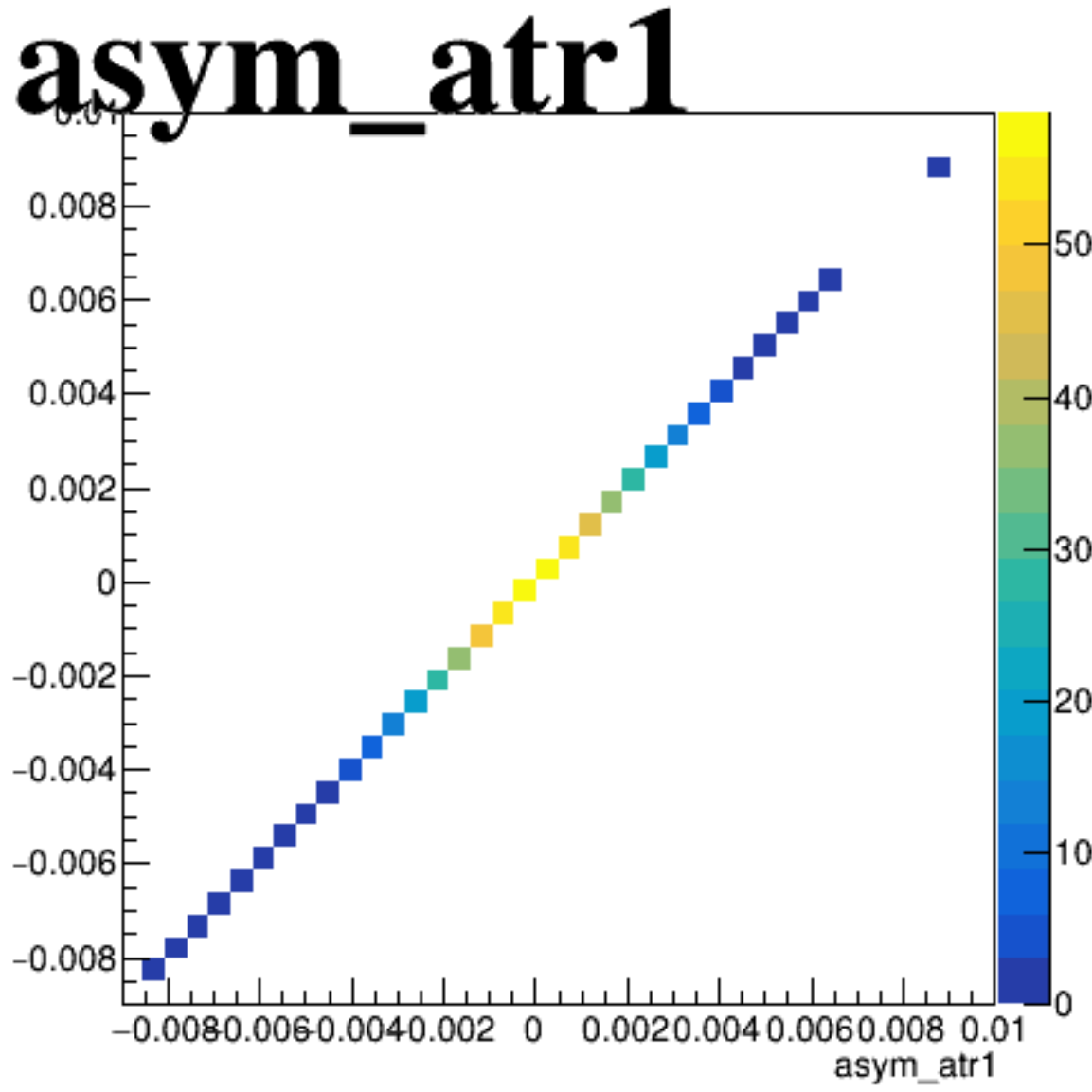
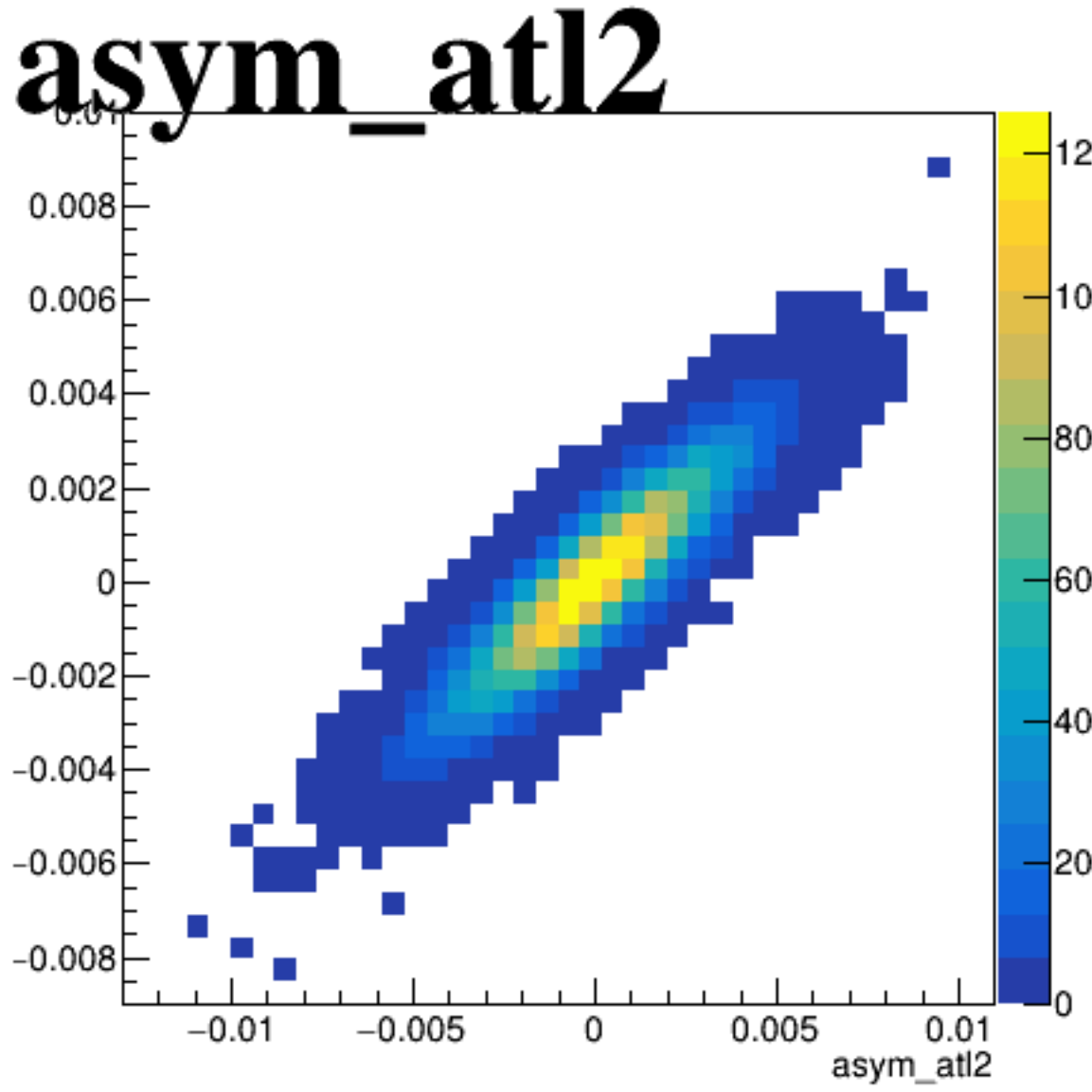
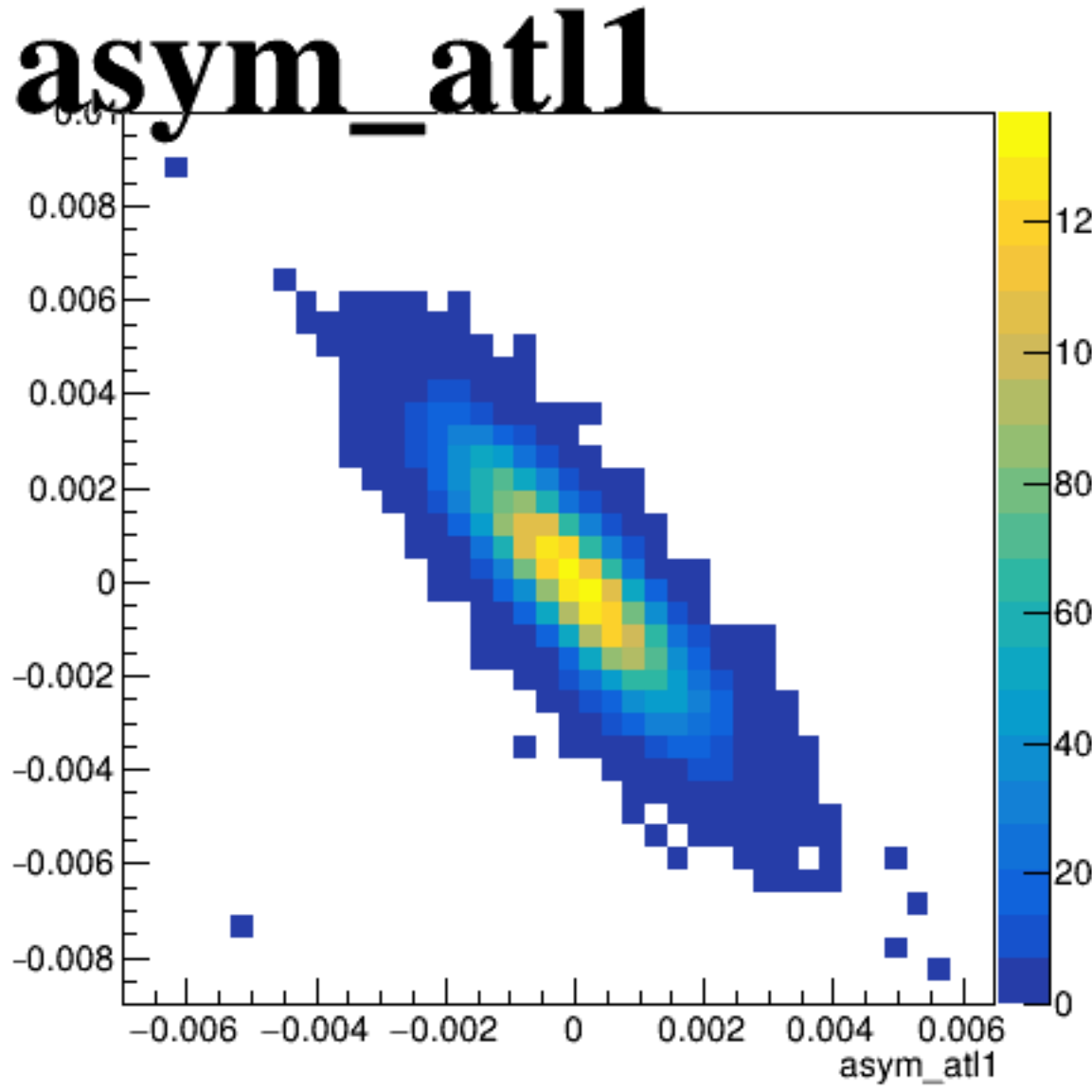
asym_atl1



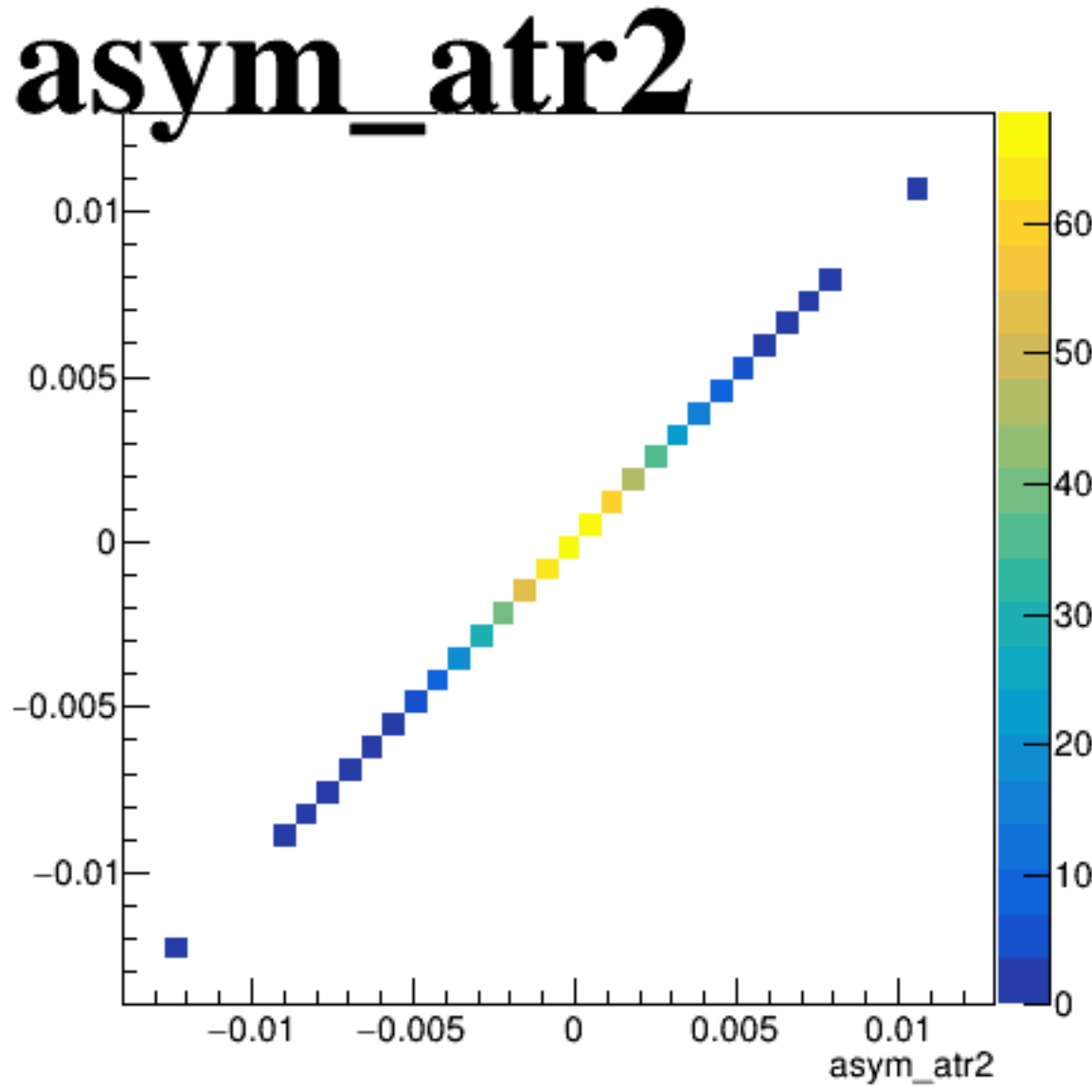
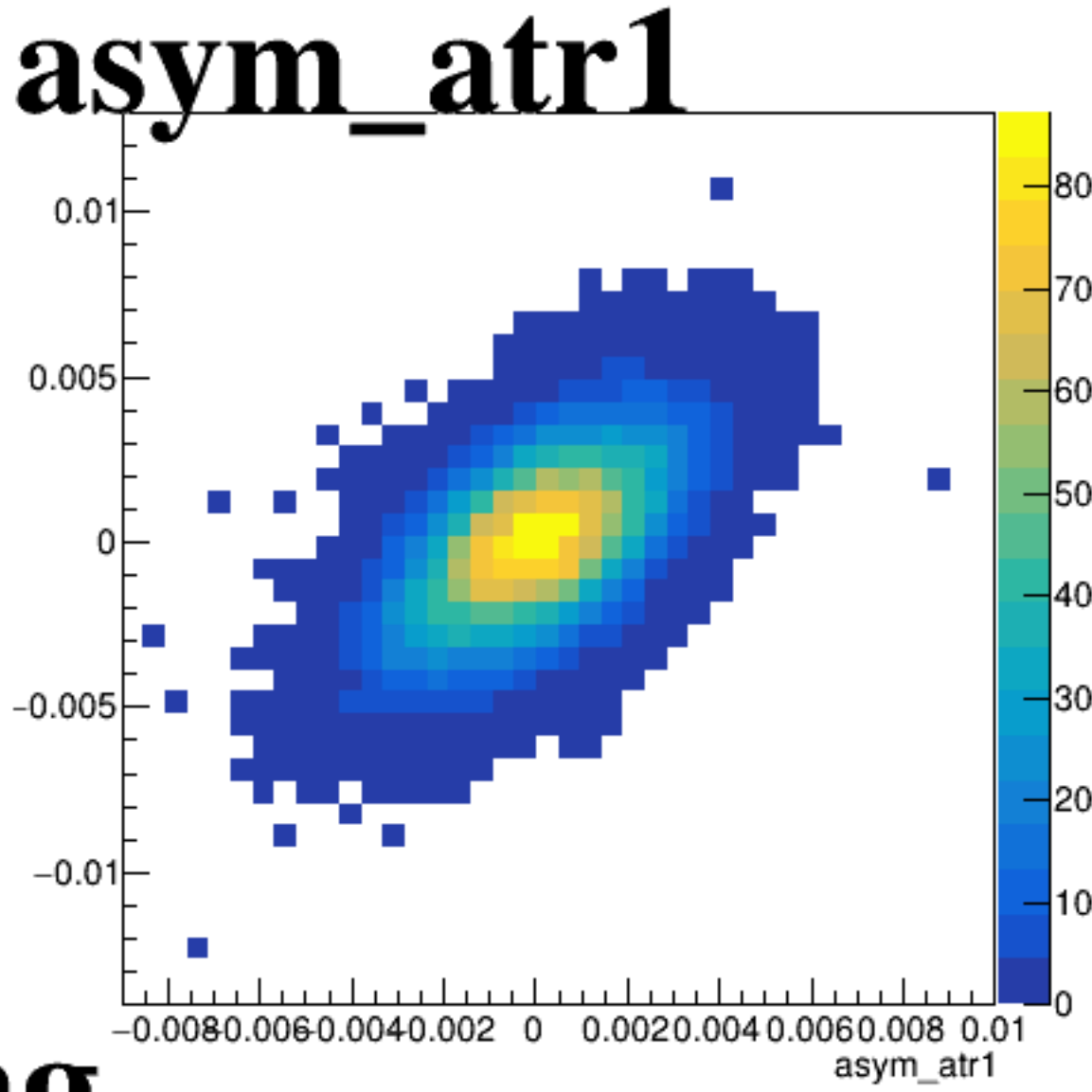
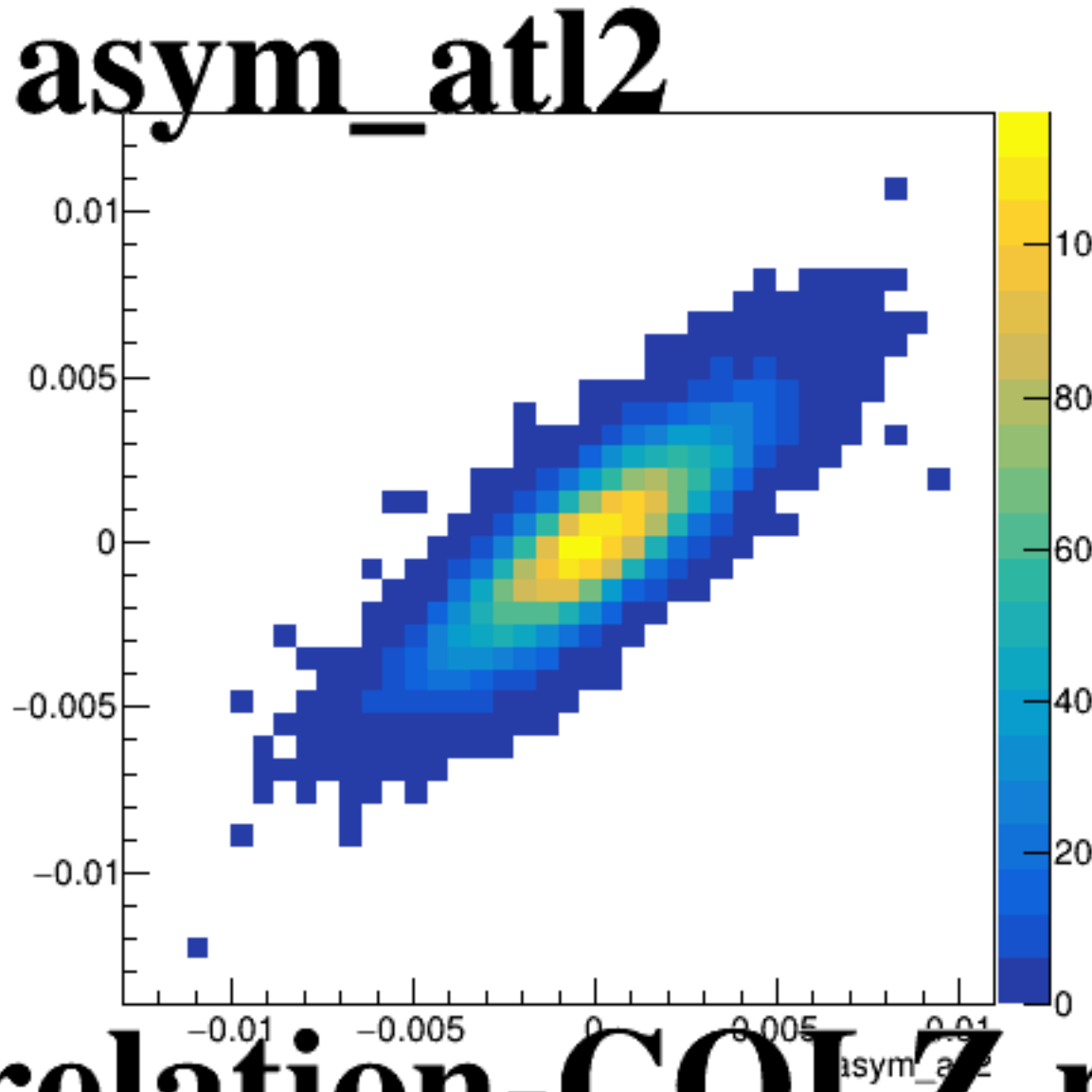
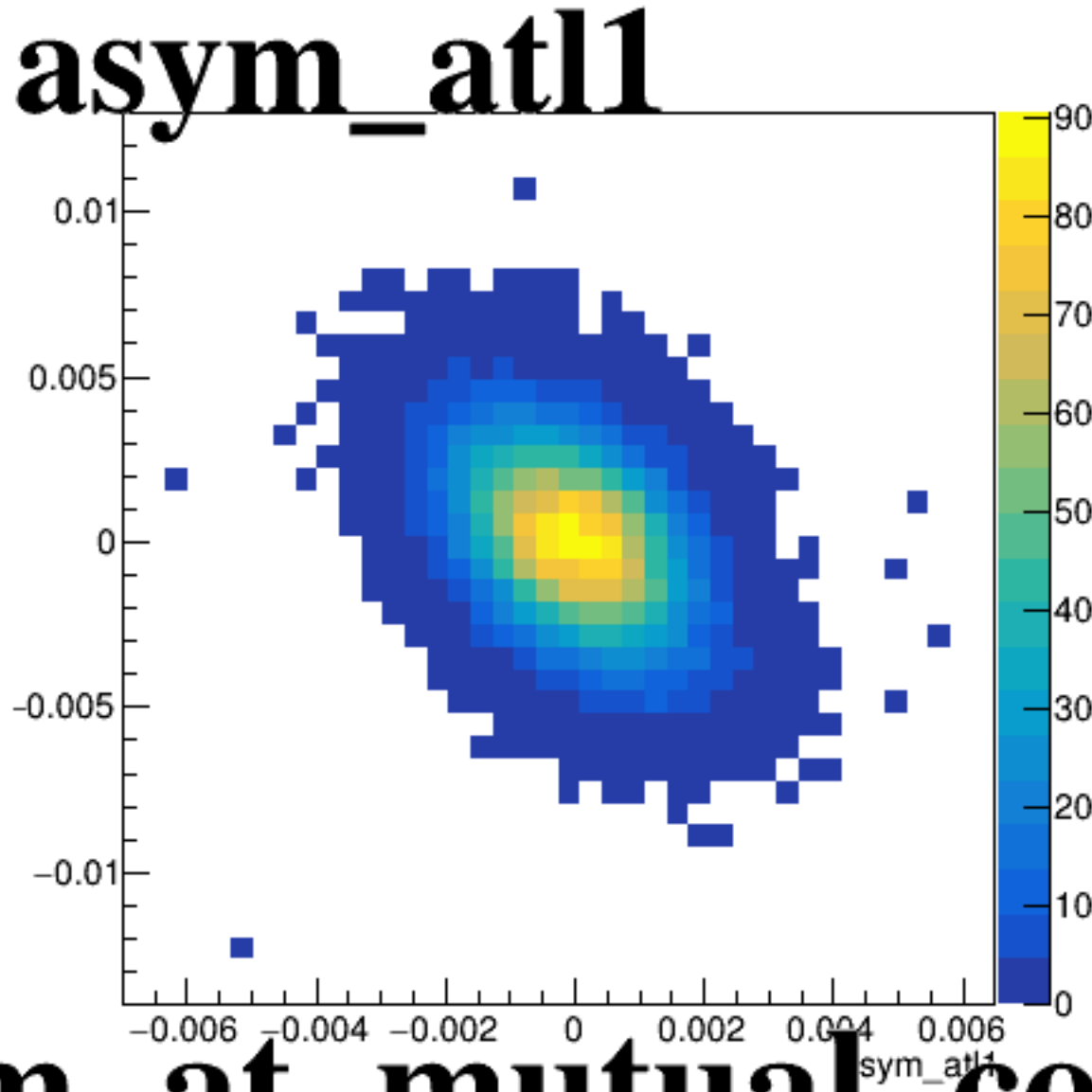
asym_atl2



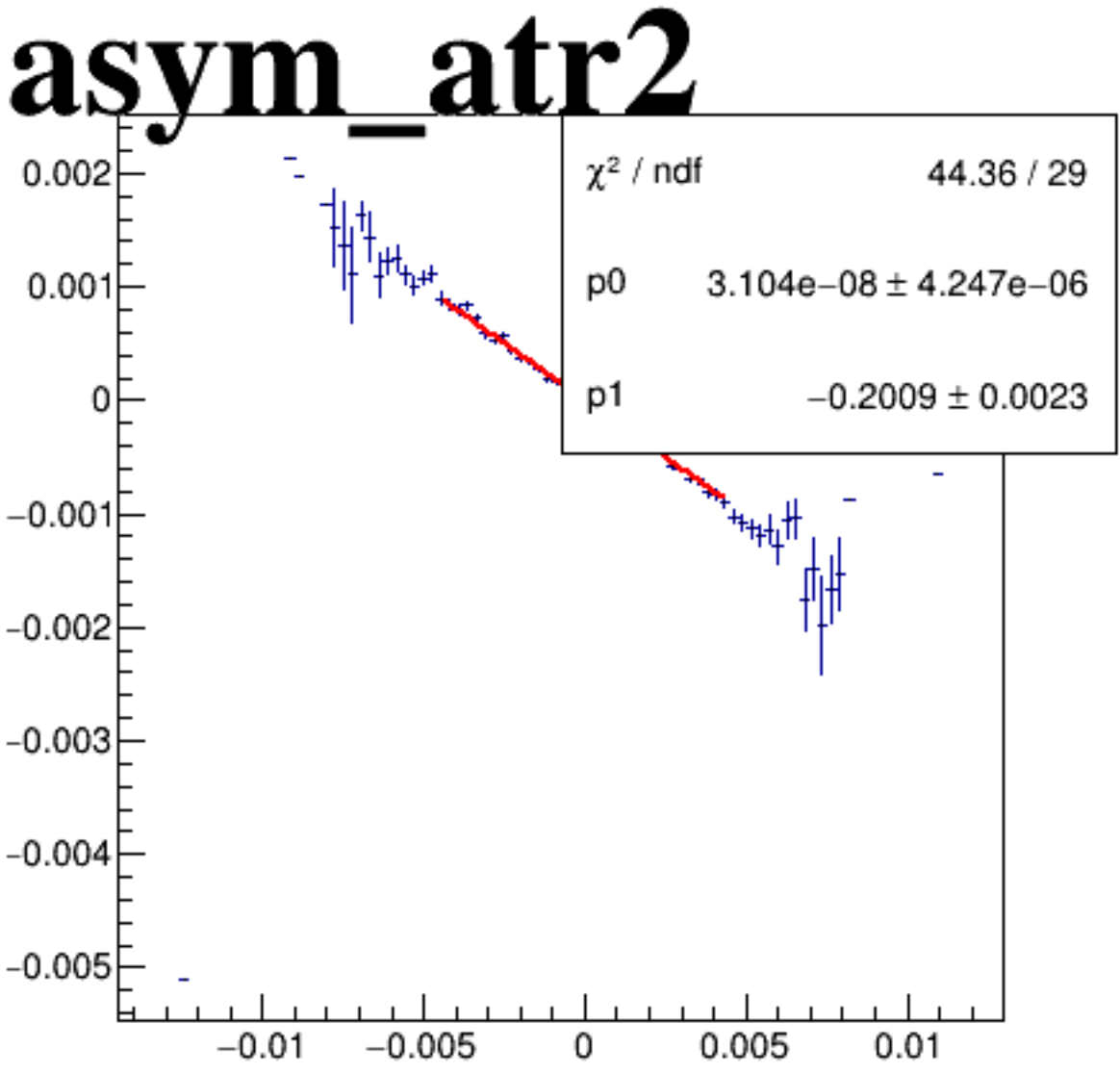
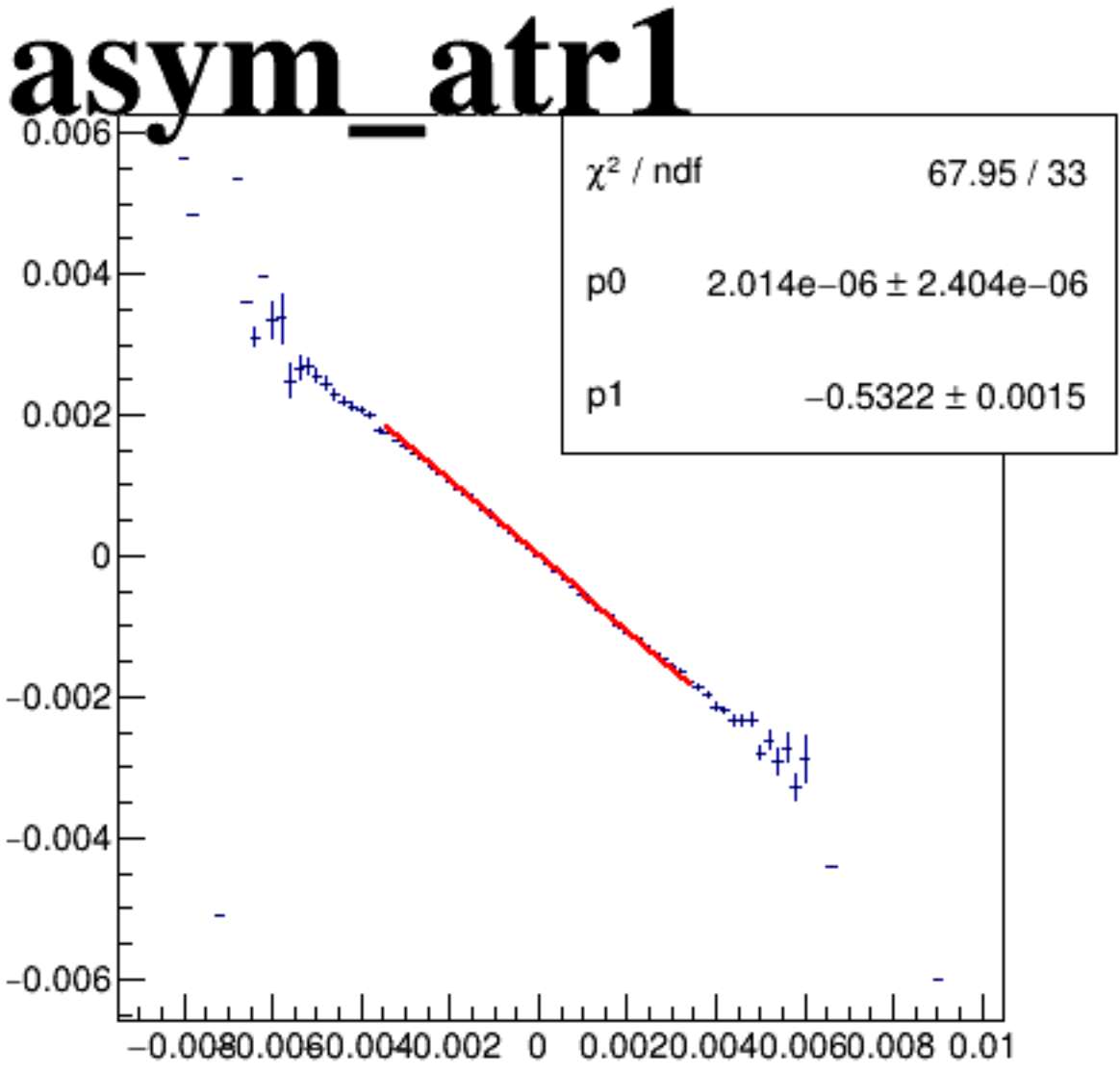
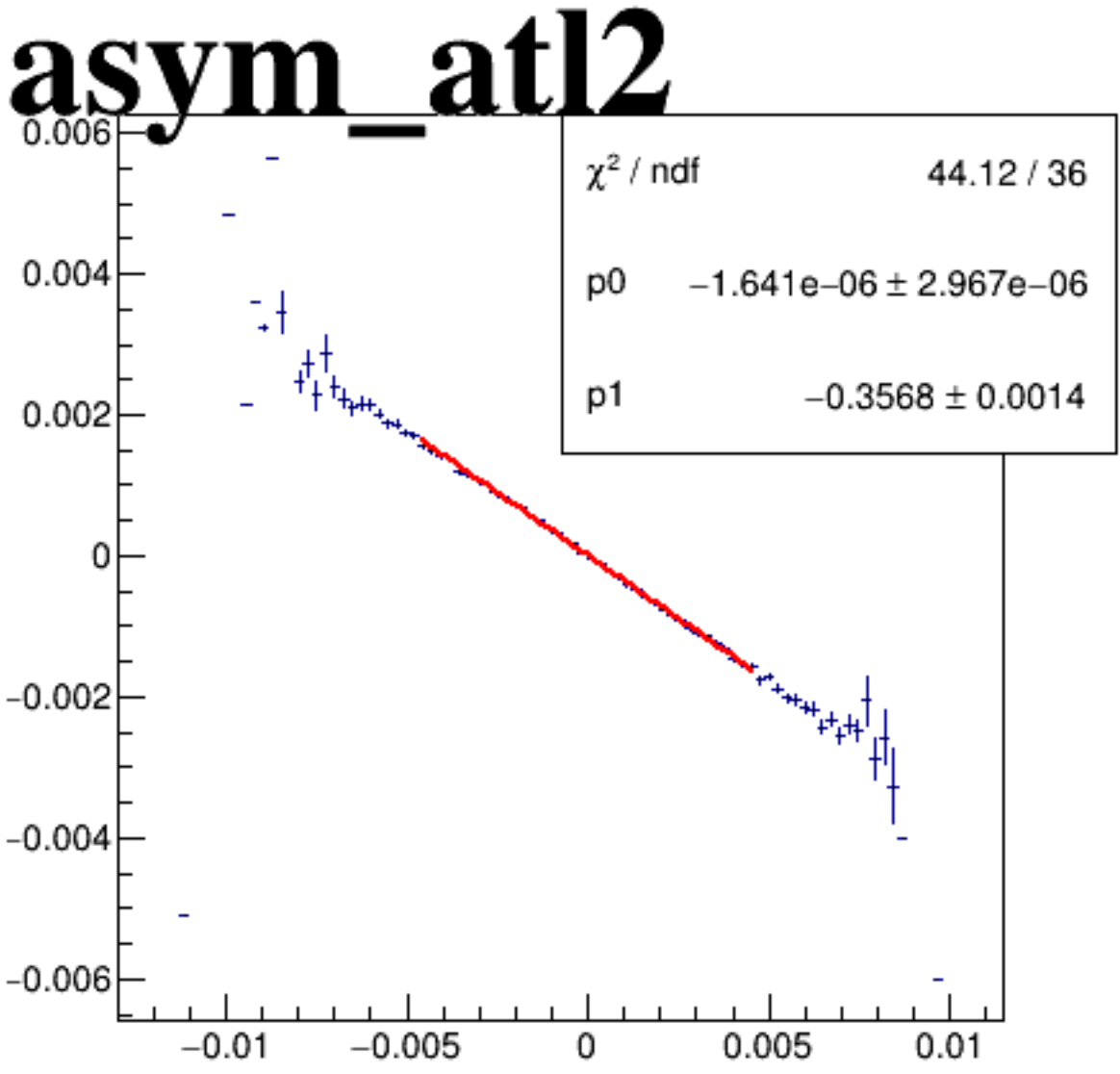
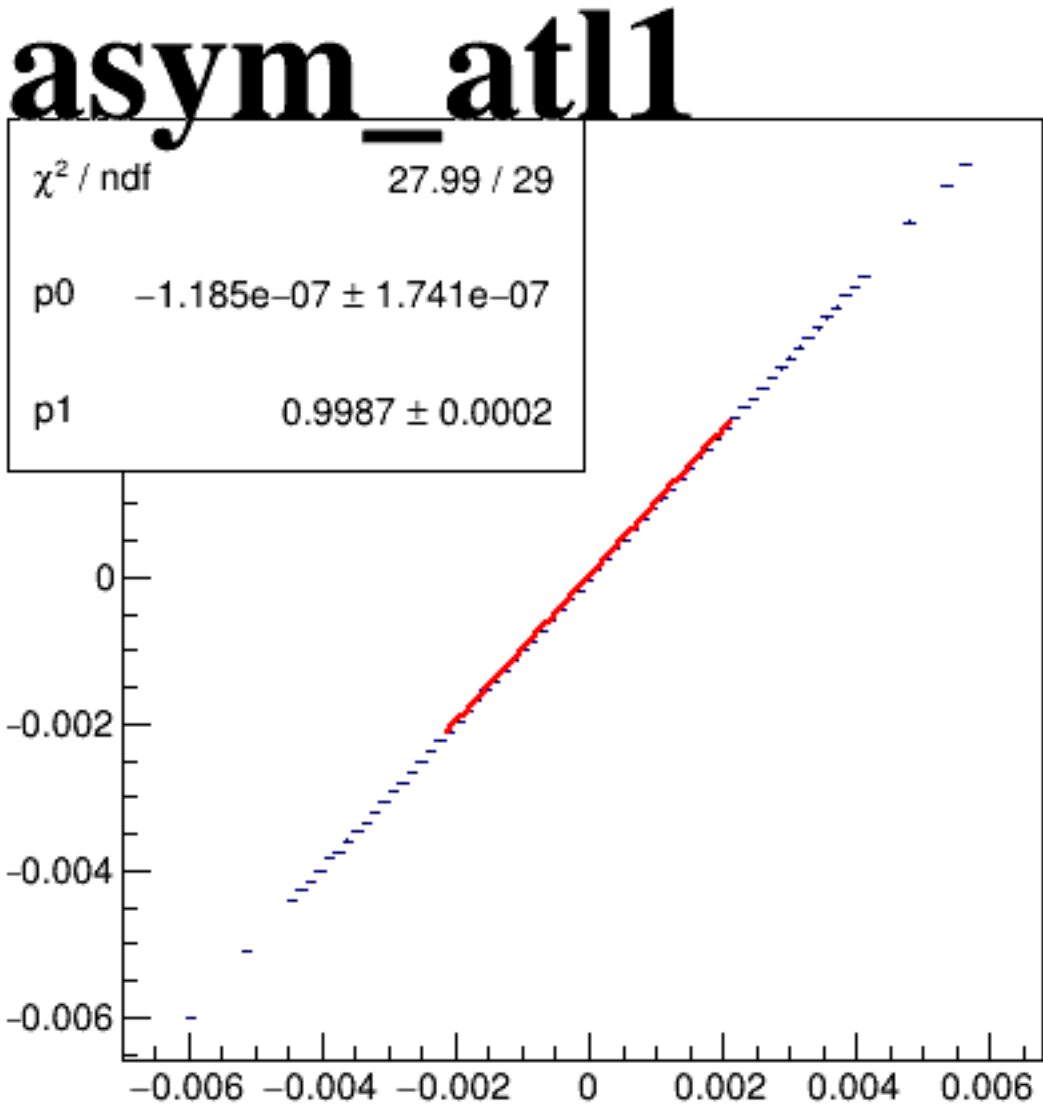
asym_atr1



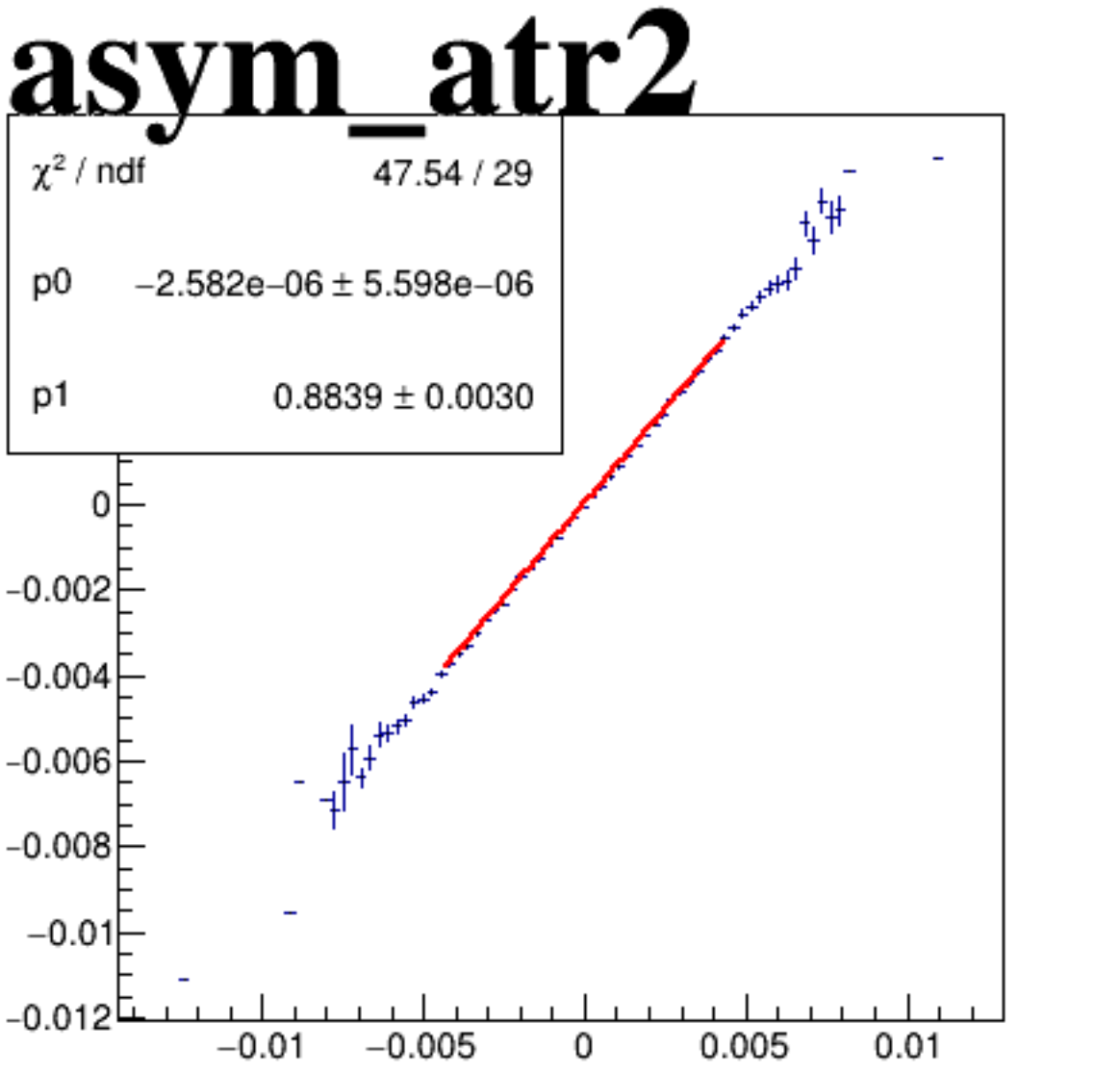
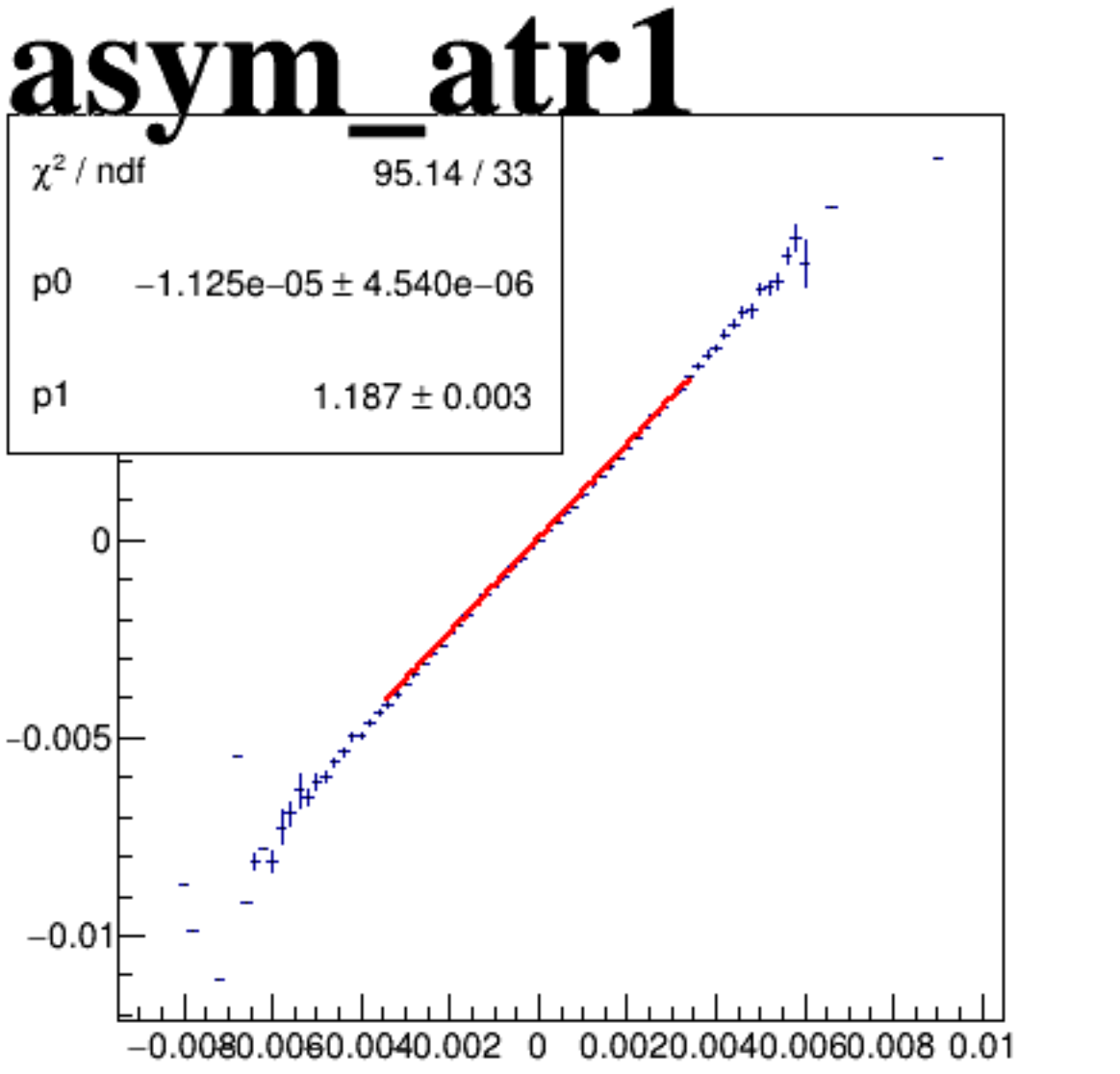
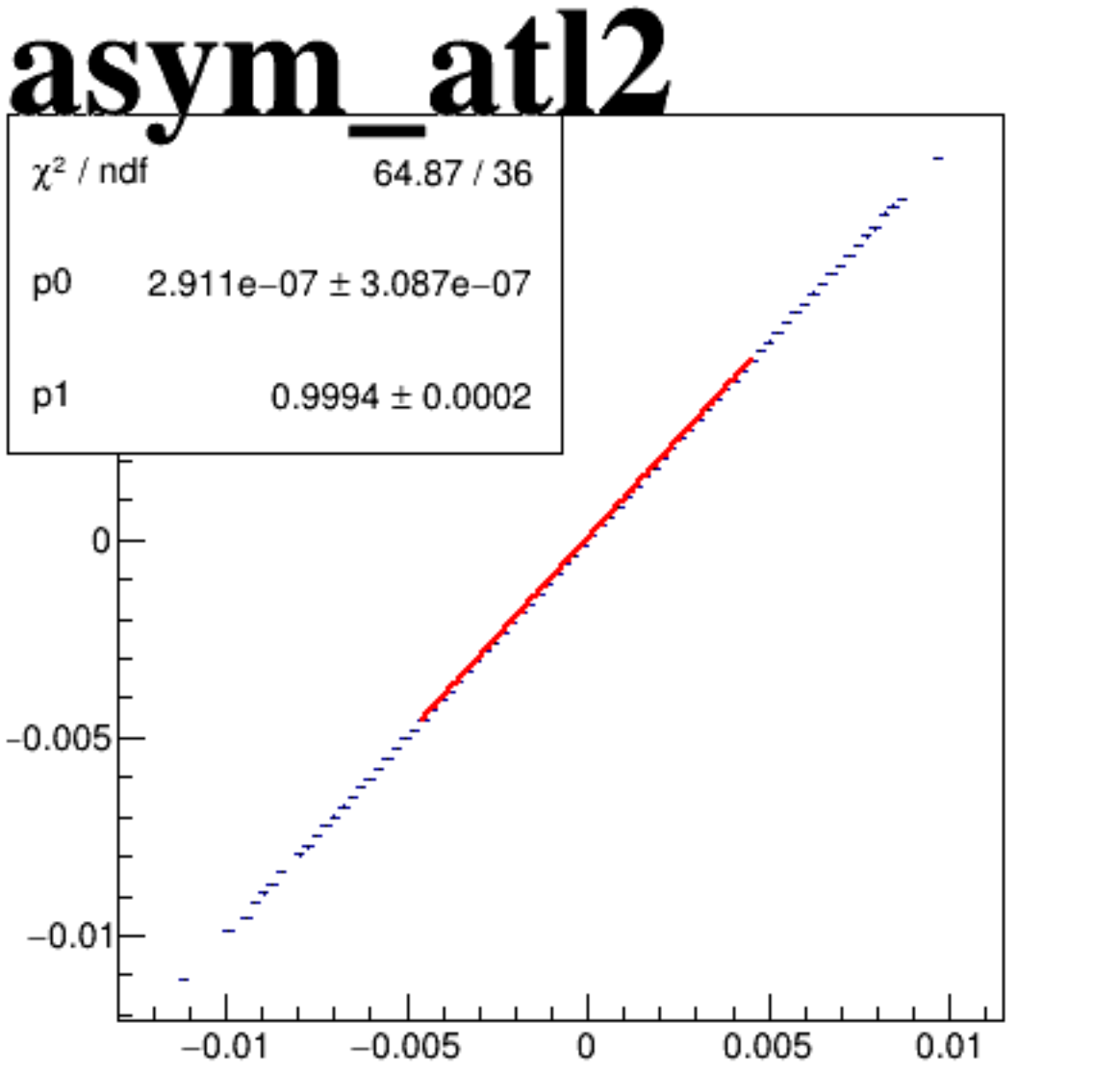
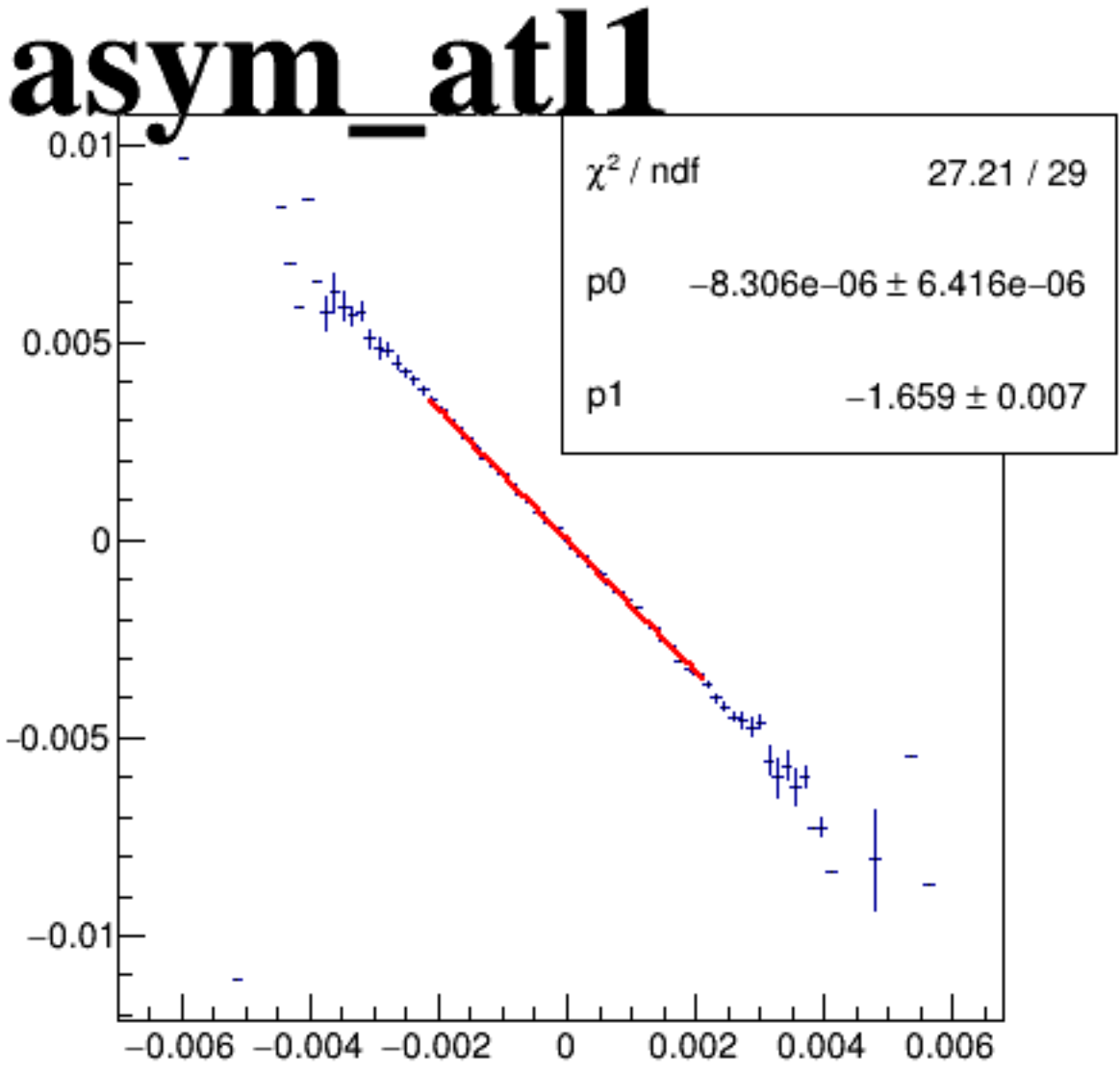
asym_atr2



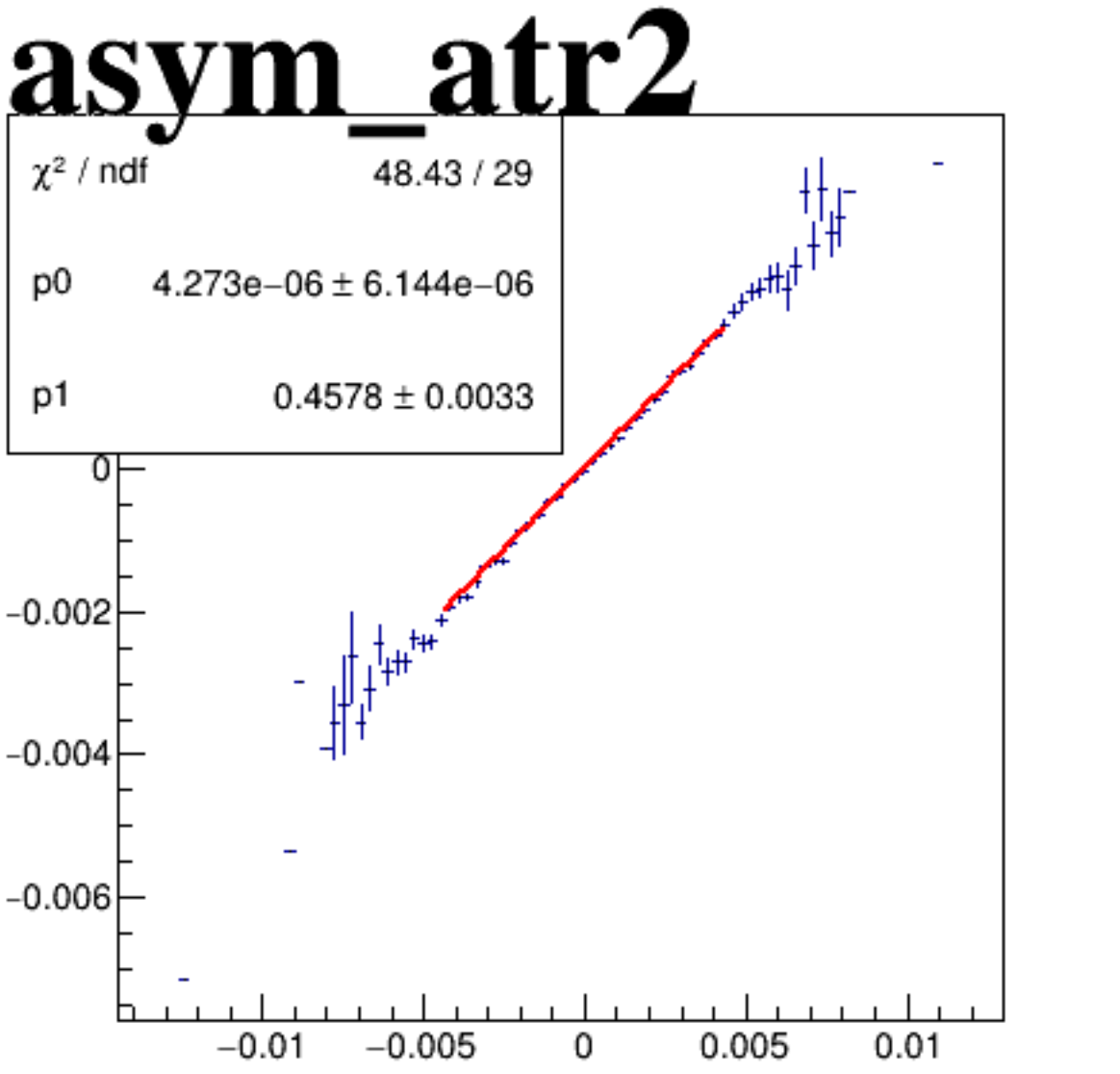
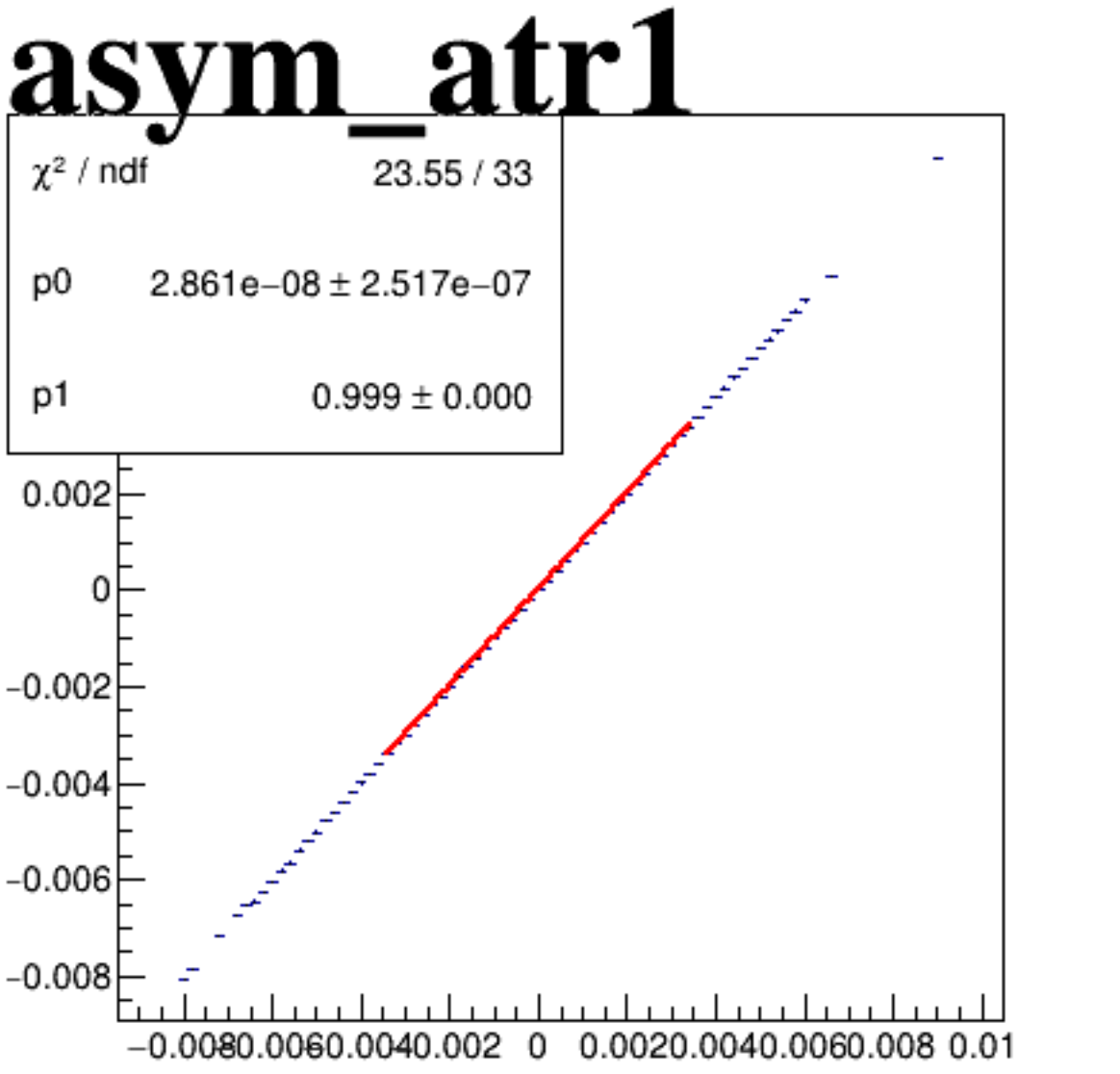
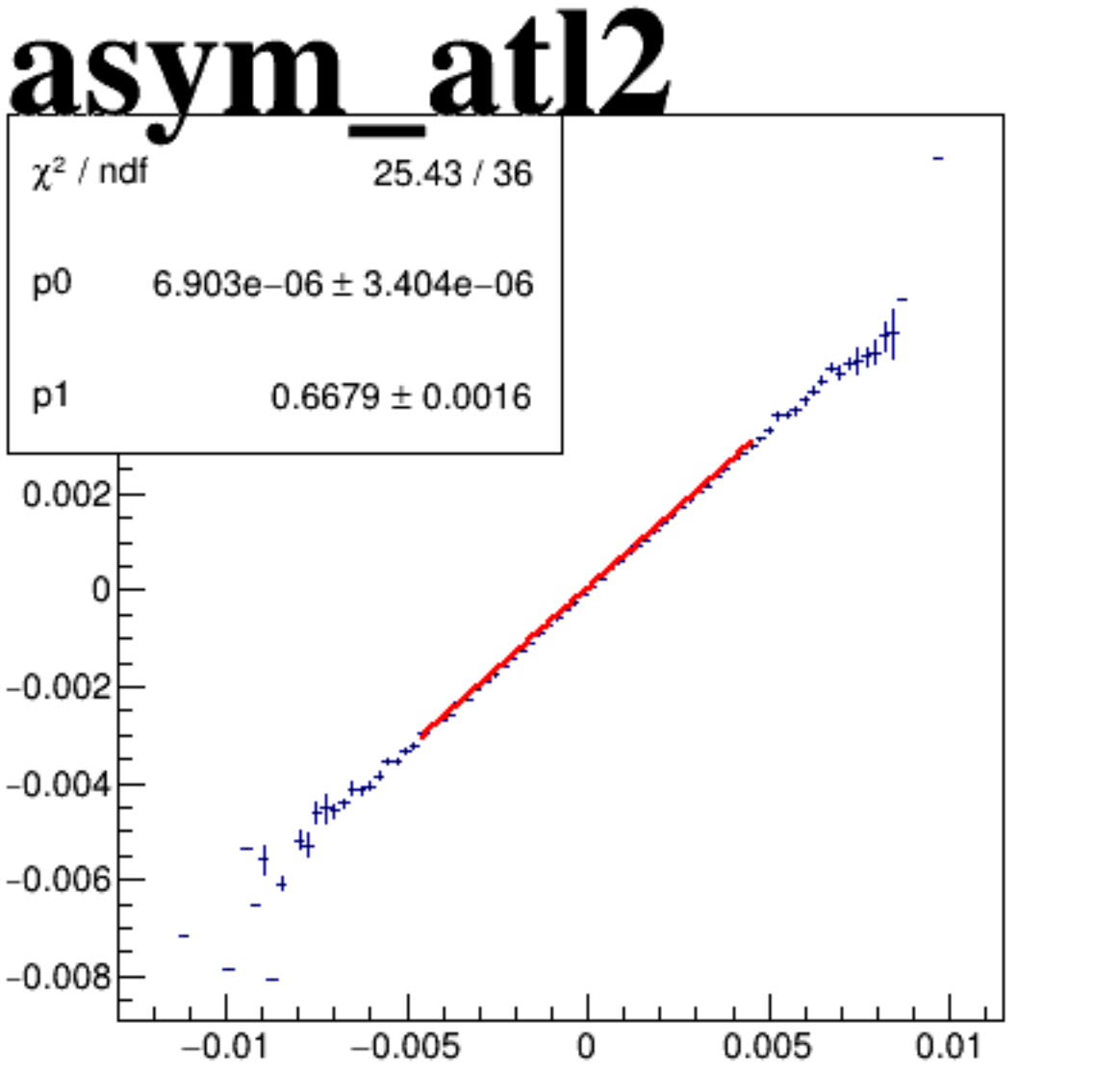
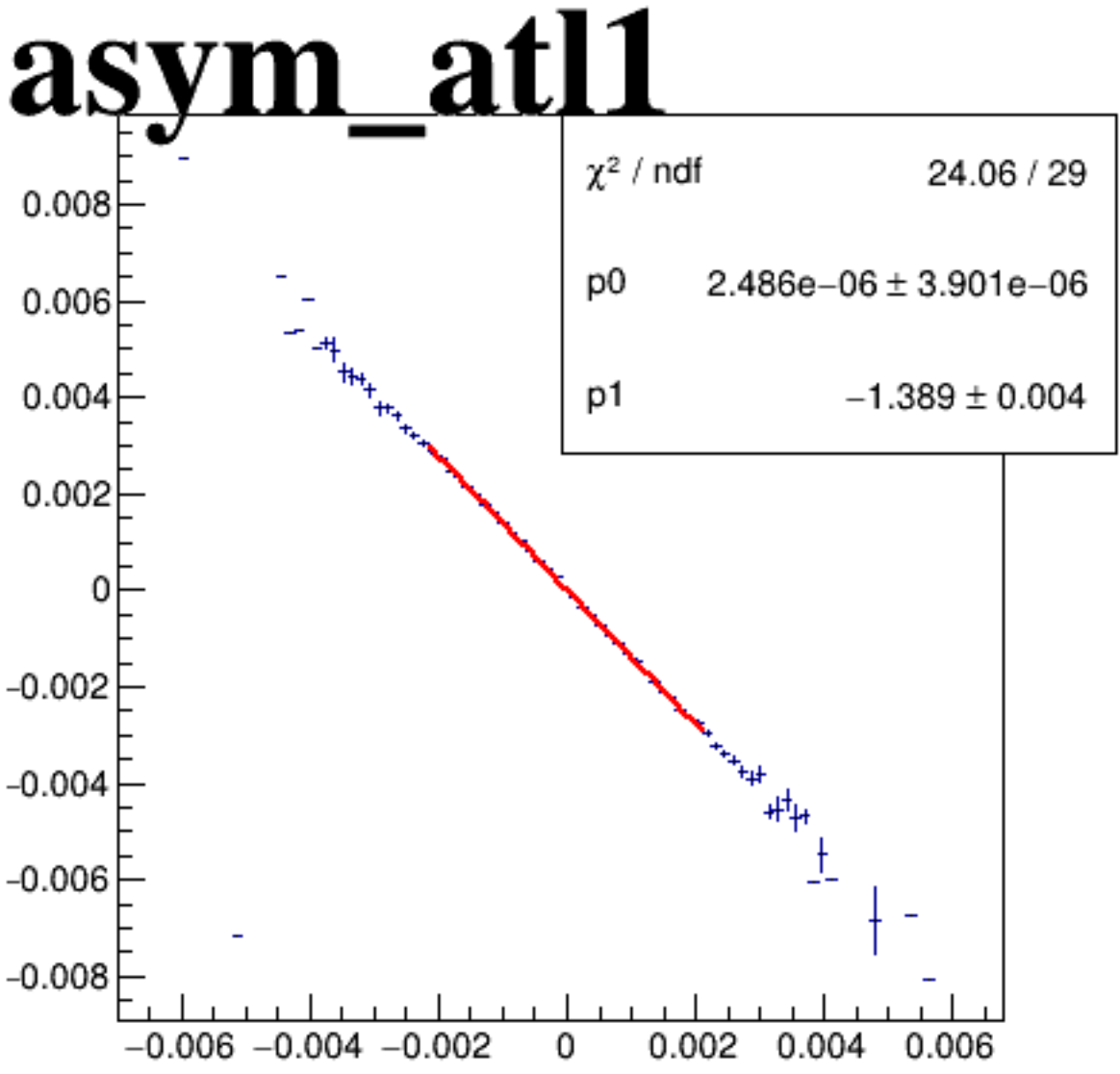
asym_atl1



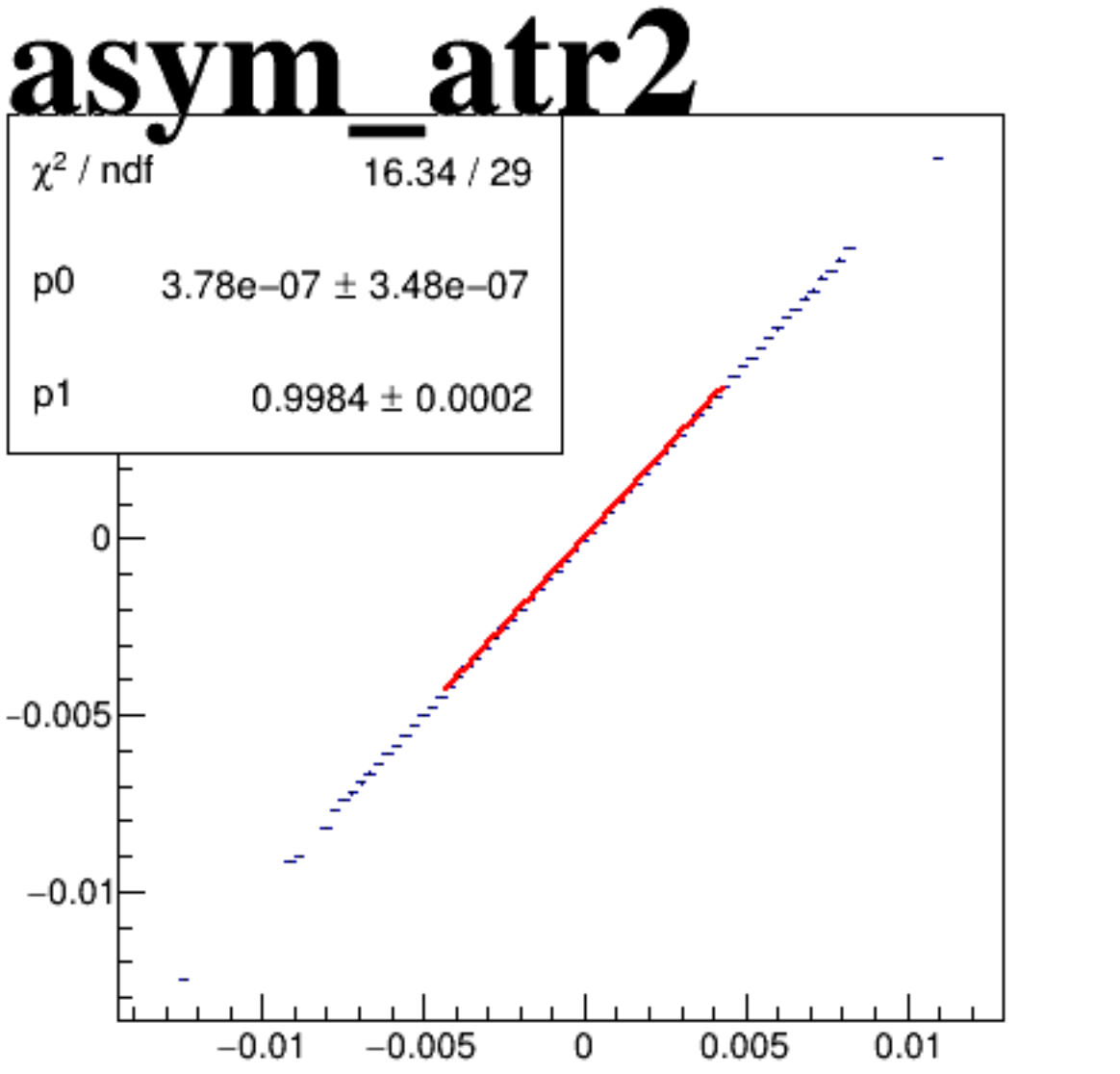
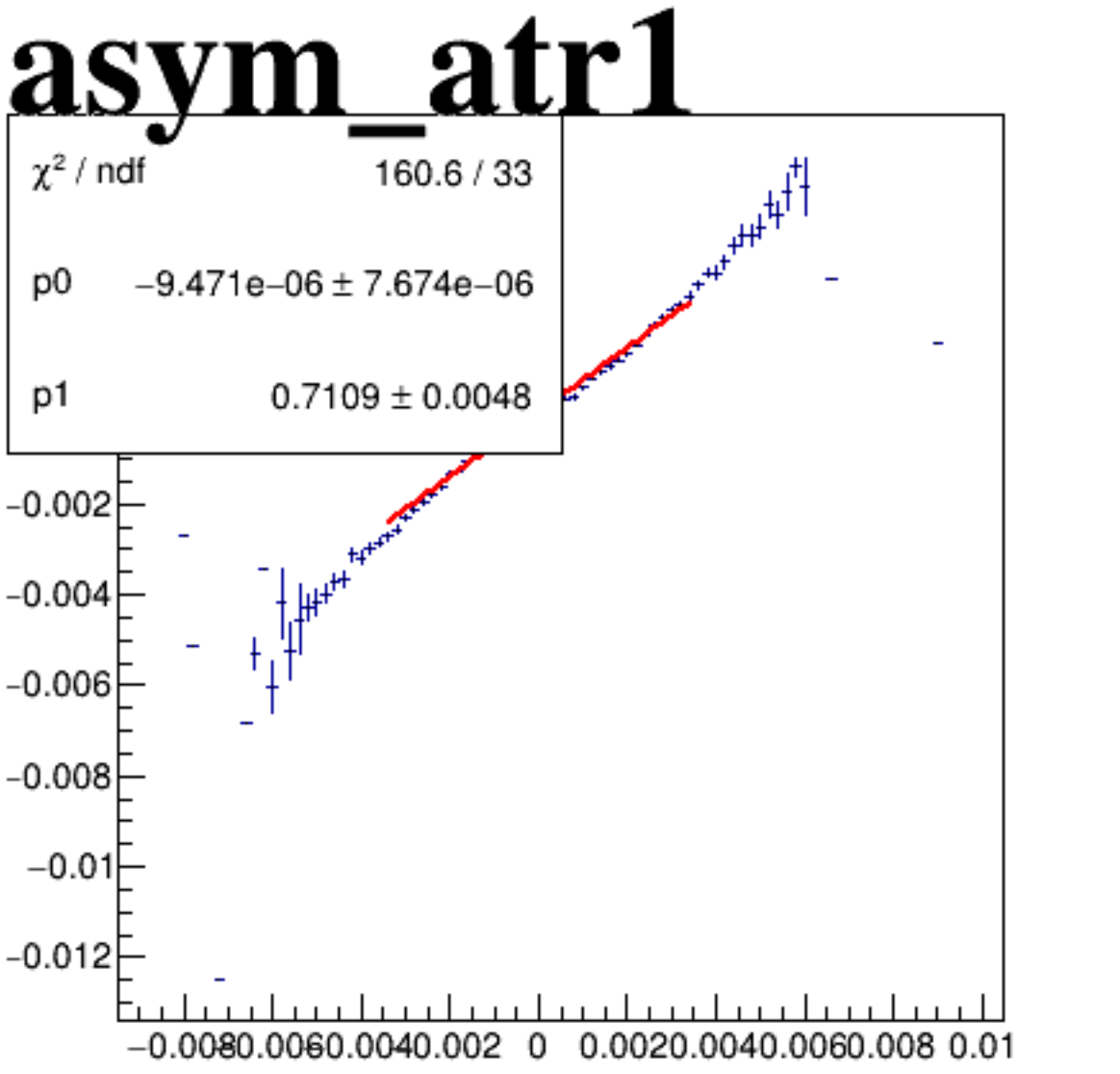
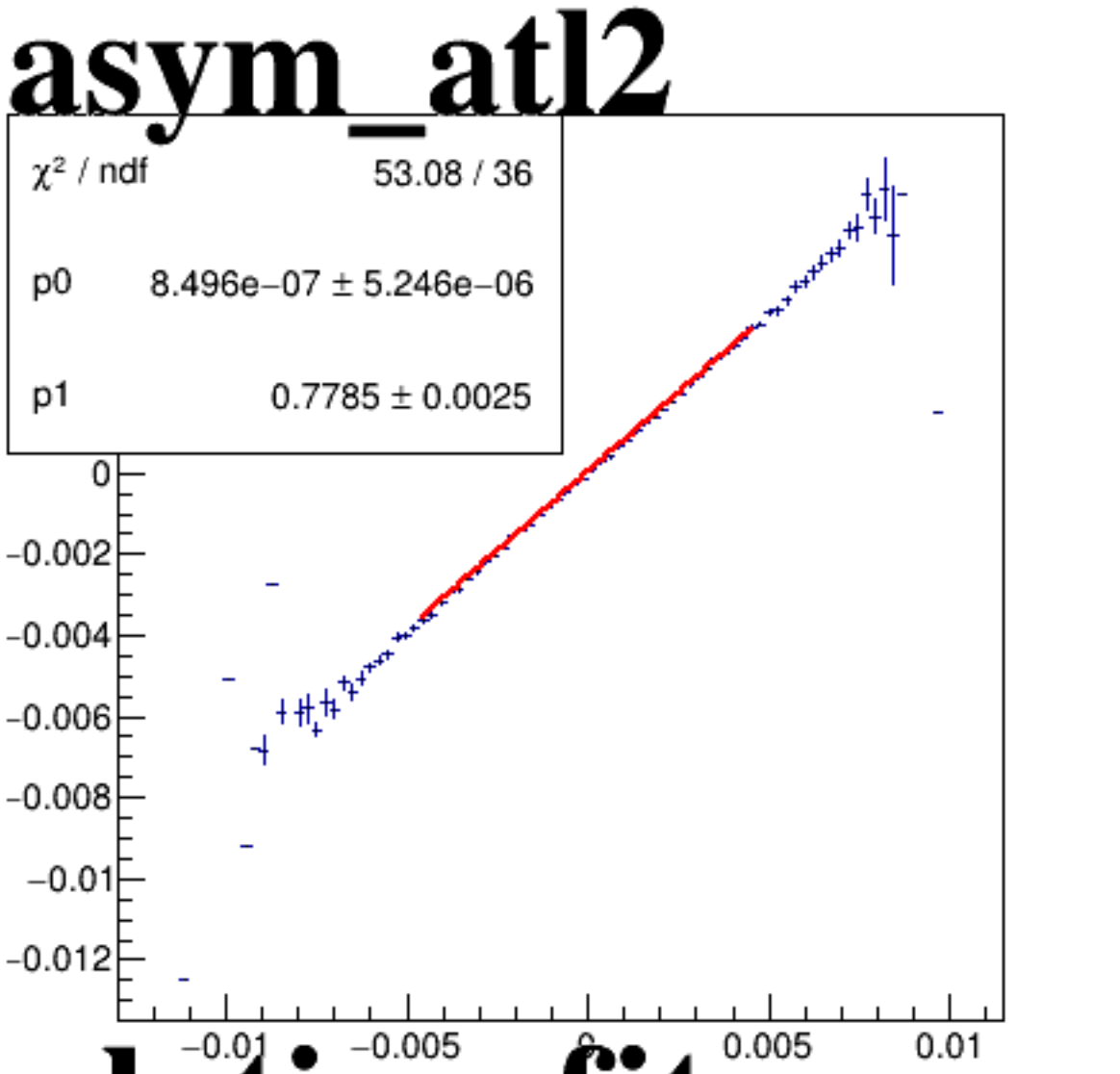
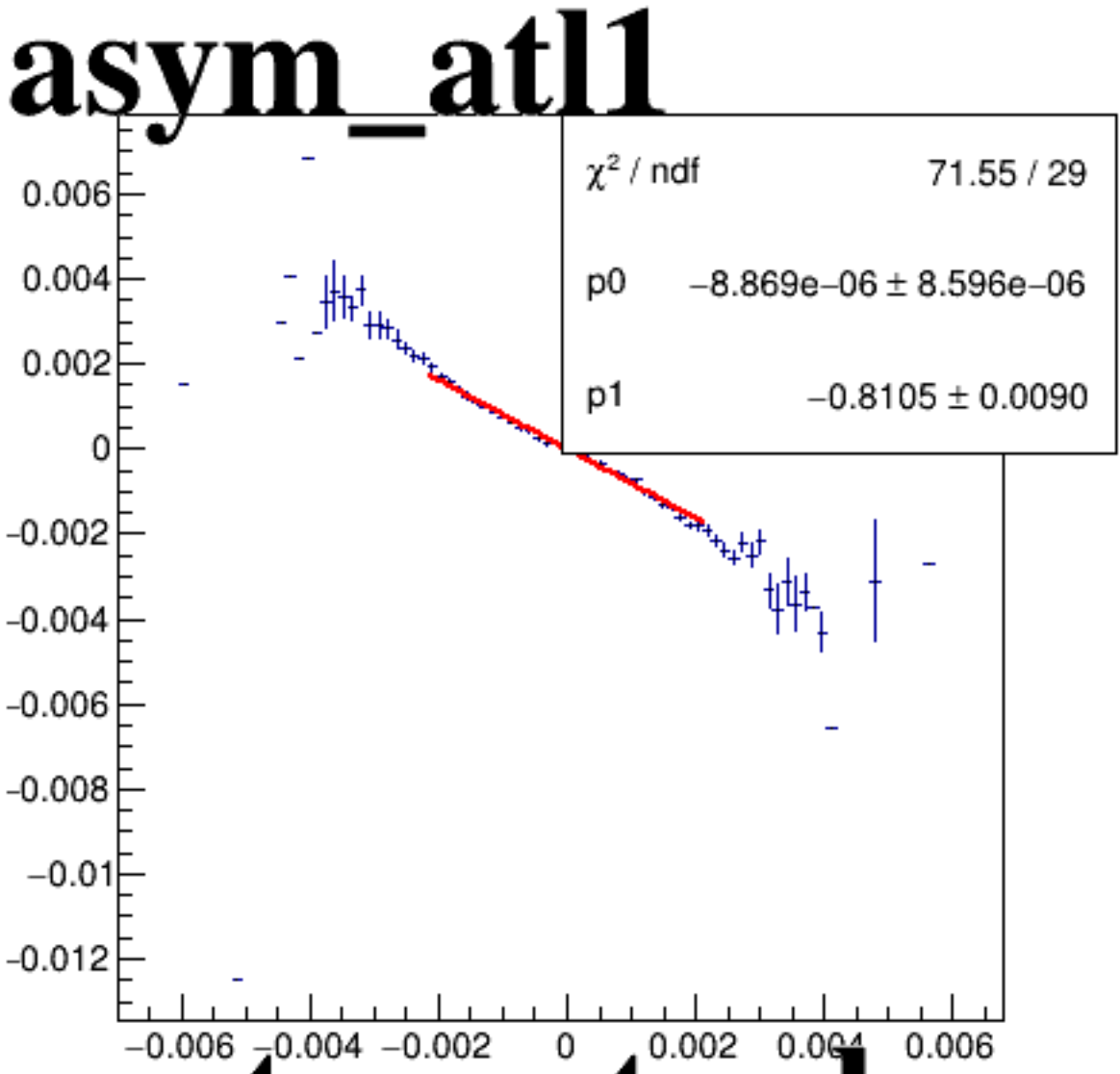
asym_atl2



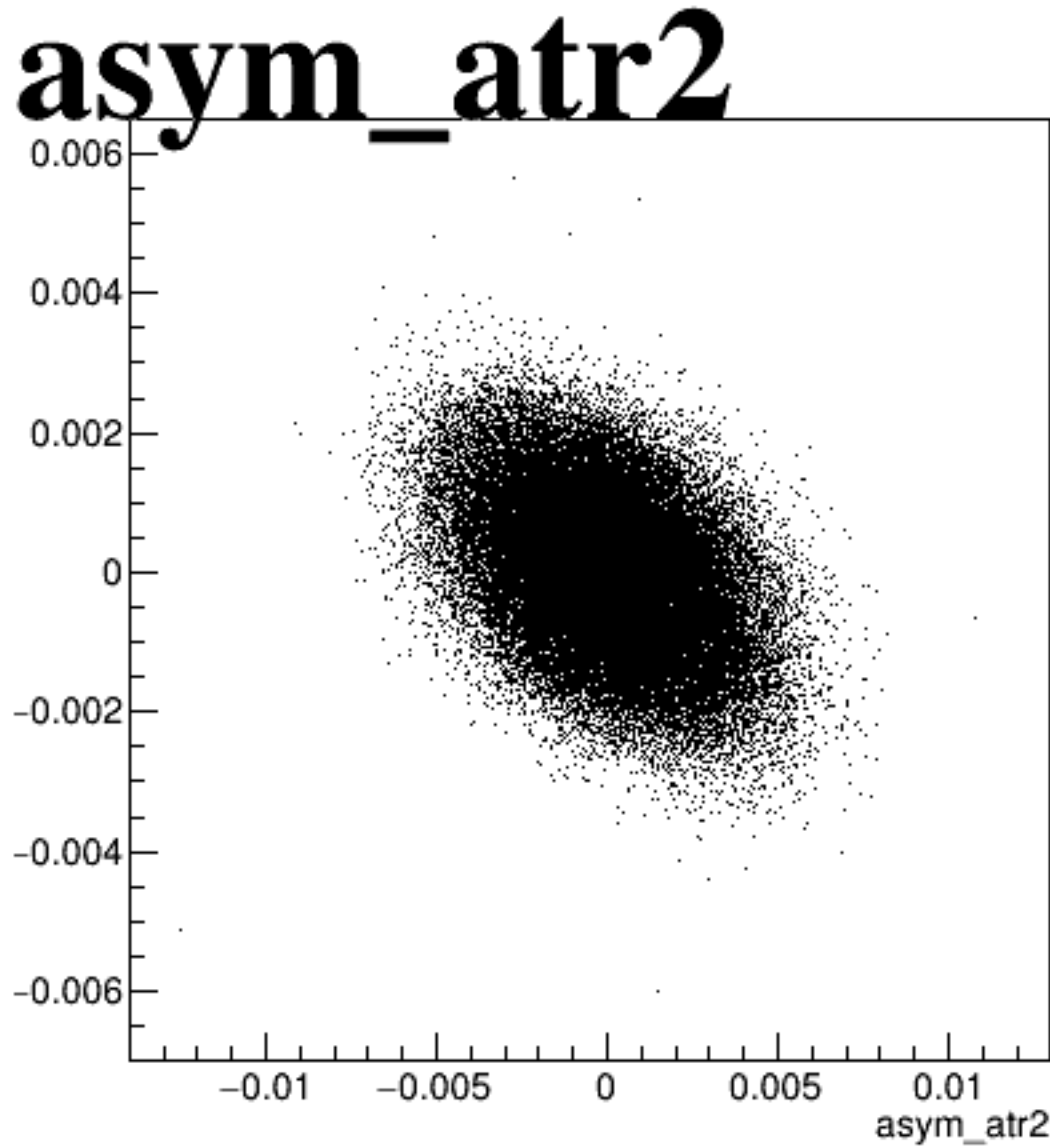
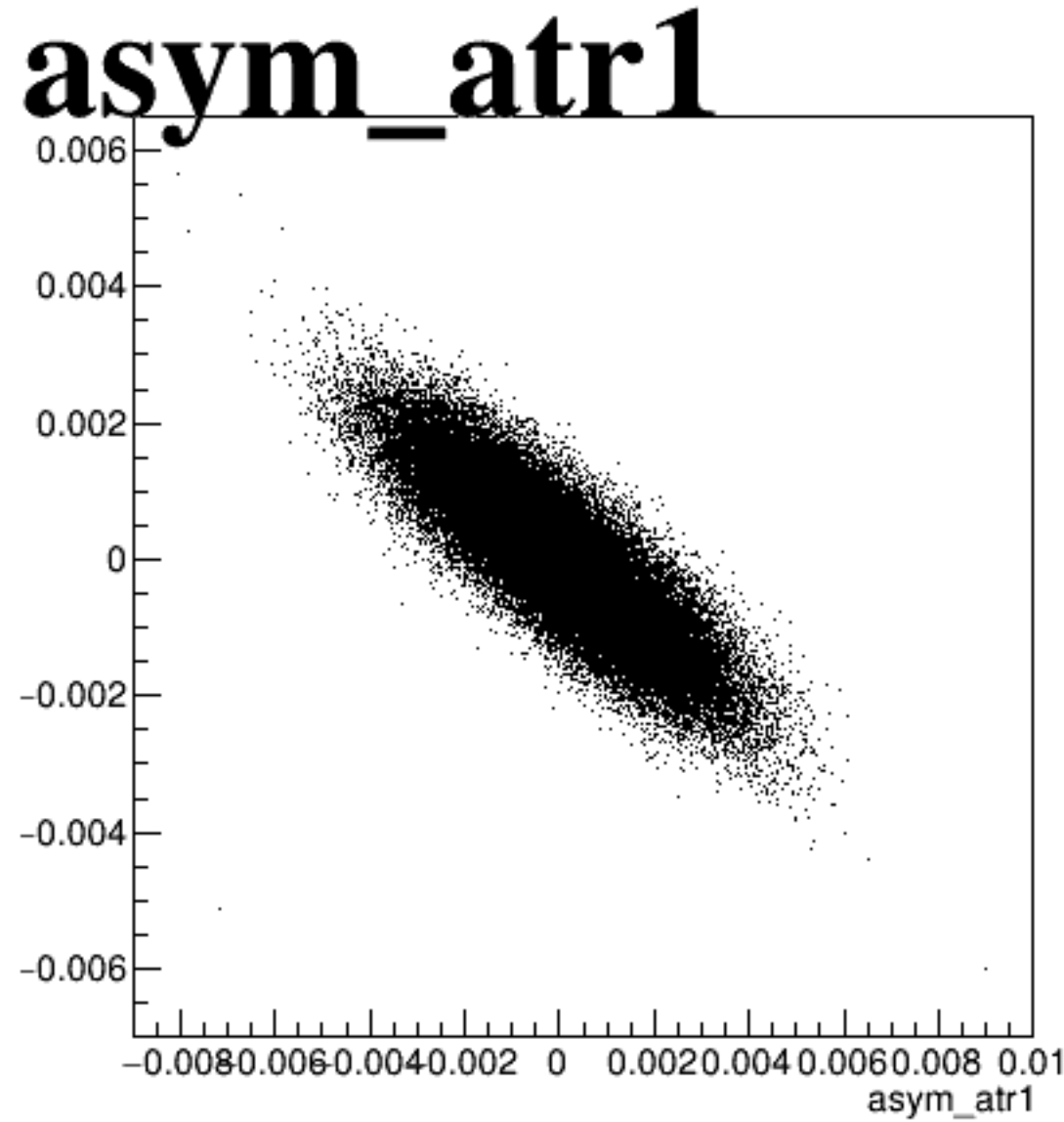
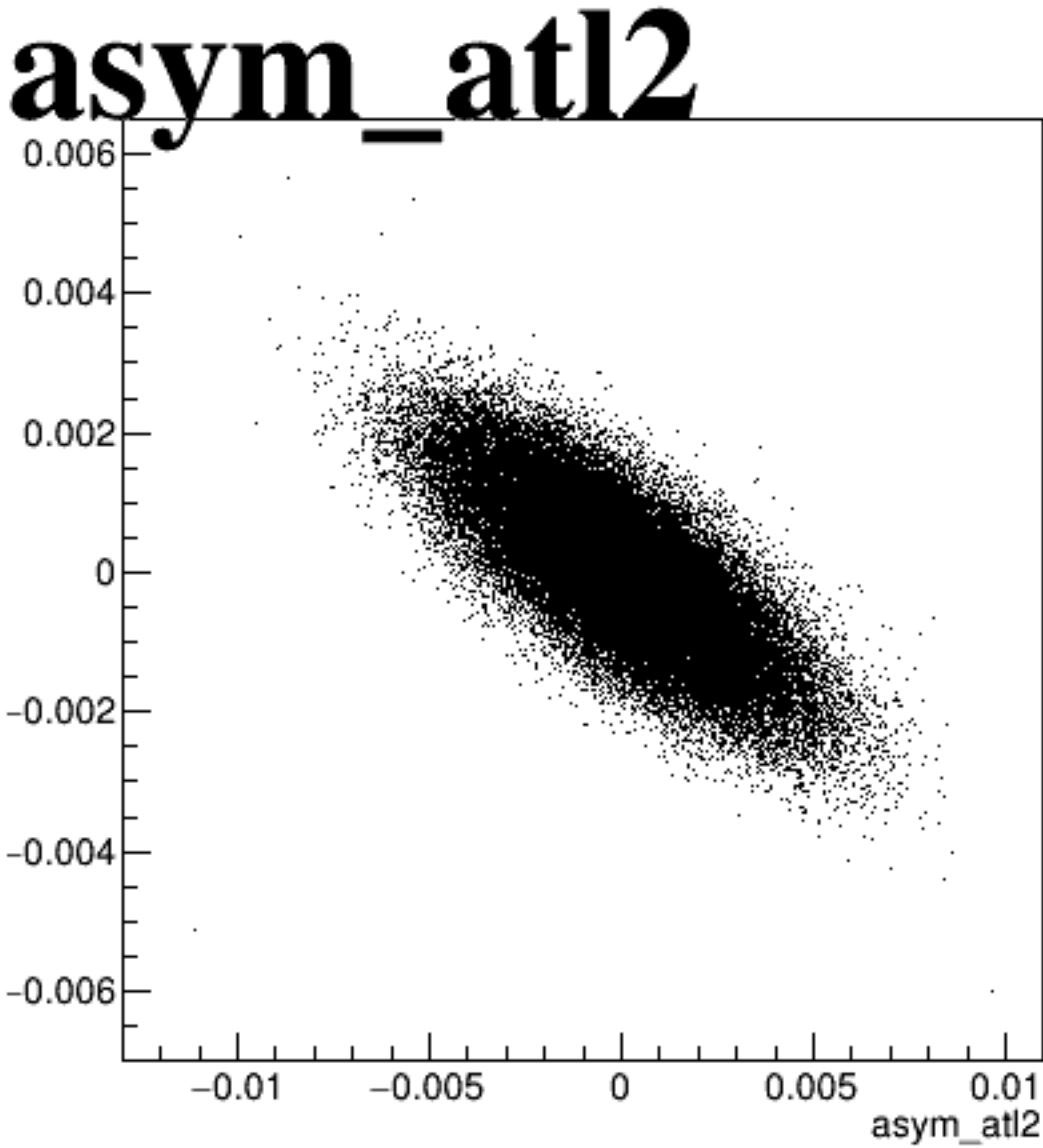
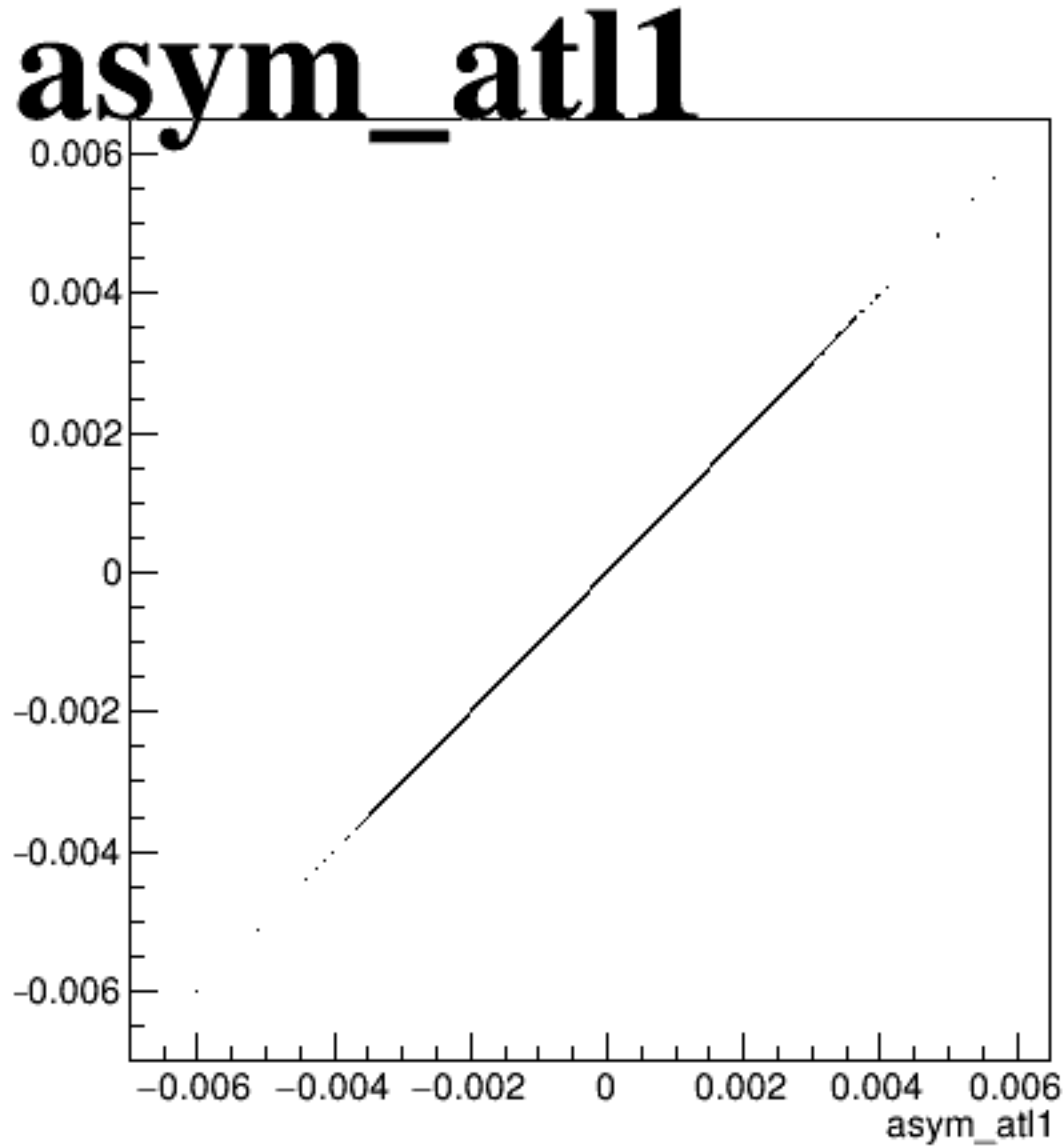
asym_atr1



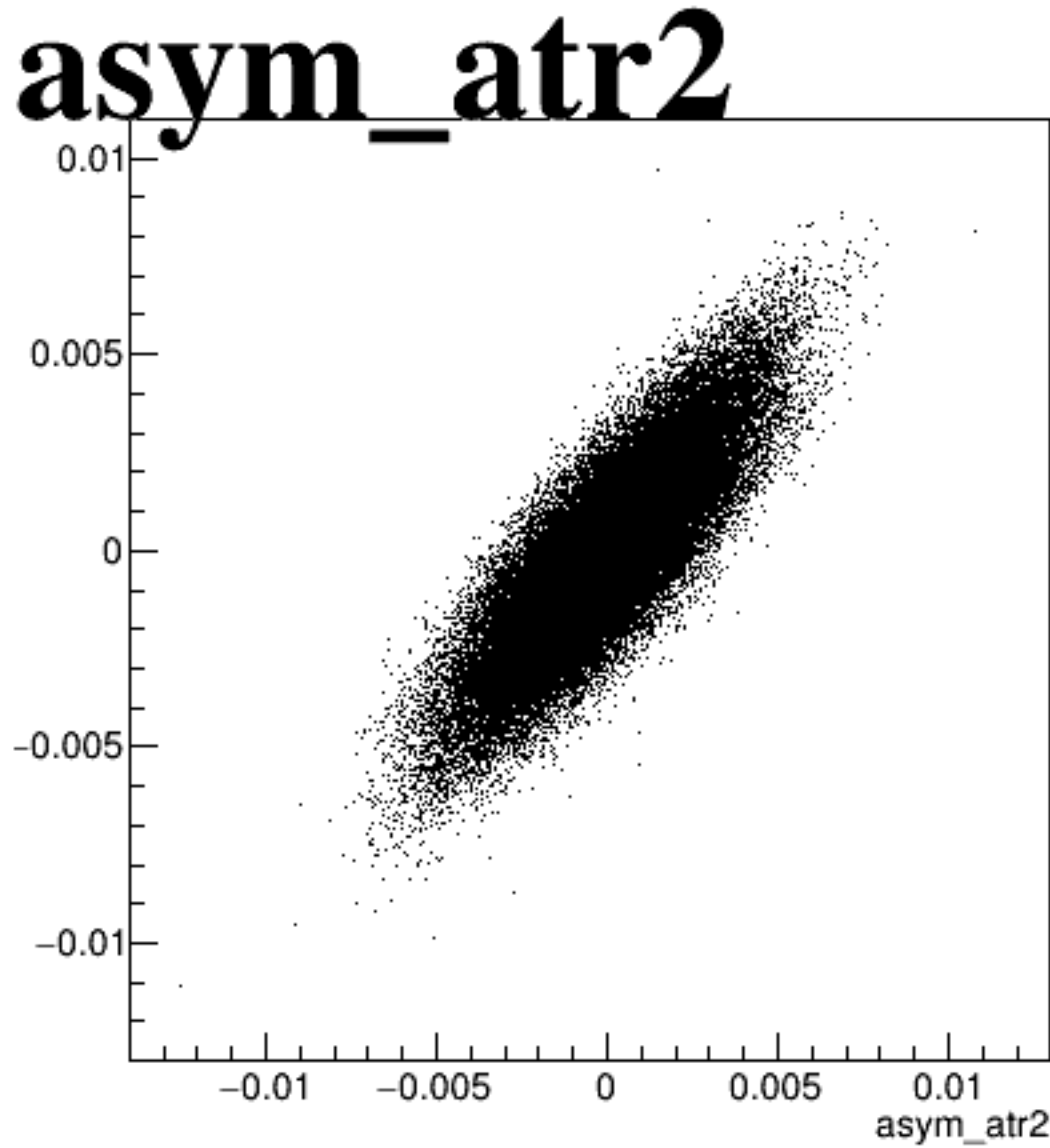
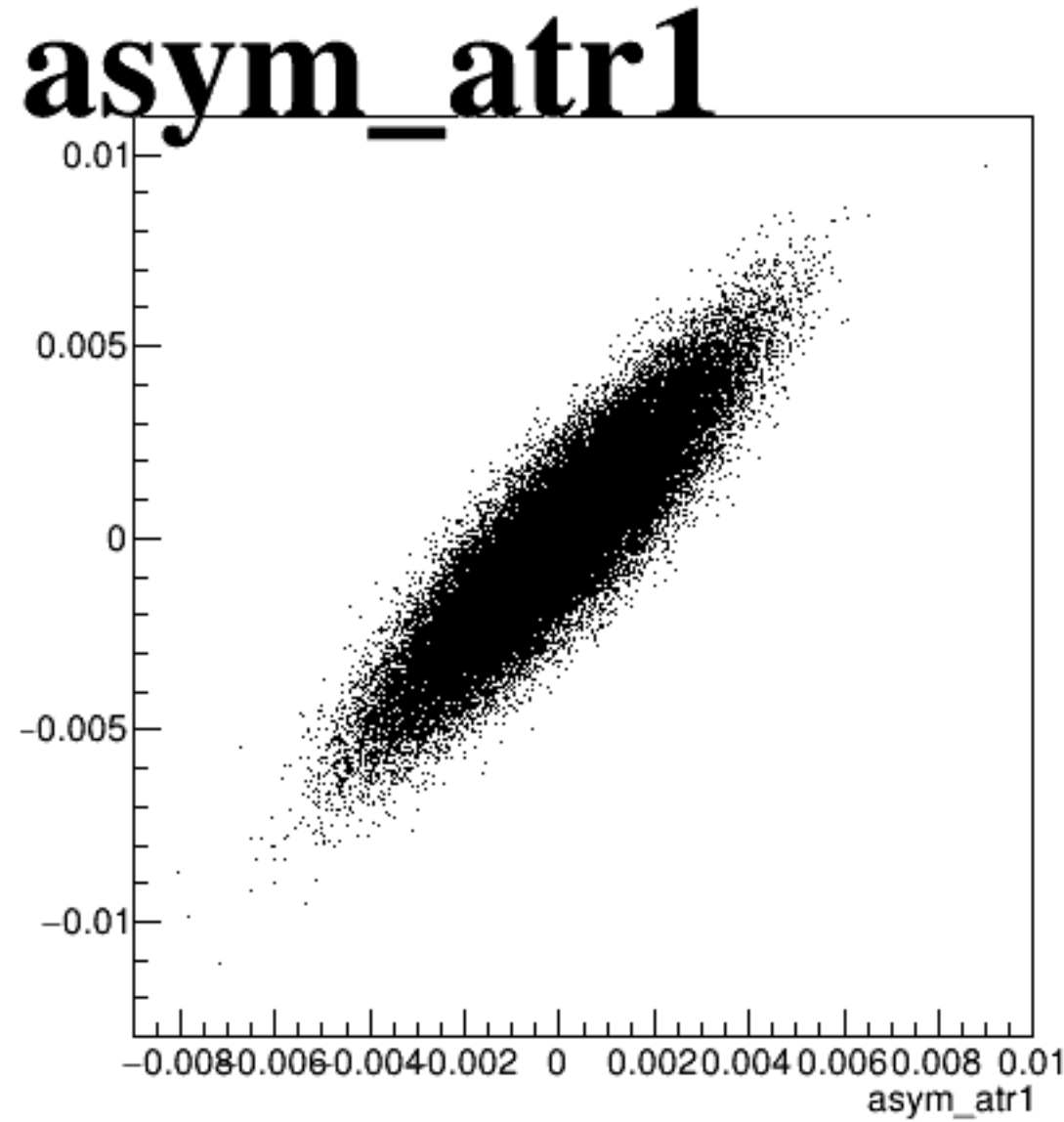
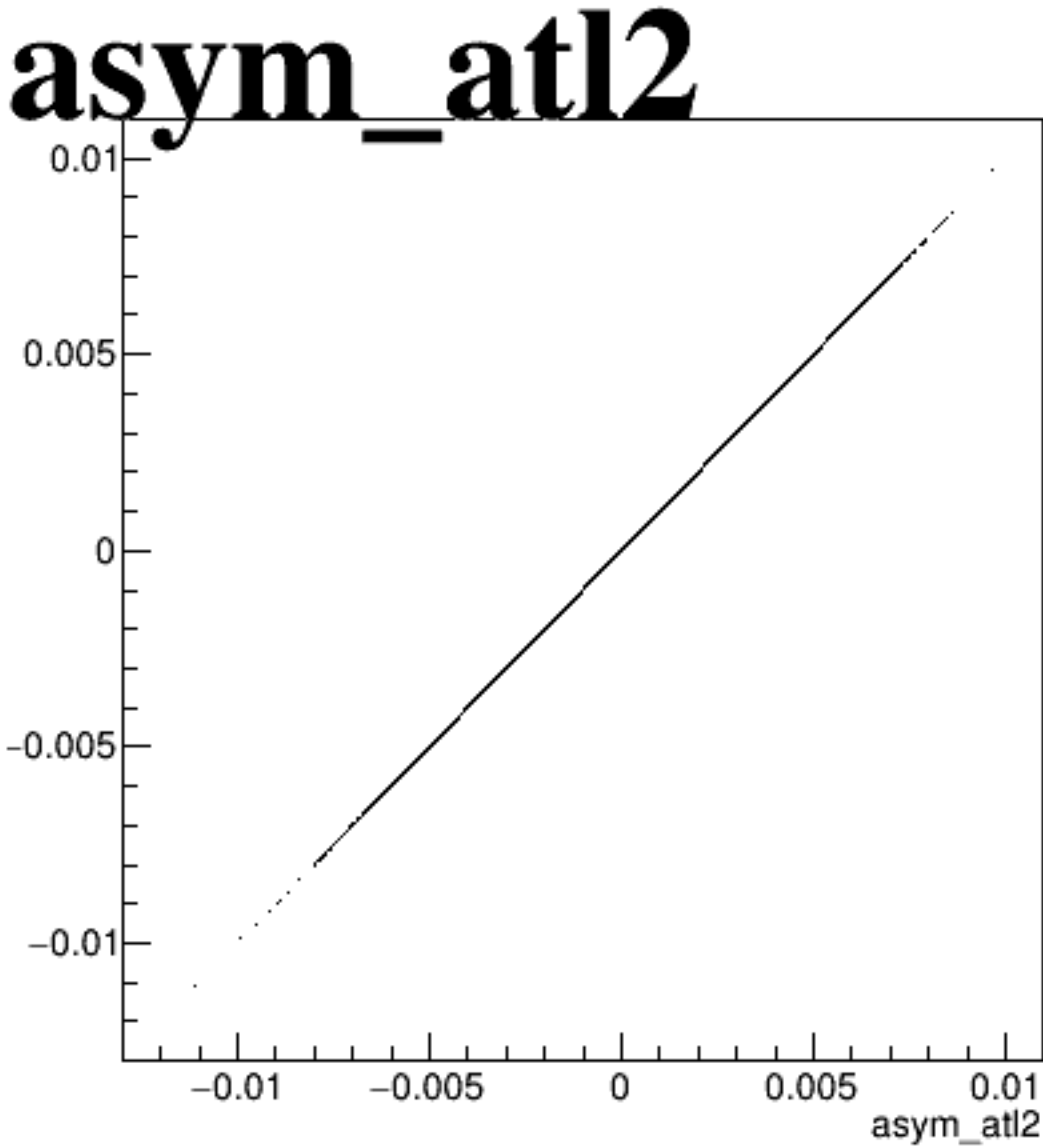
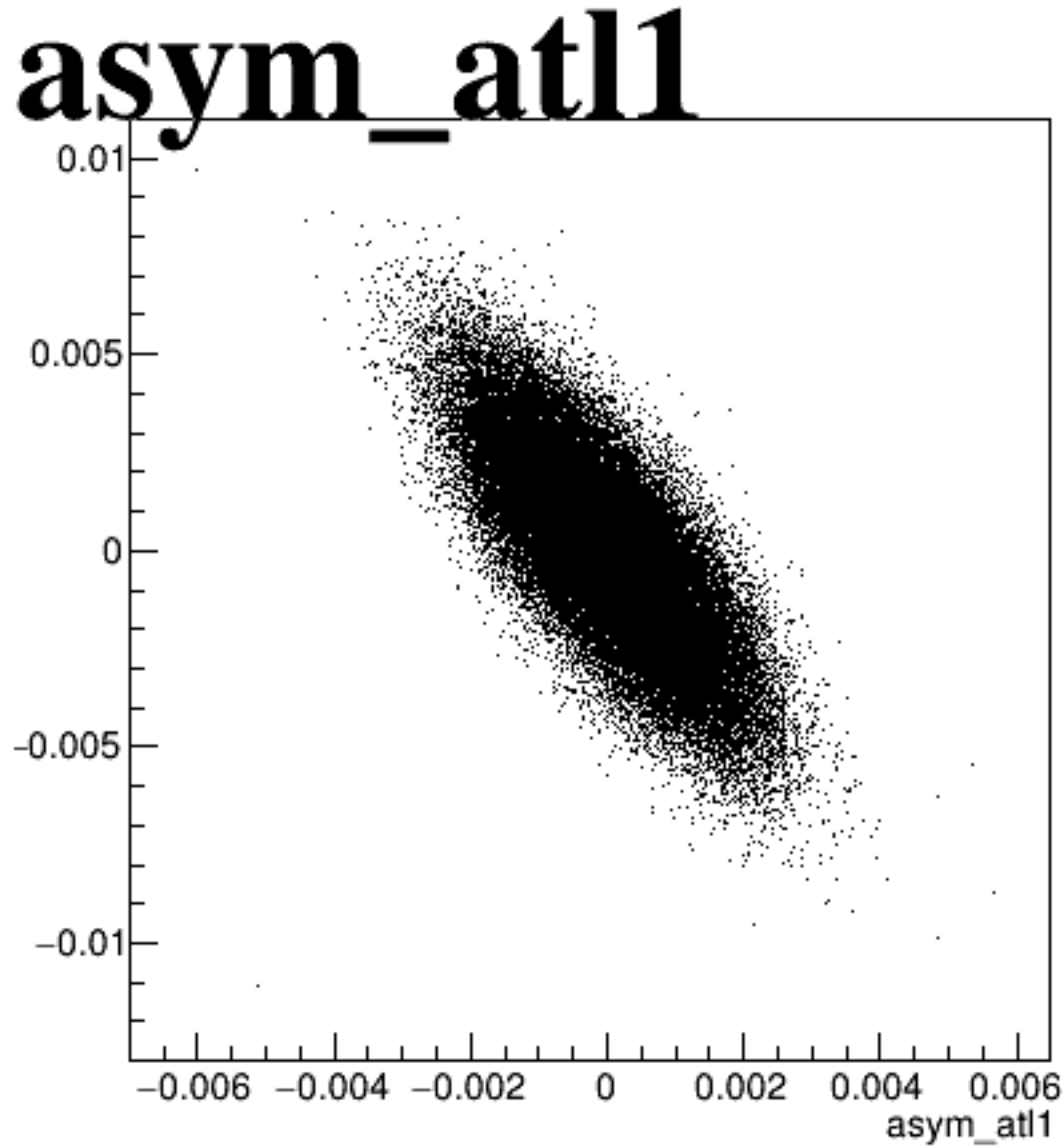
asym_atr2



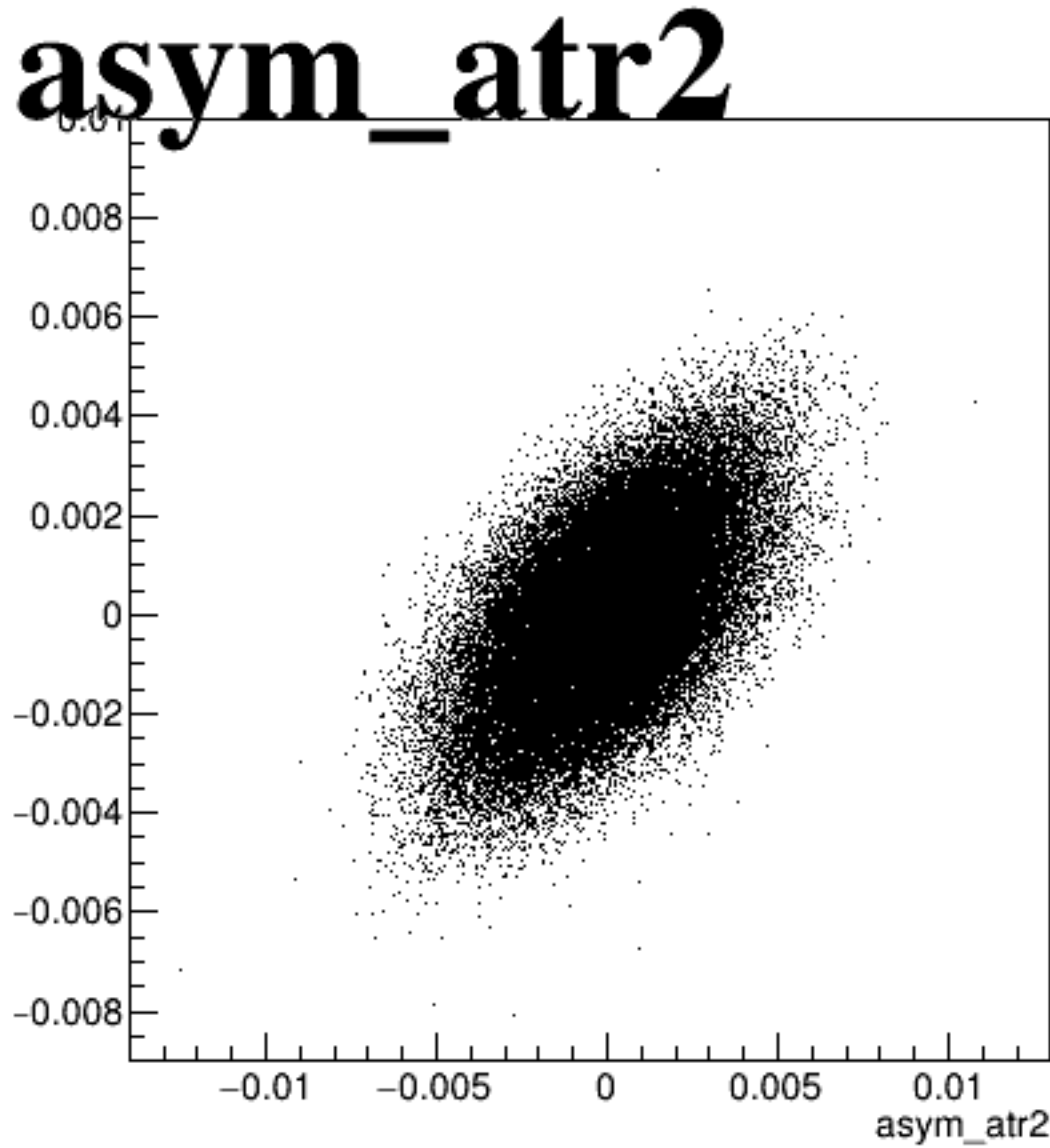
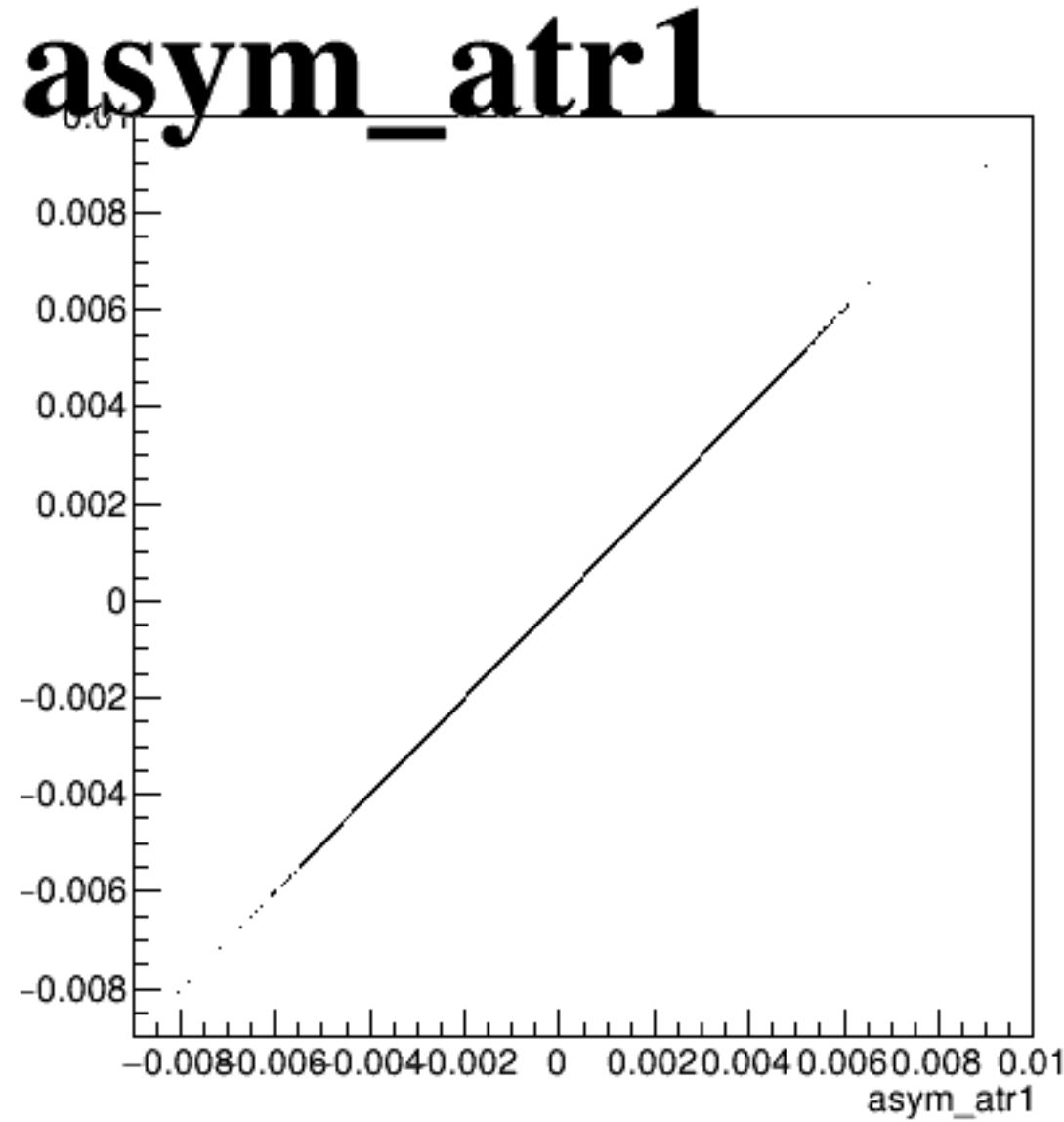
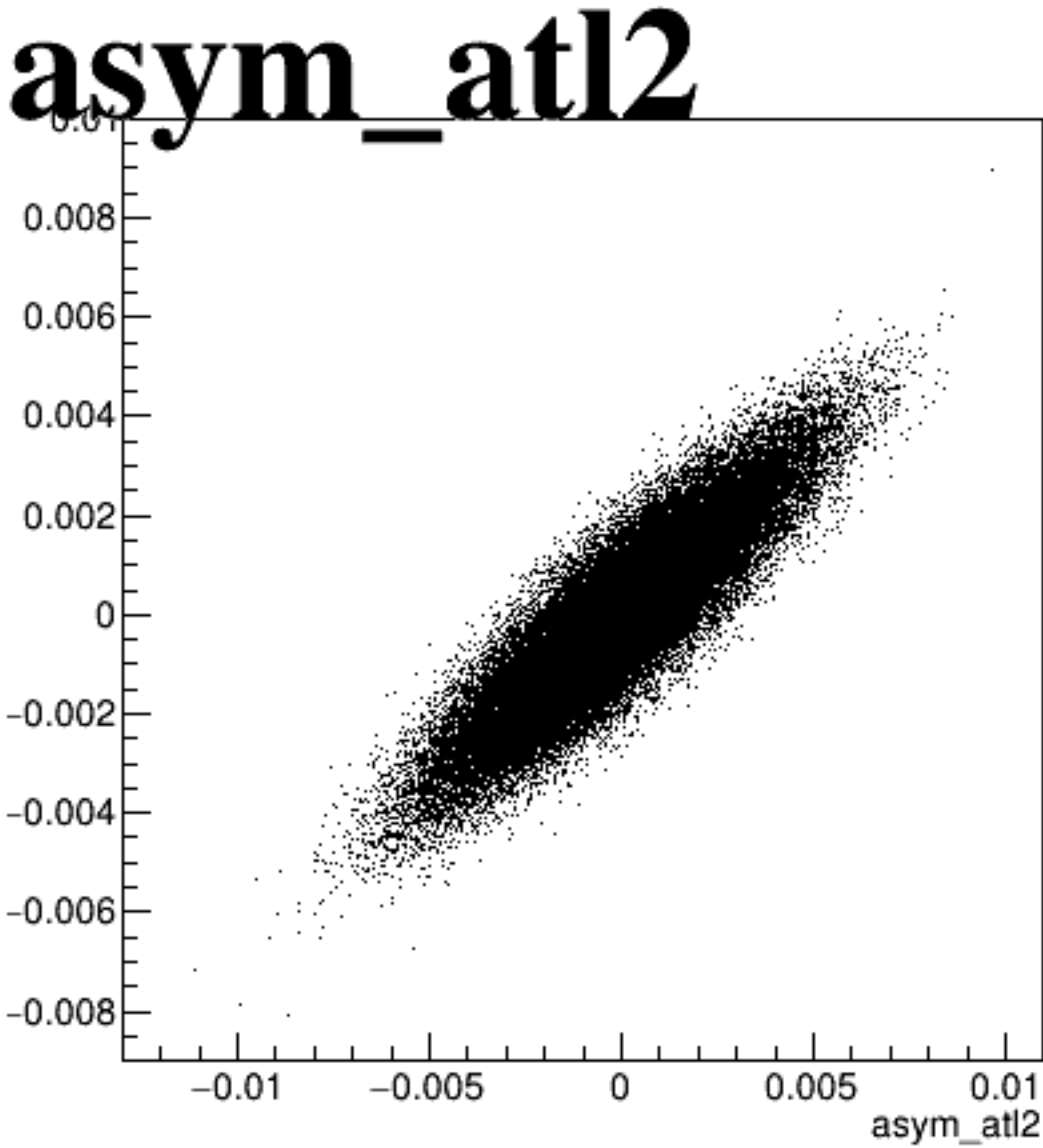
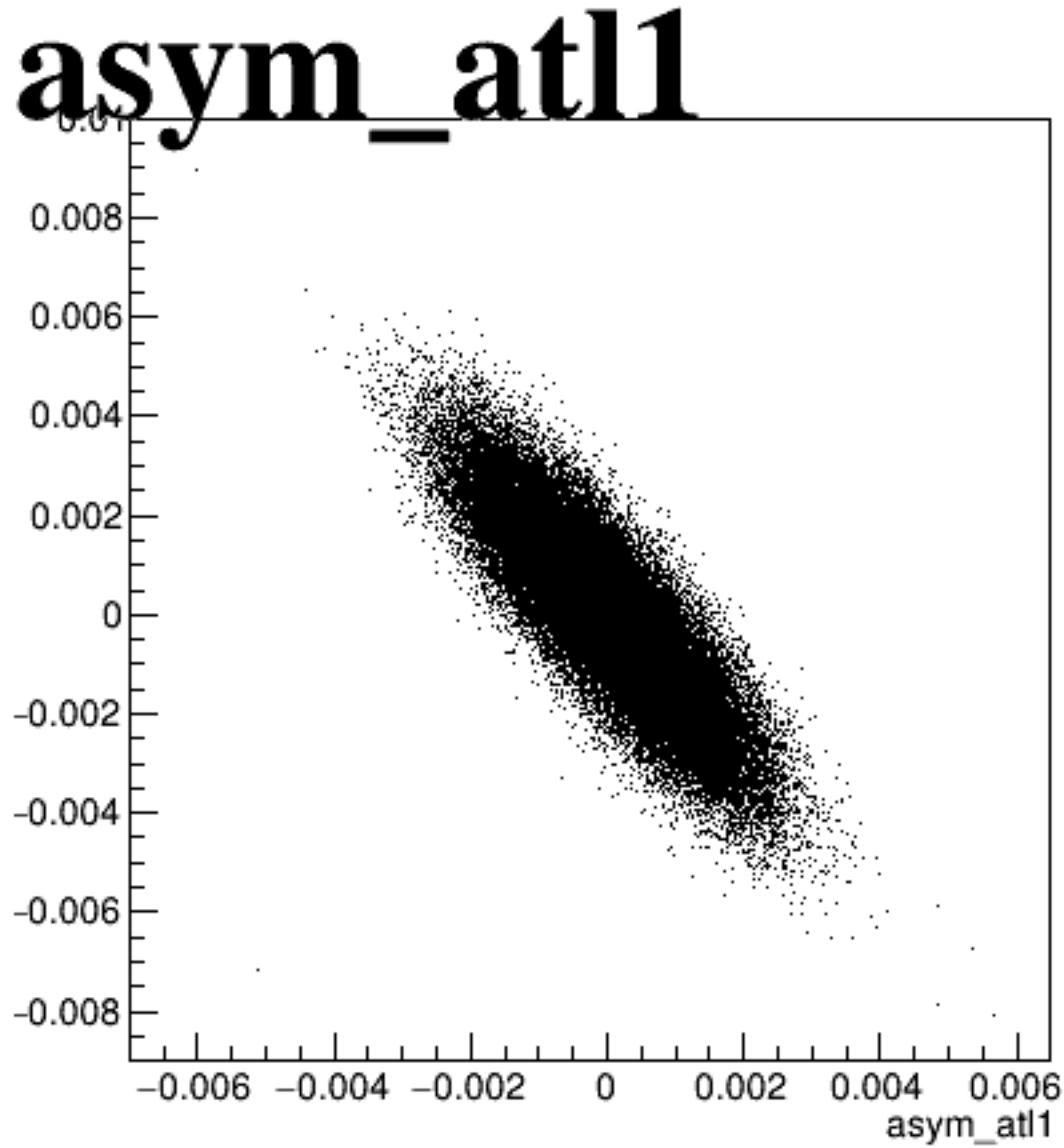
asym_atl1



asym_atl2



asym_atr1



asym_atr2

