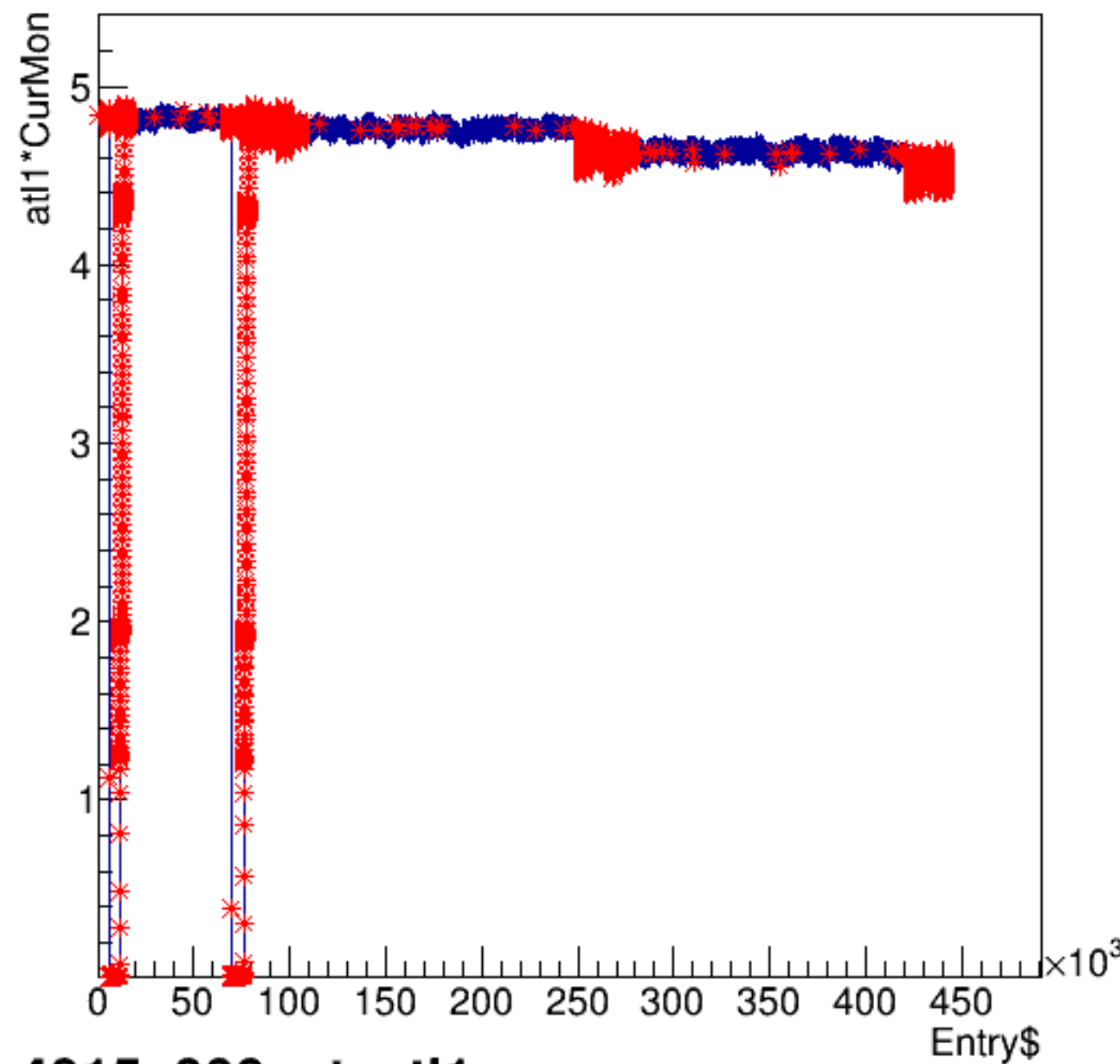
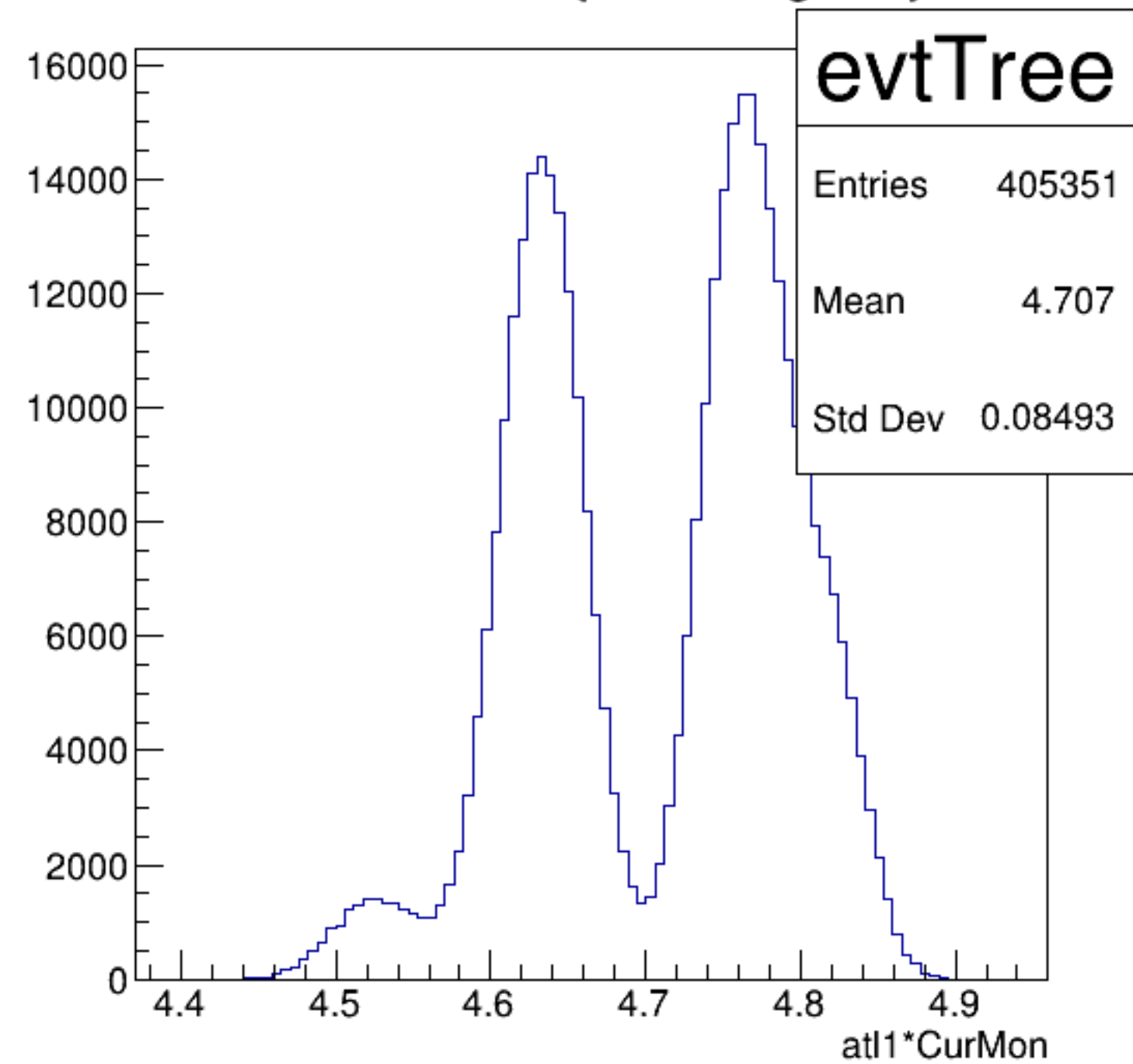


at1\*CurMon:Entry\$

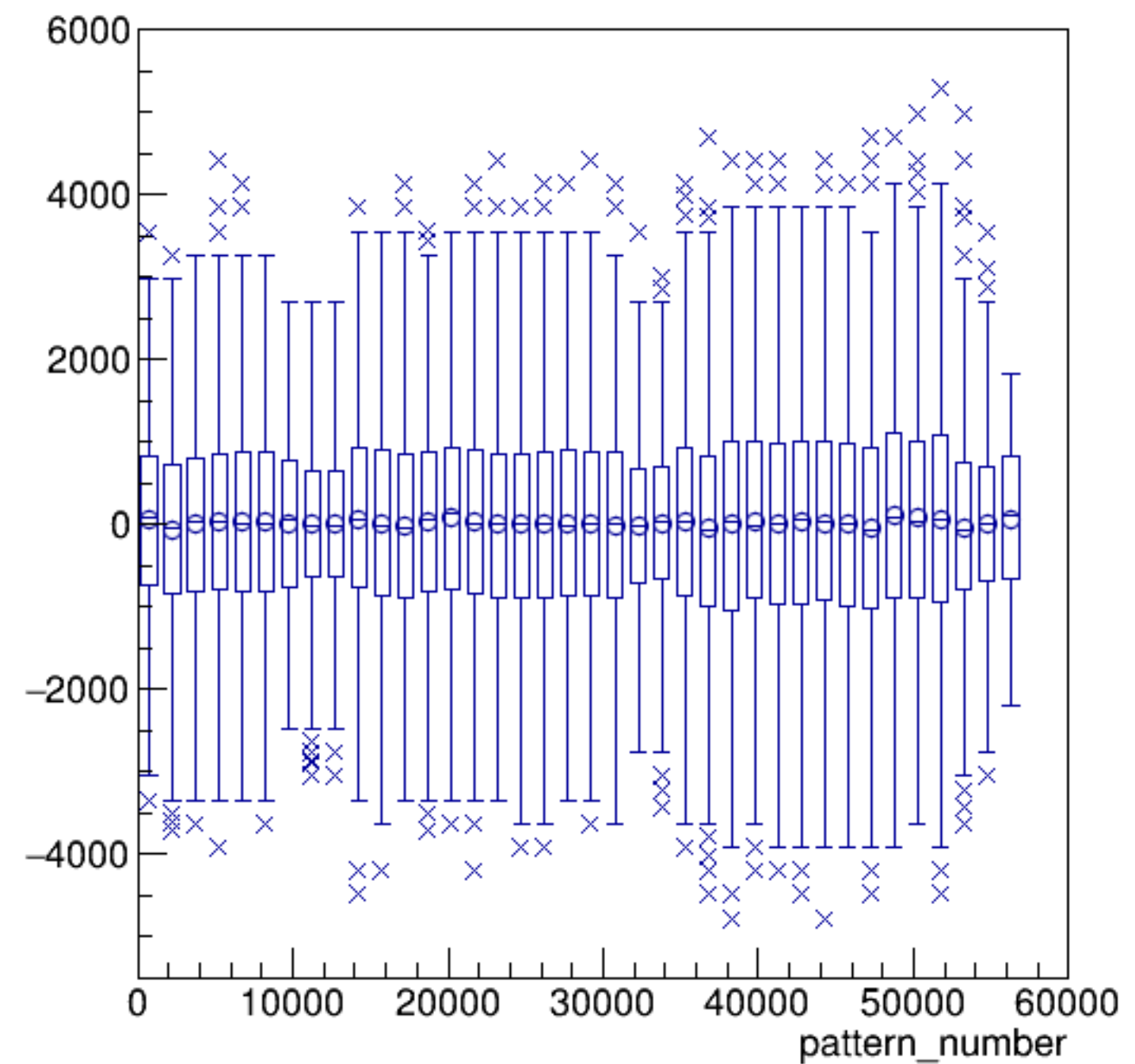


run4015\_000\_at\_atl1.png

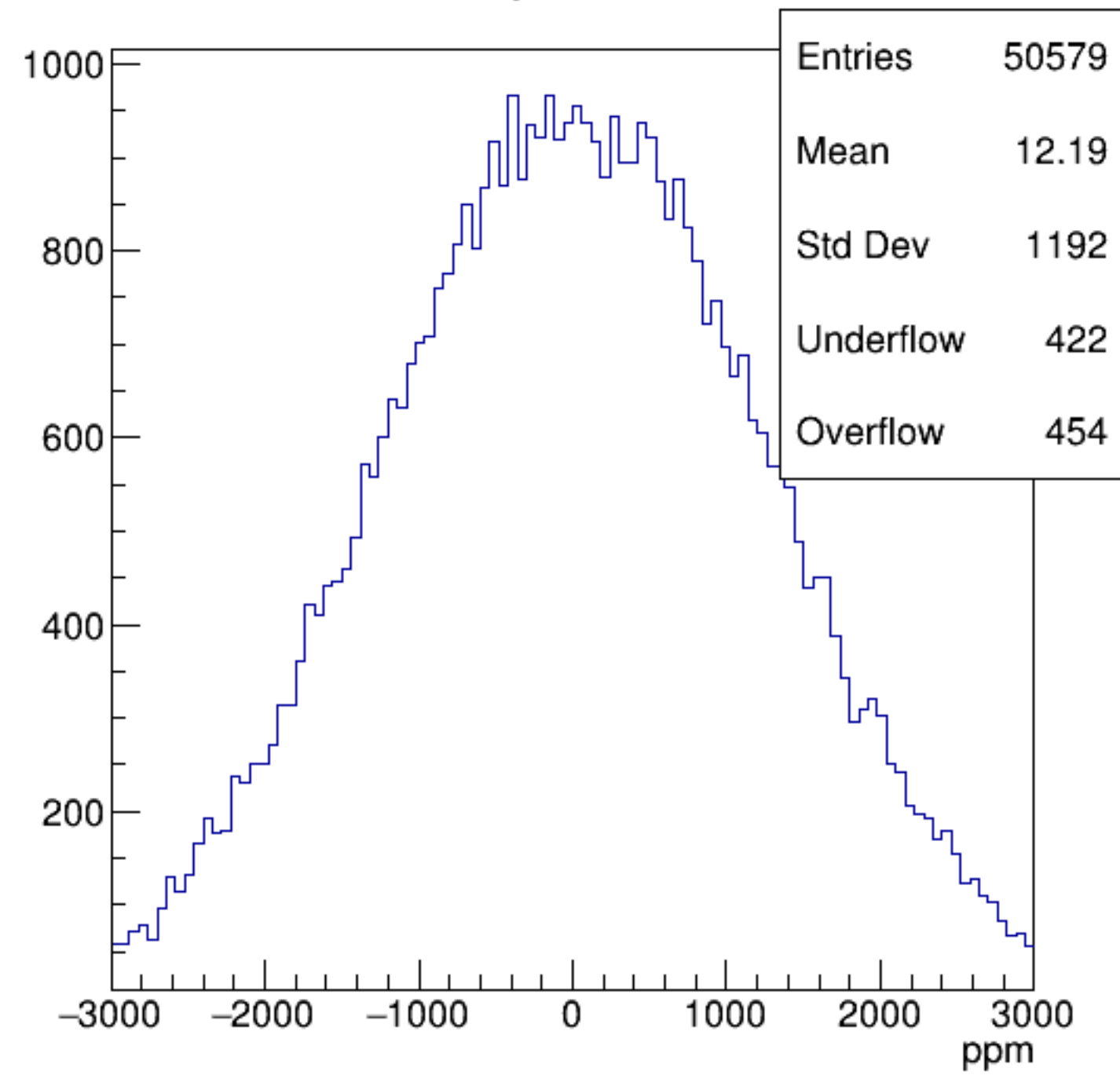
at1\*CurMon {ErrorFlag==0}



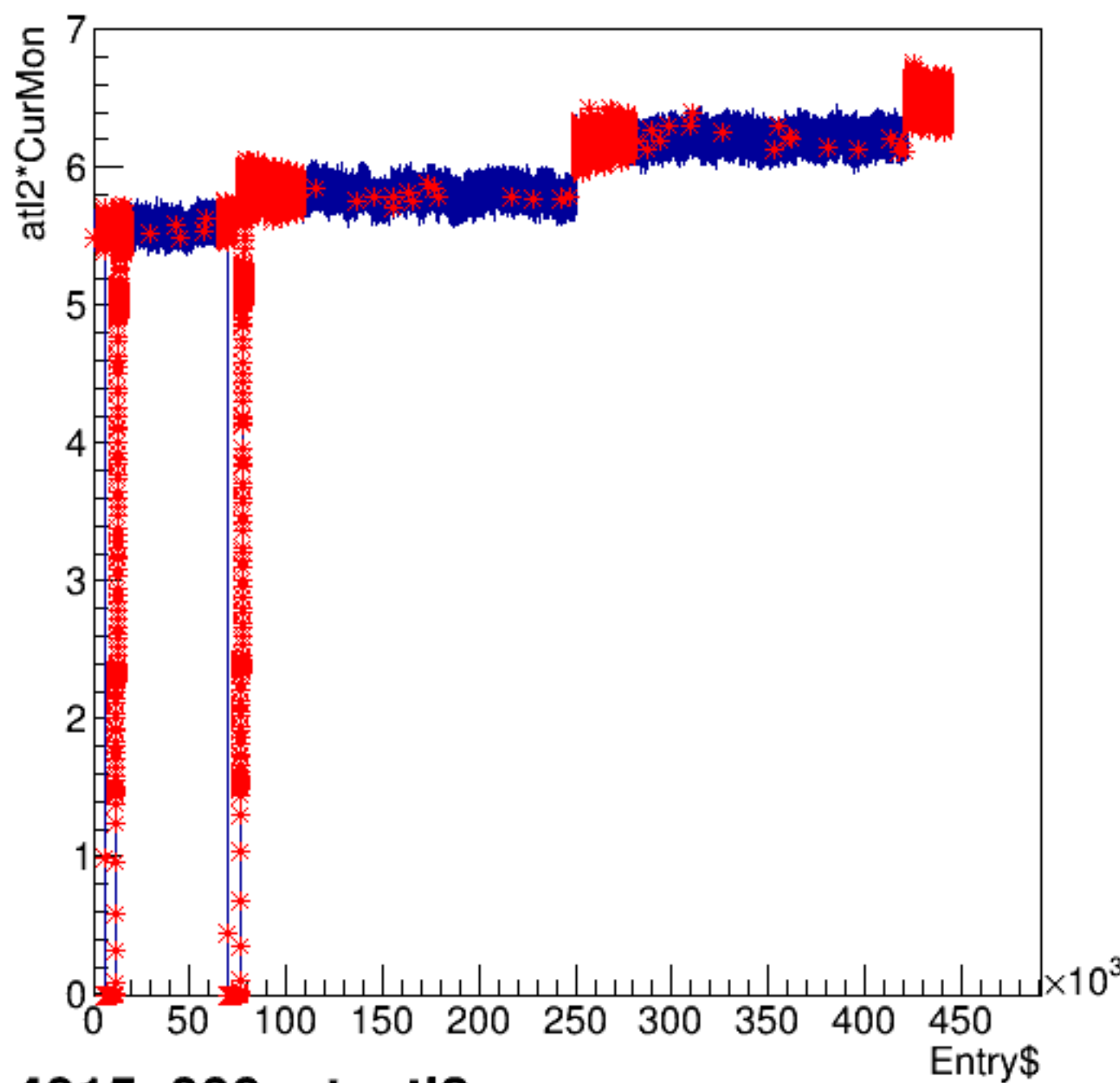
asym\_atl1/ppm:pattern\_number {ErrorFlag==0}



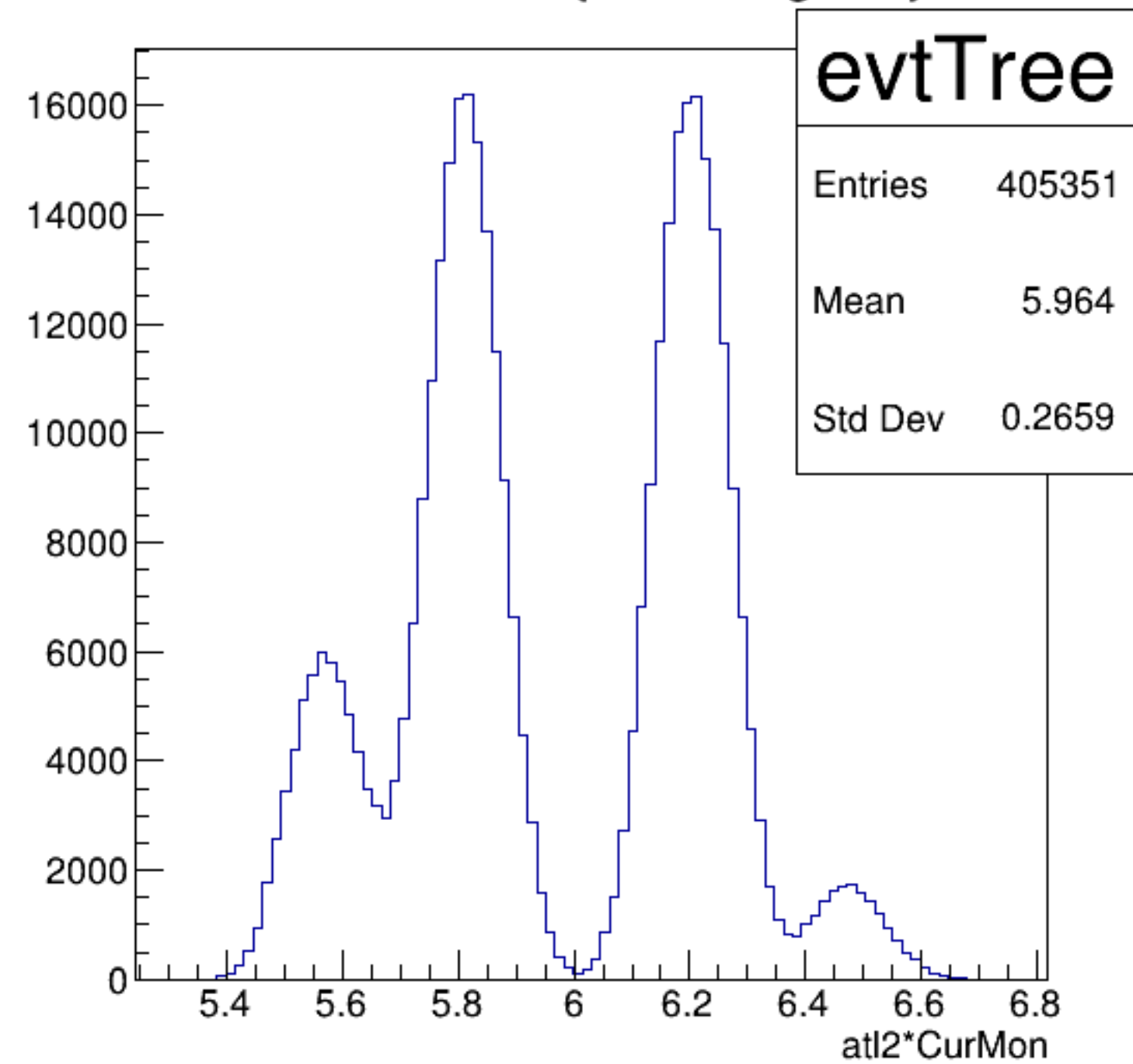
asym\_atl1



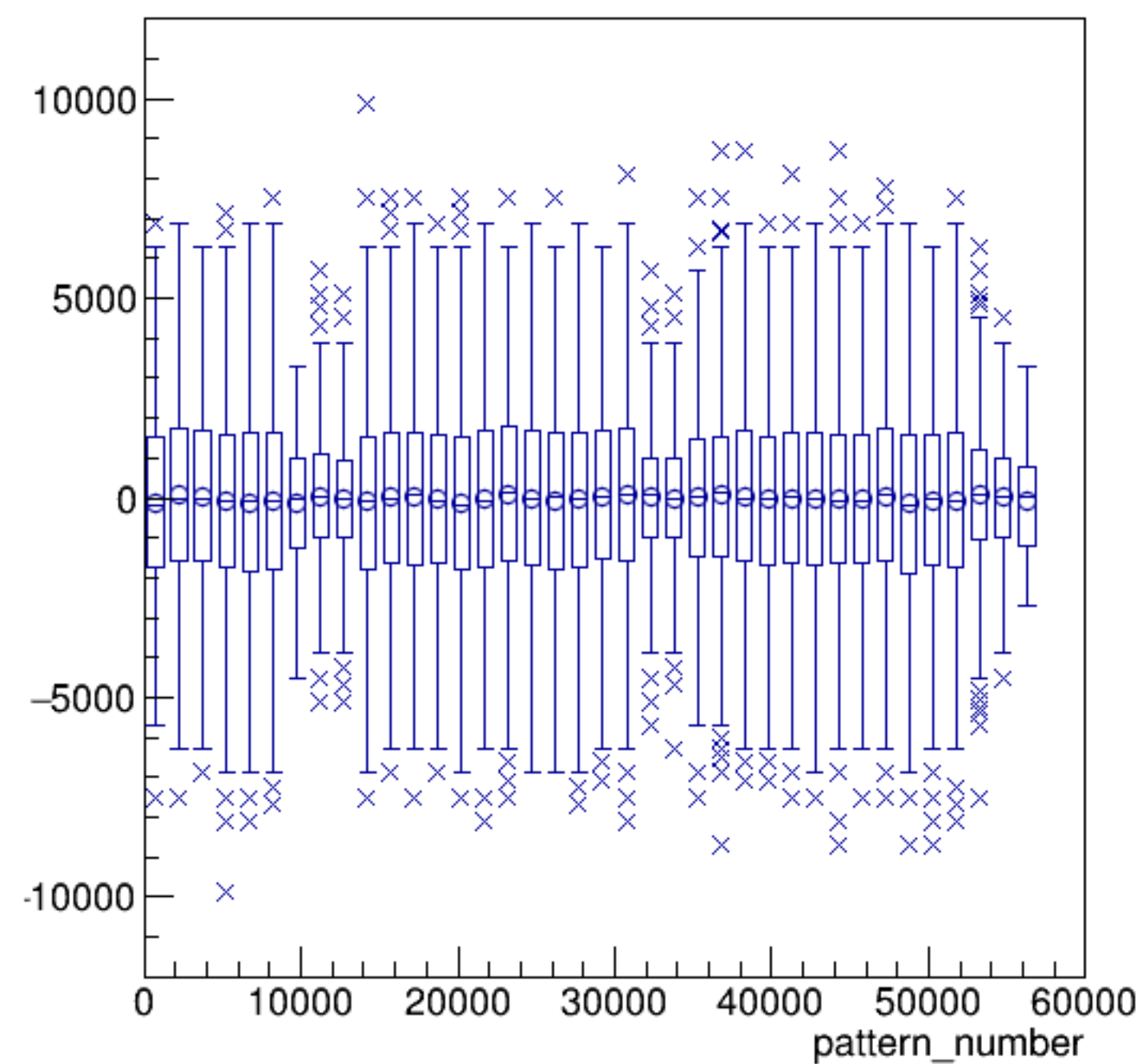
atl2\*CurMon:Entry\$



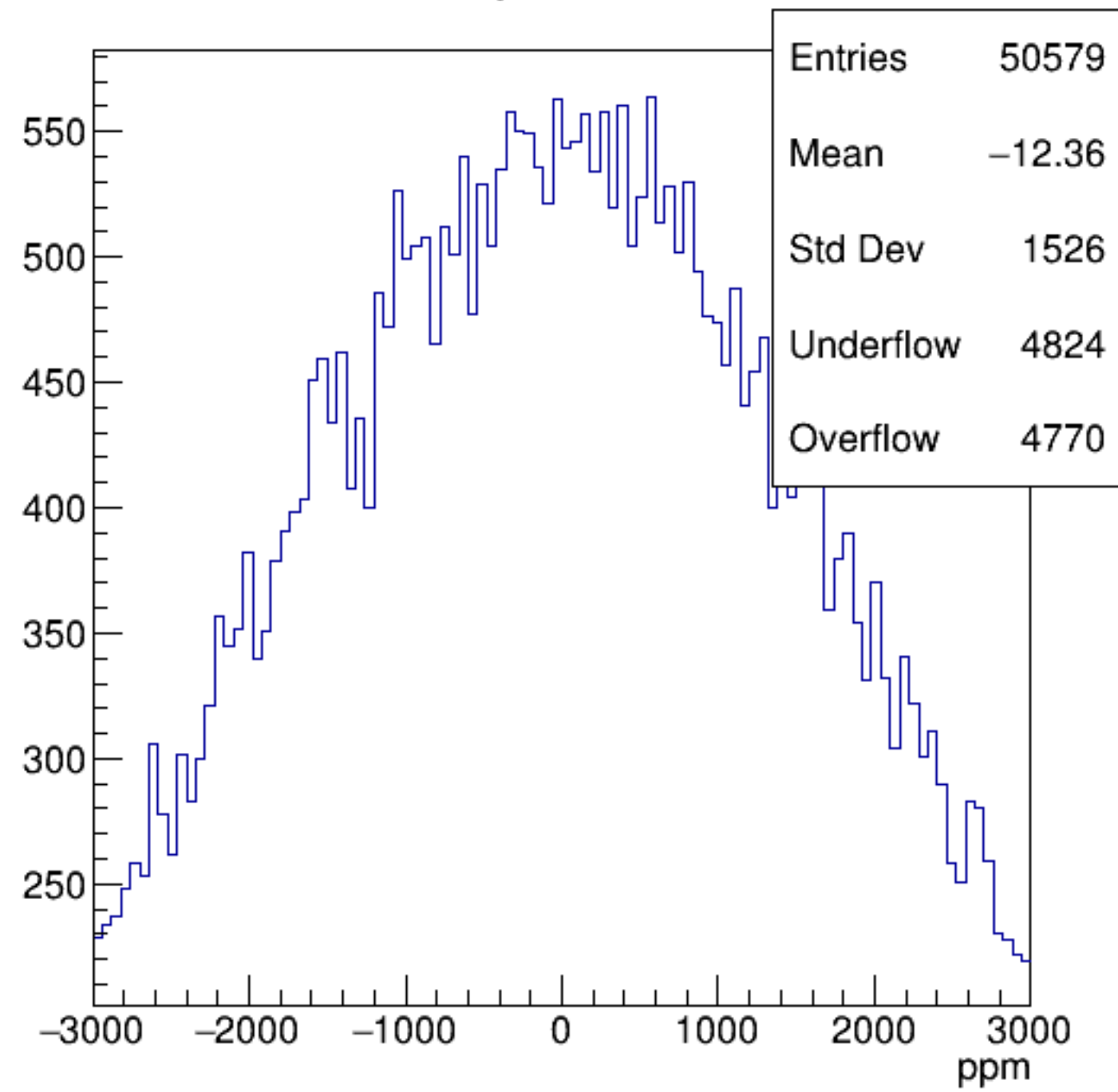
atl2\*CurMon {ErrorFlag==0}



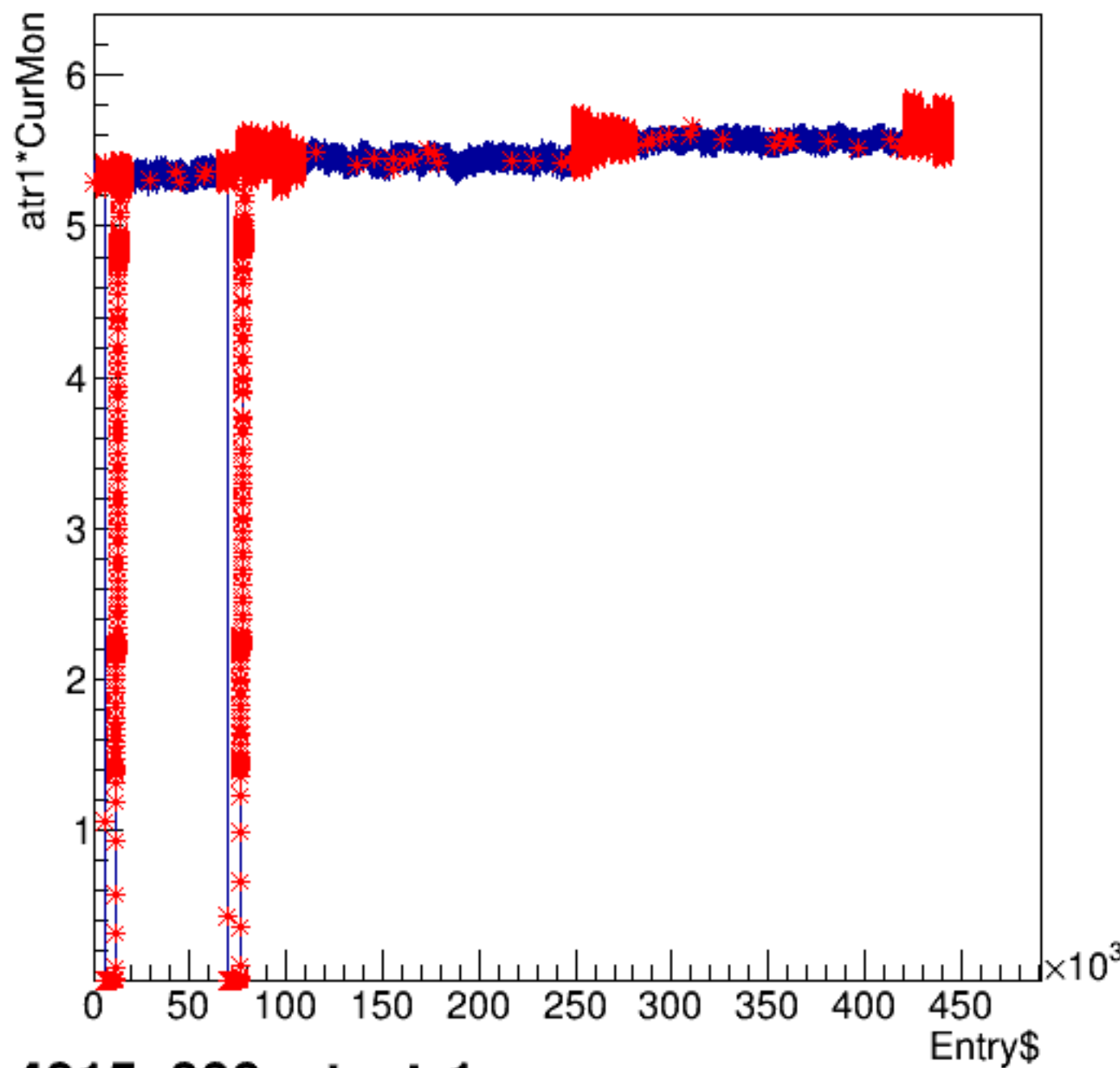
asym\_atl2/ppm:pattern\_number {ErrorFlag==0}



asym\_atl2

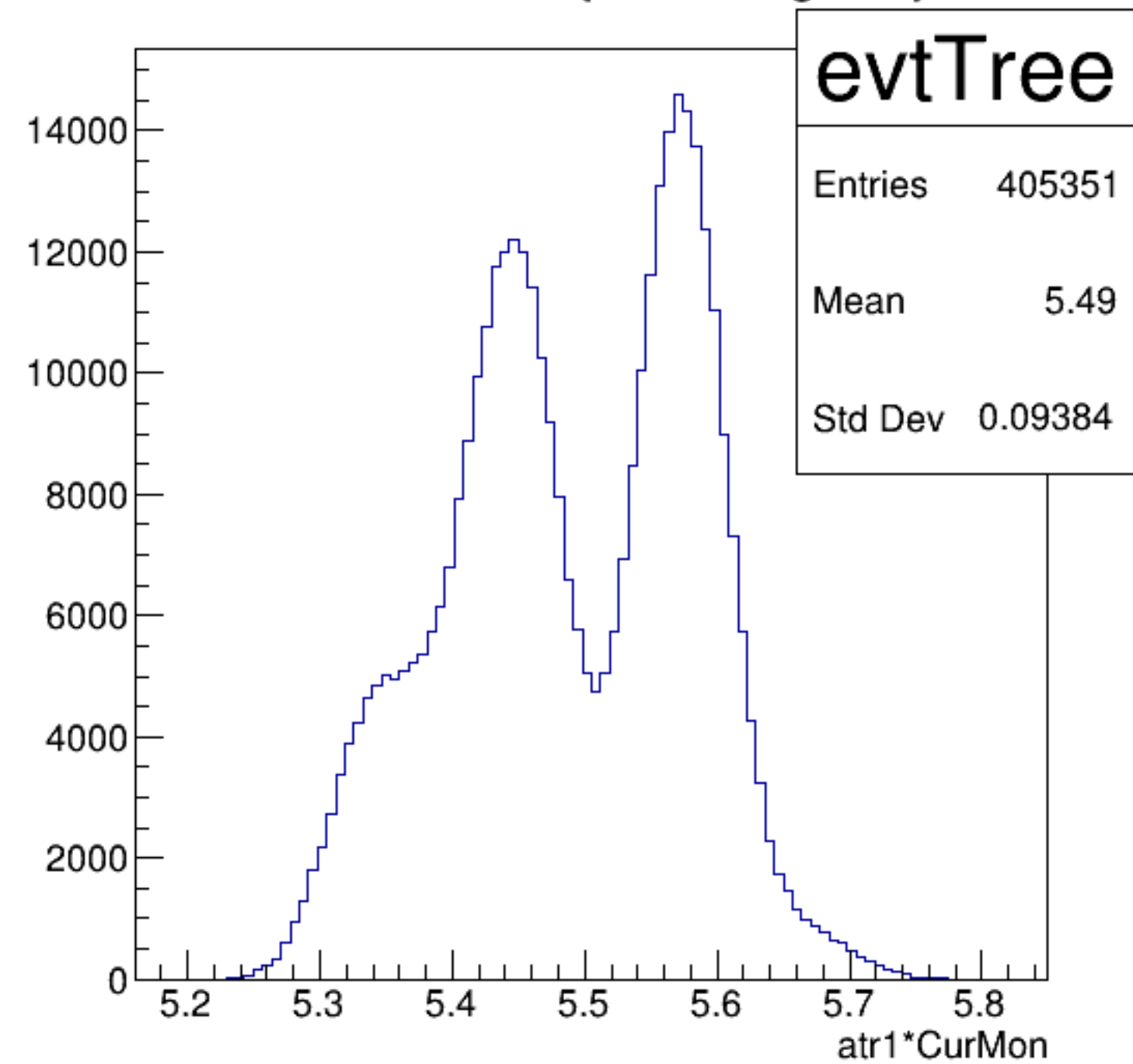


atr1\*CurMon:Entry\$

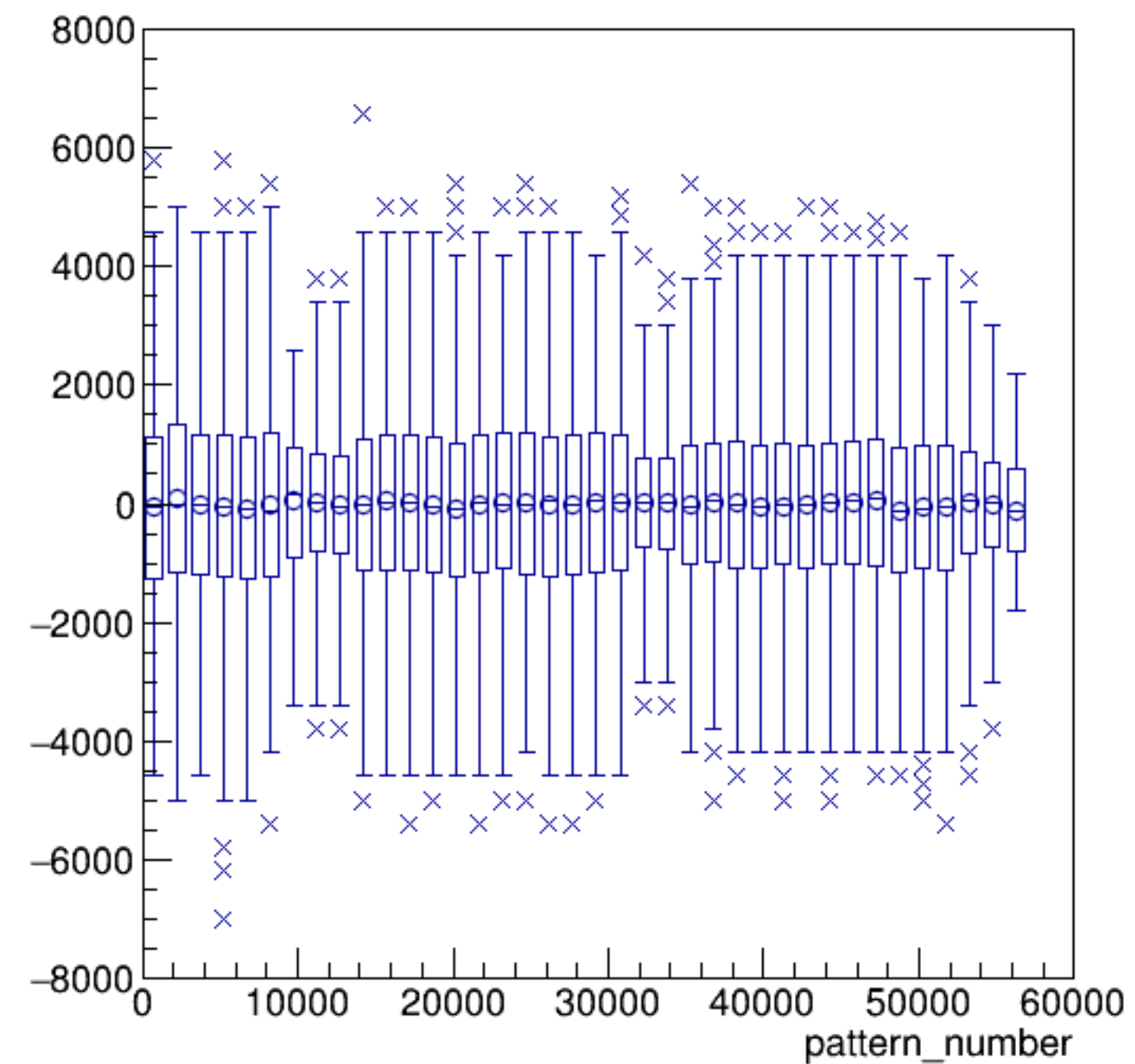


run4015\_000\_at atr1.png

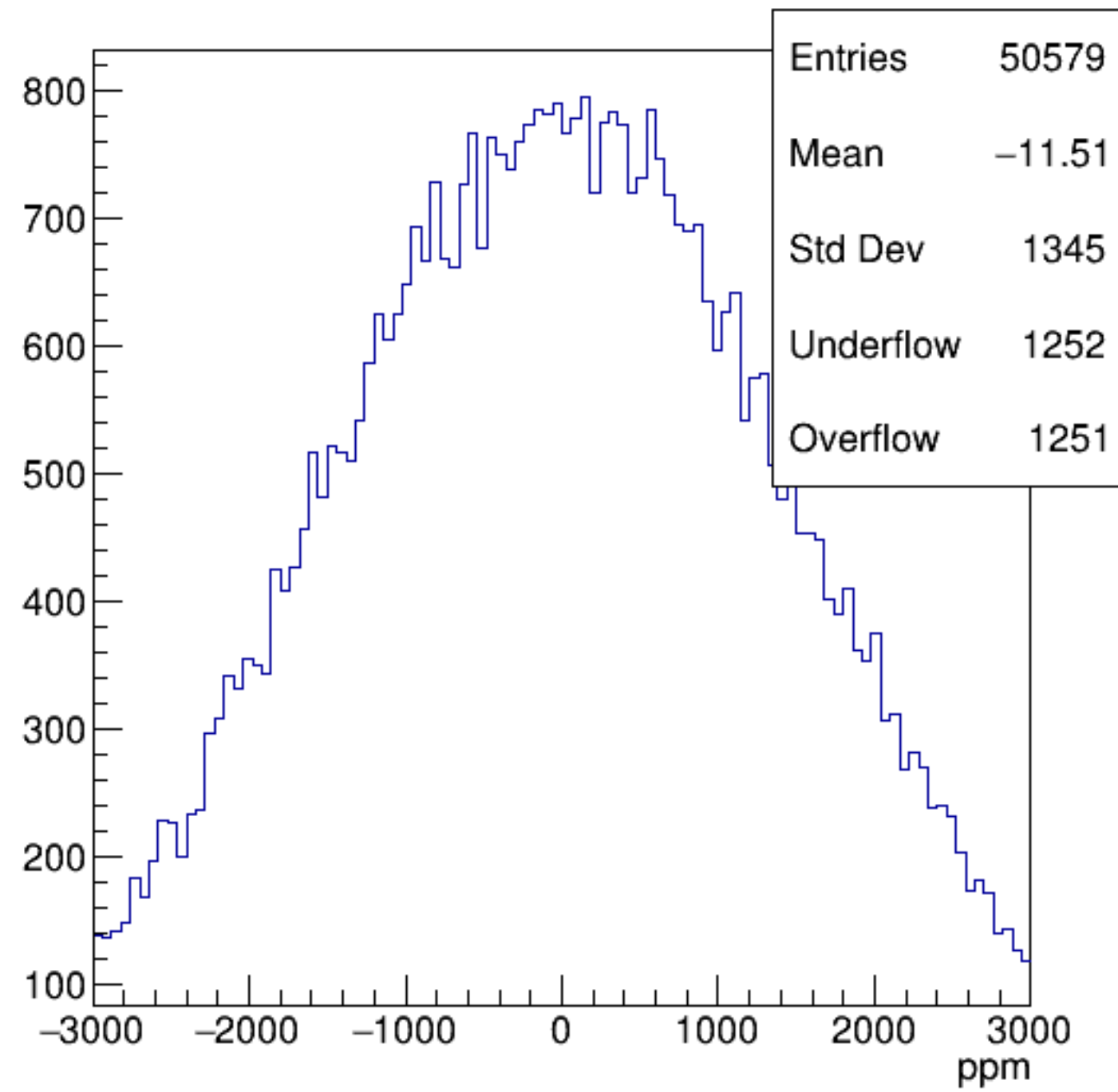
atr1\*CurMon {ErrorFlag==0}



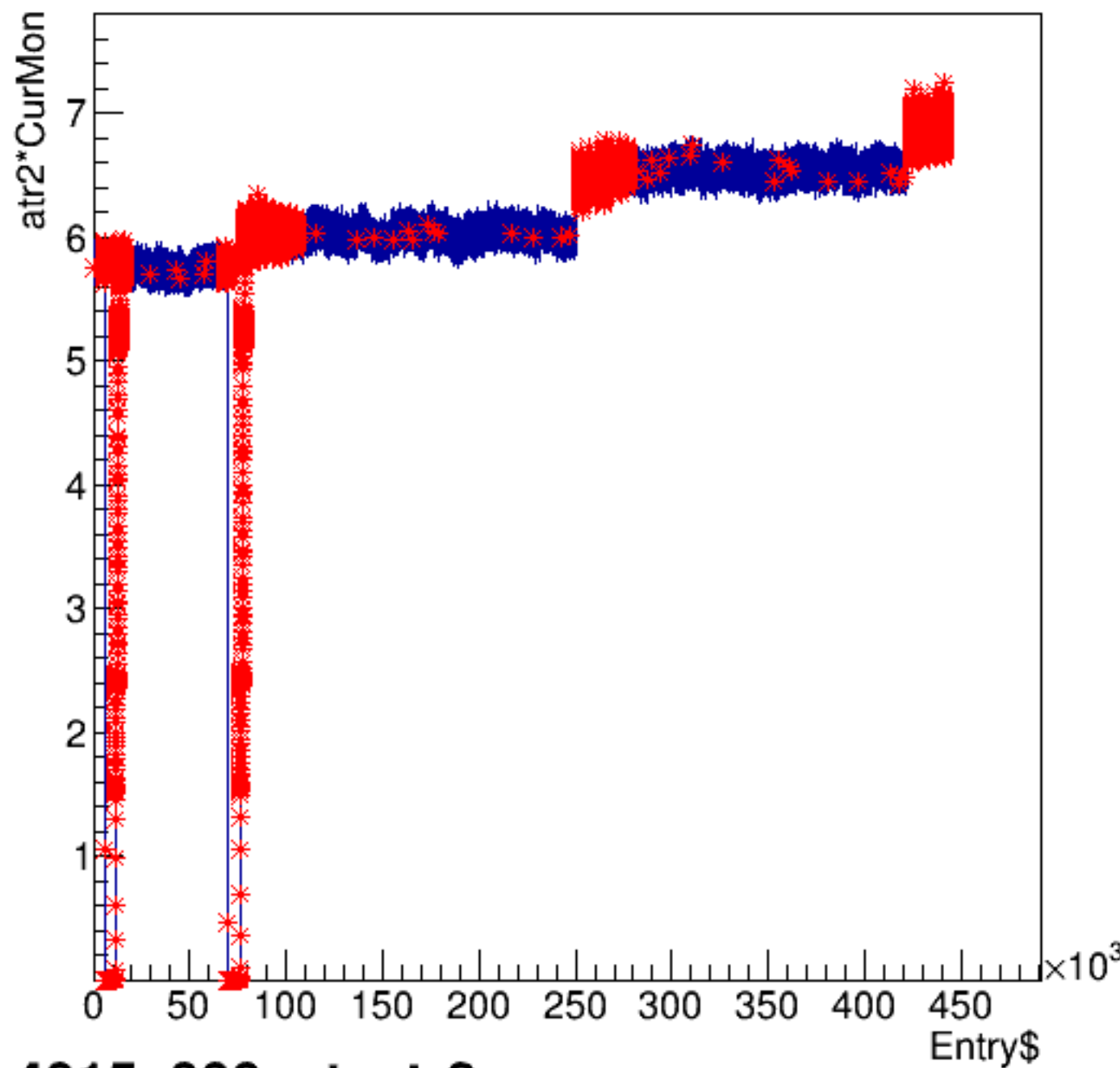
asym\_atr1/ppm:pattern\_number {ErrorFlag==0}



asym\_atr1

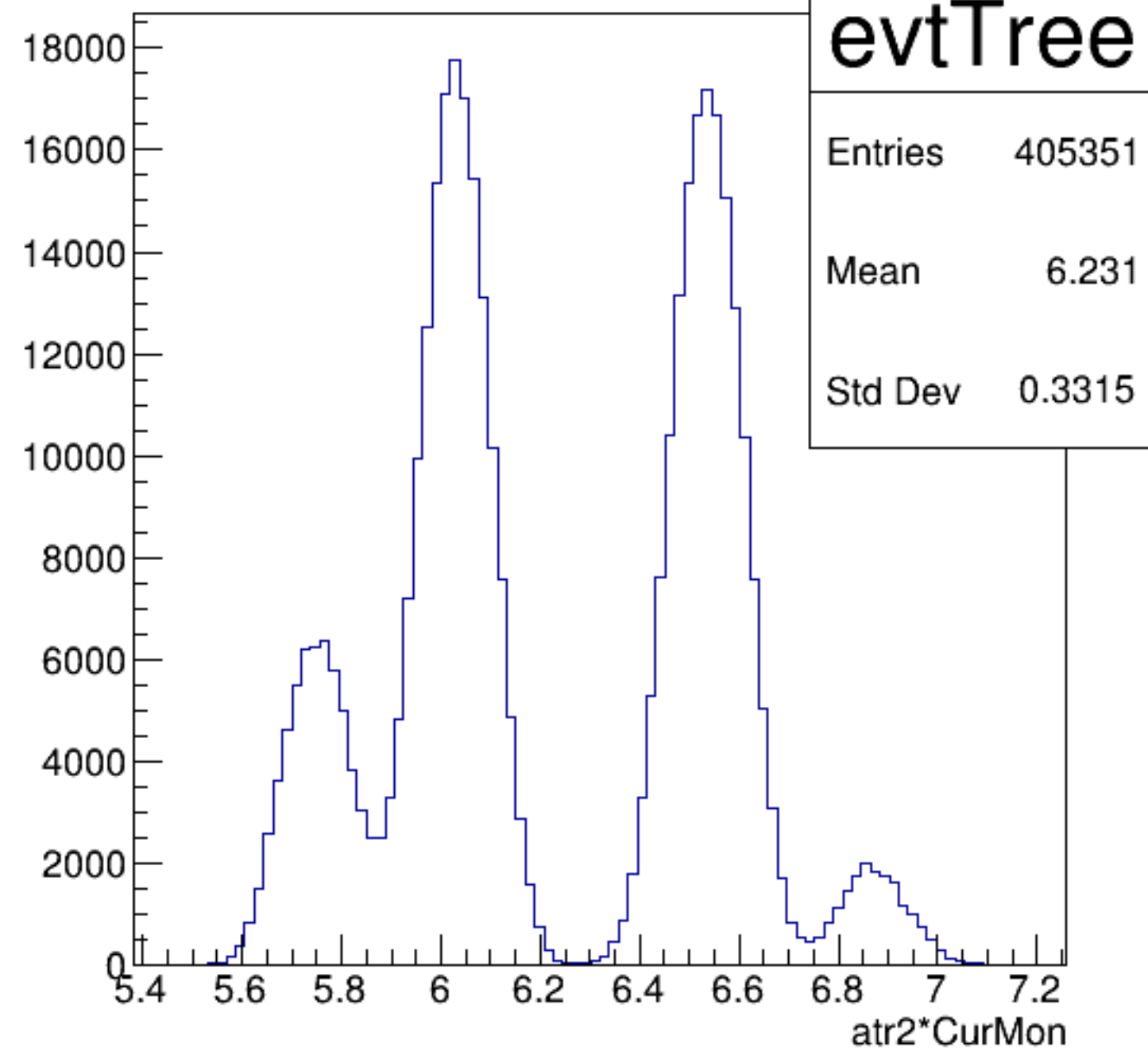


atr2\*CurMon:Entry\$



run4015\_000\_at\_atr2.png

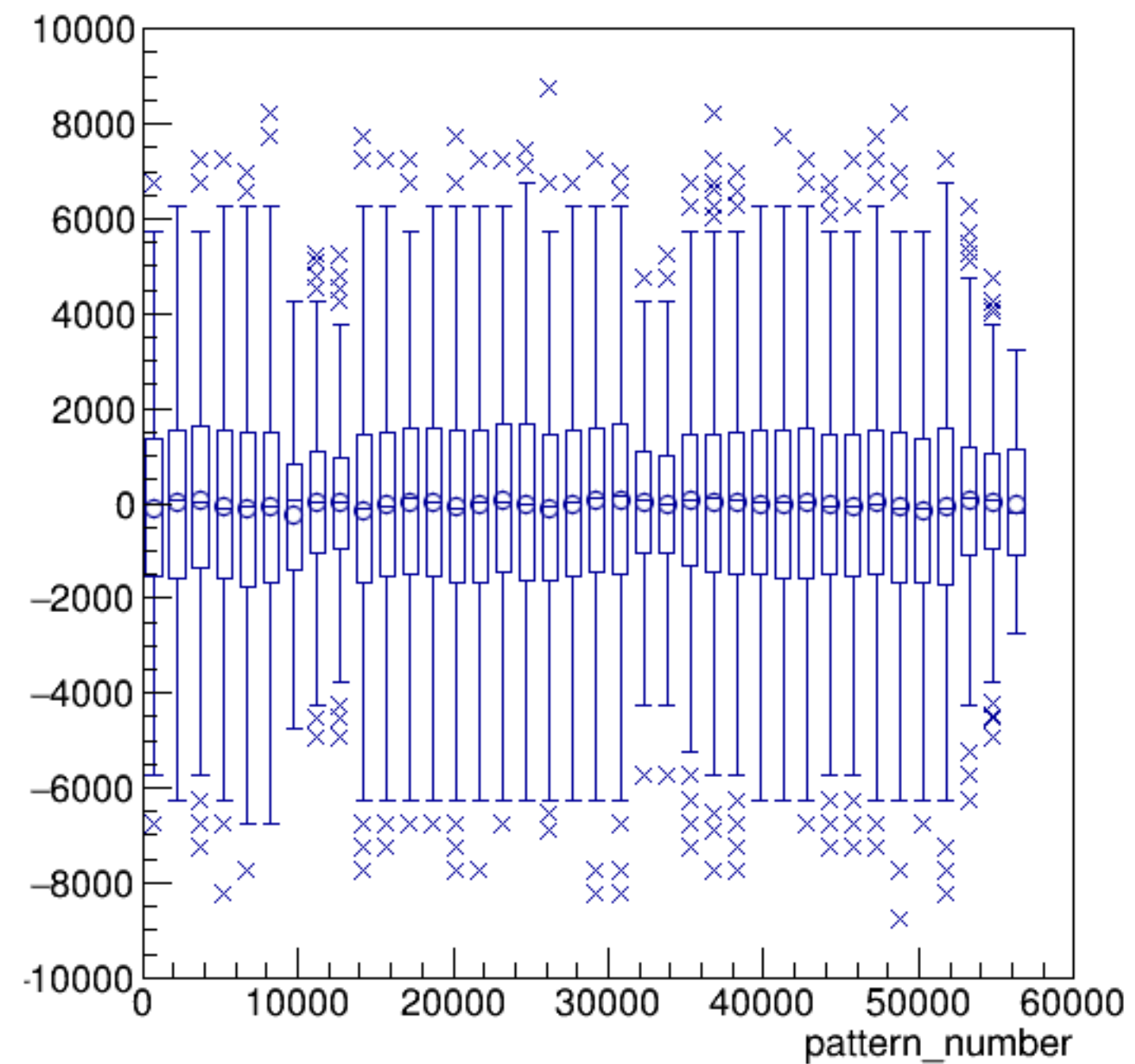
atr2\*CurMon {ErrorFlag==0}



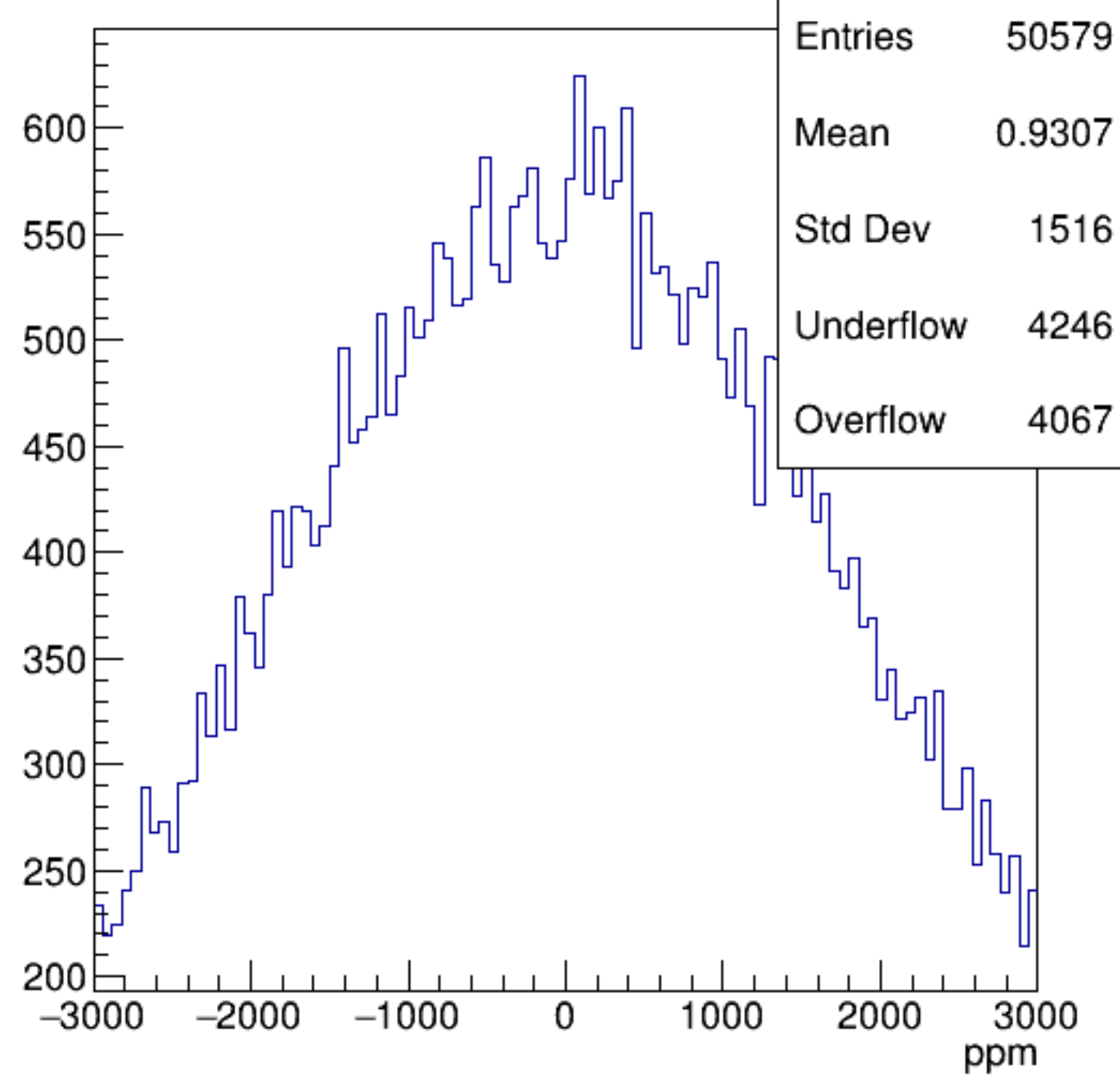
evtTree

Entries	405351
Mean	6.231
Std Dev	0.3315

asym\_atr2/ppm:pattern\_number {ErrorFlag==0}

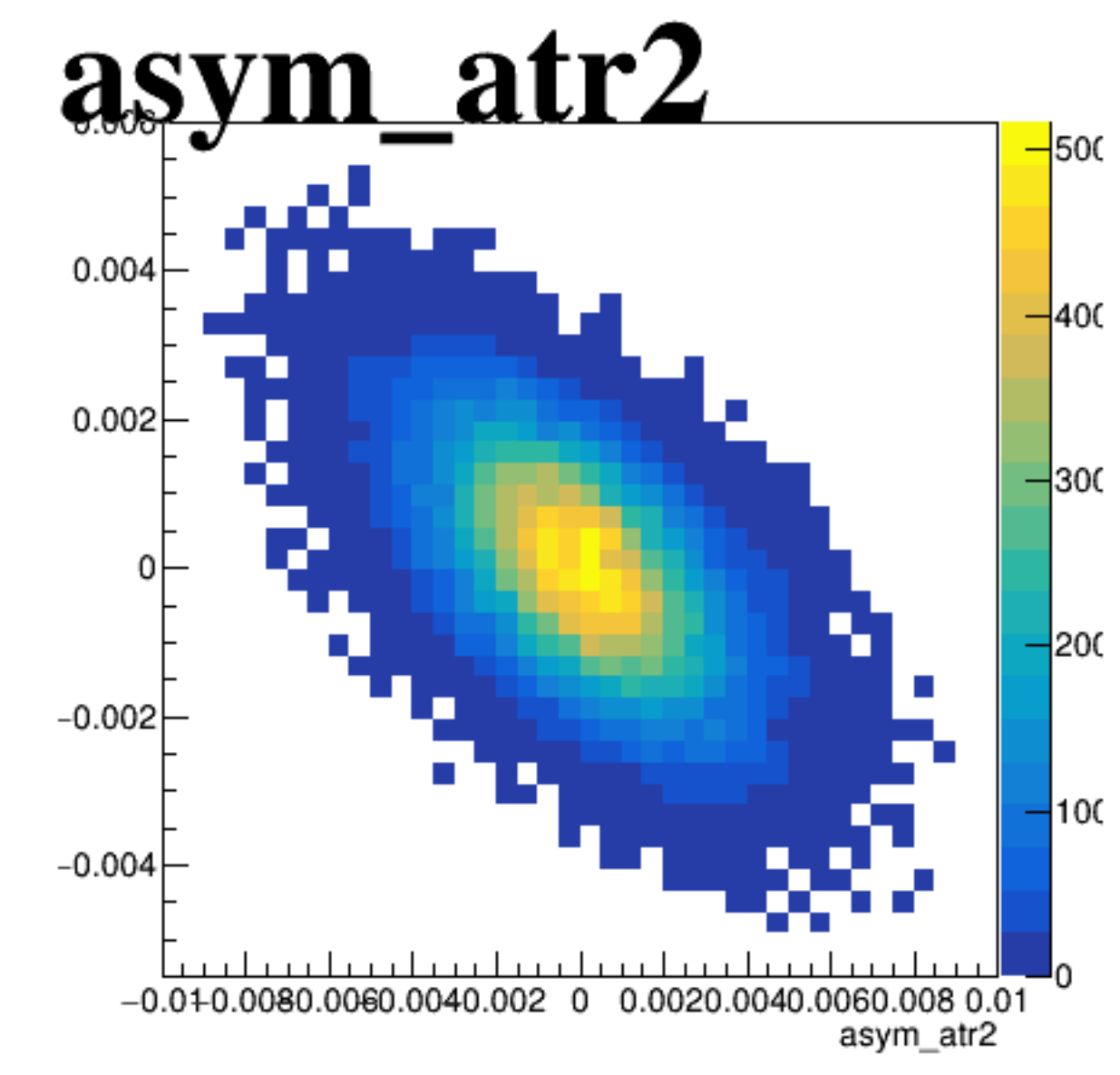
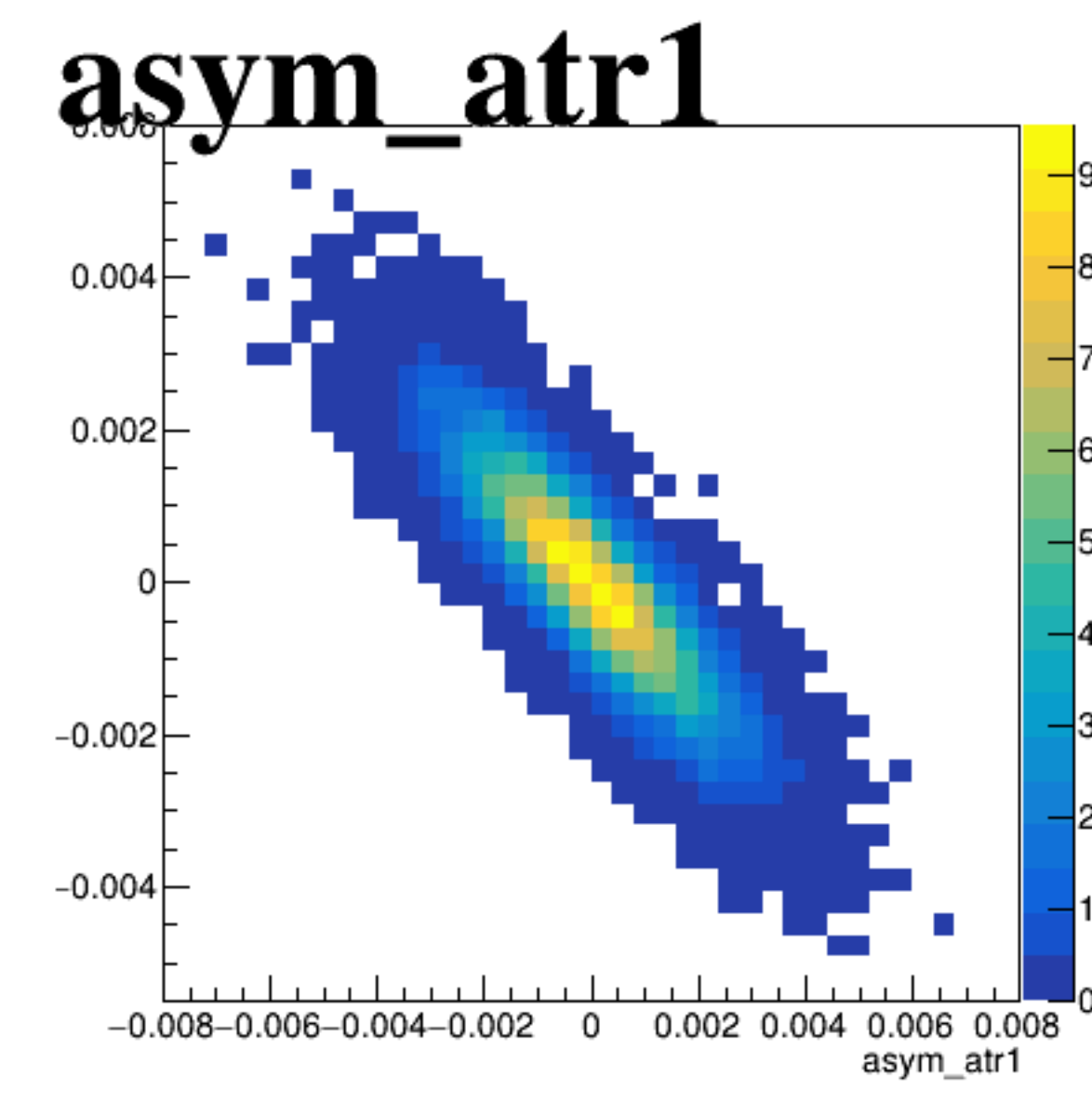
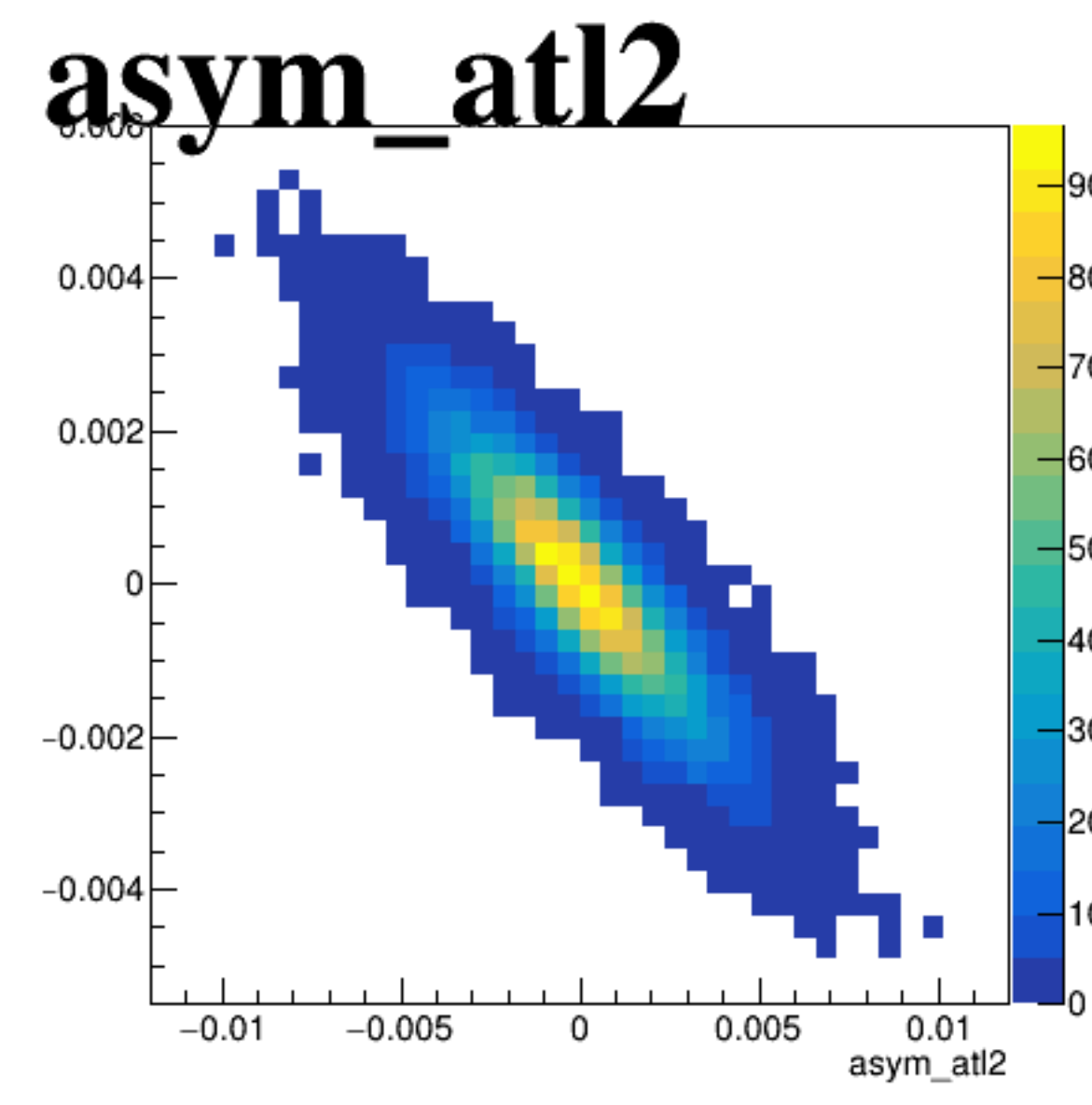
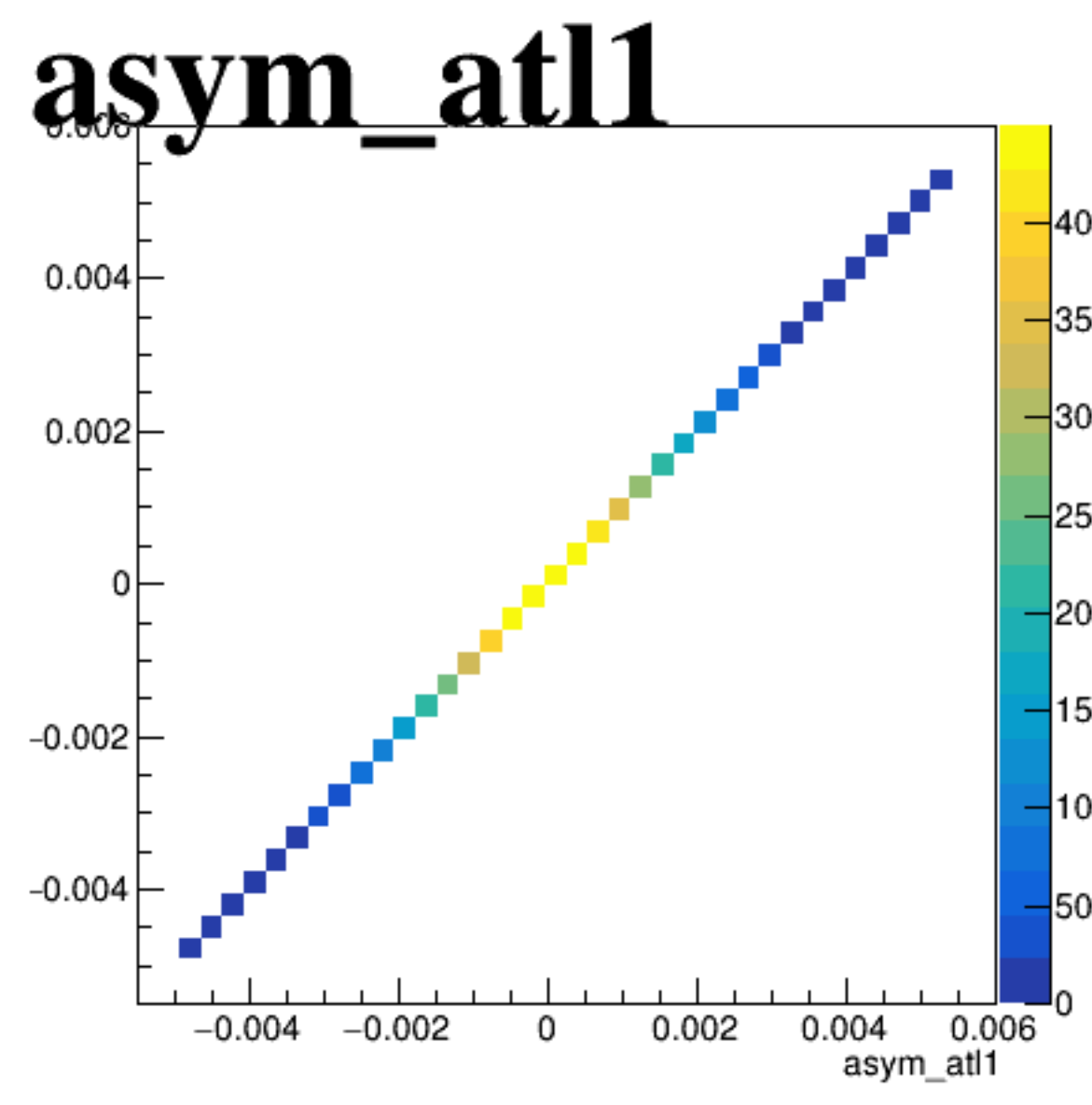


asym\_atr2

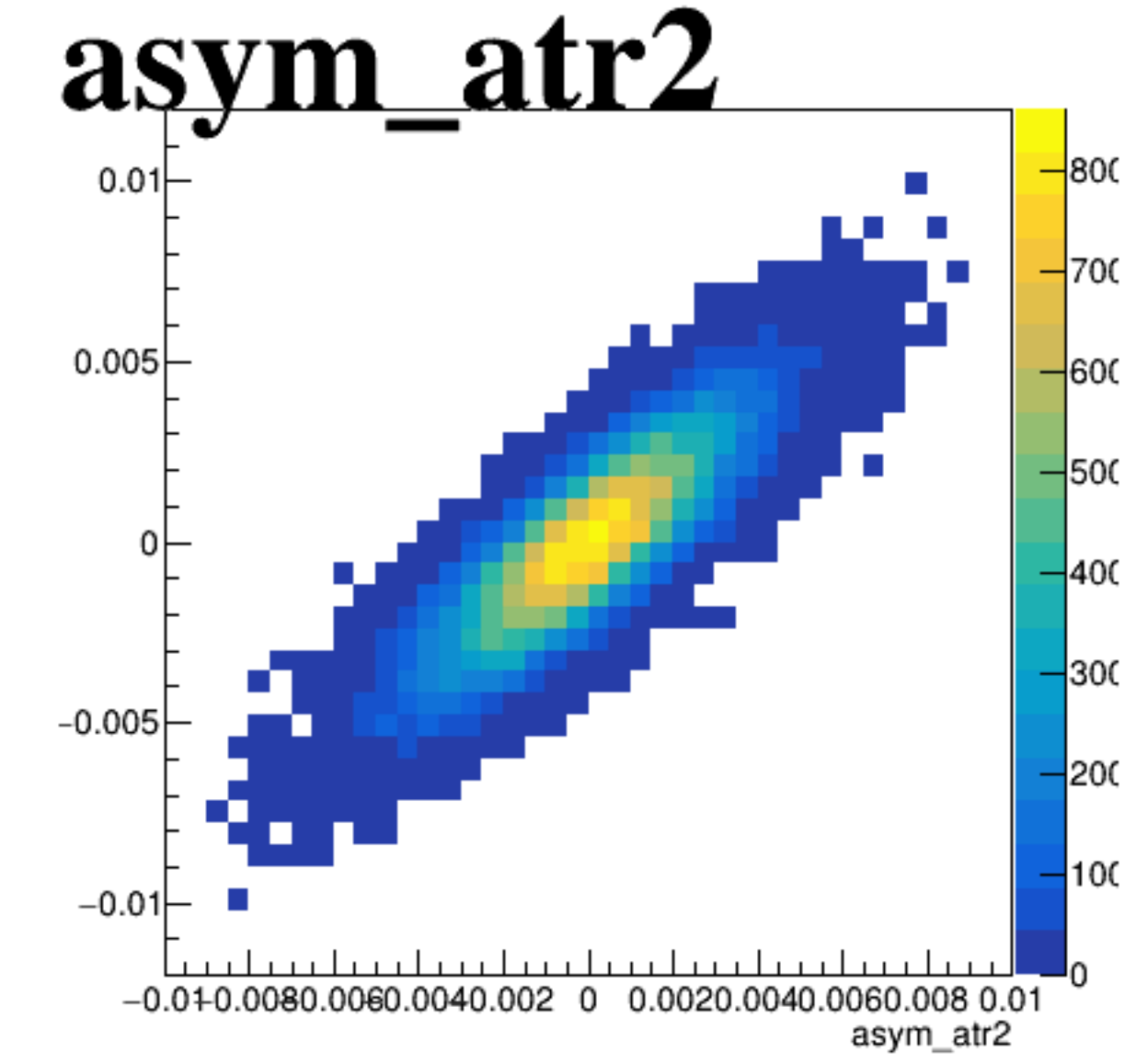
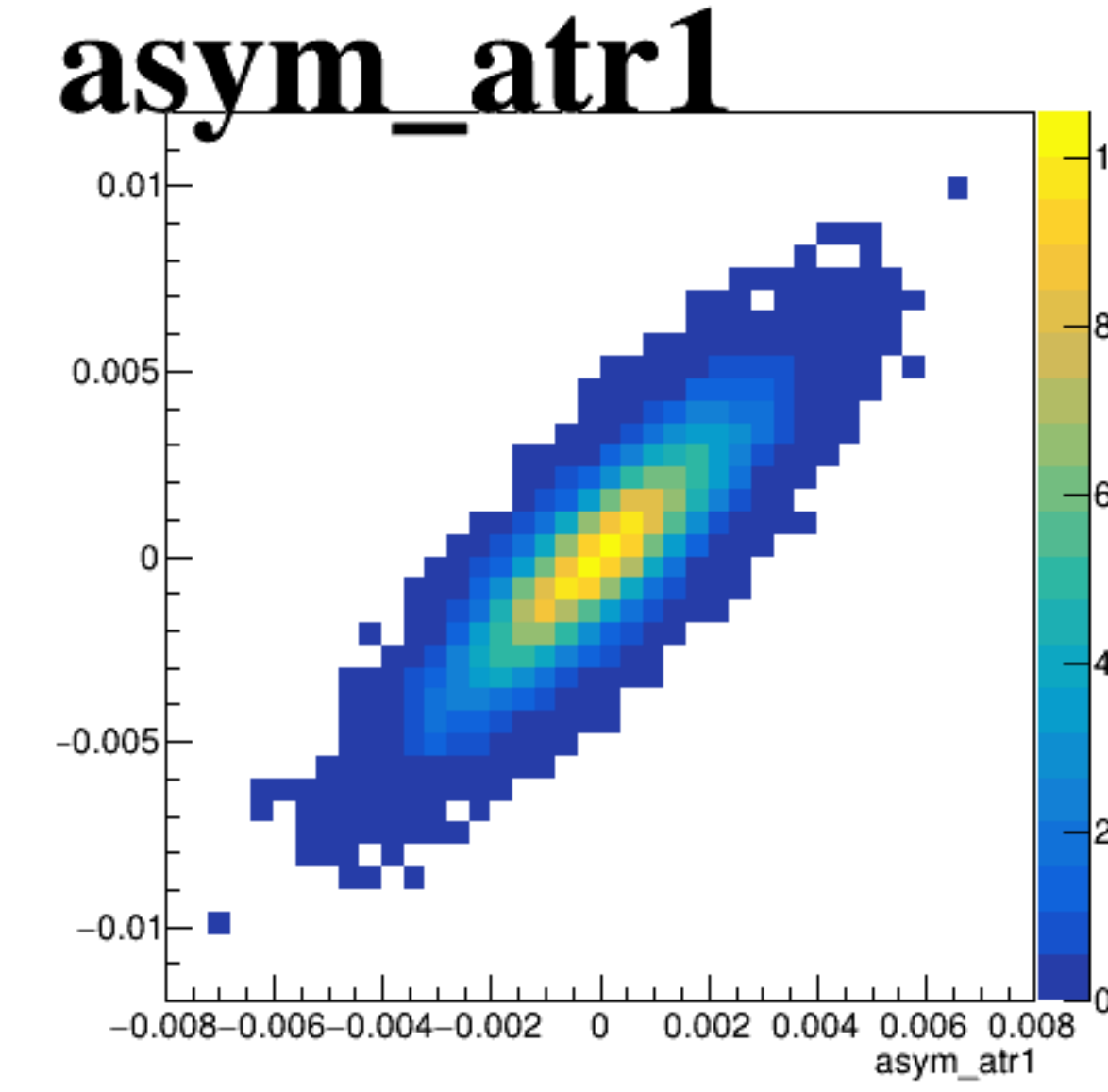
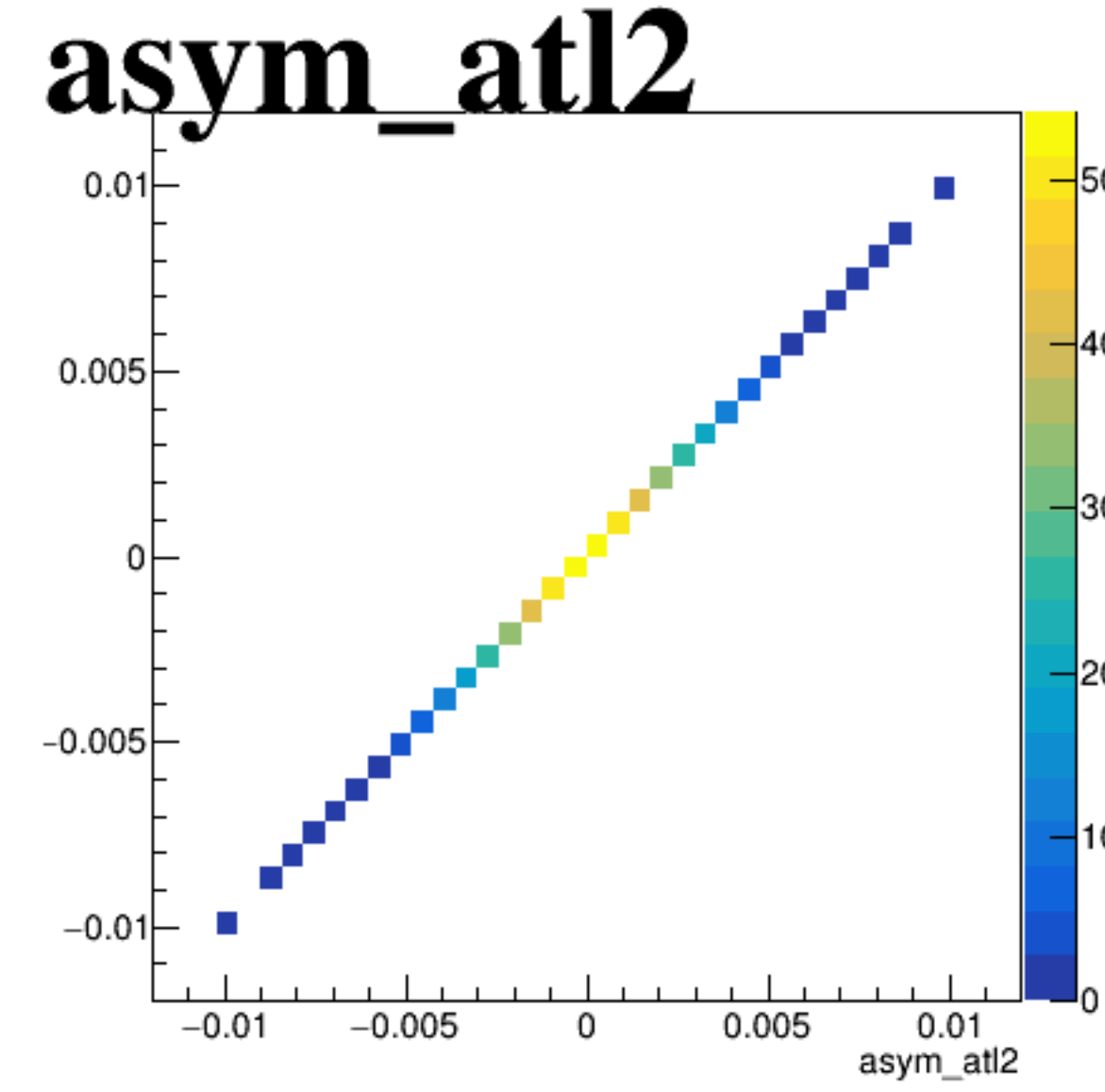
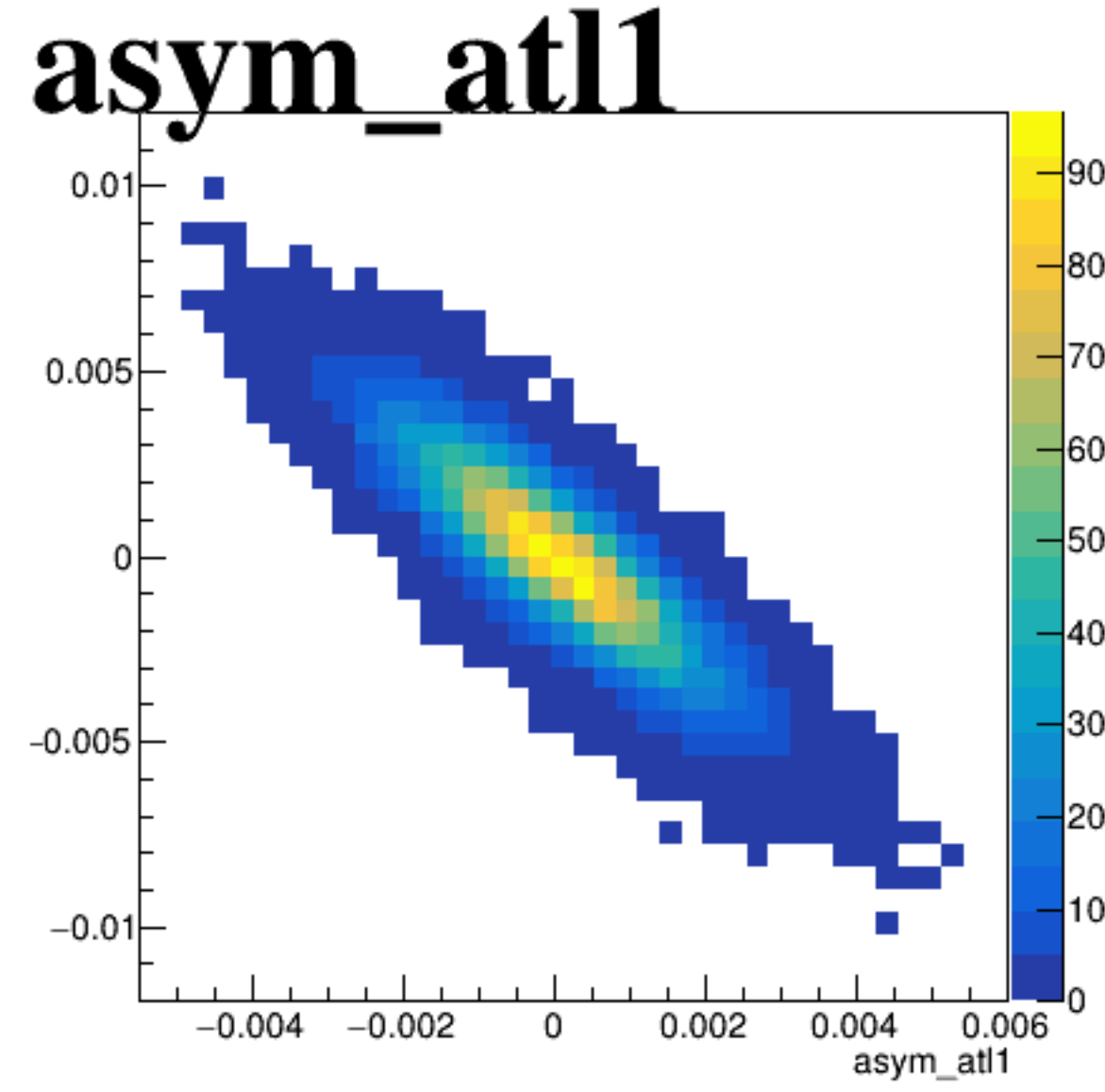


Entries	50579
Mean	0.9307
Std Dev	1516
Underflow	4246
Overflow	4067

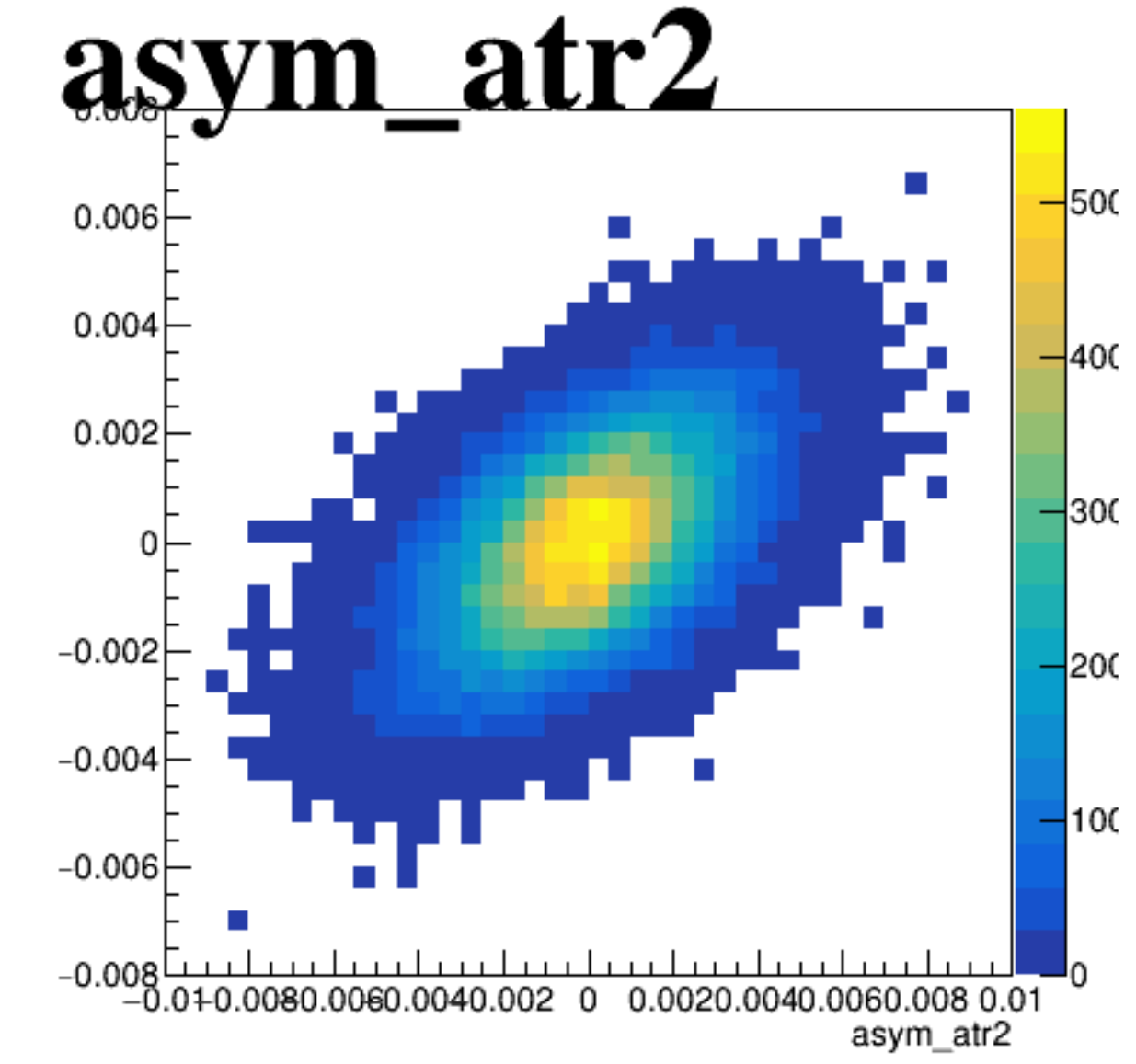
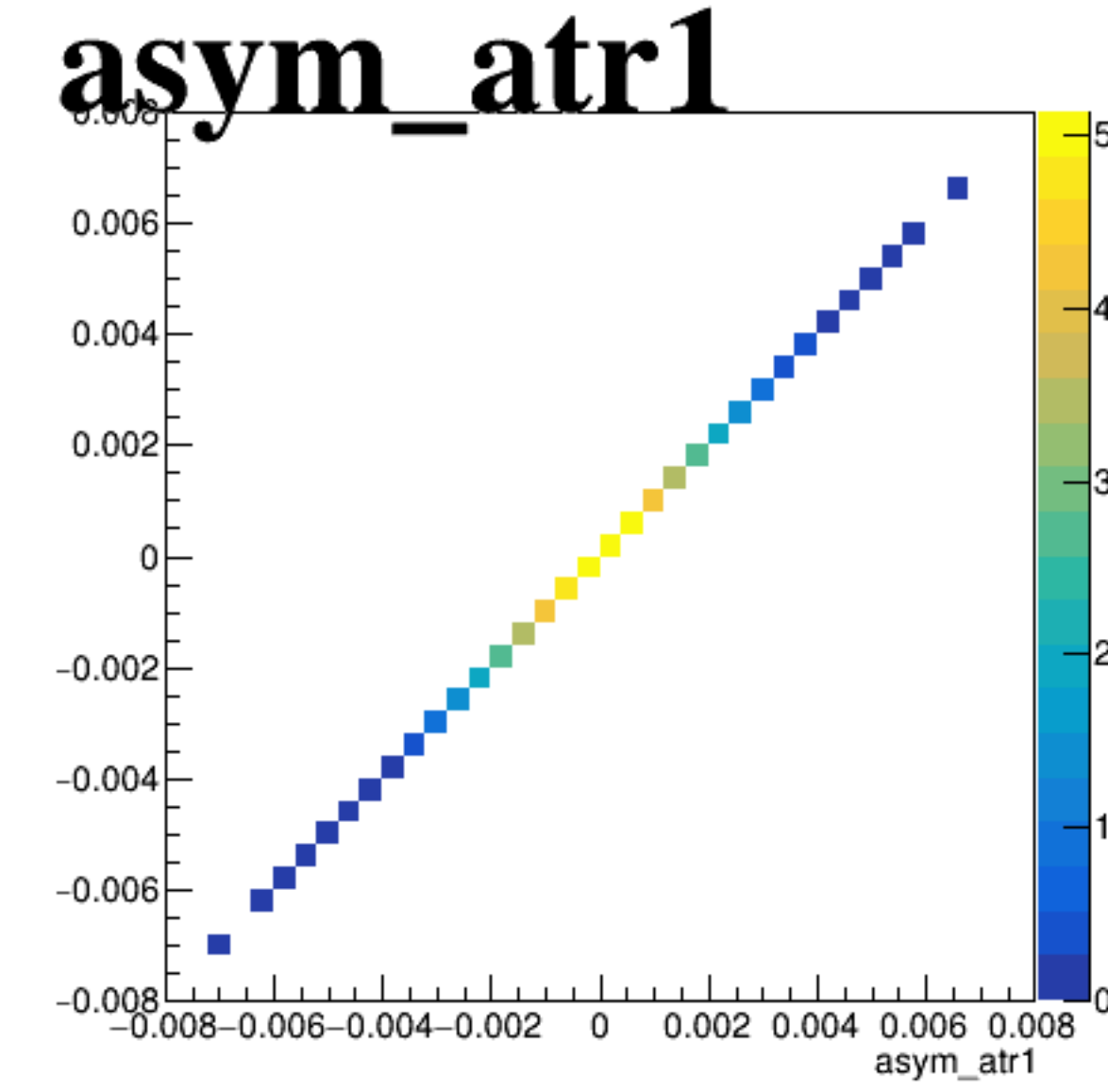
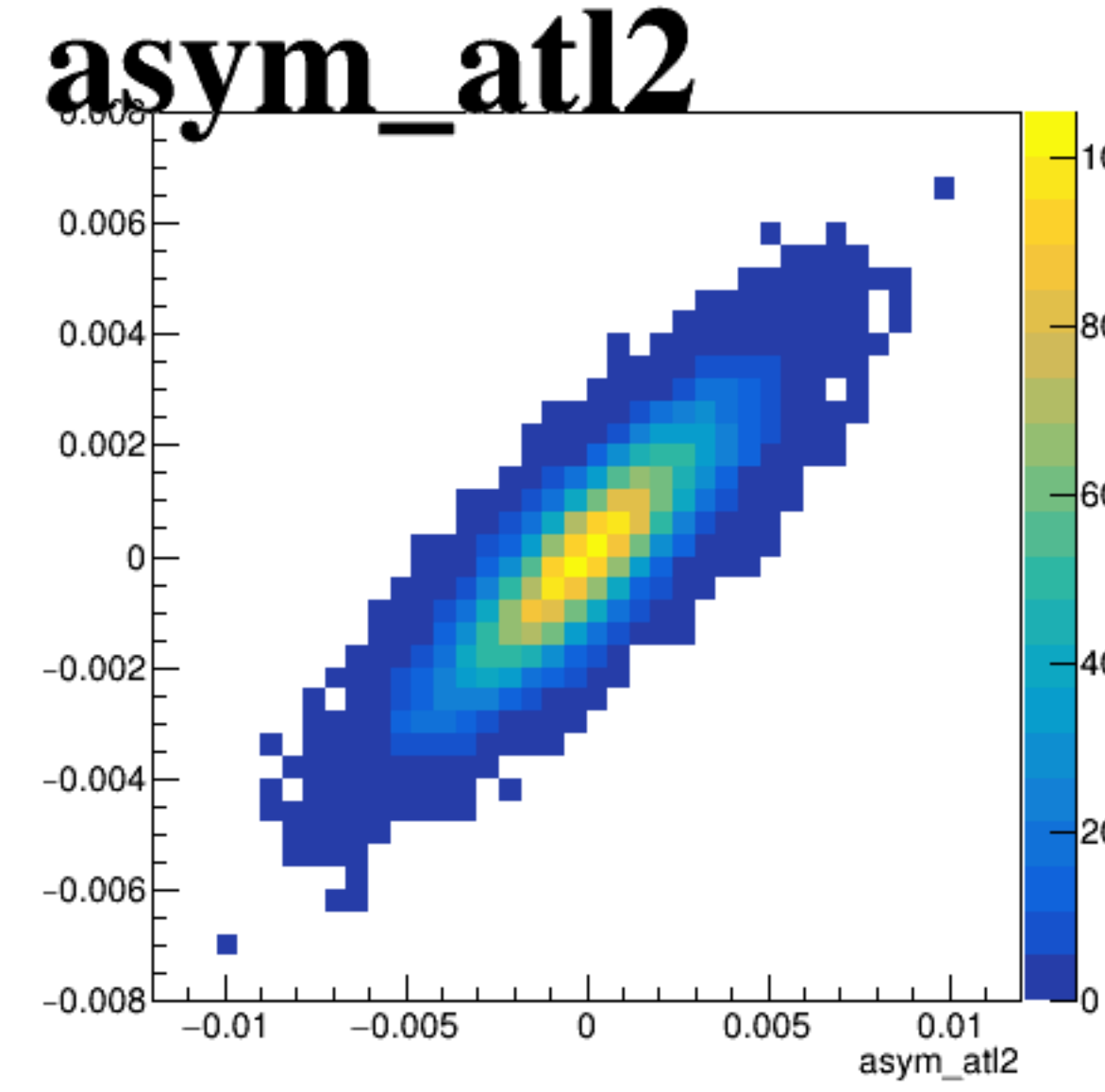
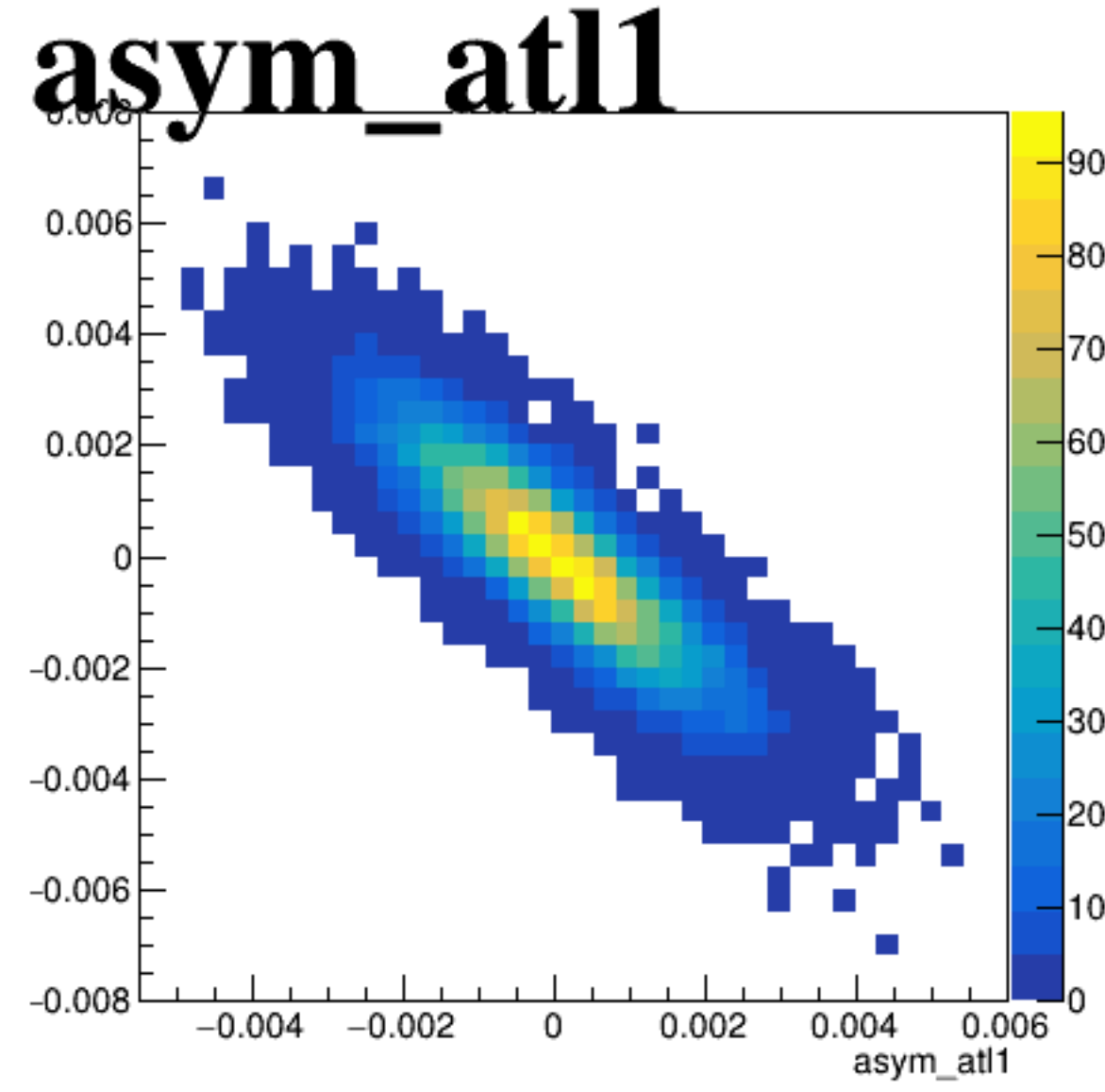
**asym\_atl1**



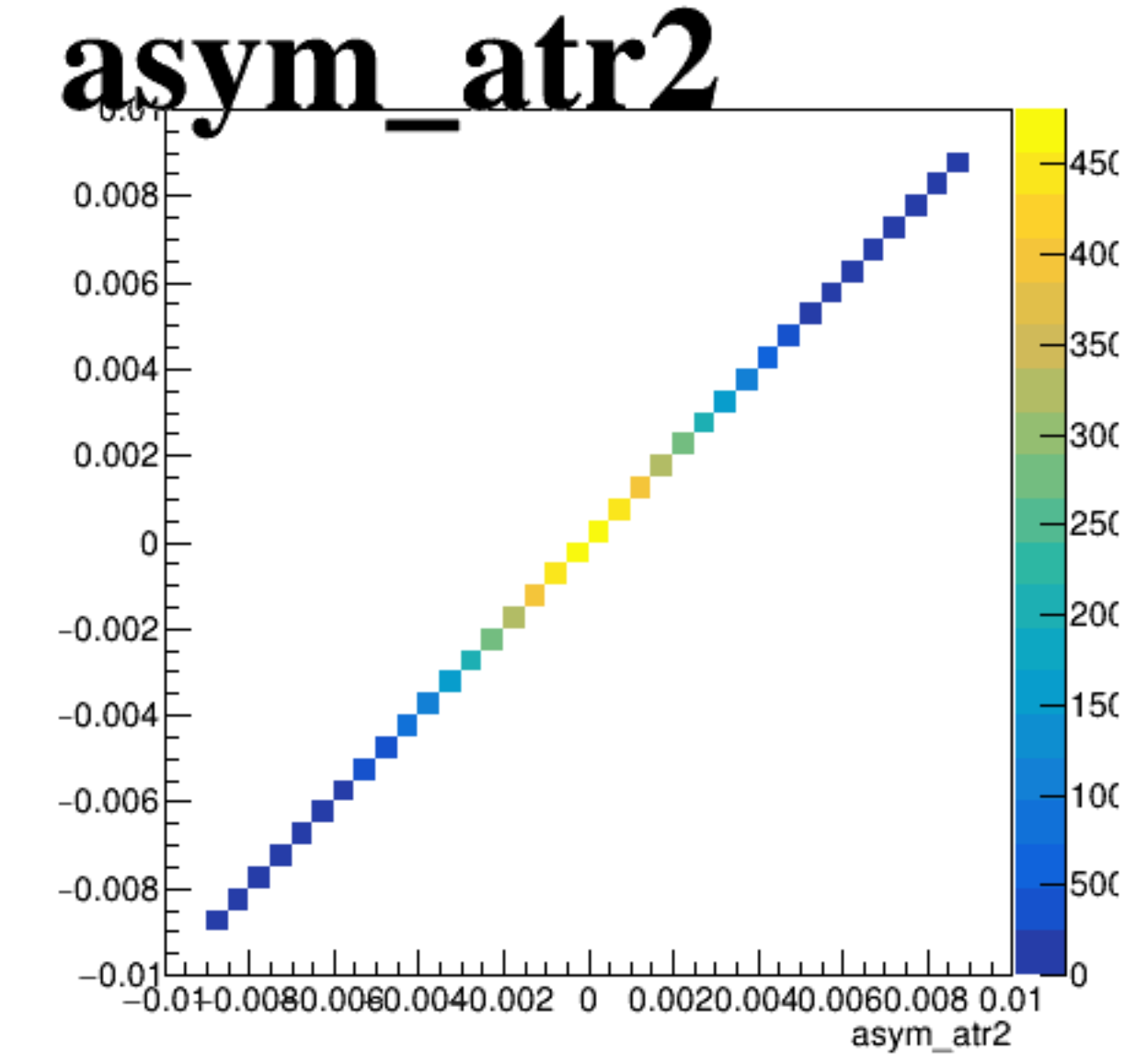
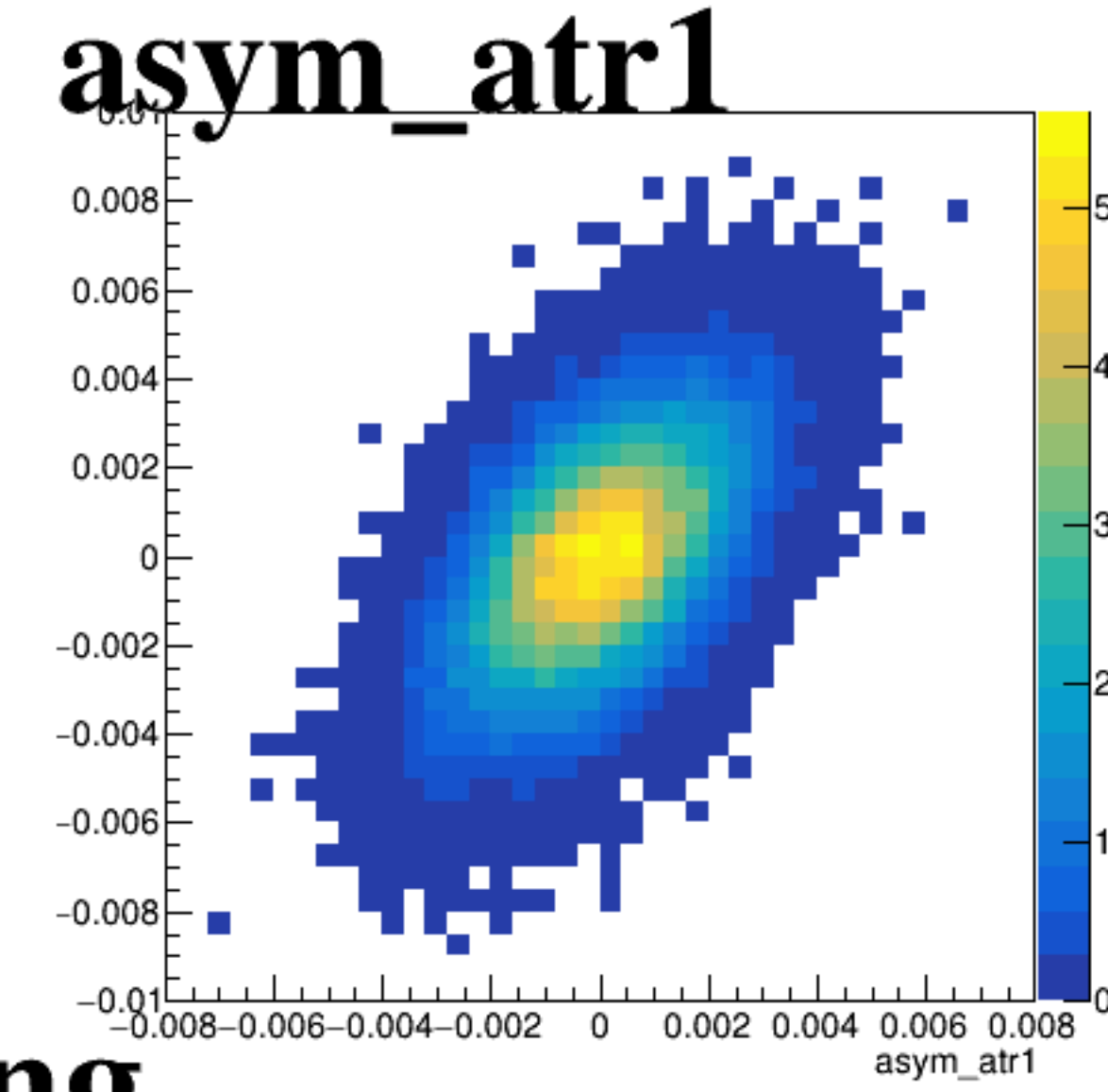
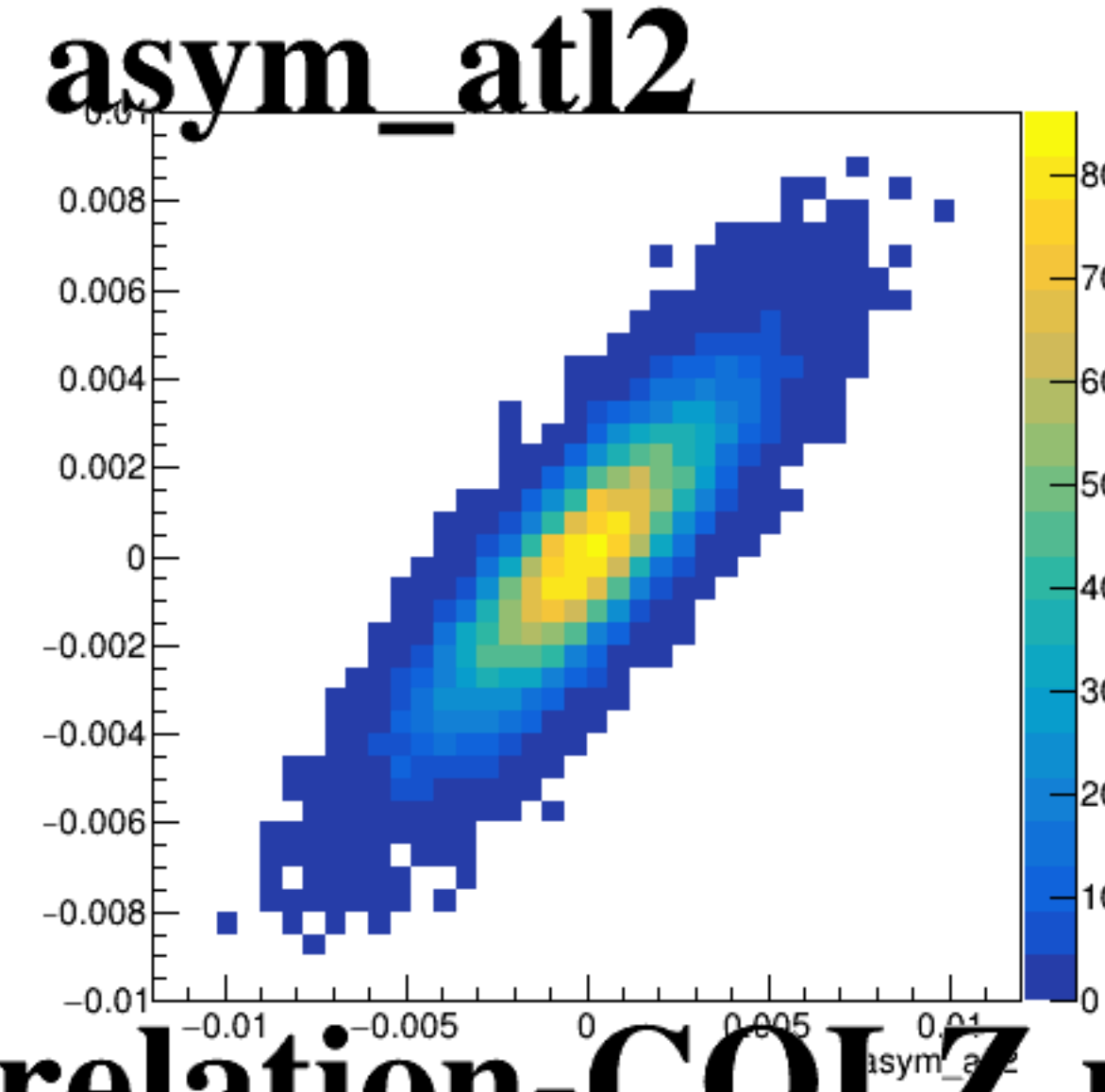
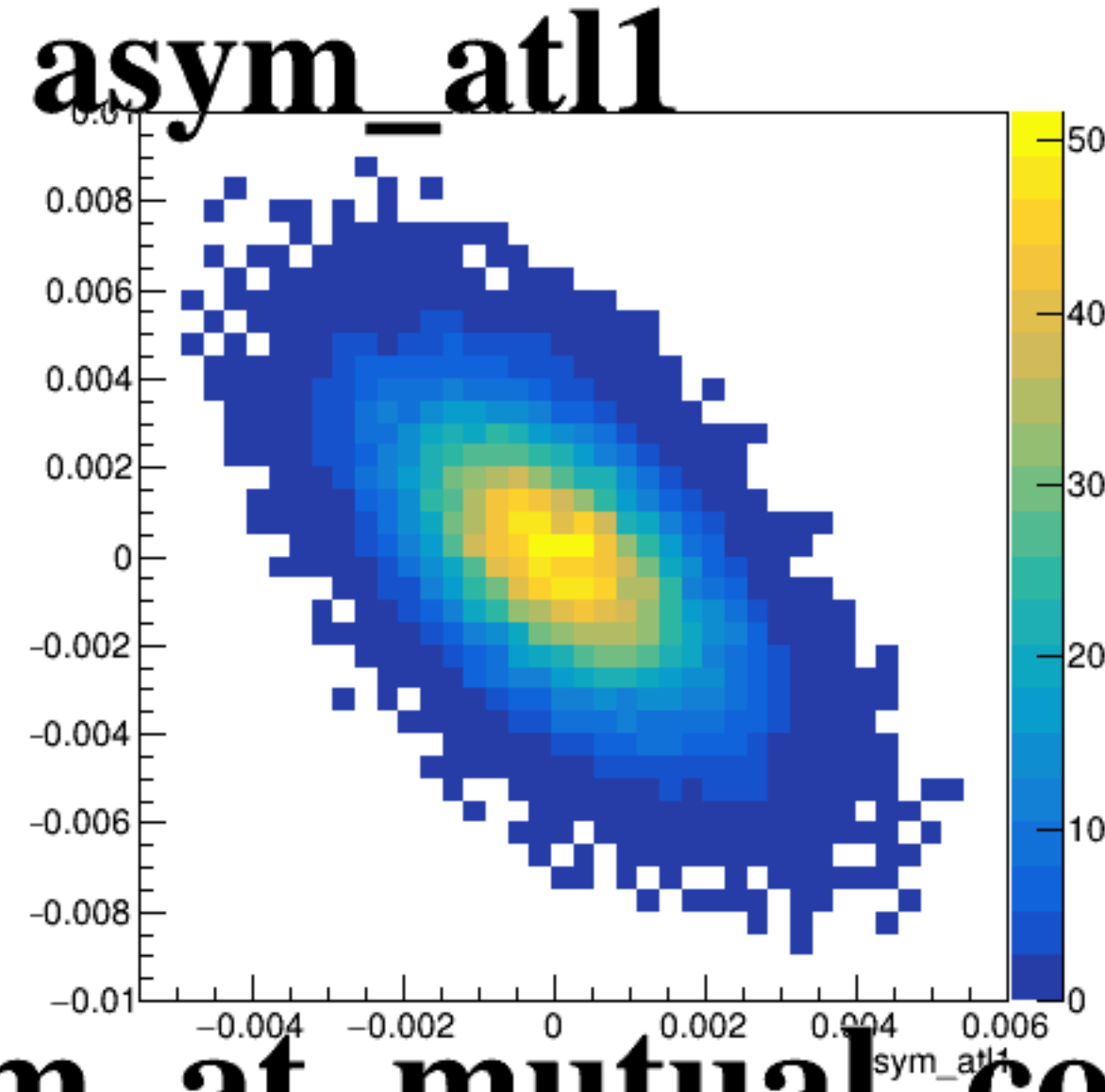
**asym\_atl2**



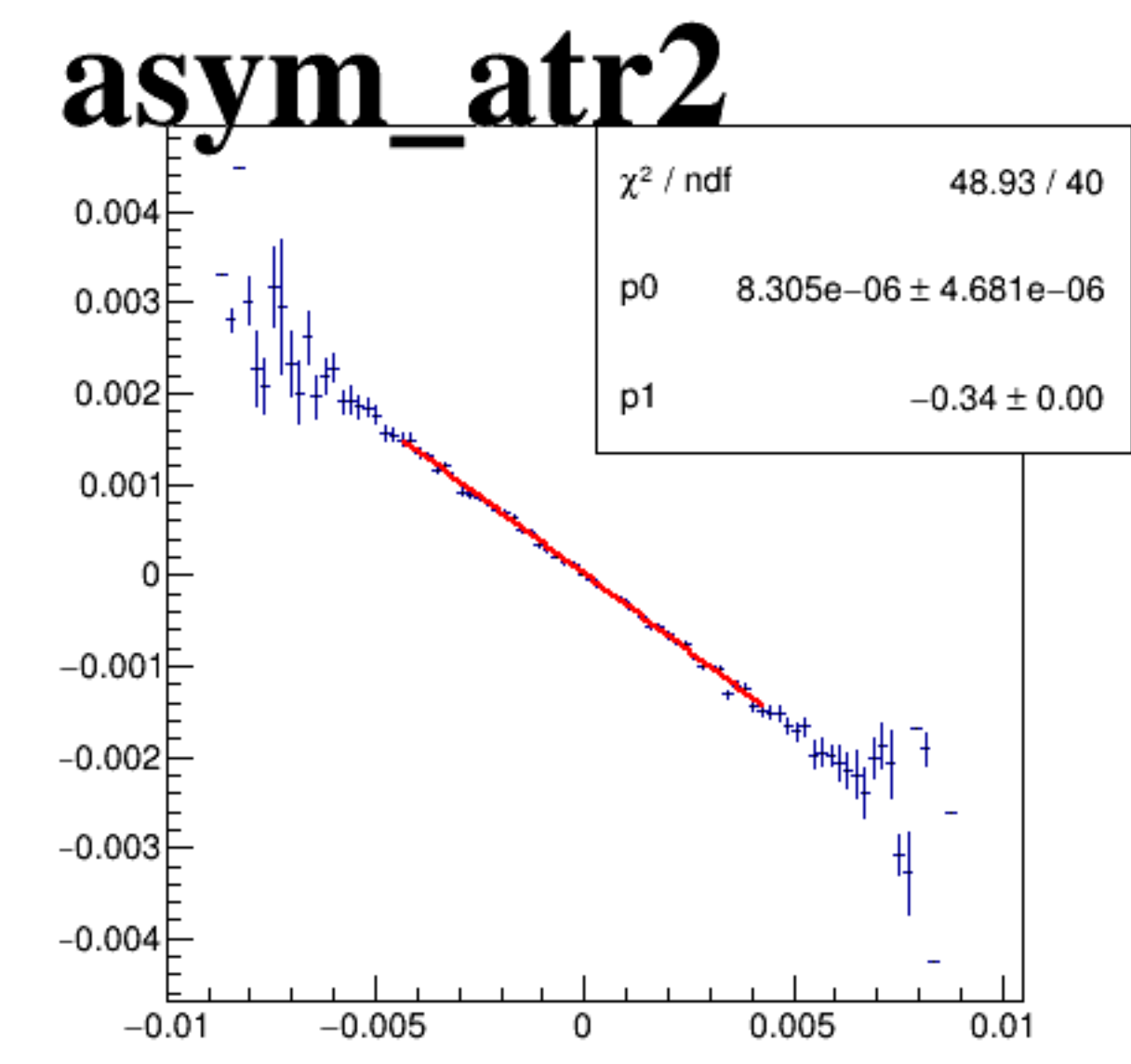
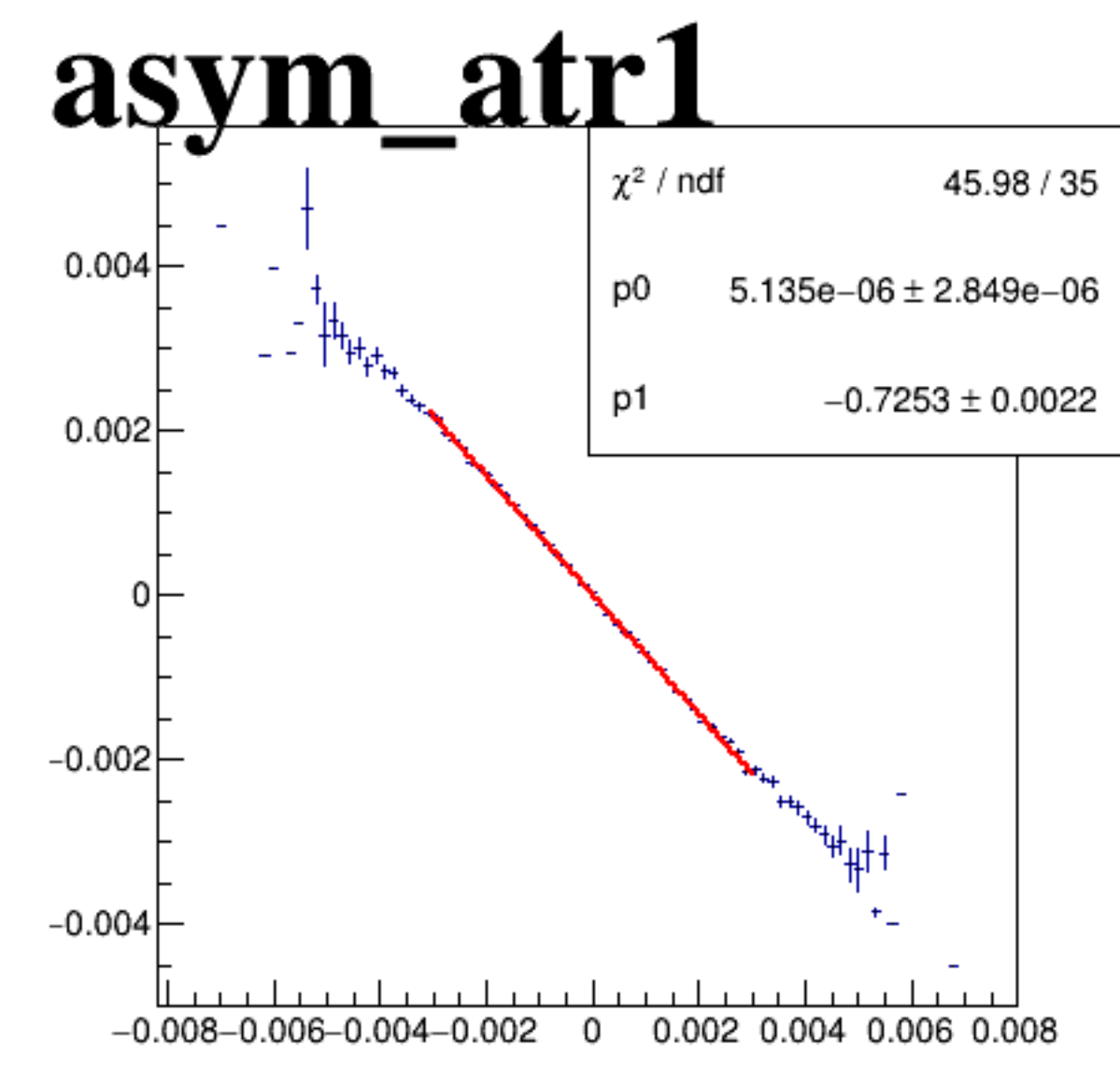
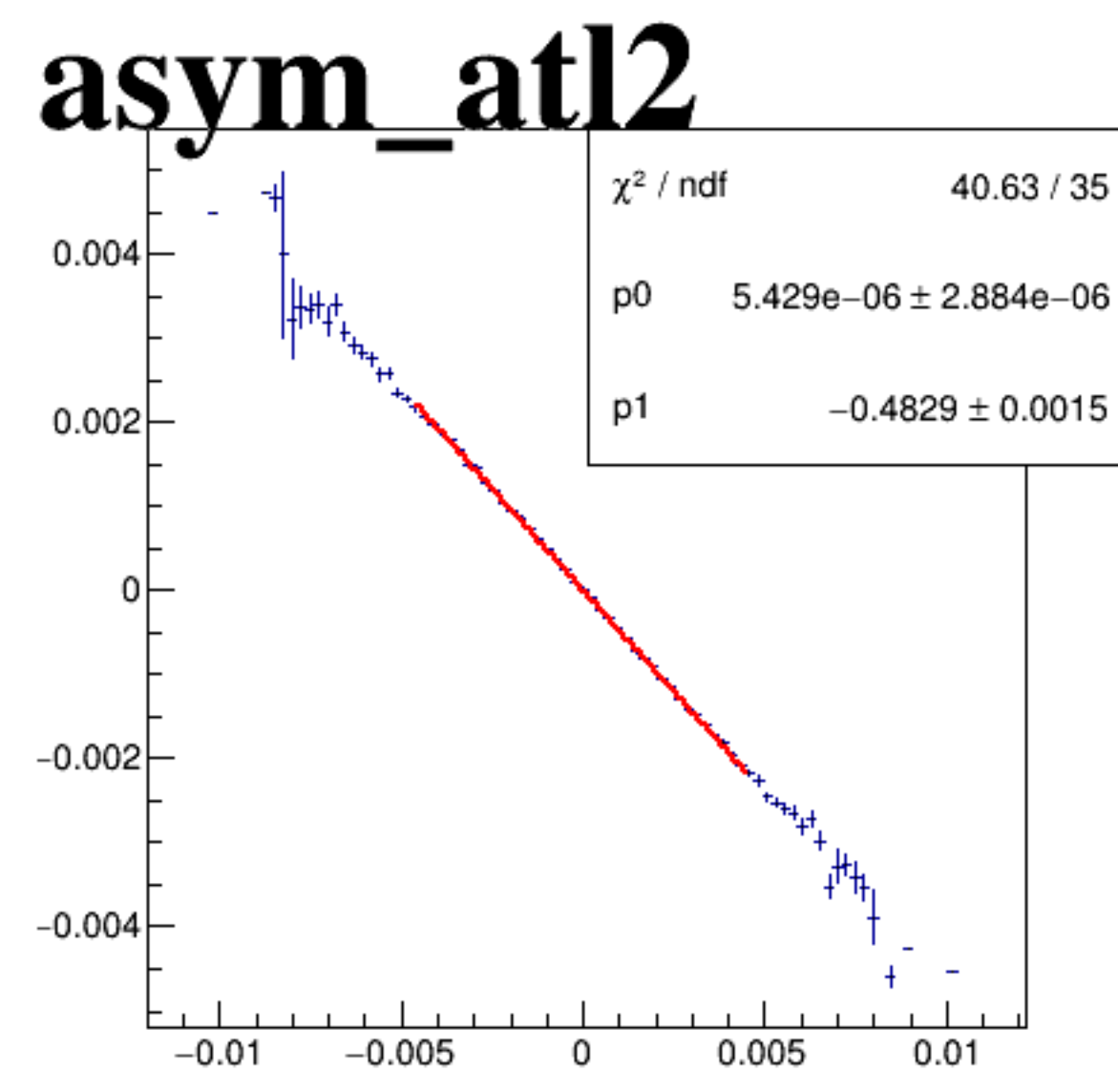
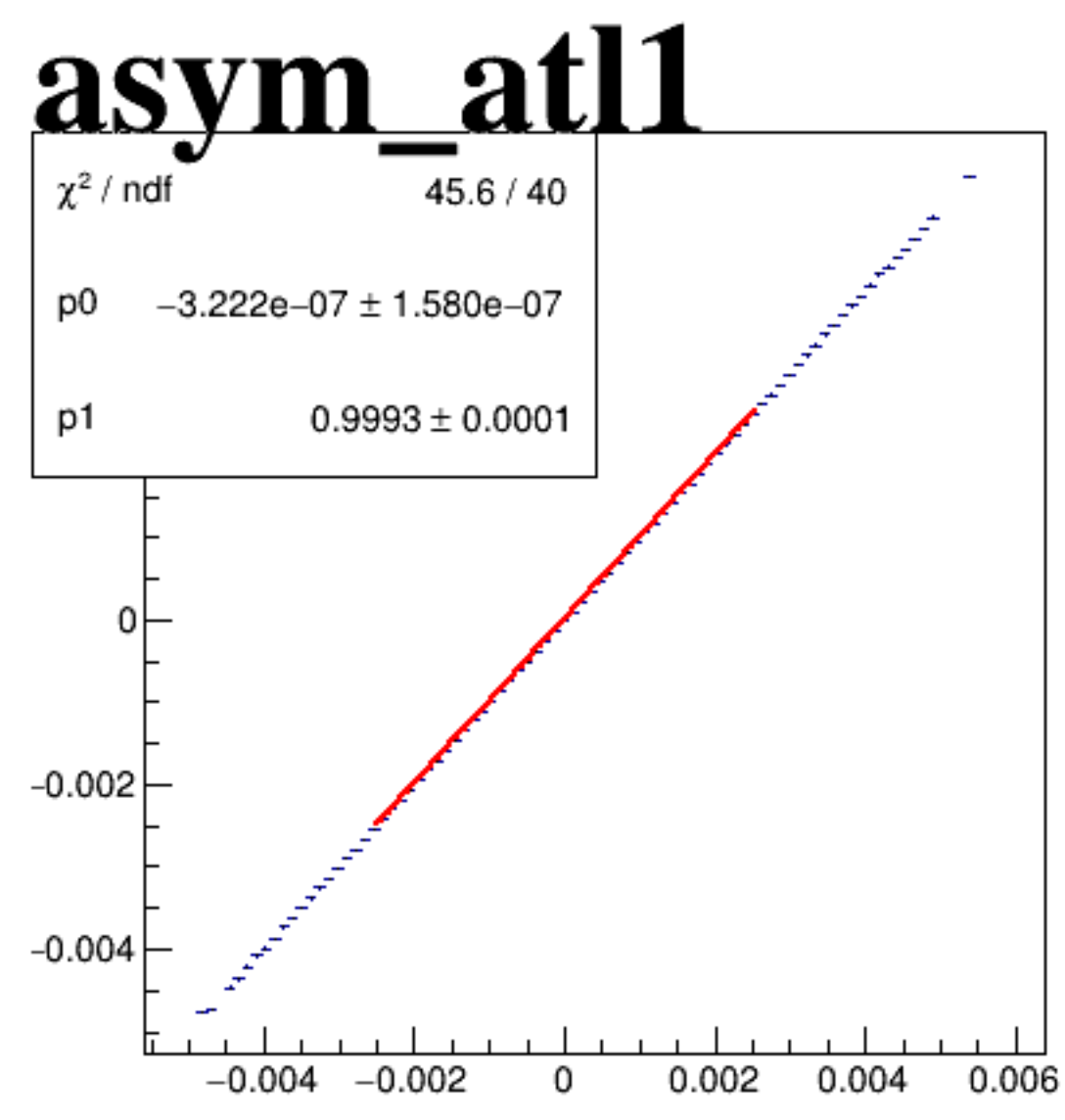
**asym\_atr1**



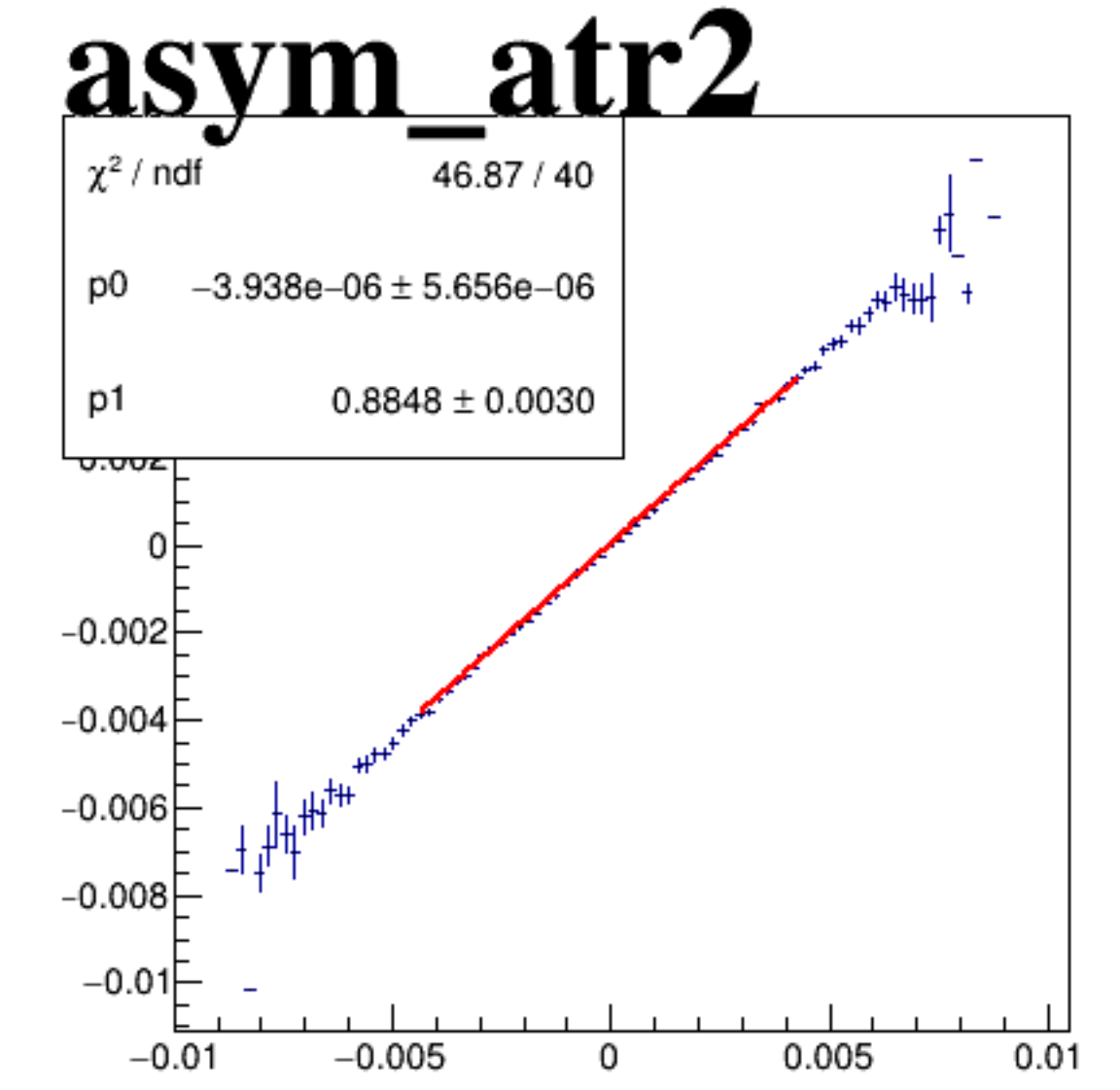
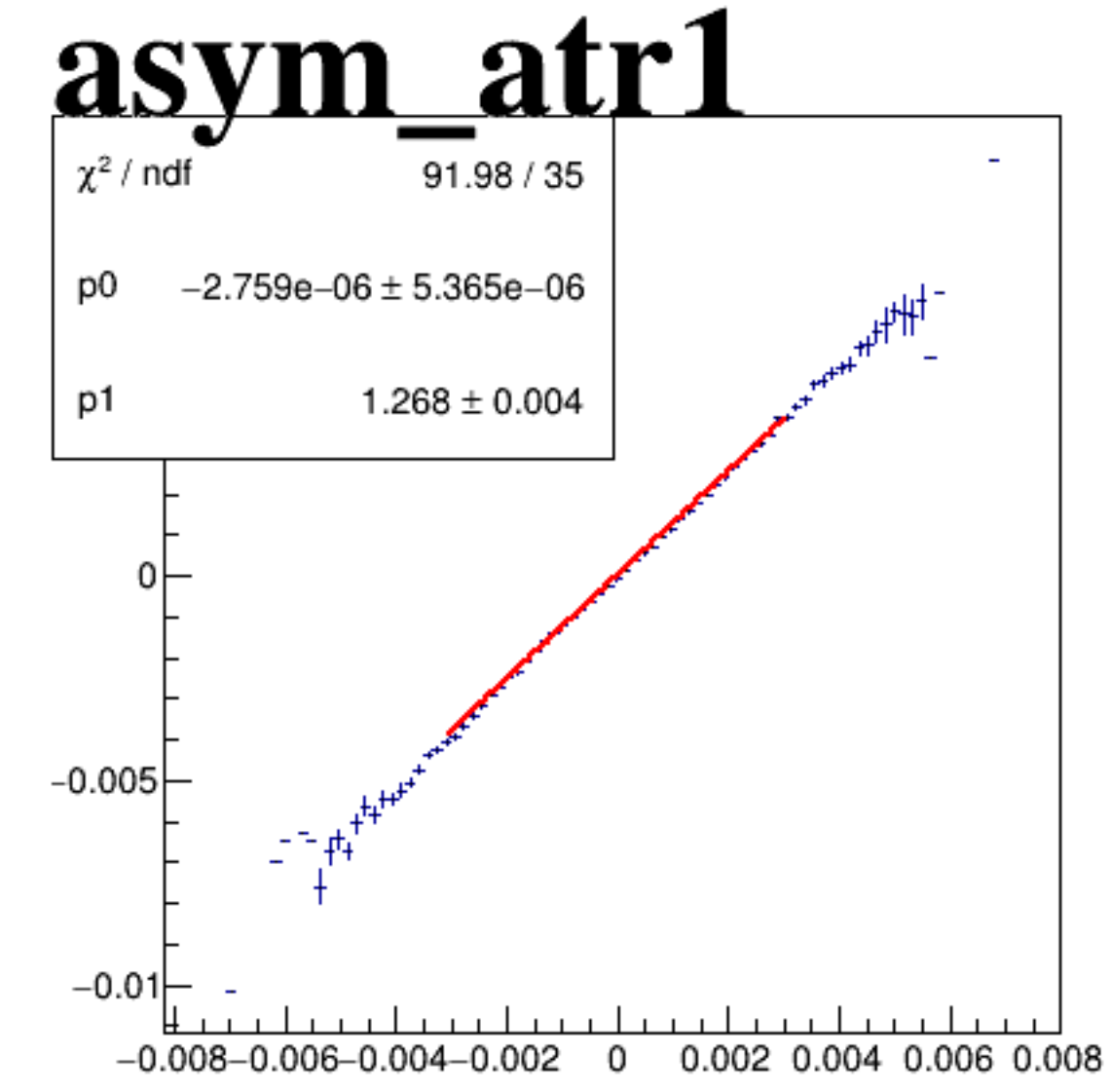
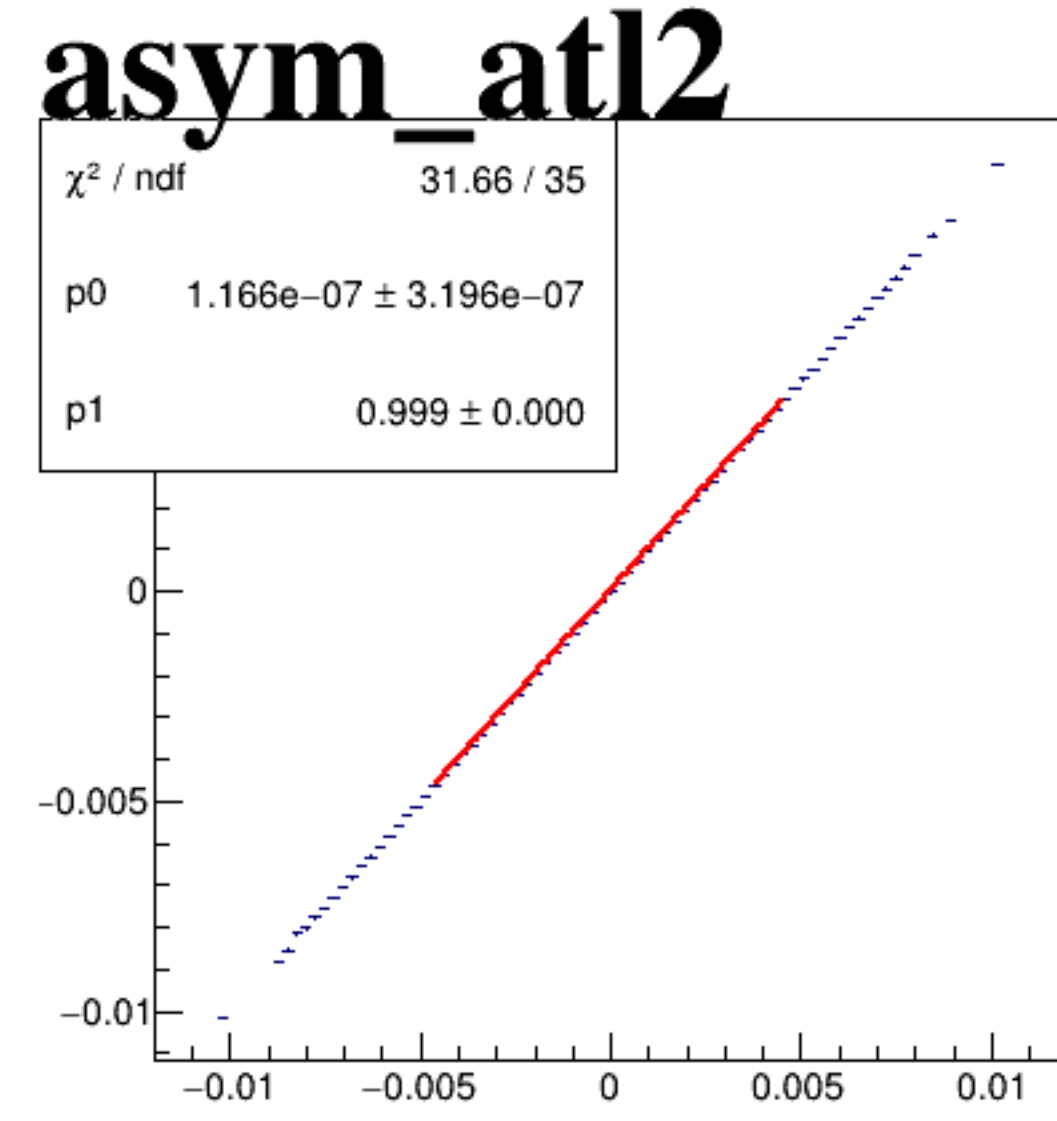
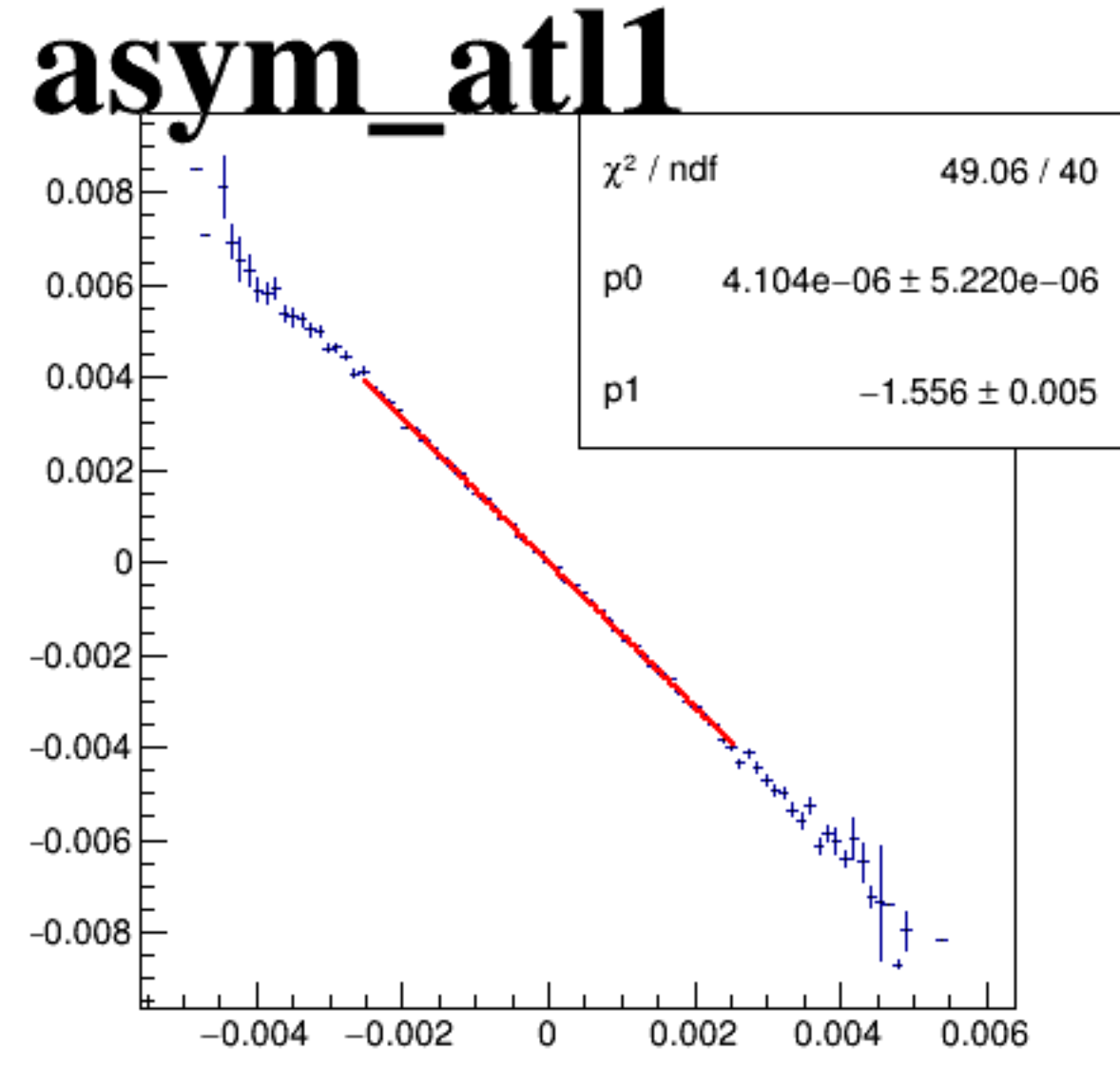
**asym\_atr2**



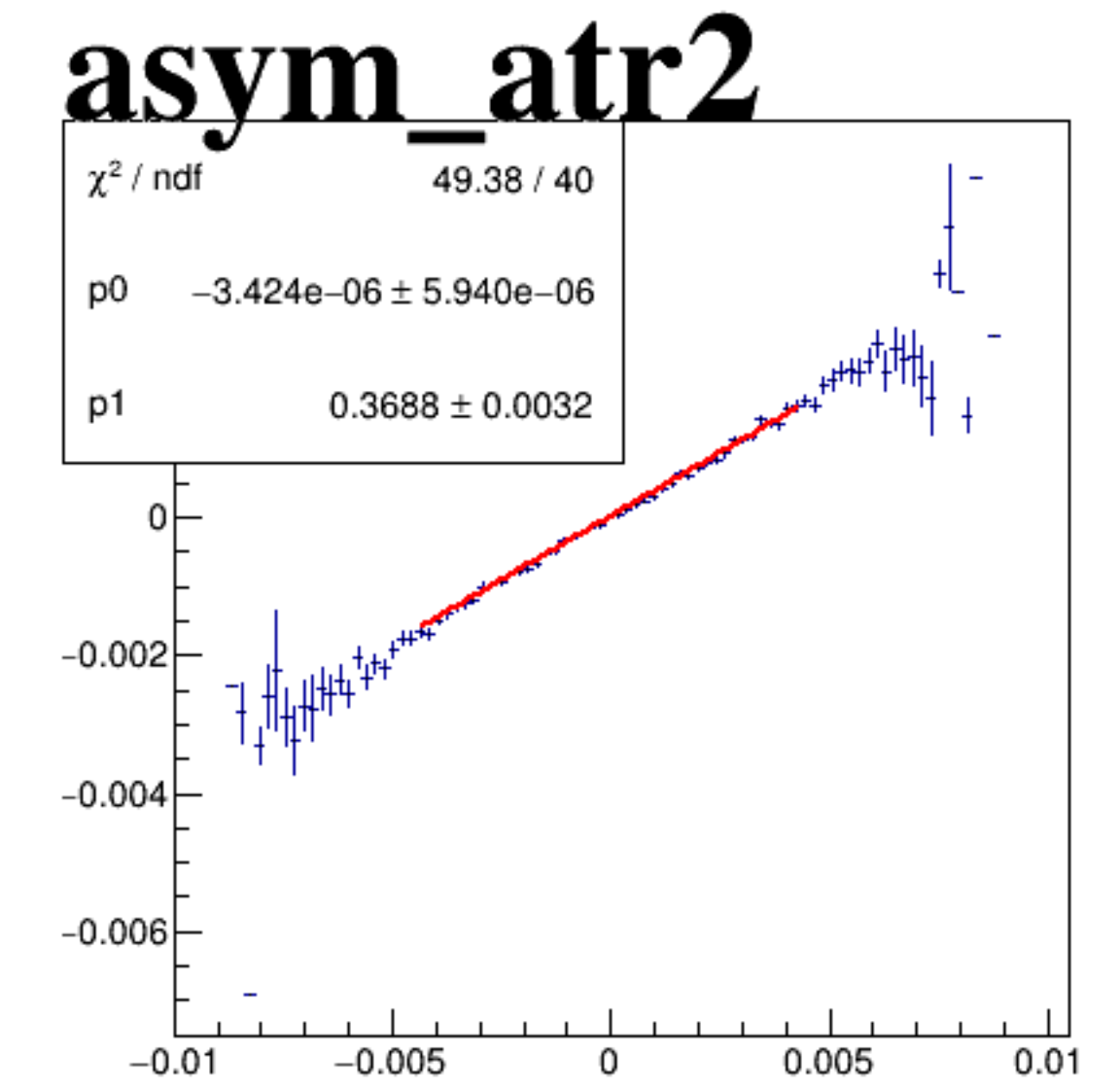
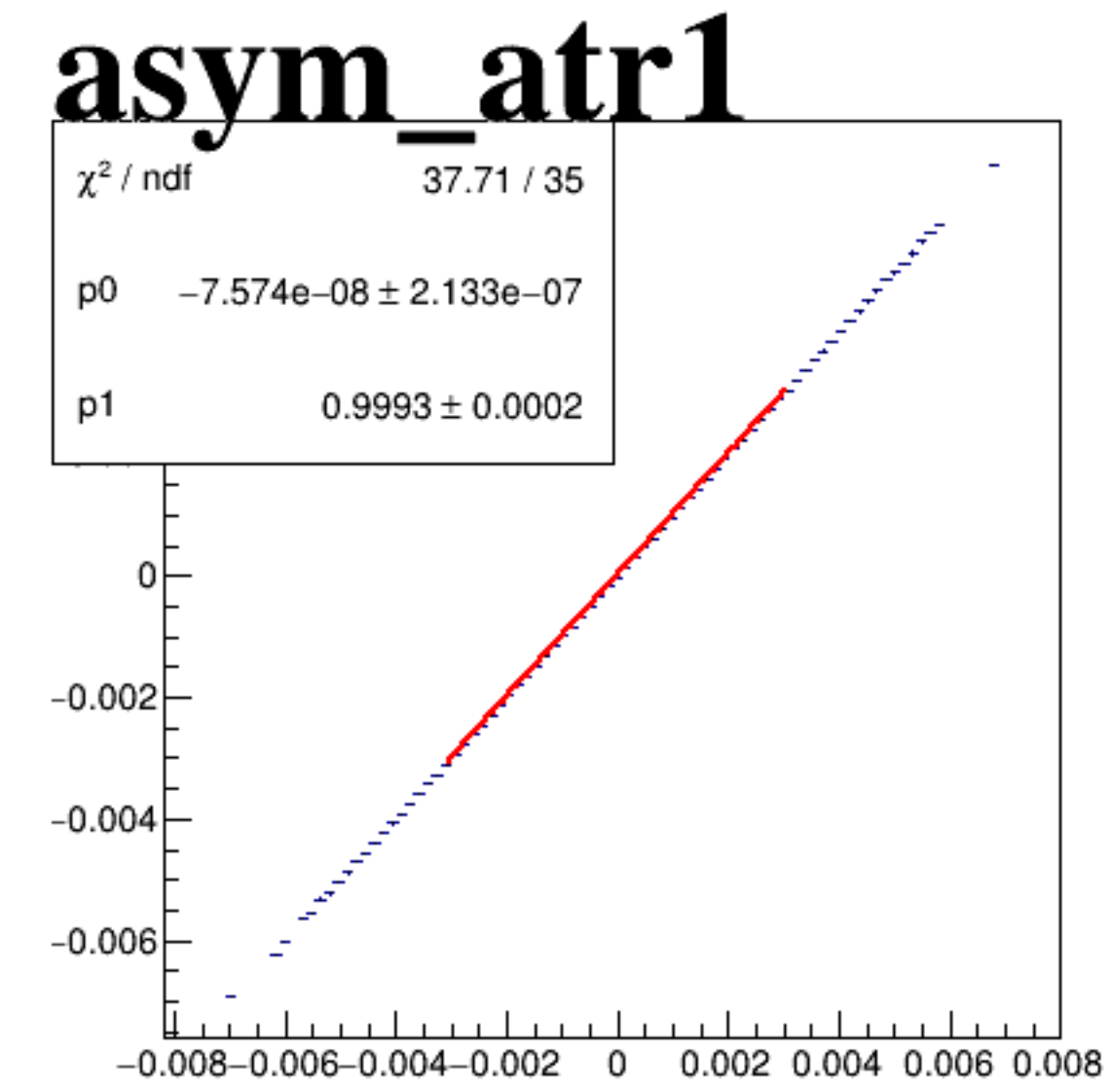
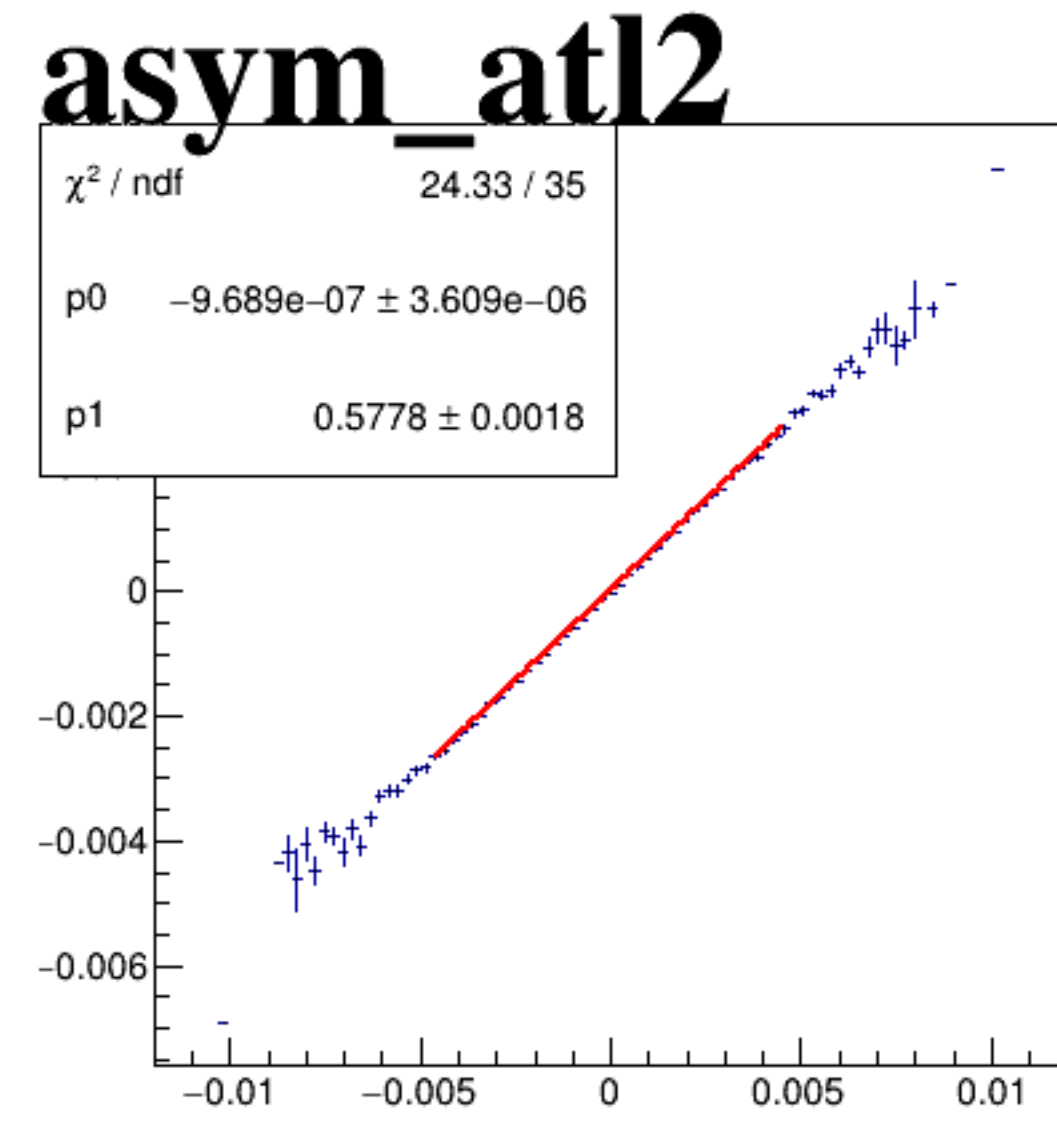
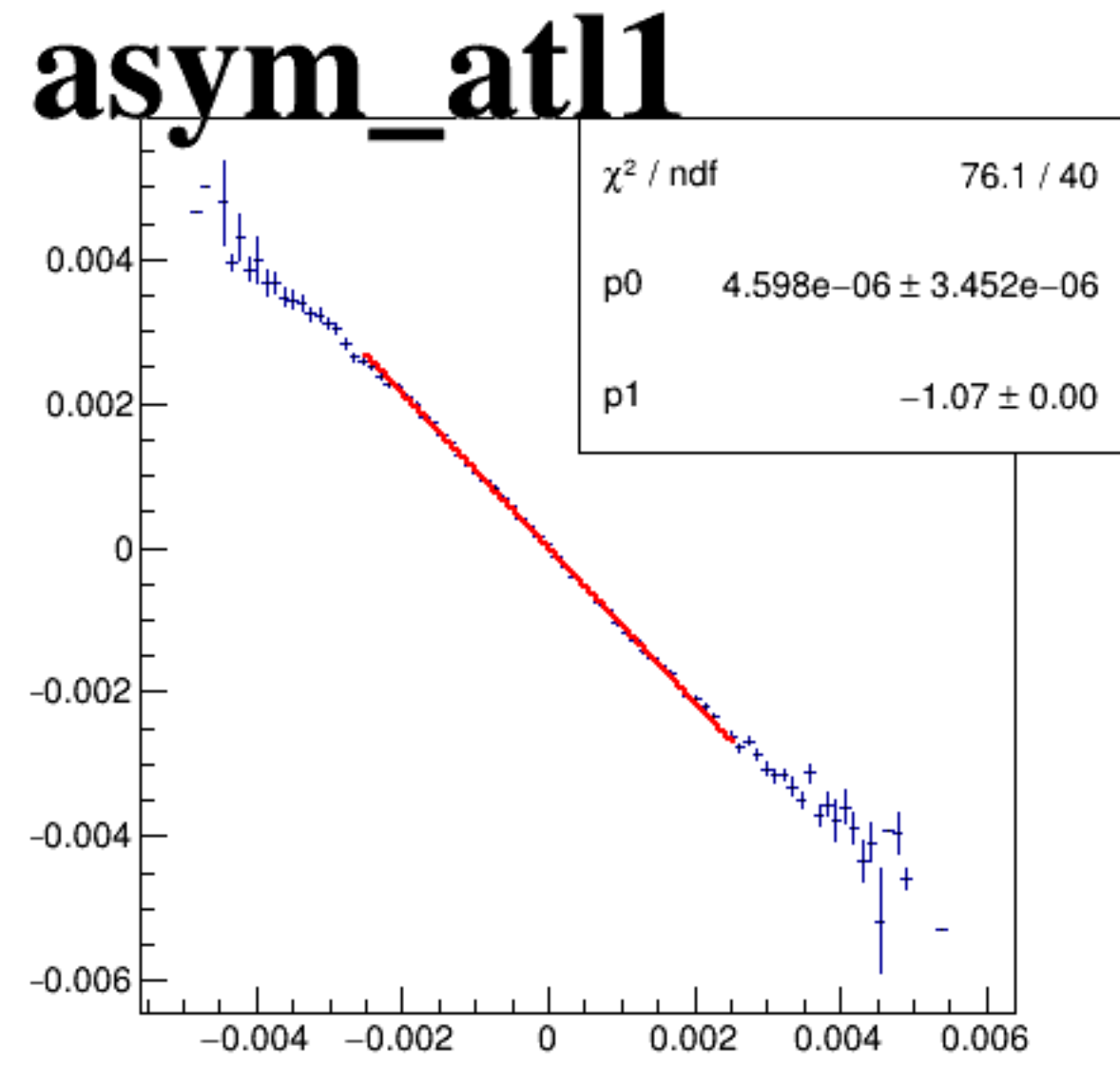
**asym\_atl1**



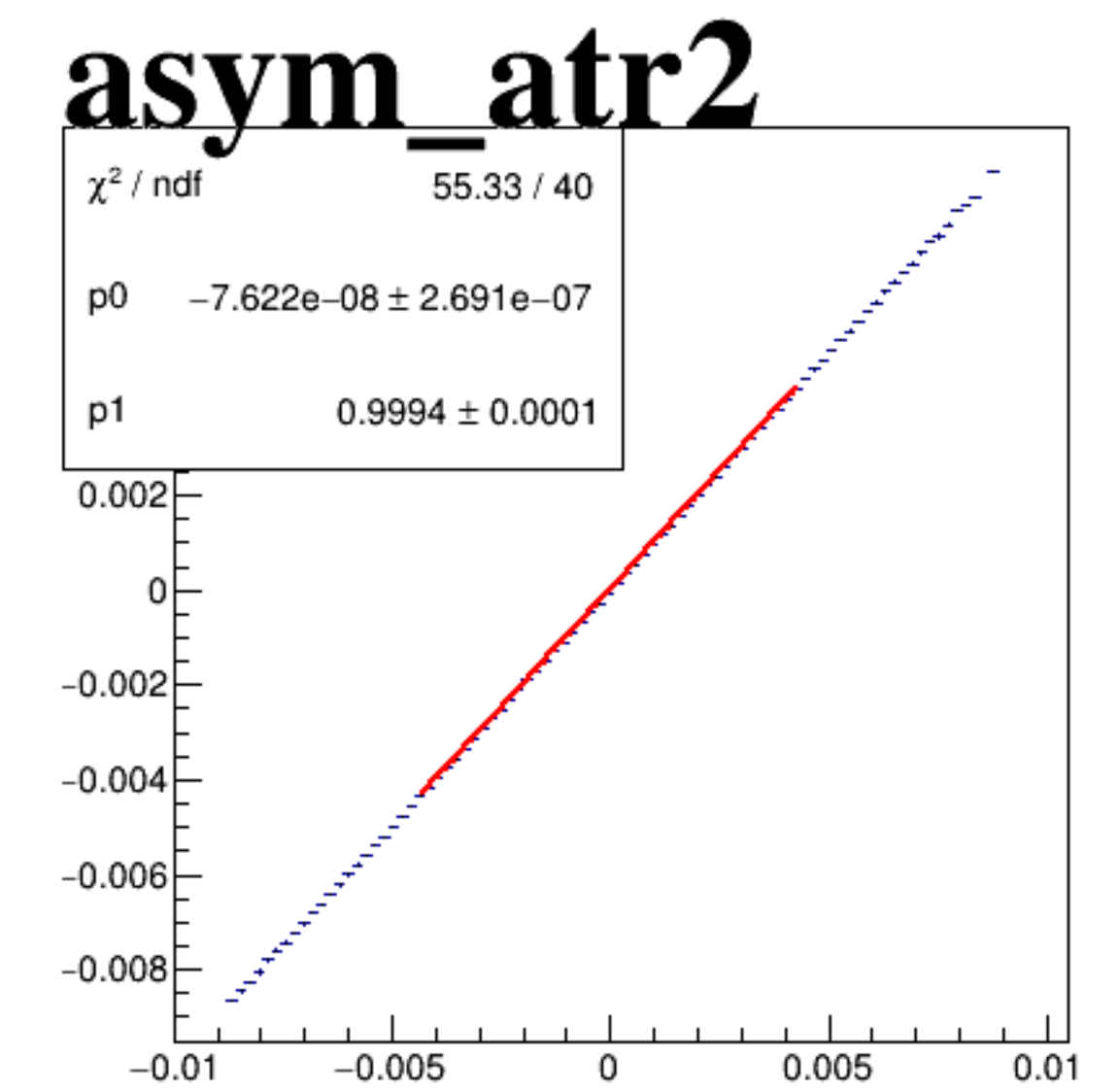
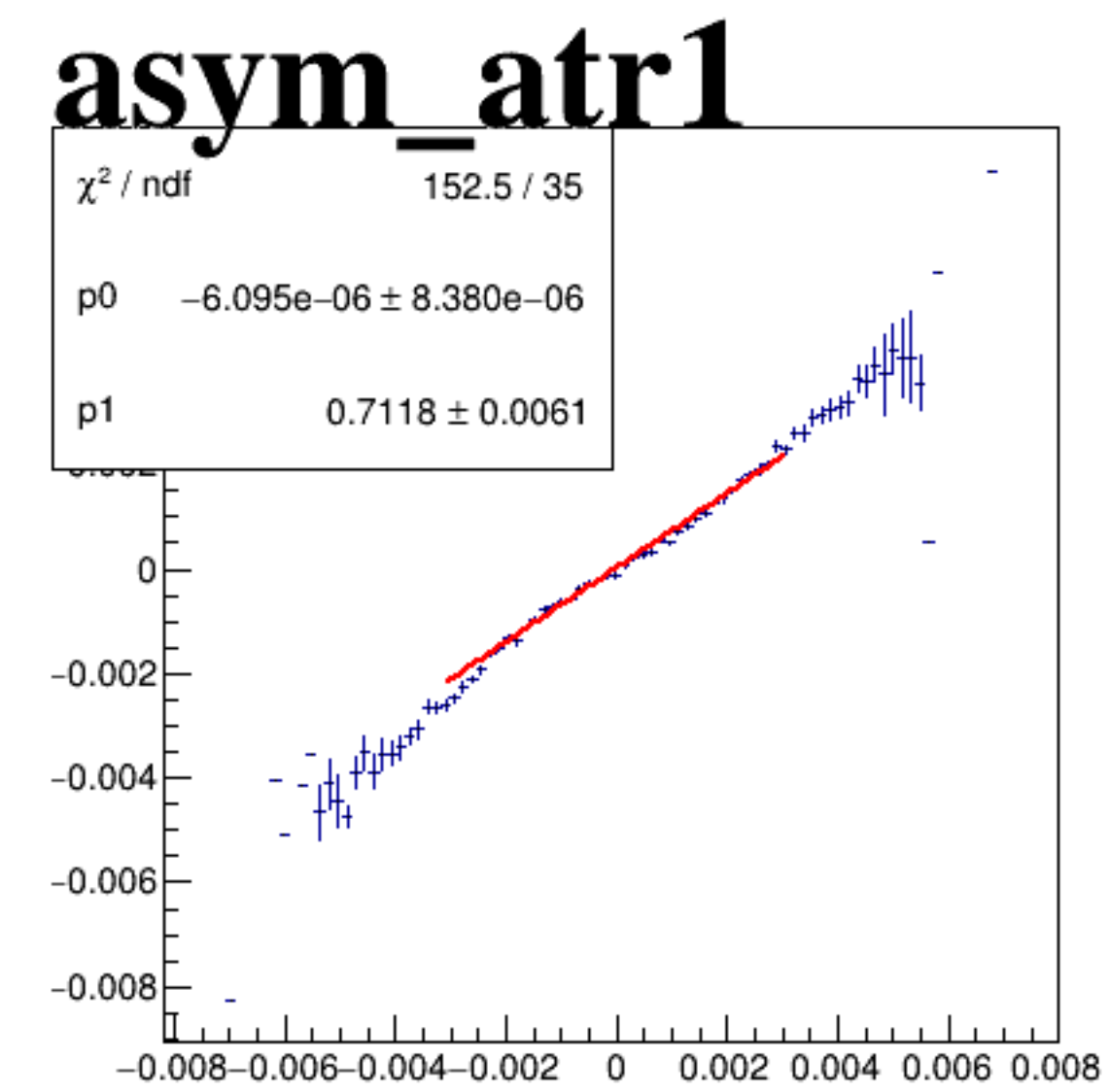
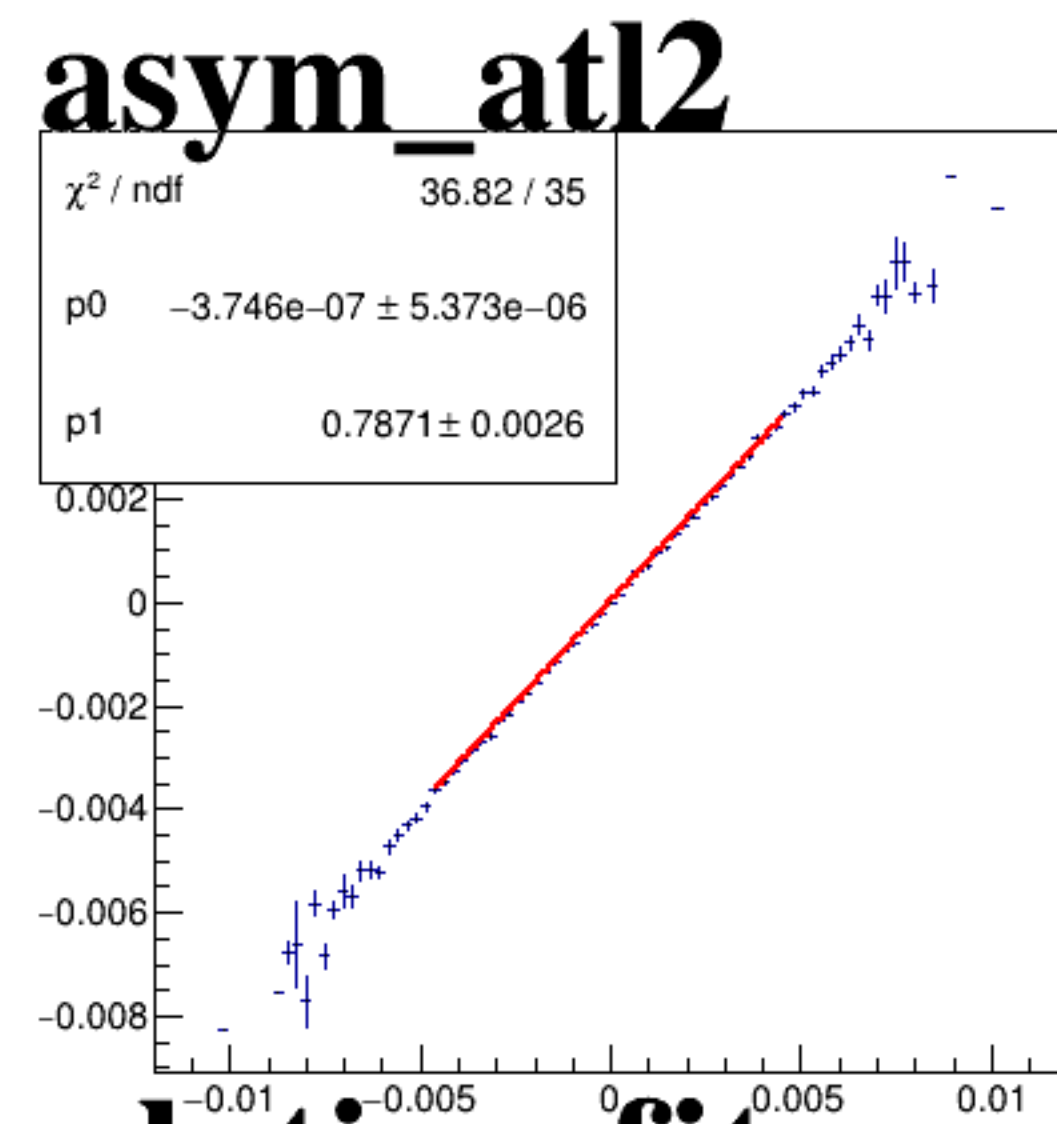
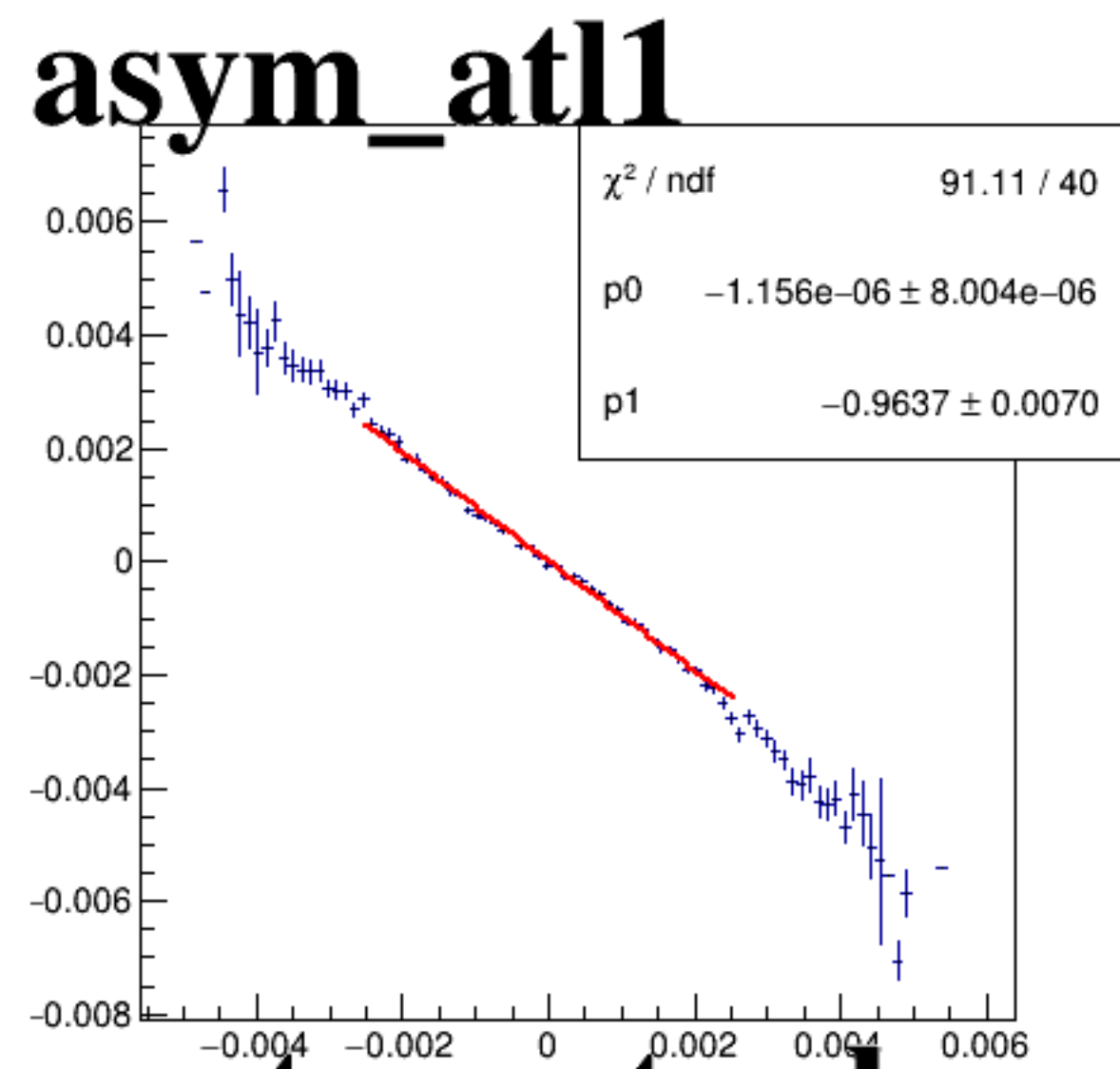
**asym\_atl2**



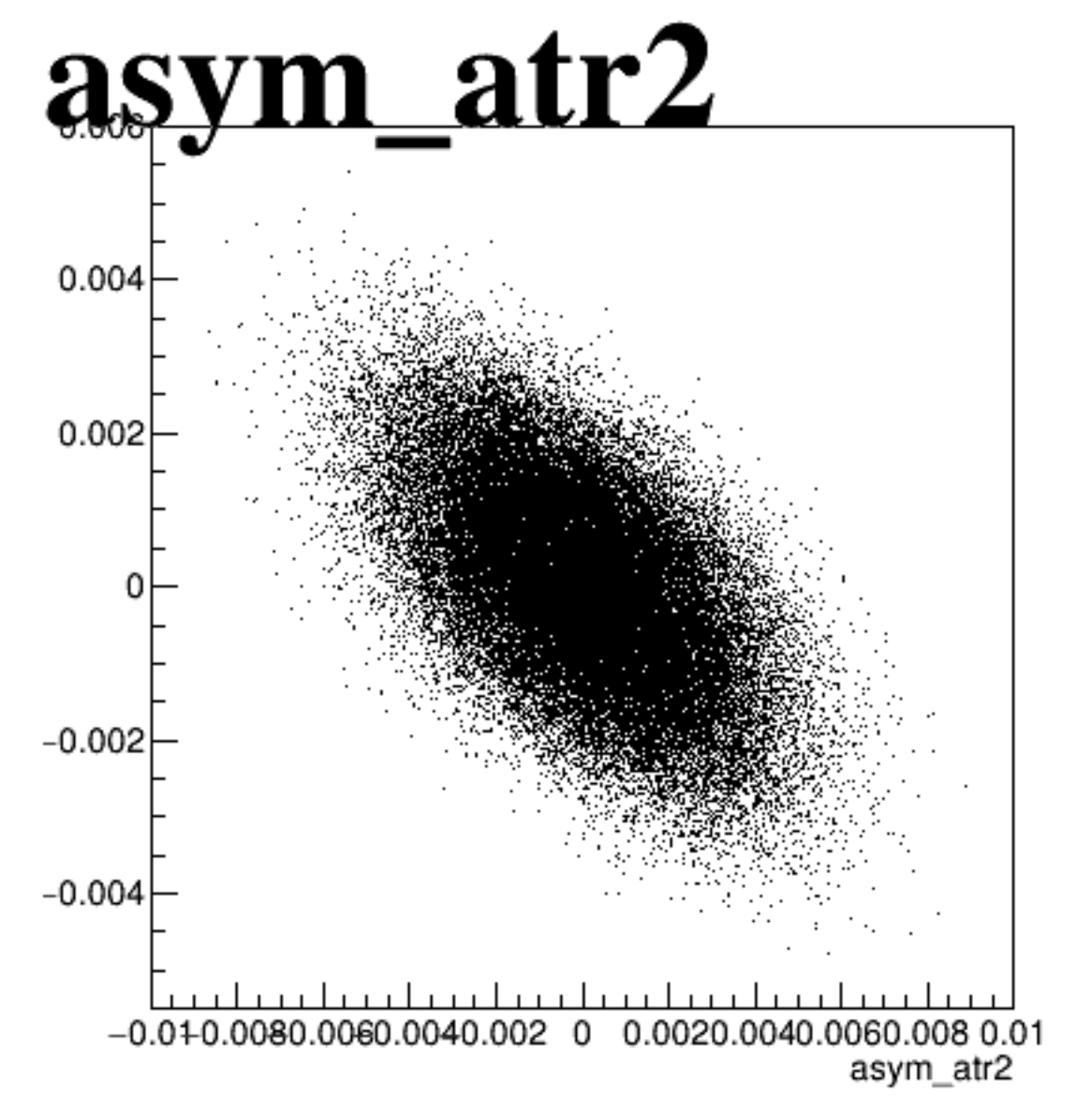
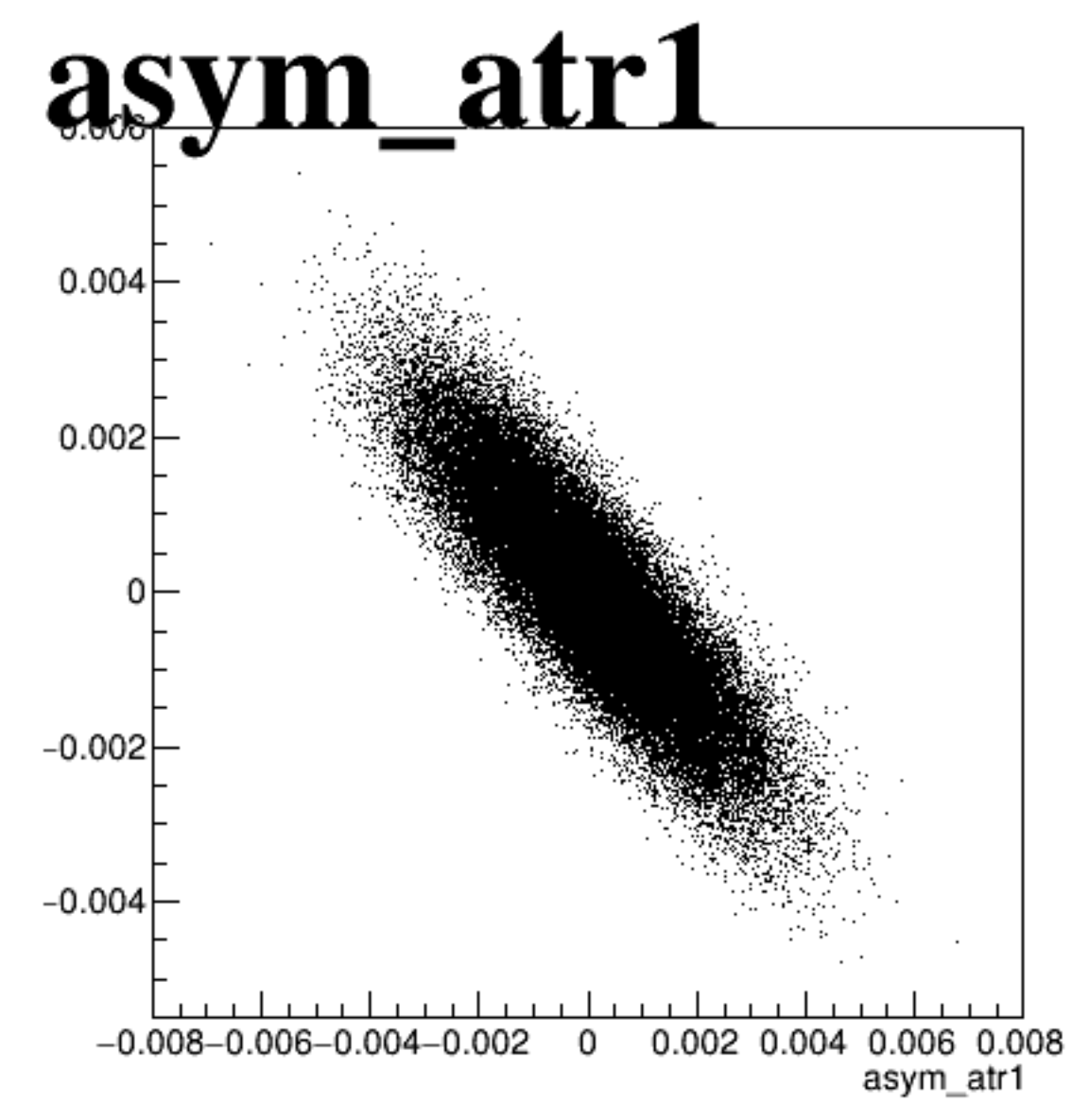
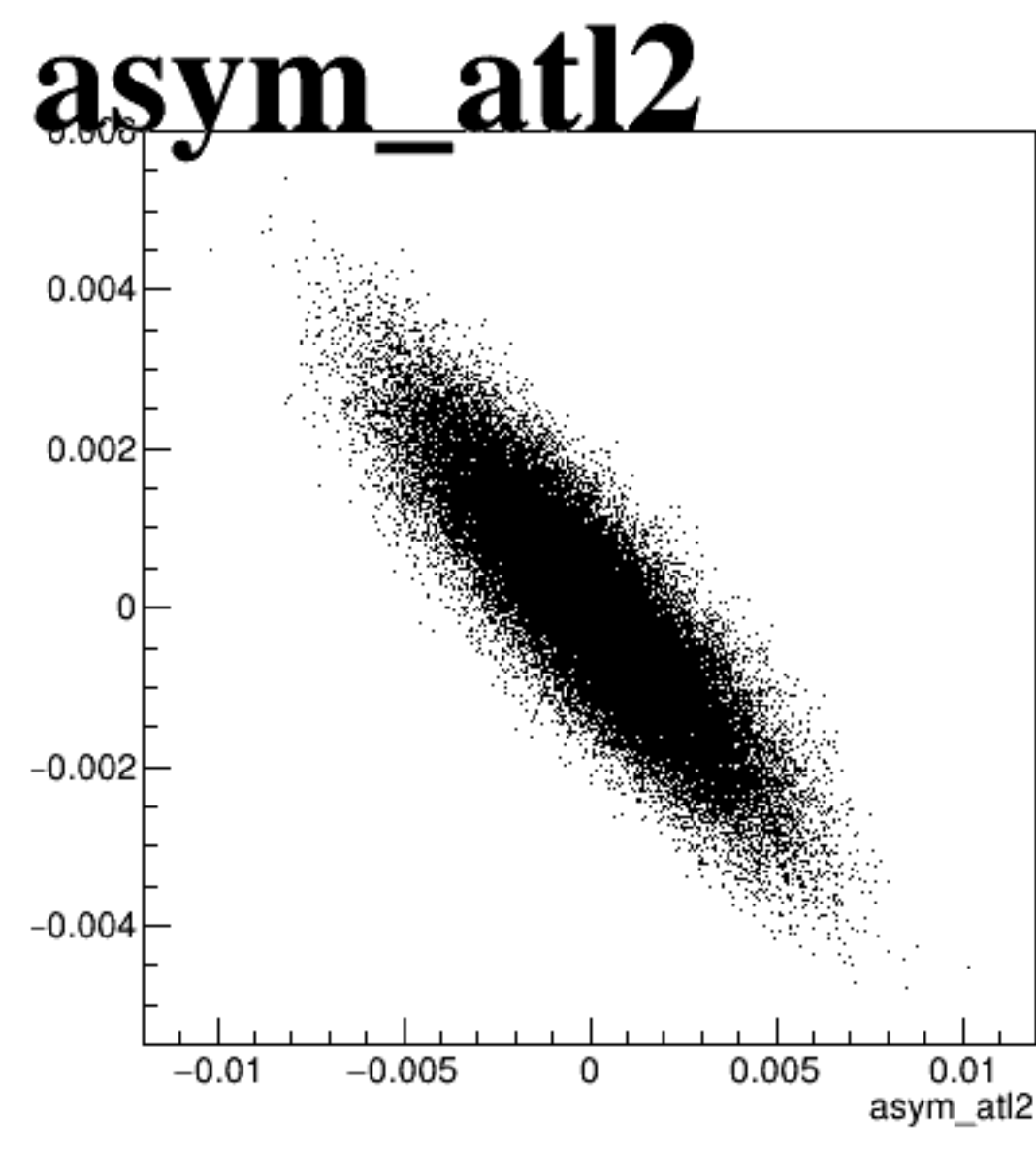
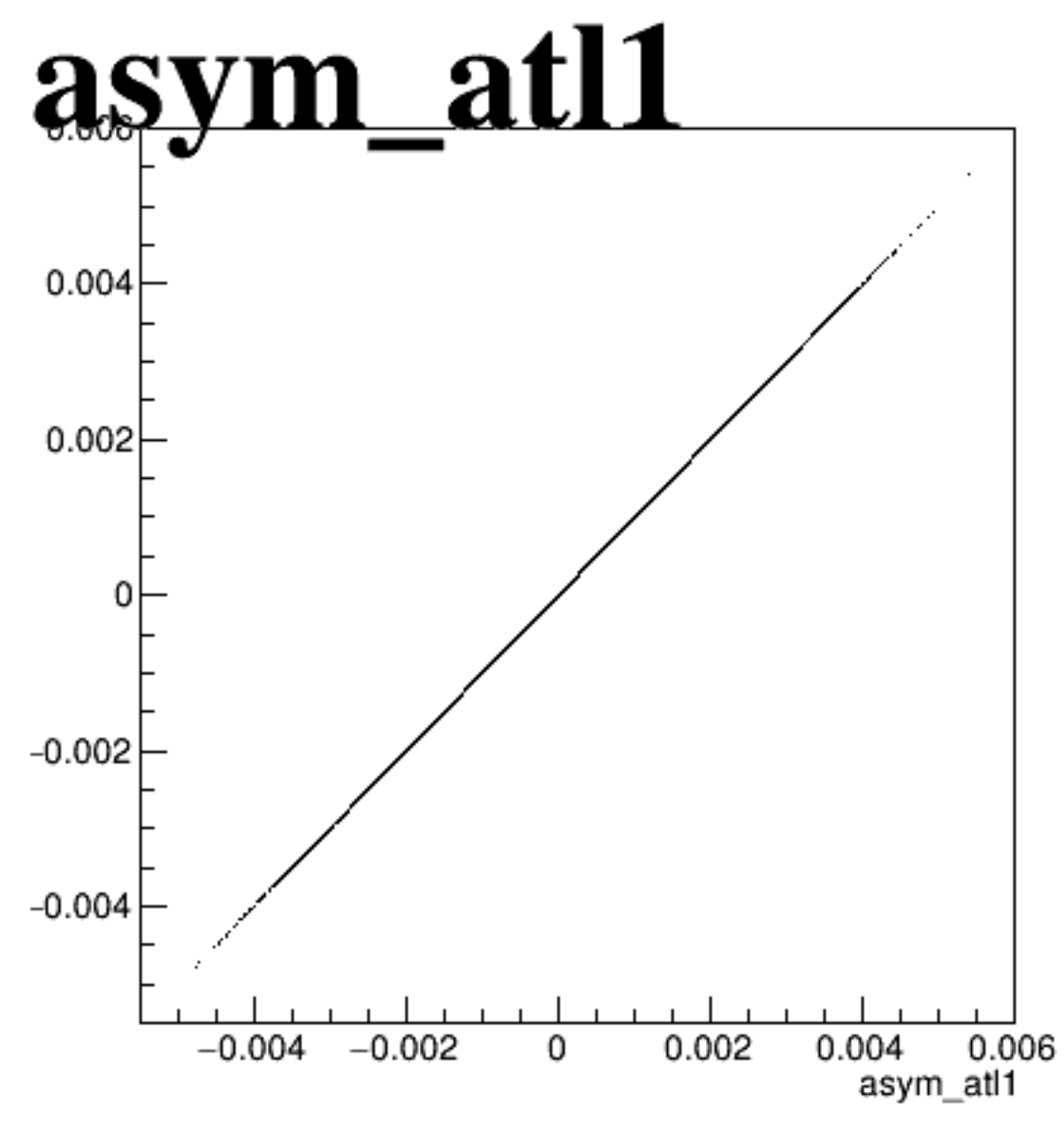
**asym\_atr1**



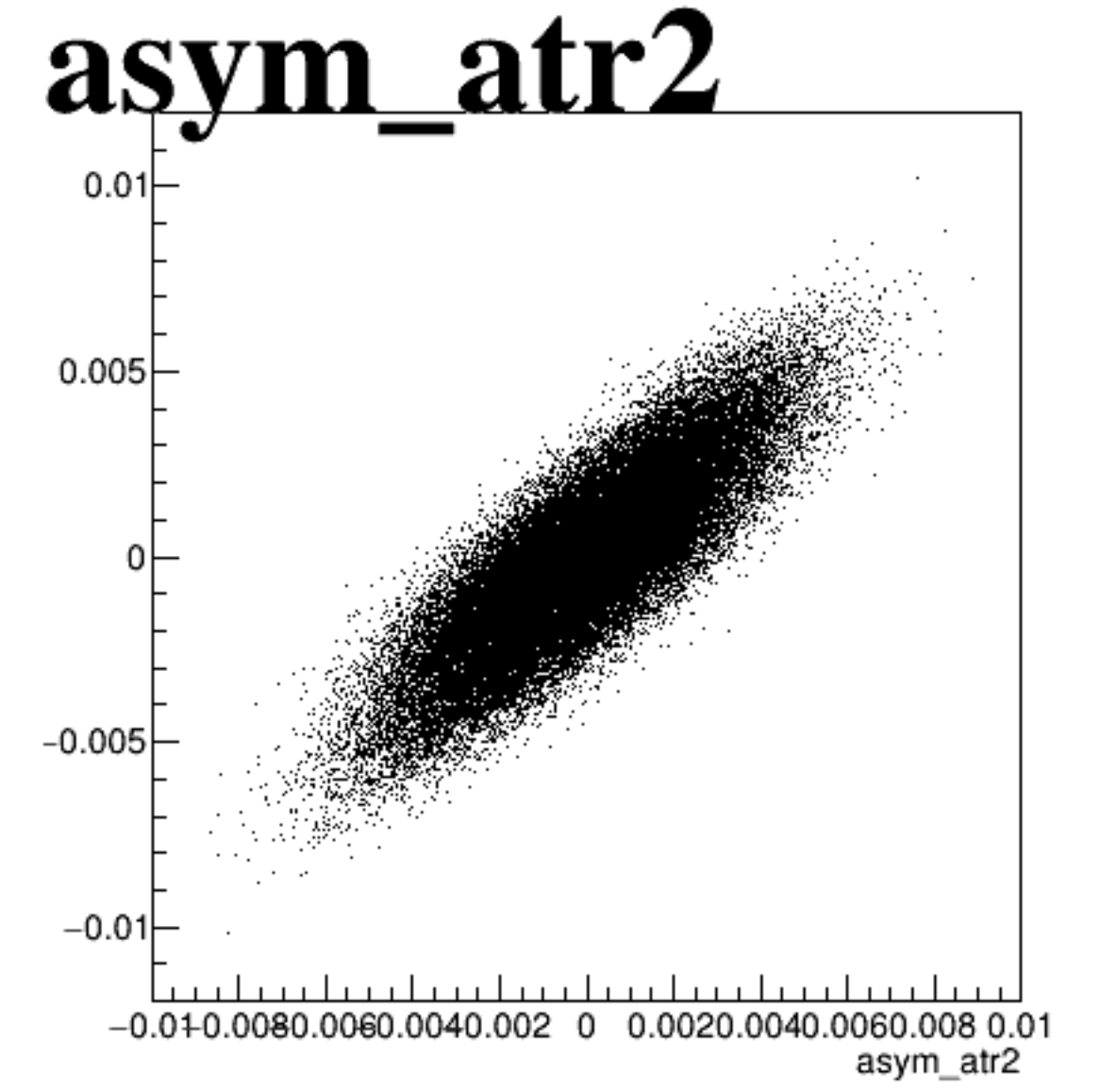
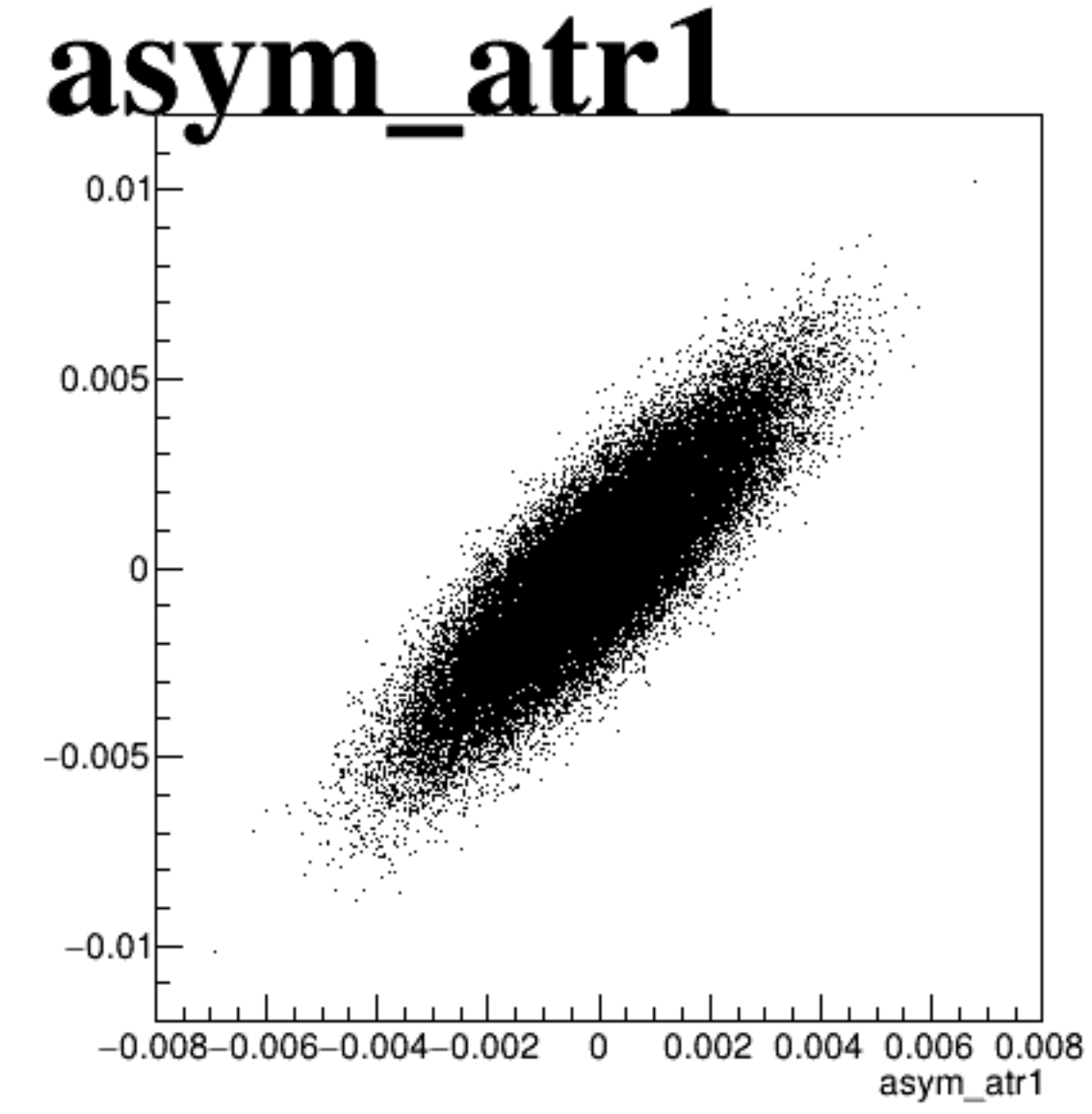
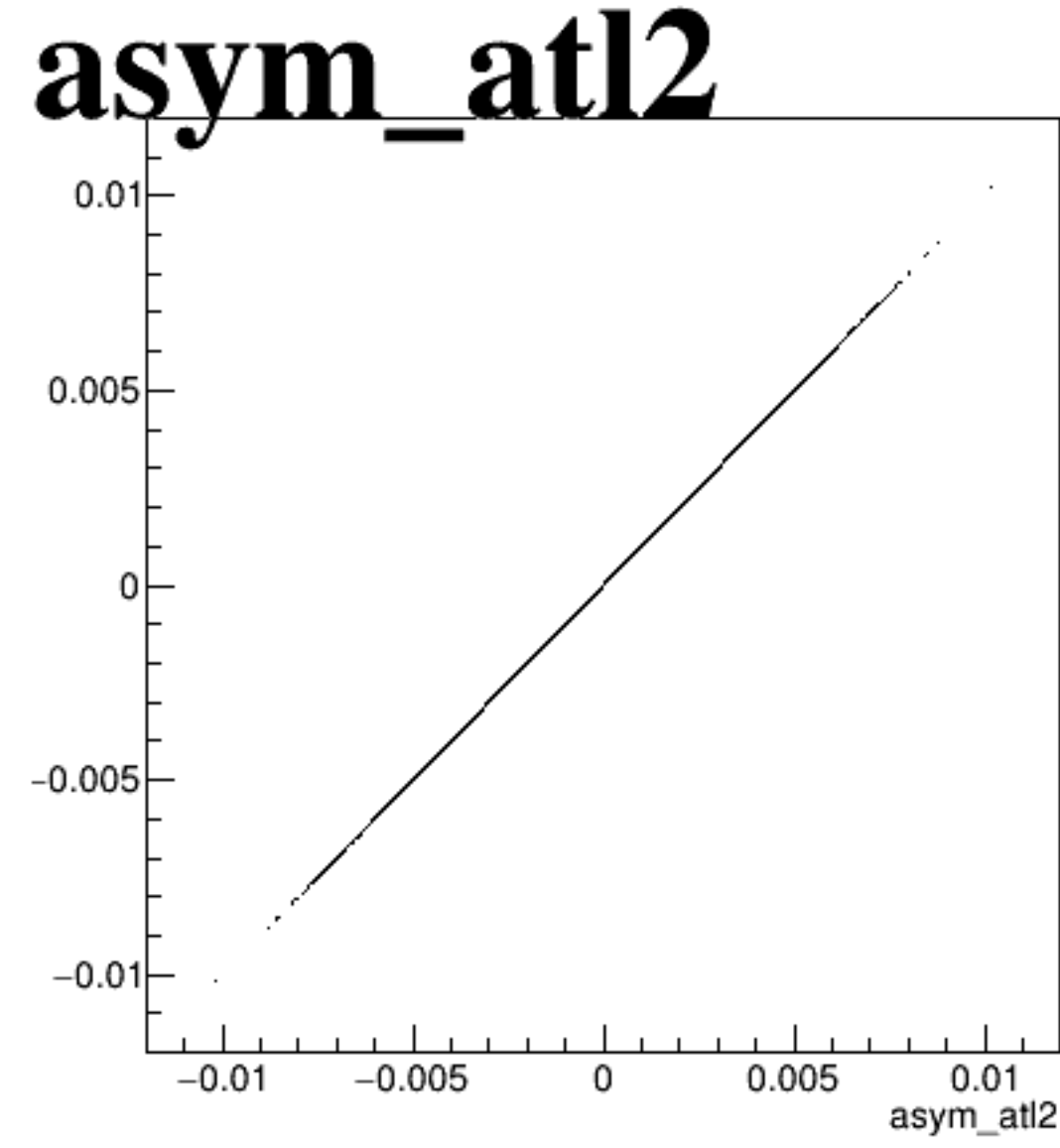
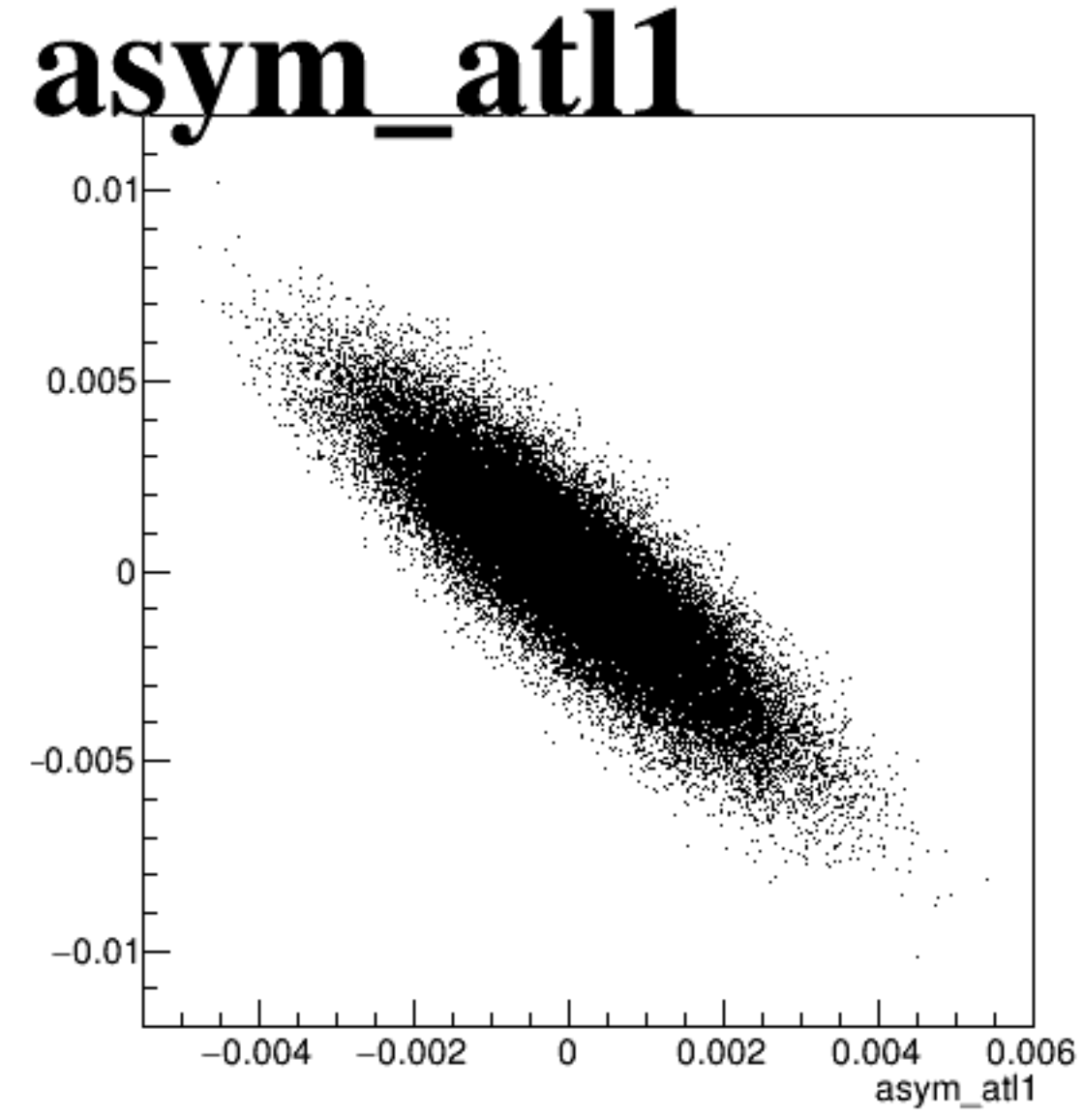
**asym\_atr2**



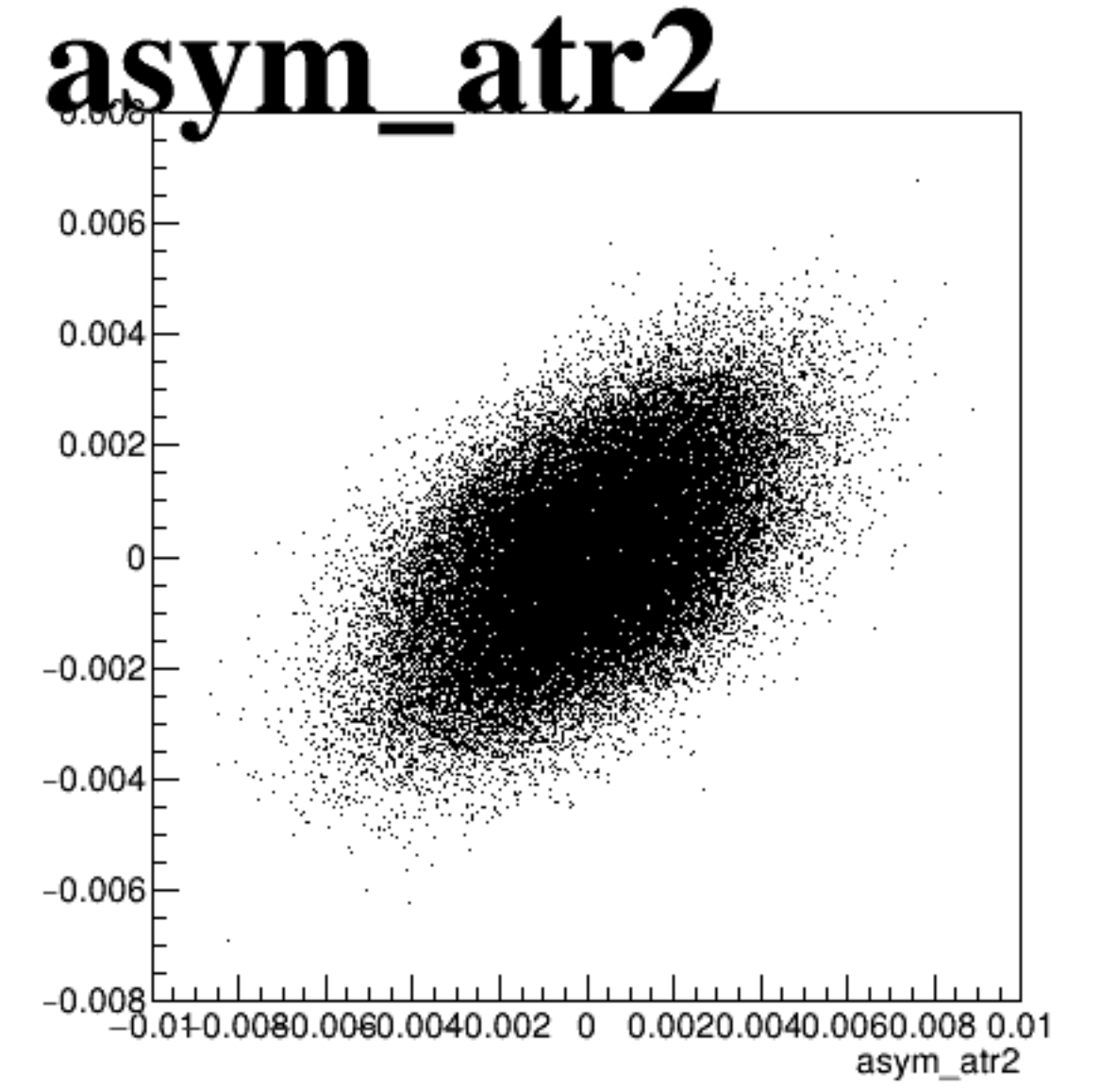
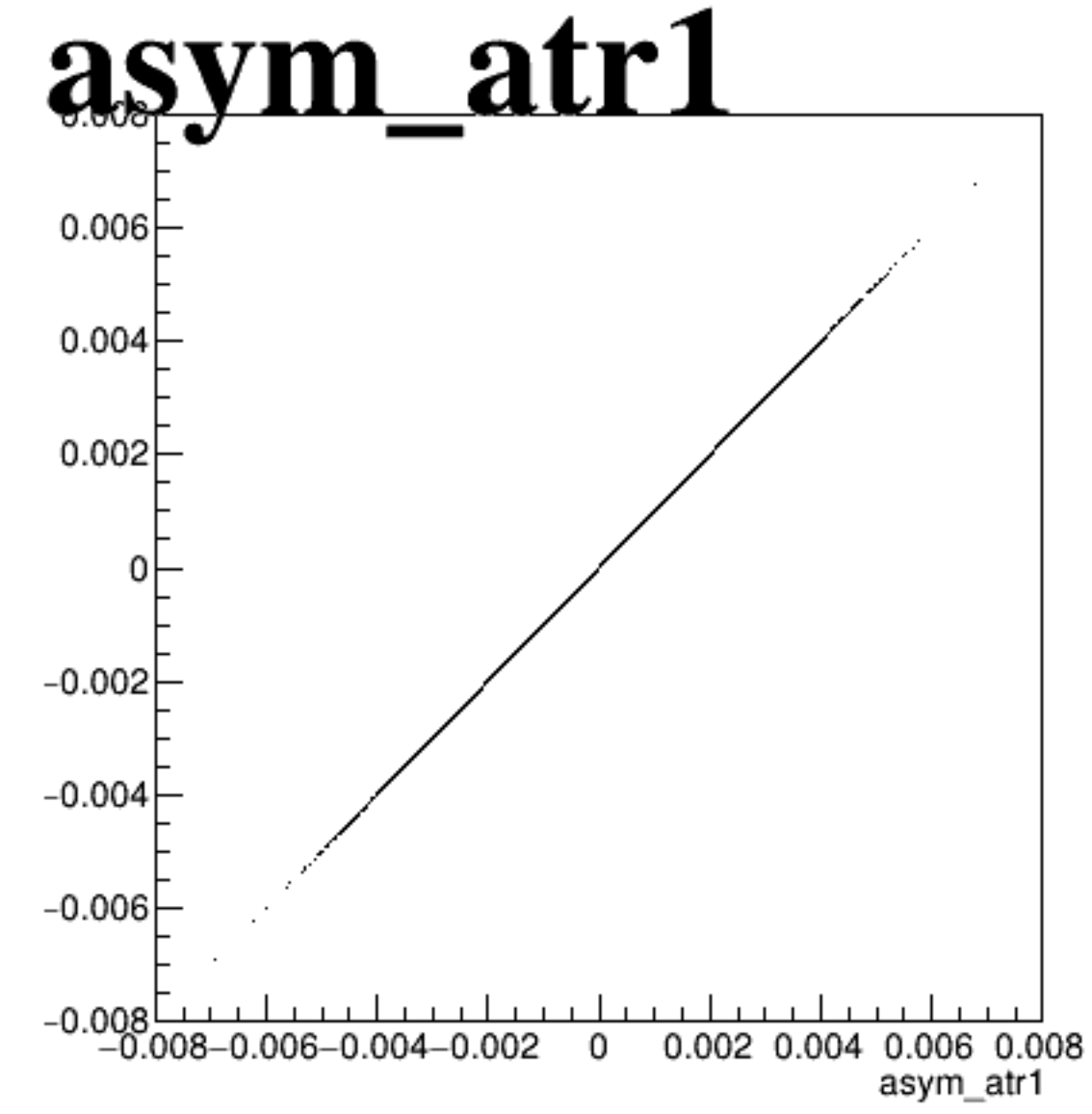
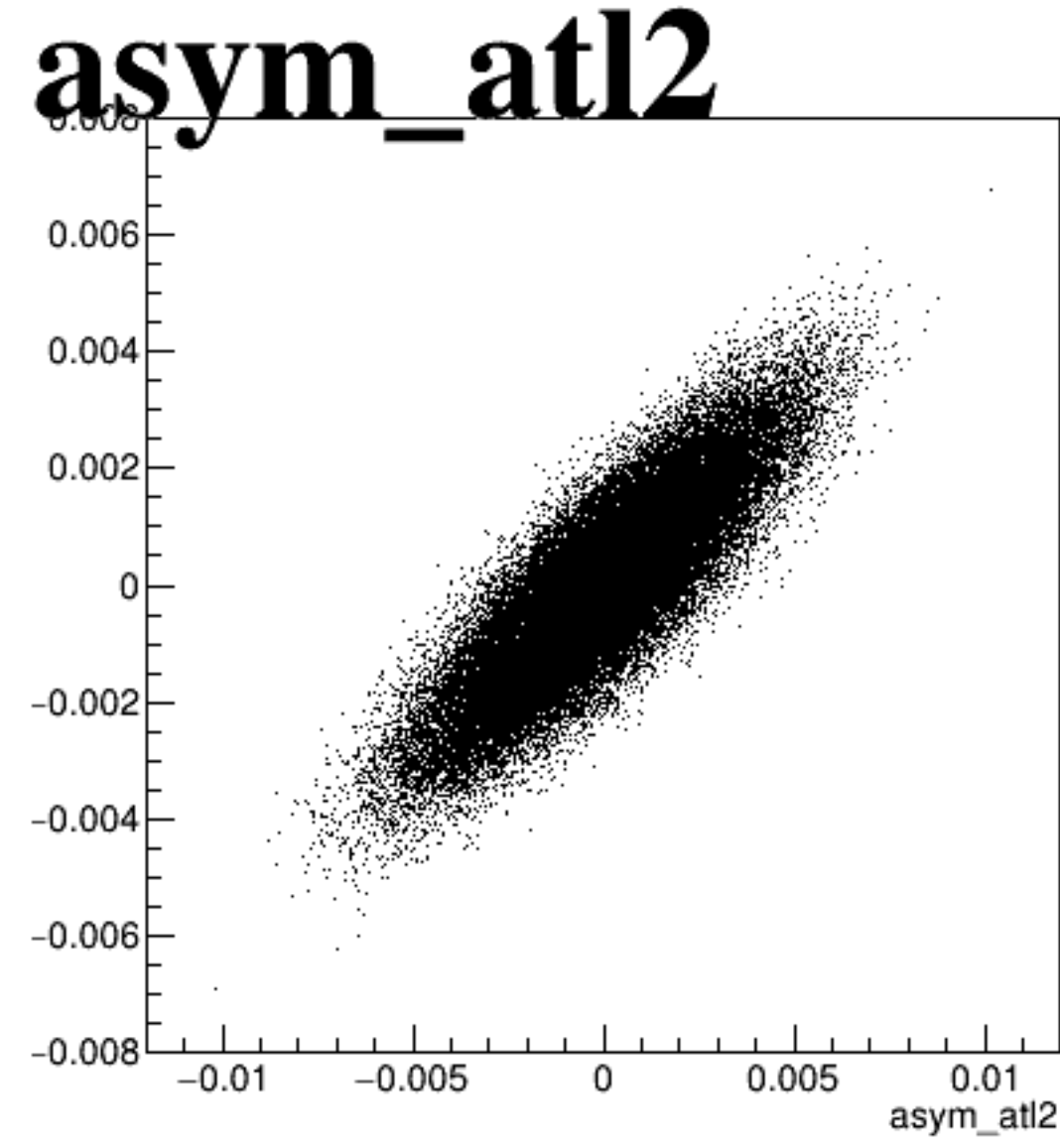
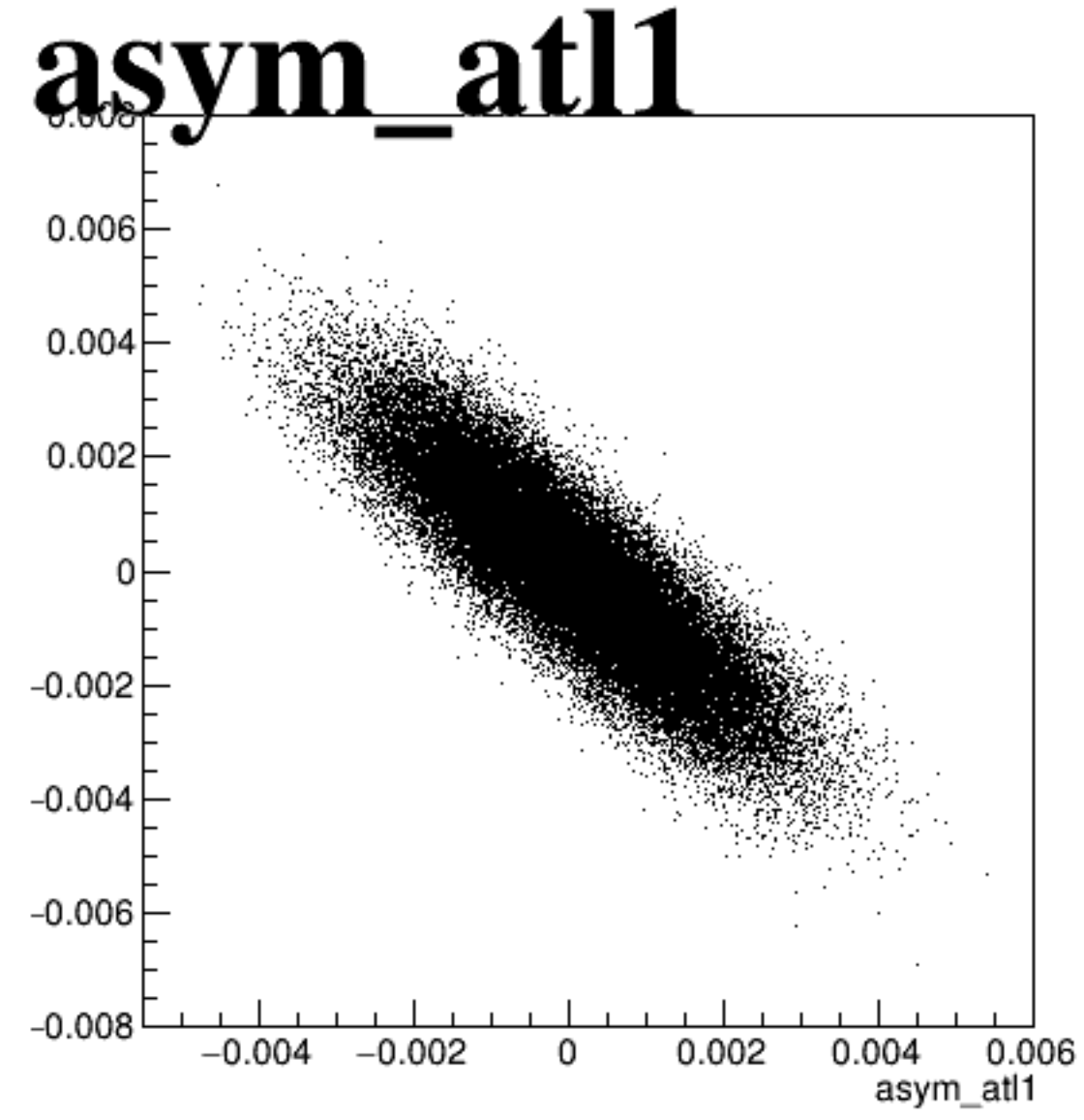
**asym\_atl1**



**asym\_atl2**



**asym\_atr1**



**asym\_atr2**

