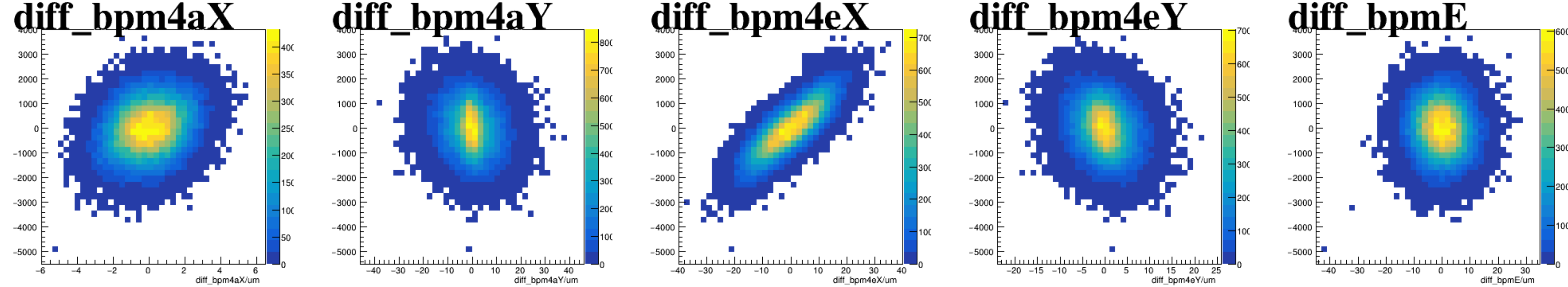
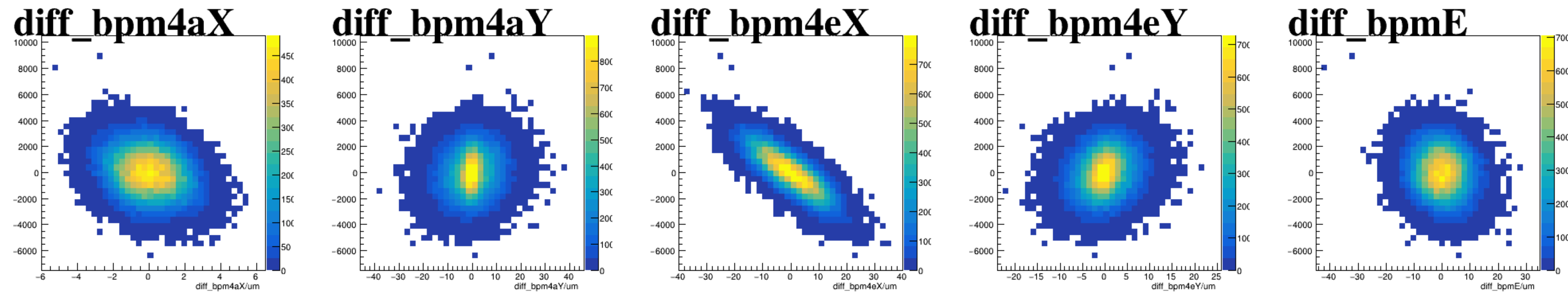


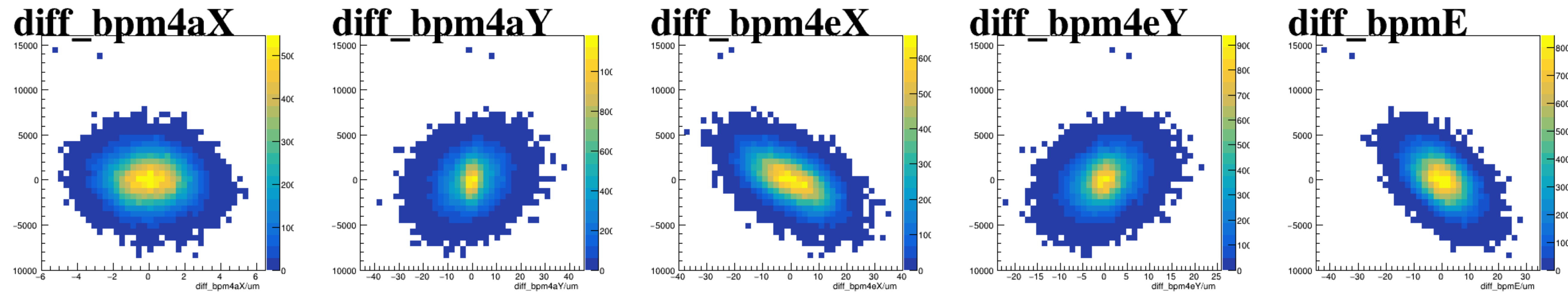
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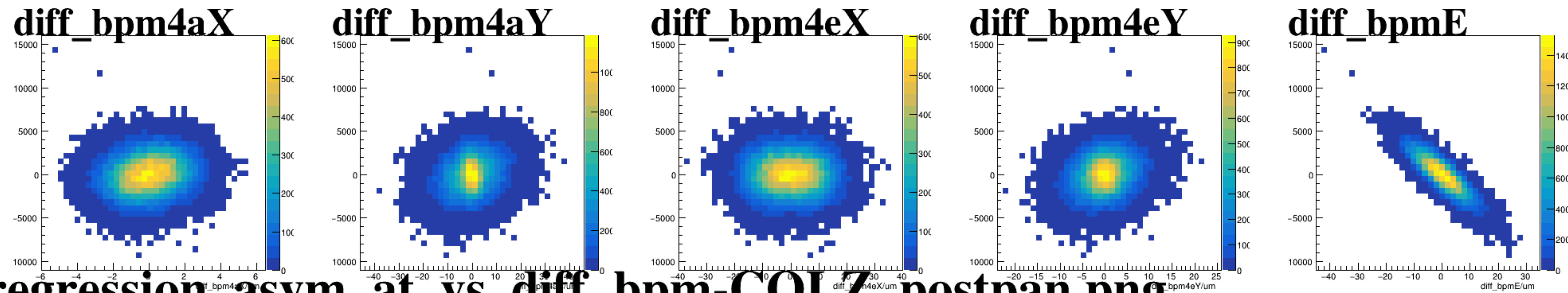
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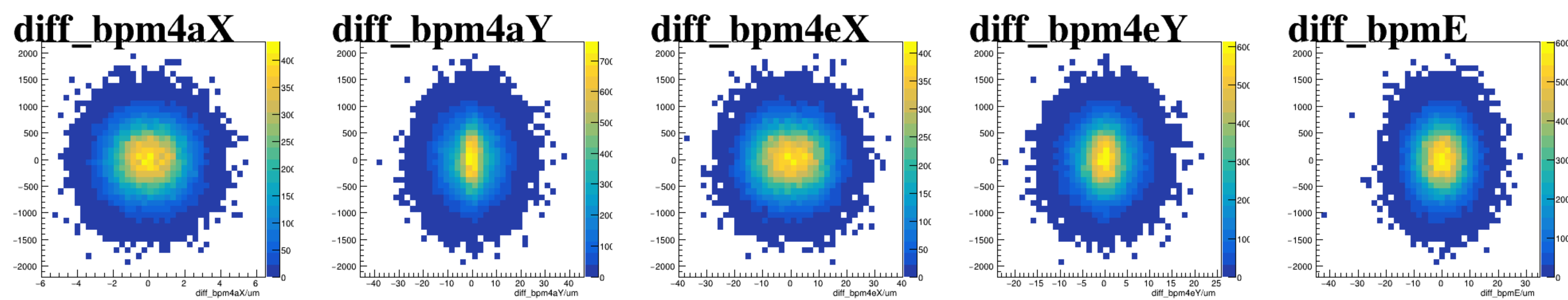
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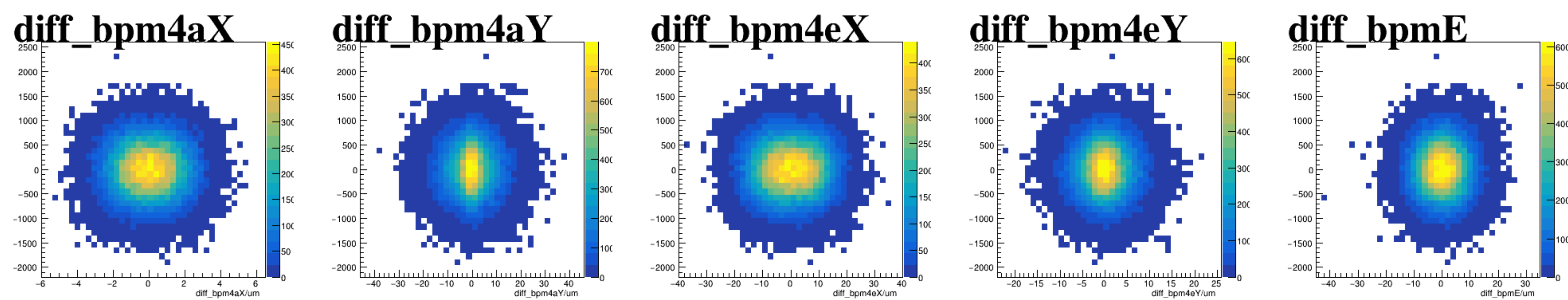
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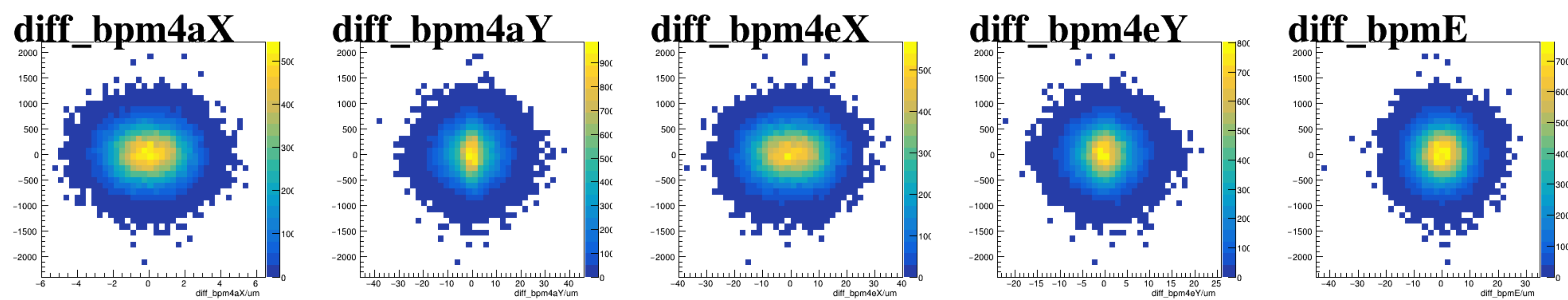
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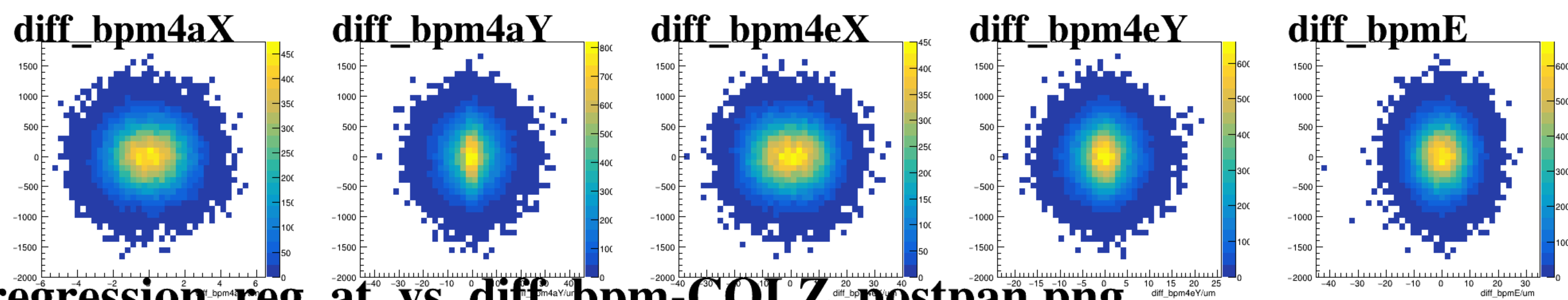
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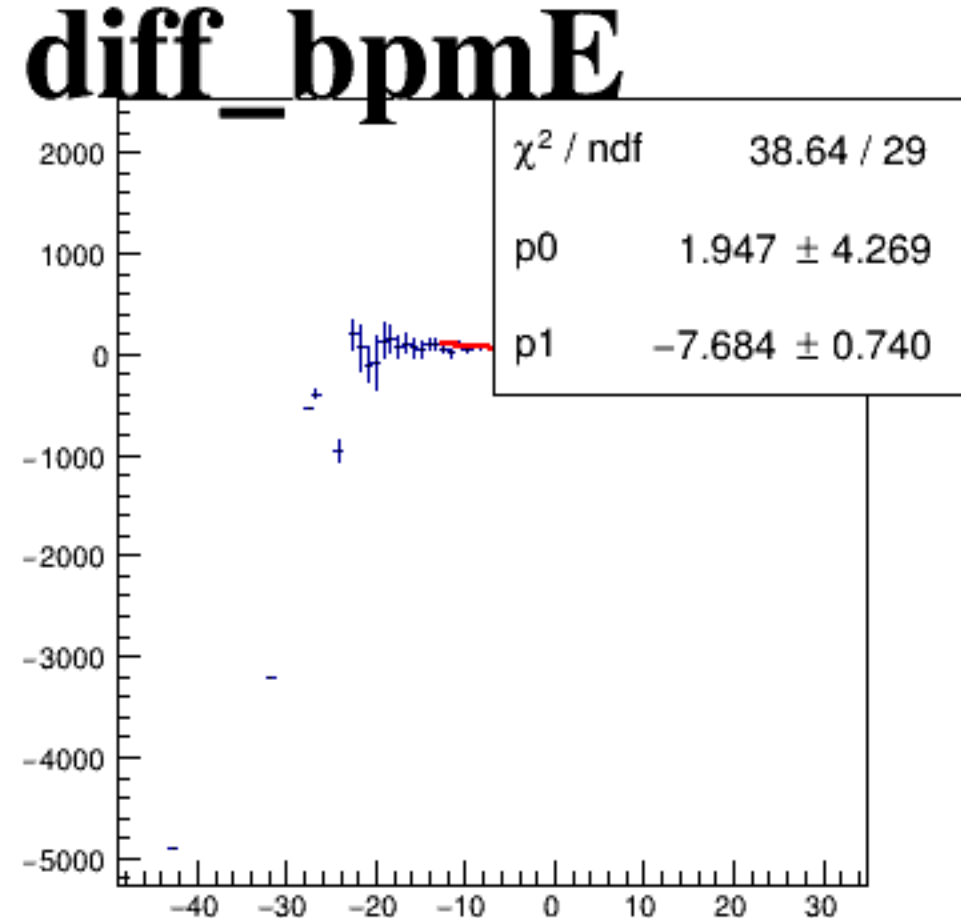
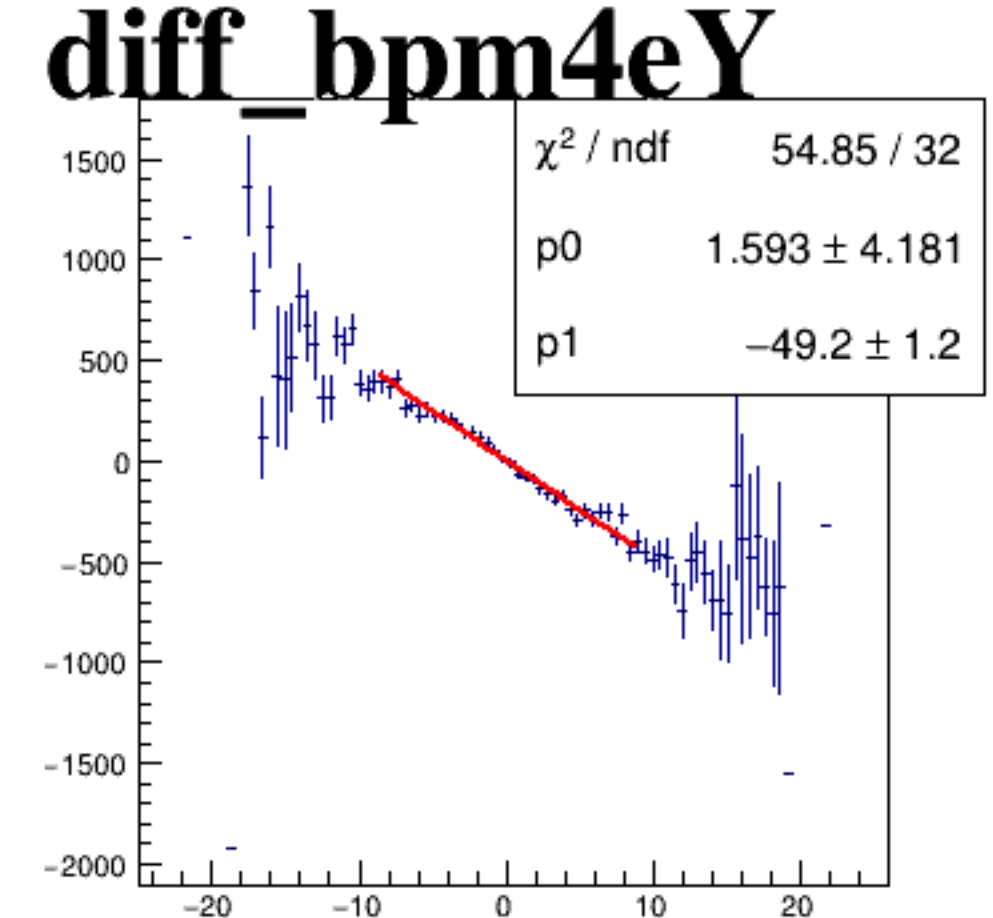
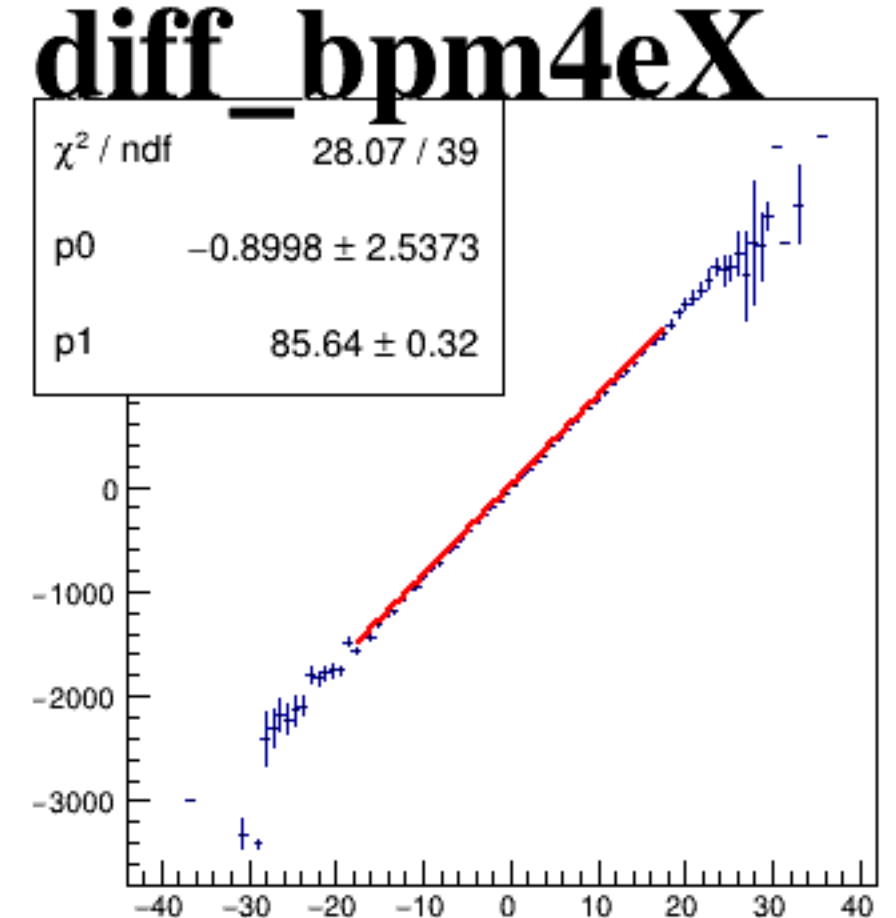
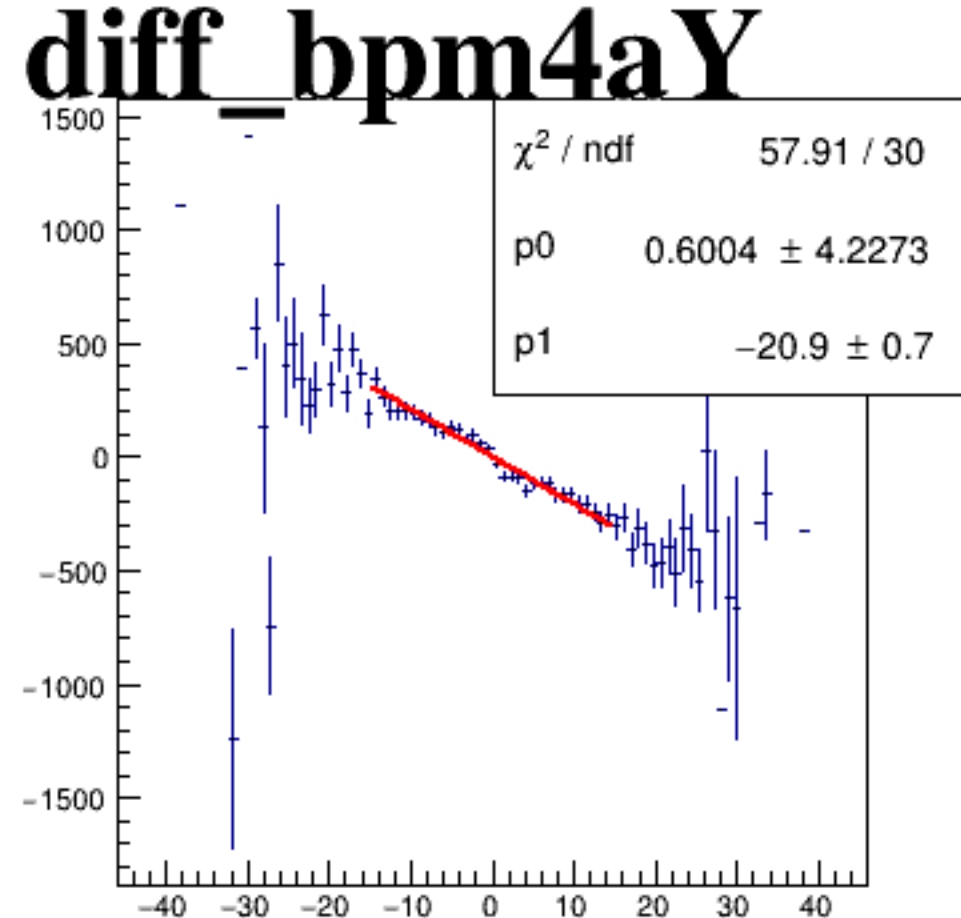
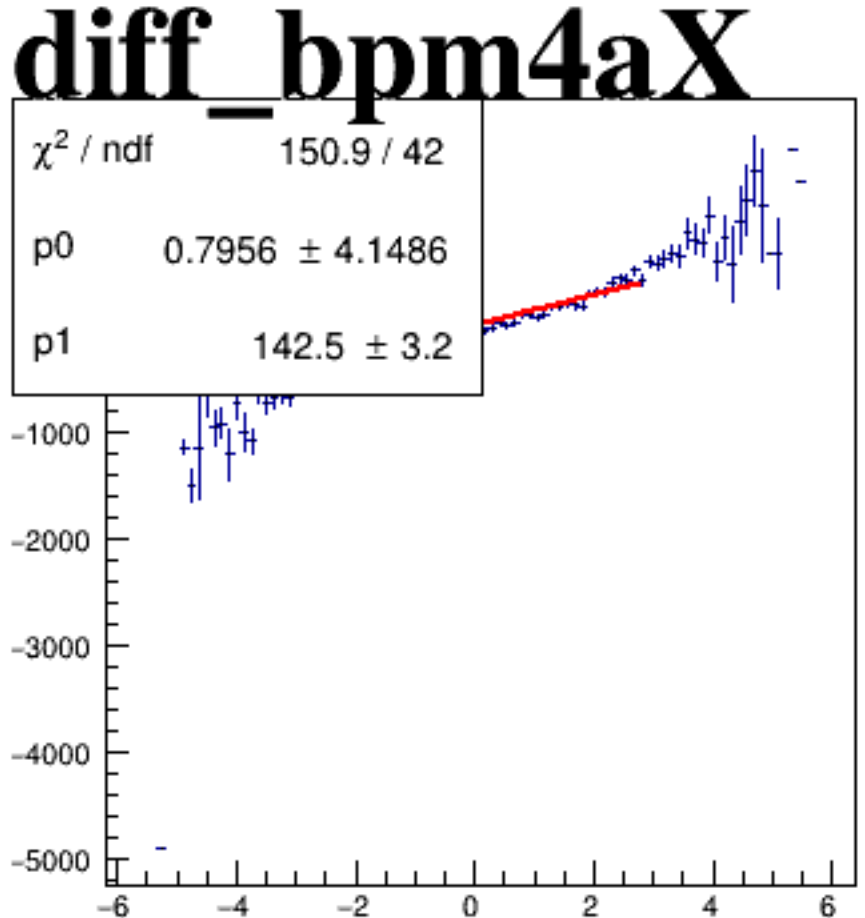
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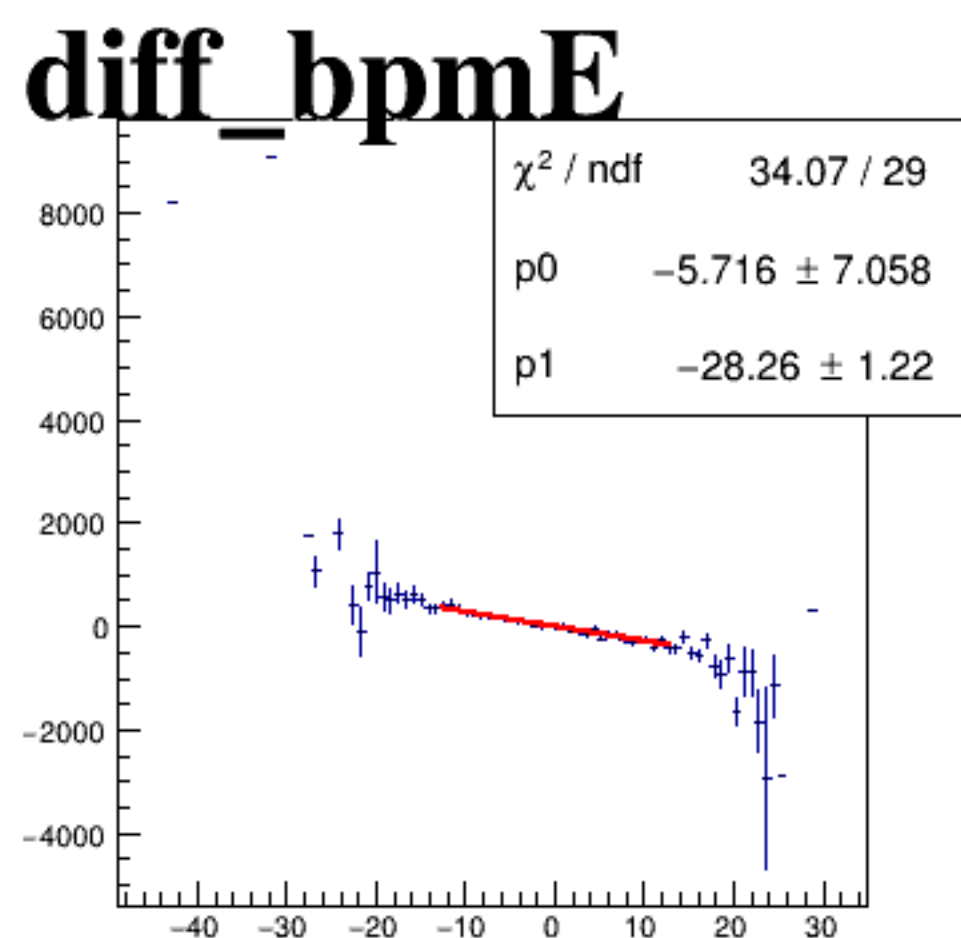
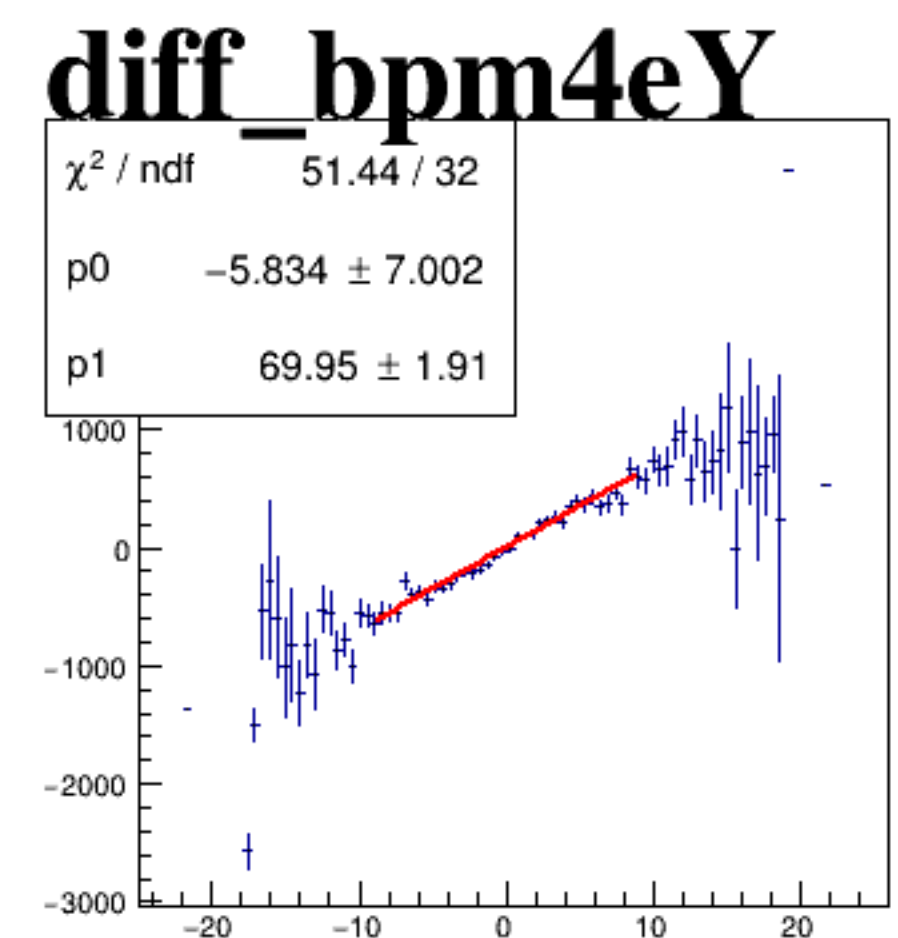
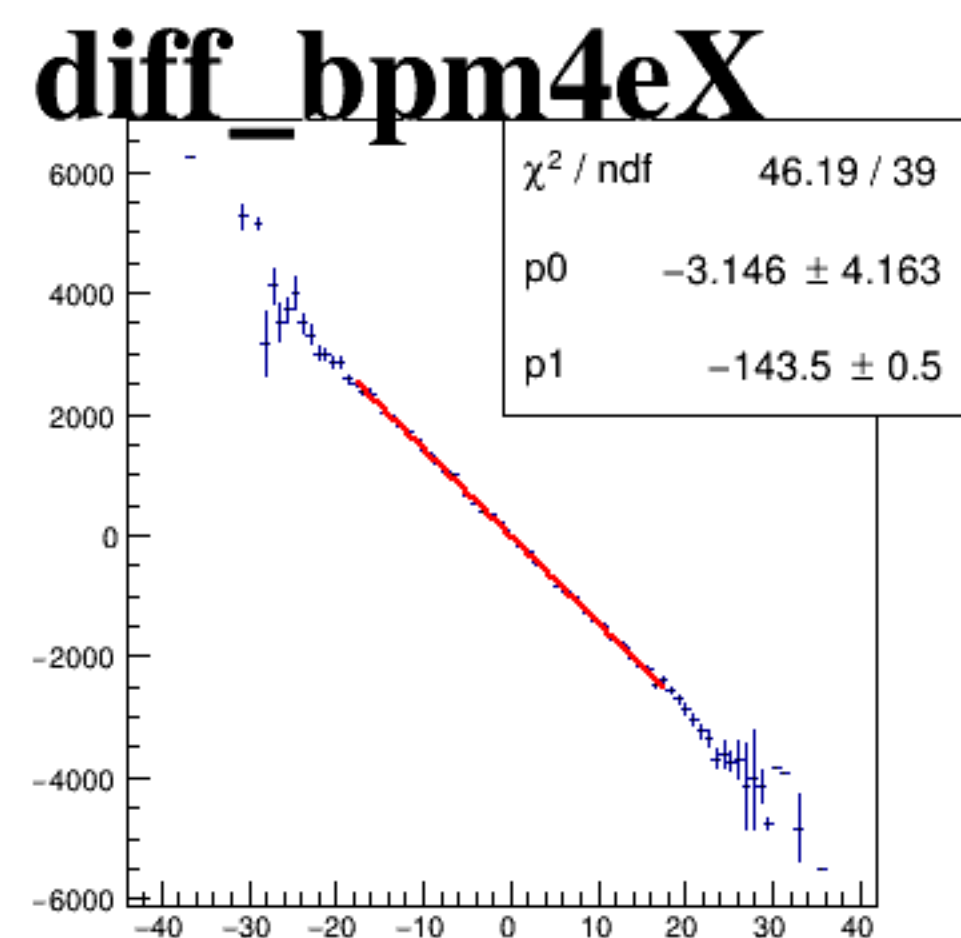
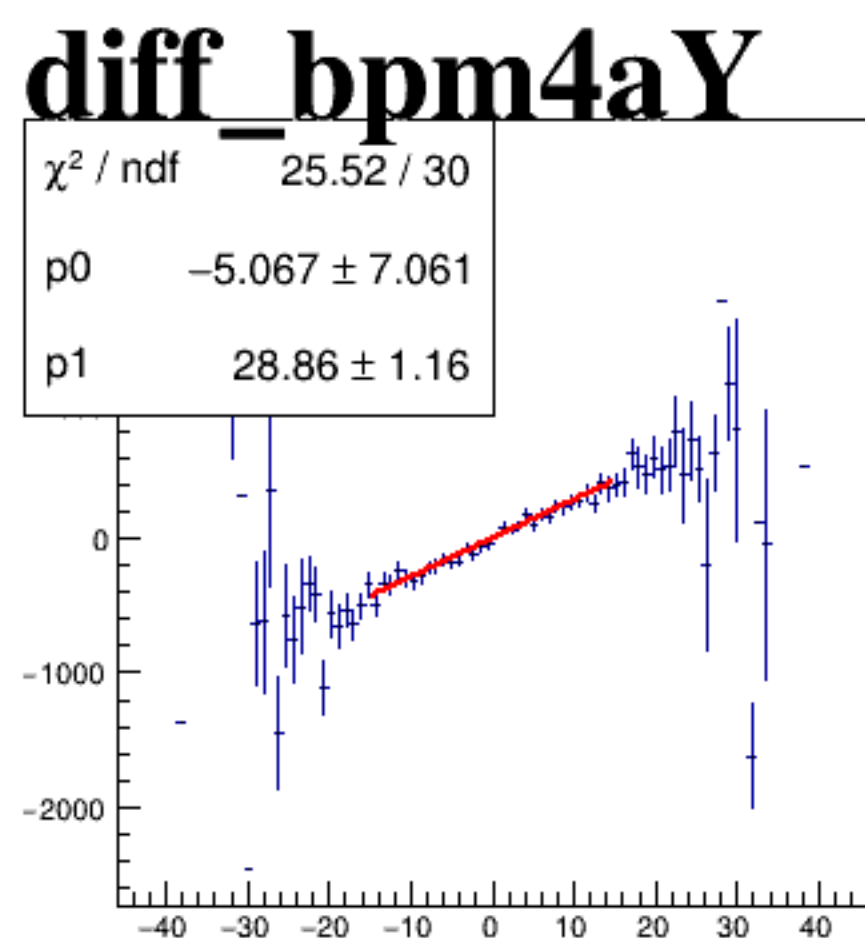
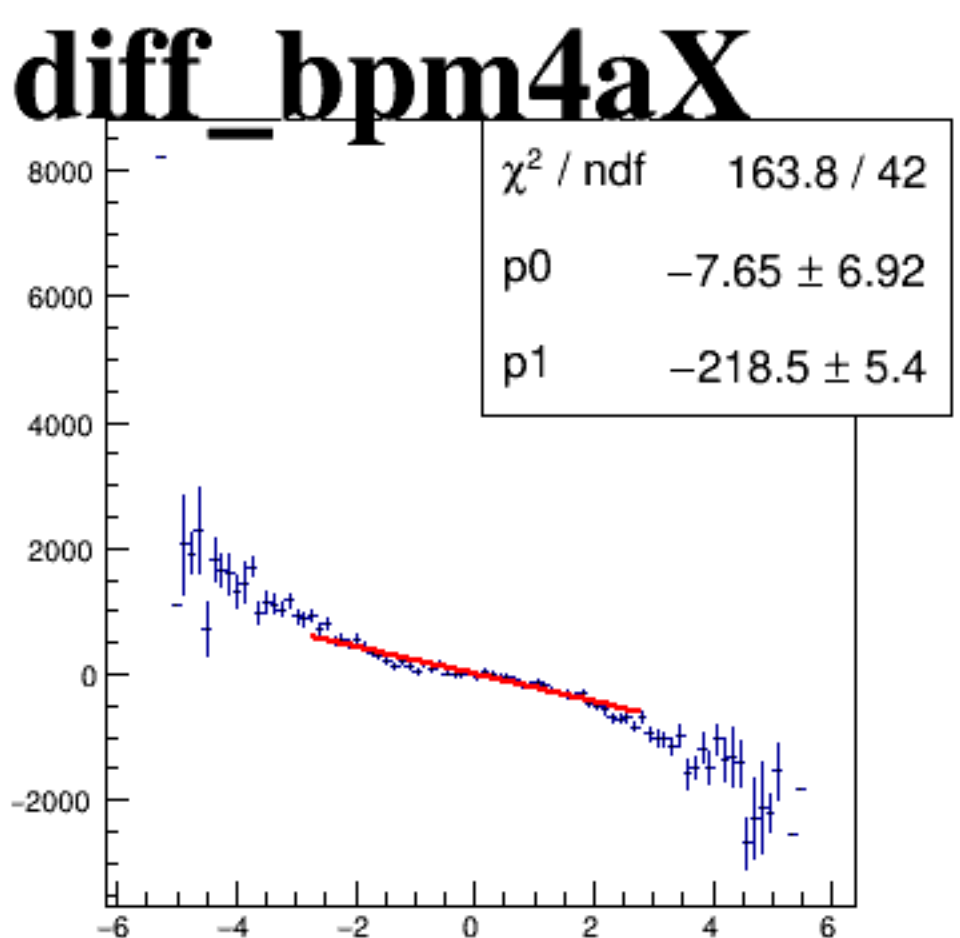
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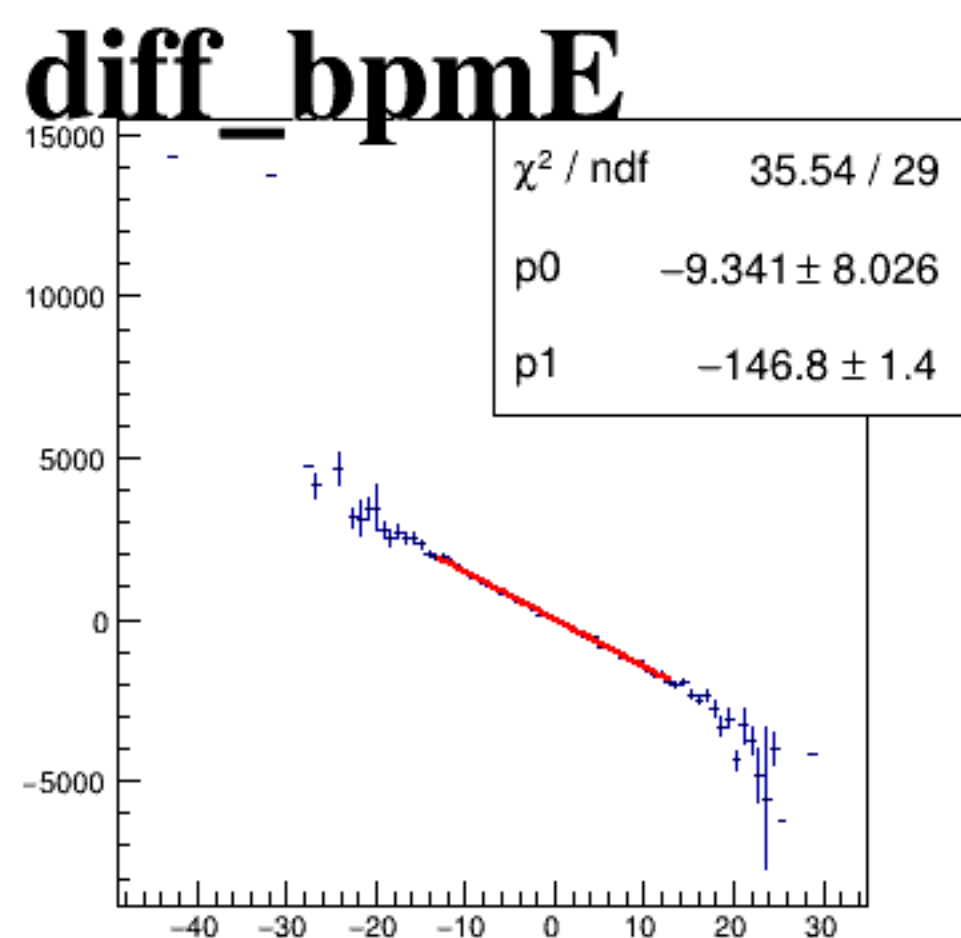
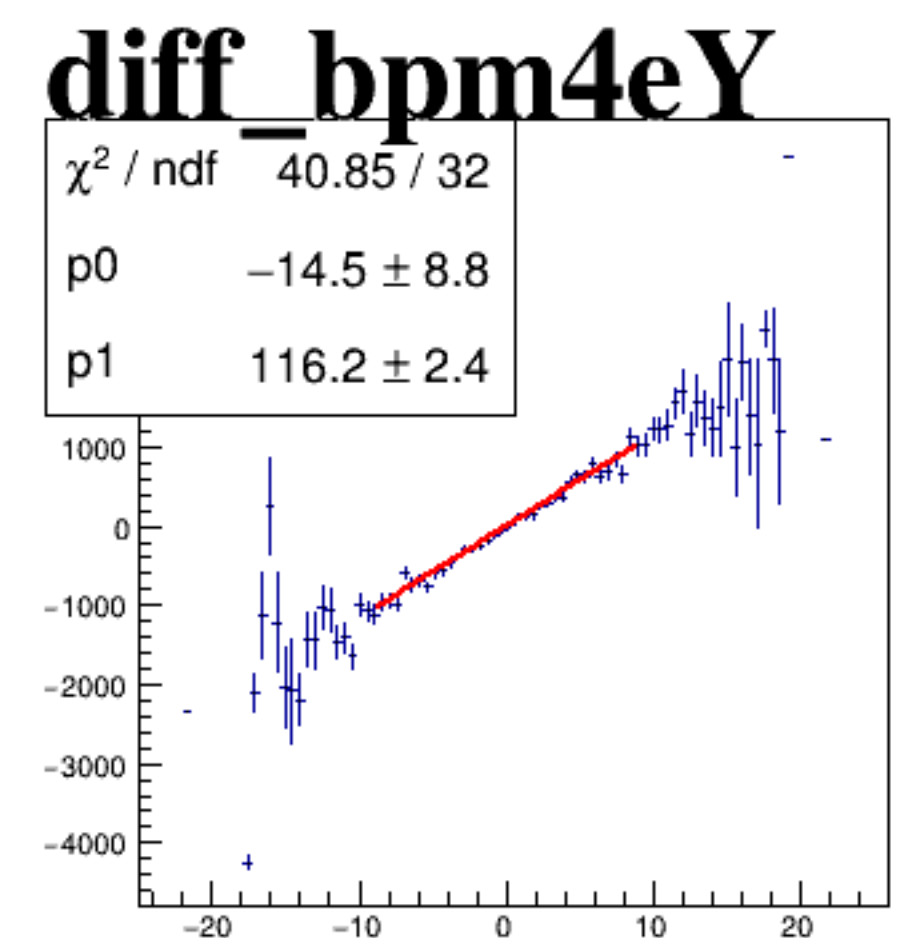
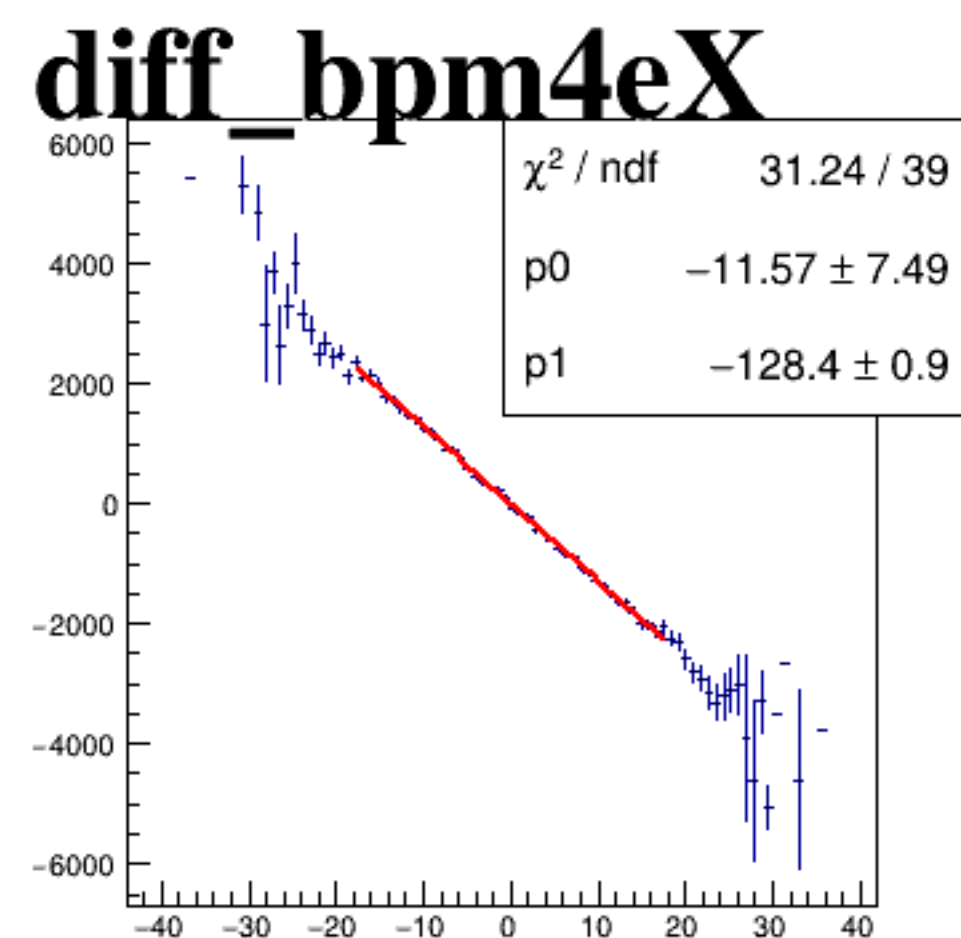
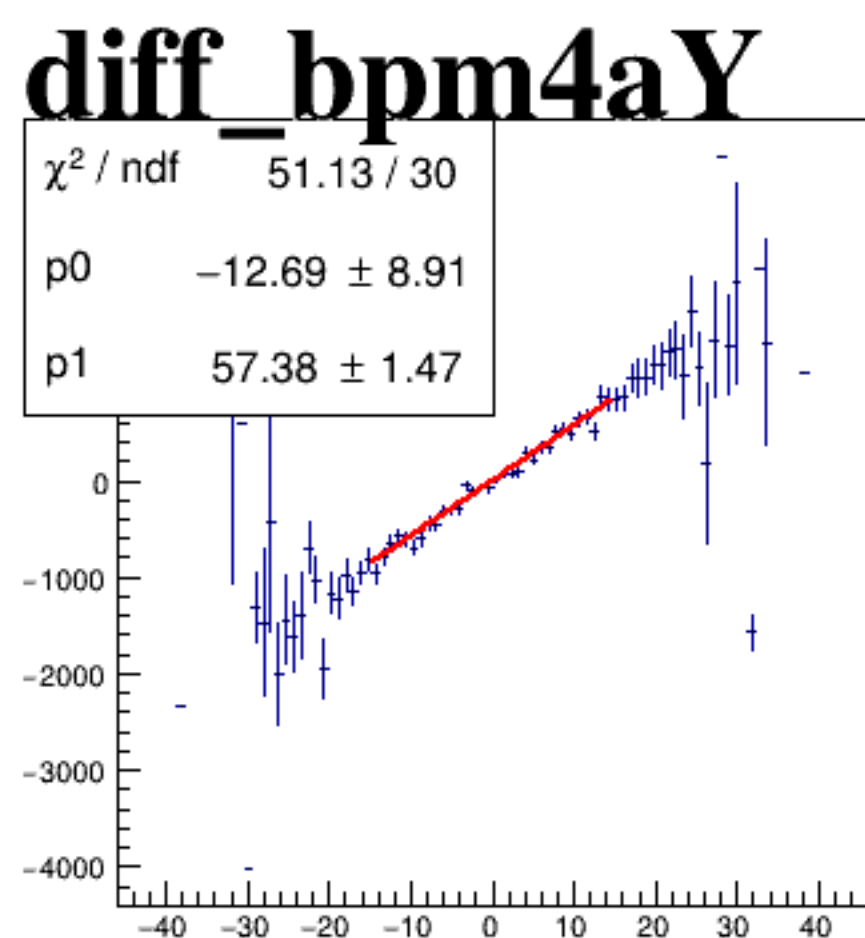
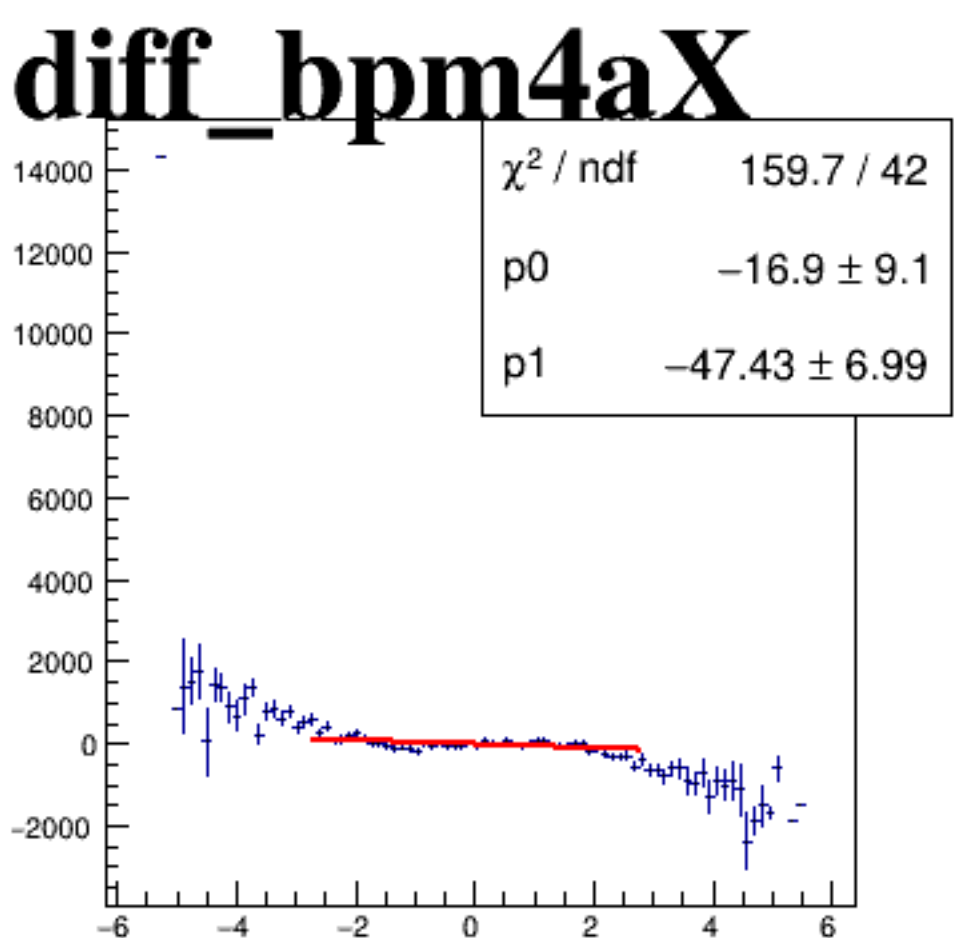
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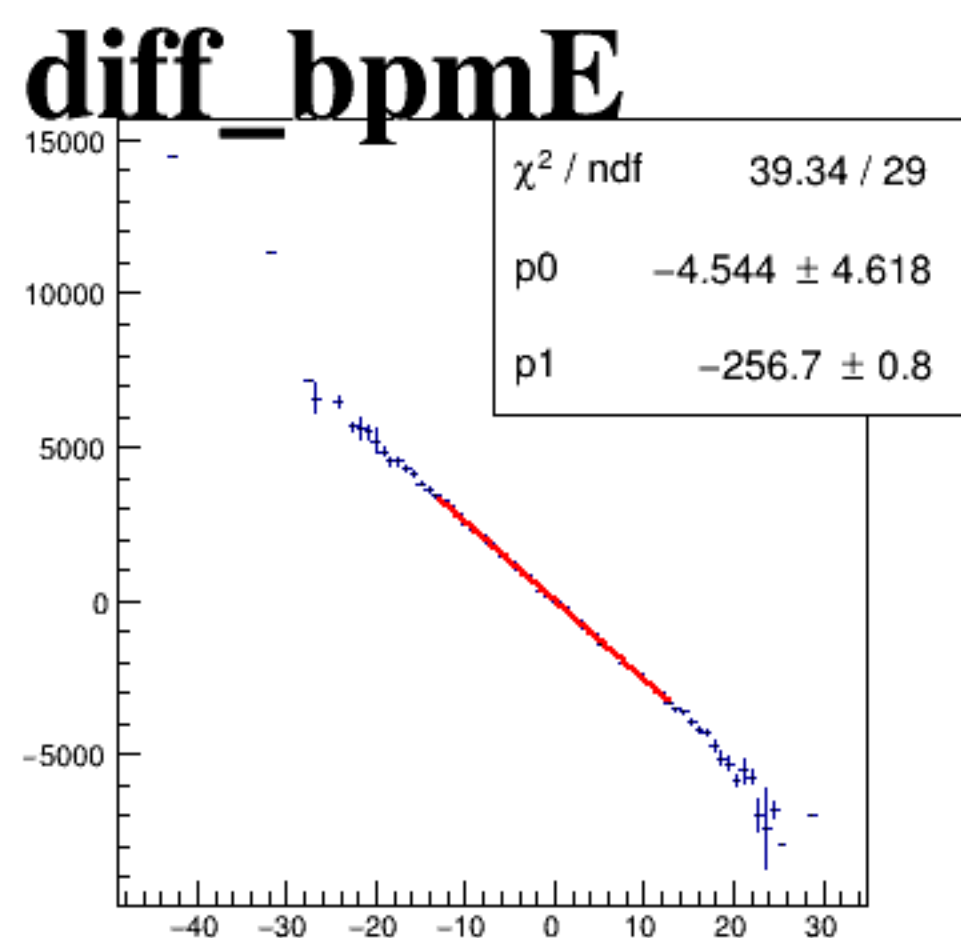
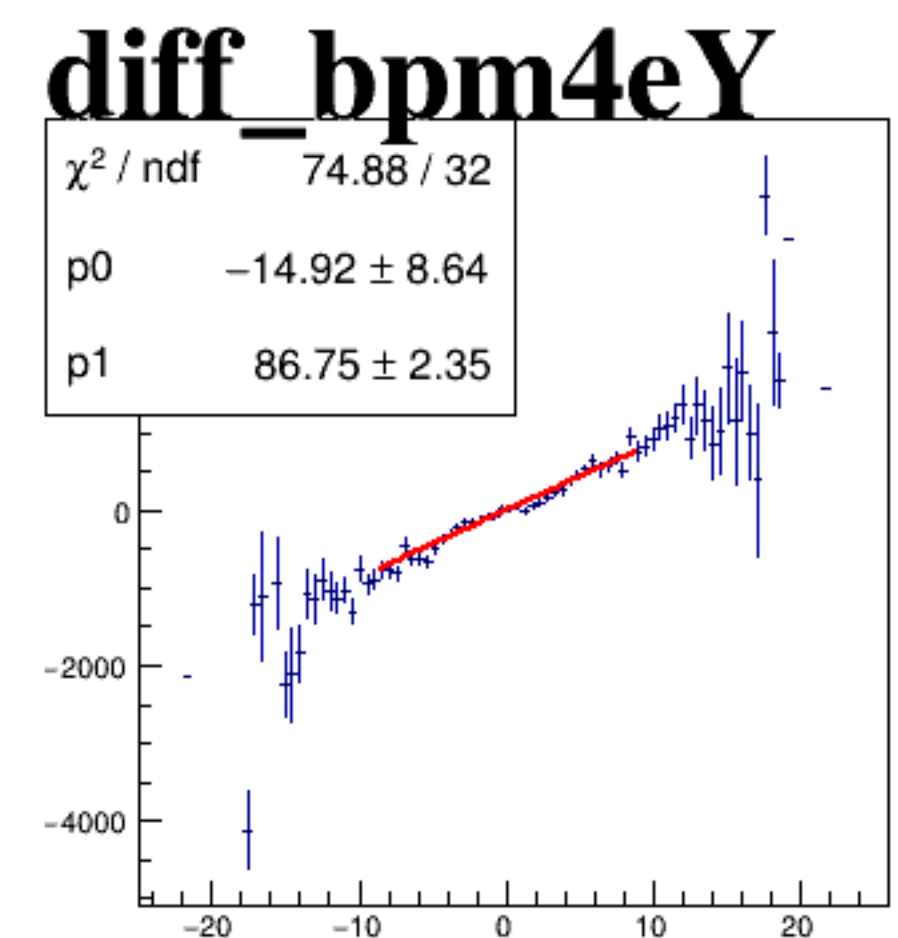
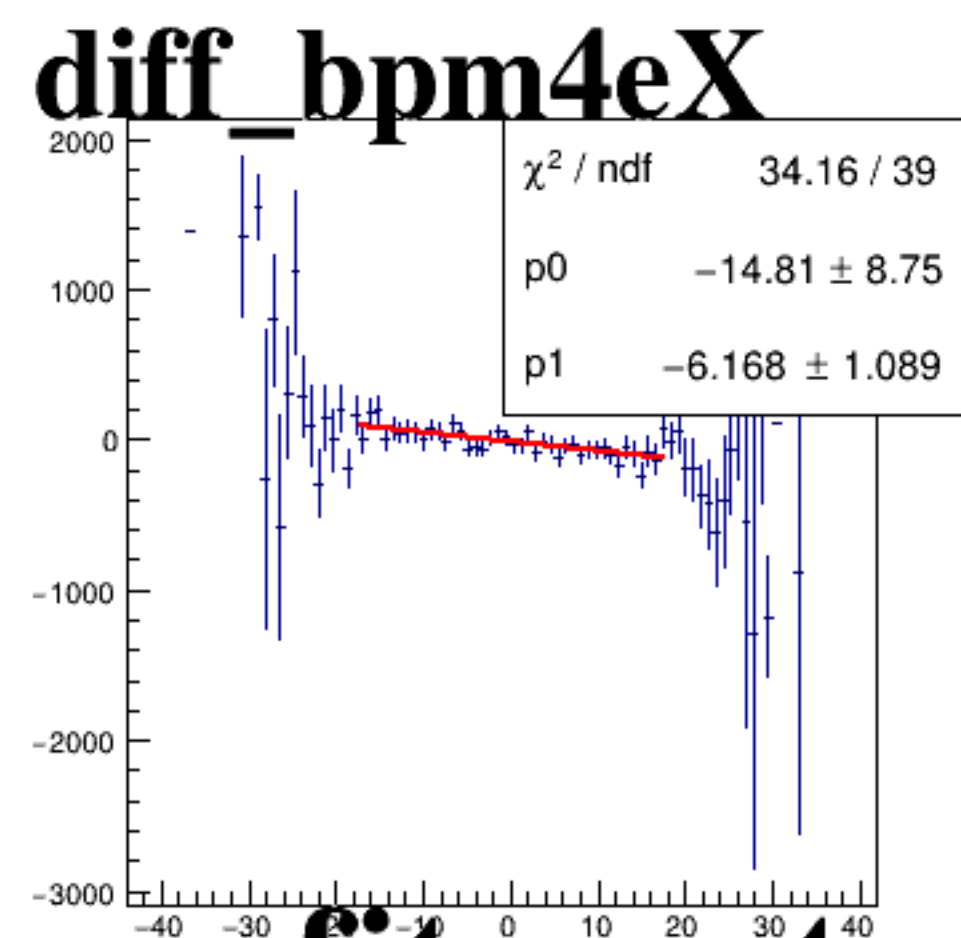
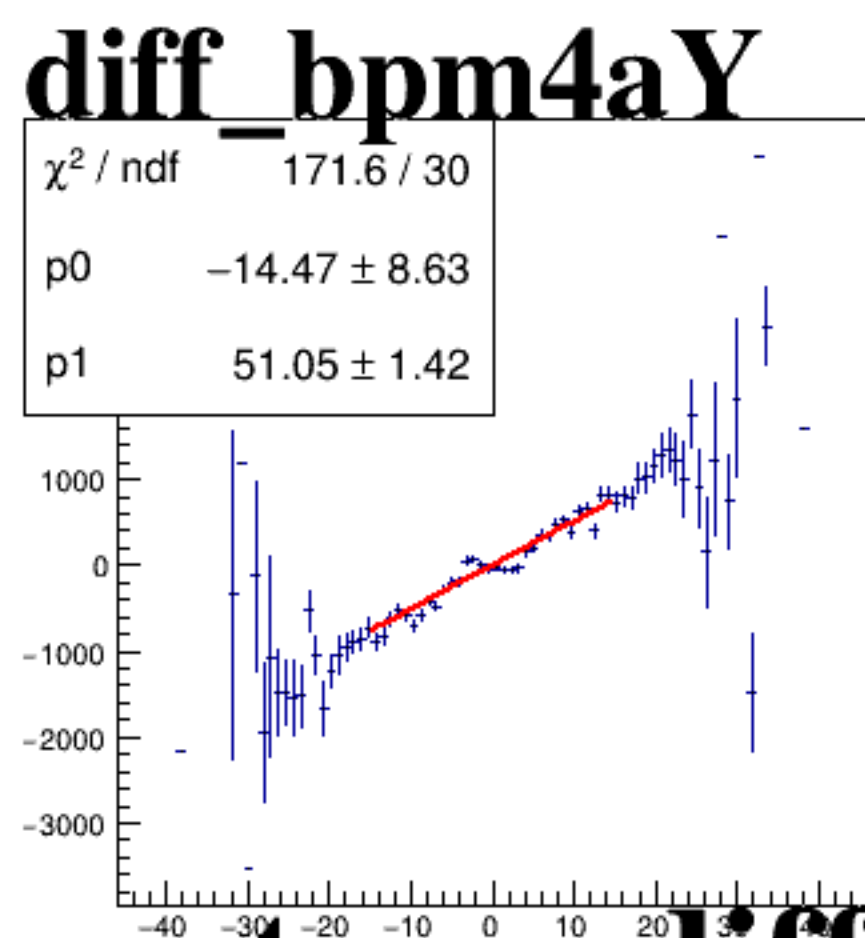
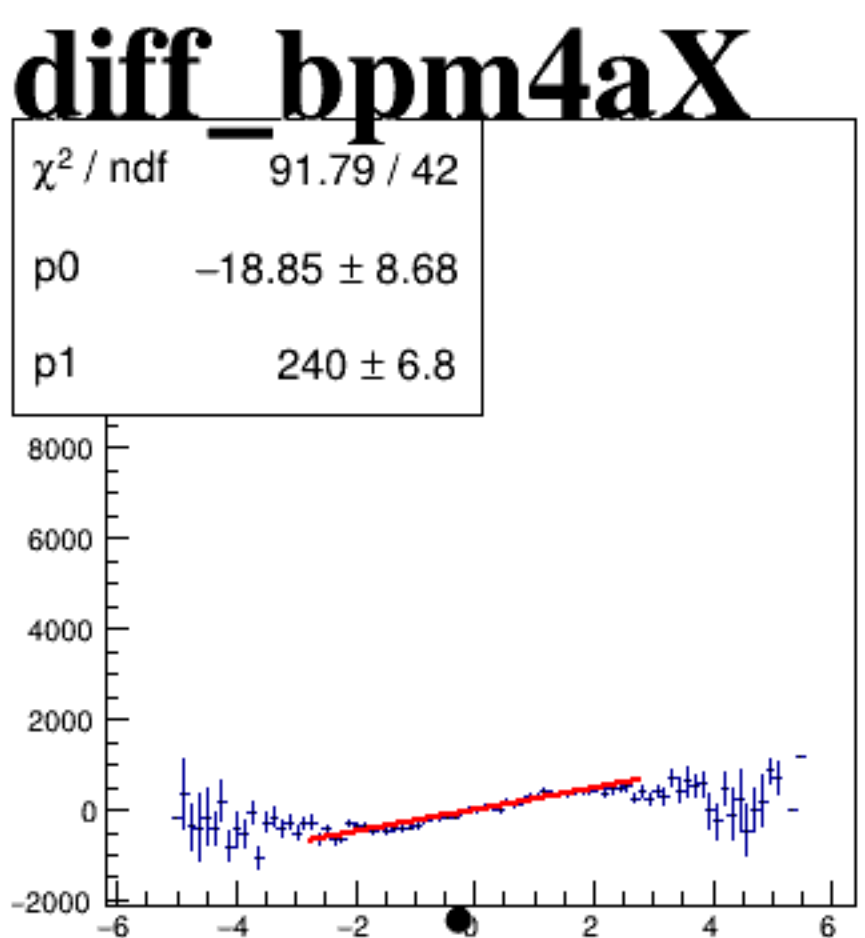
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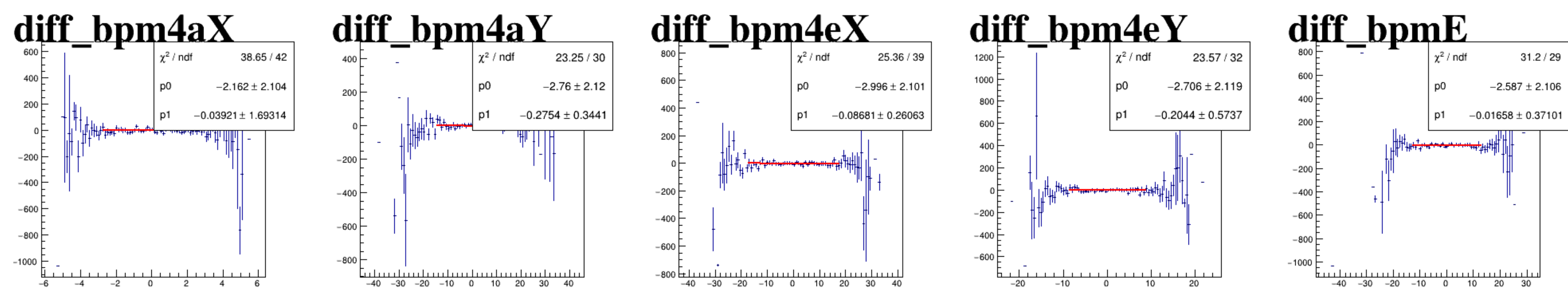
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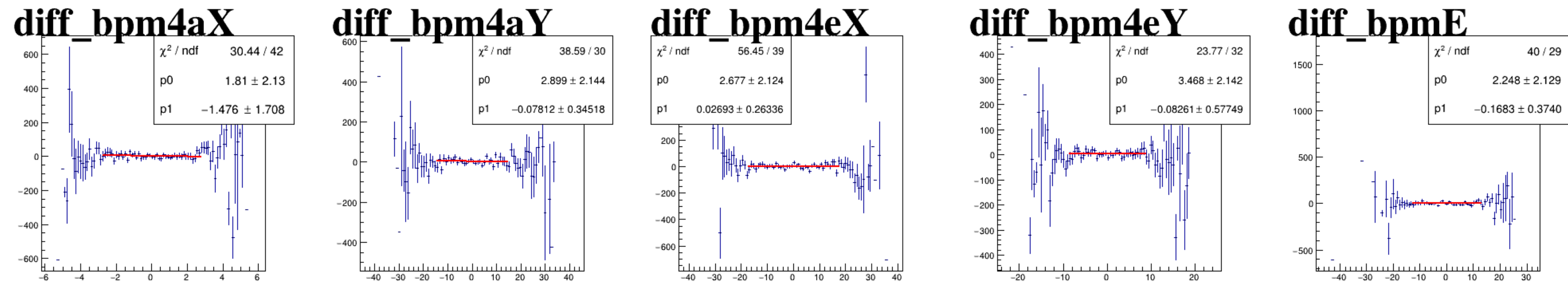
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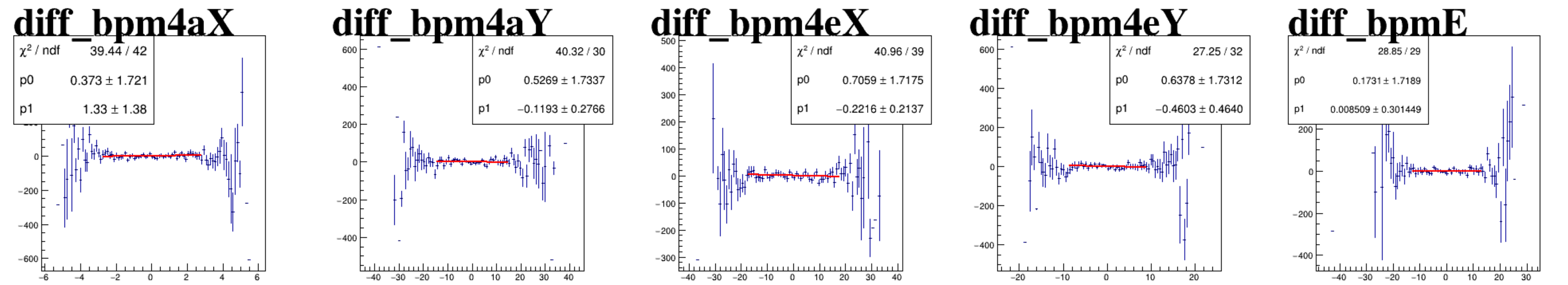
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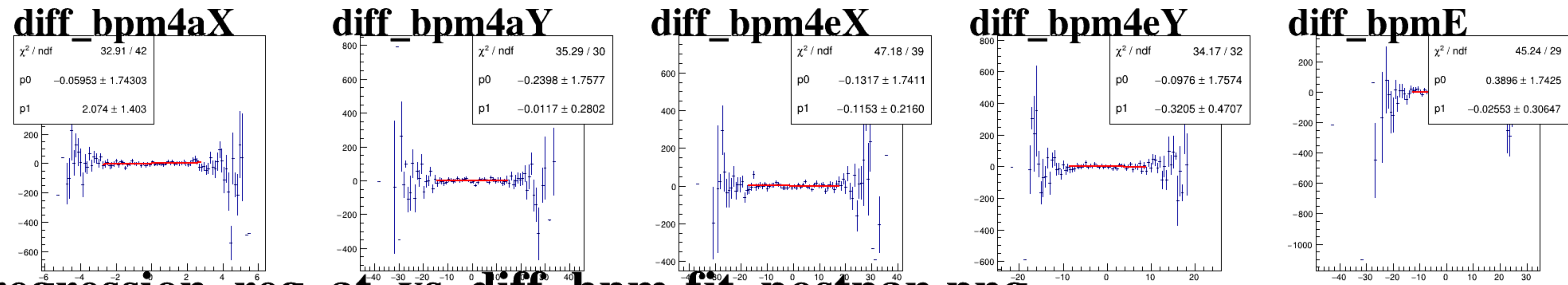
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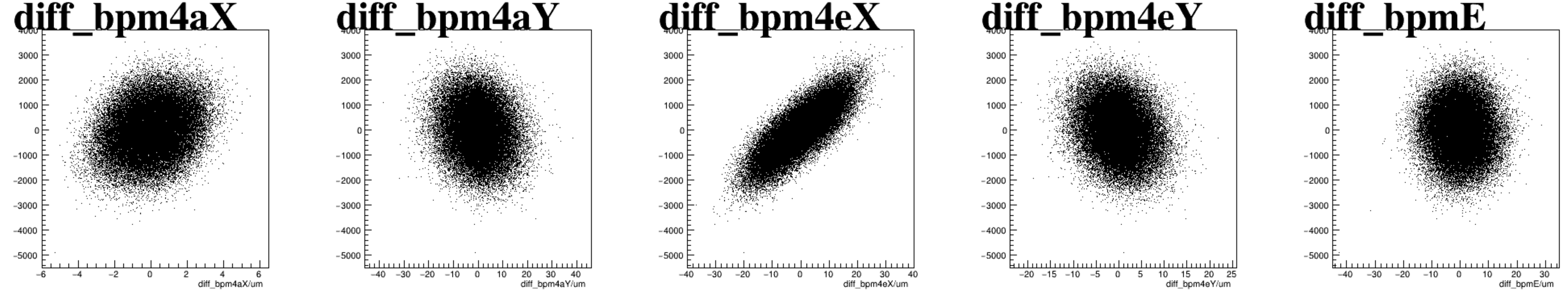
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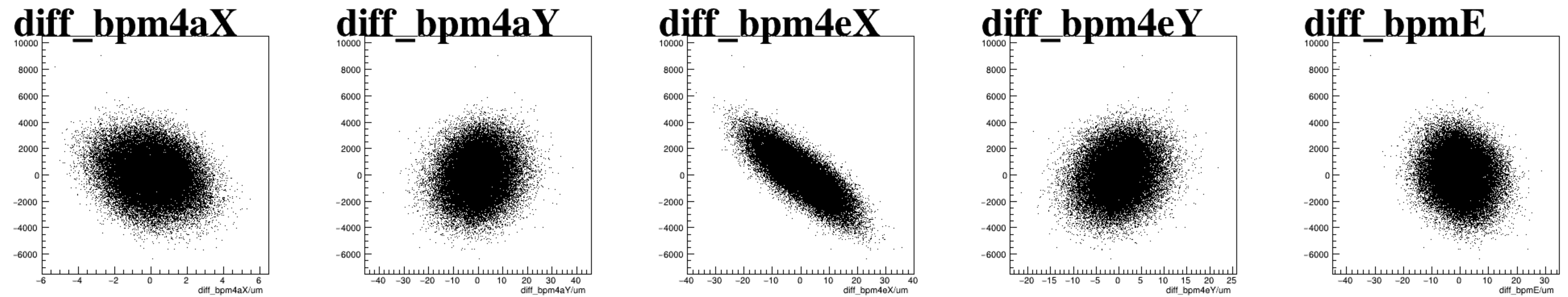
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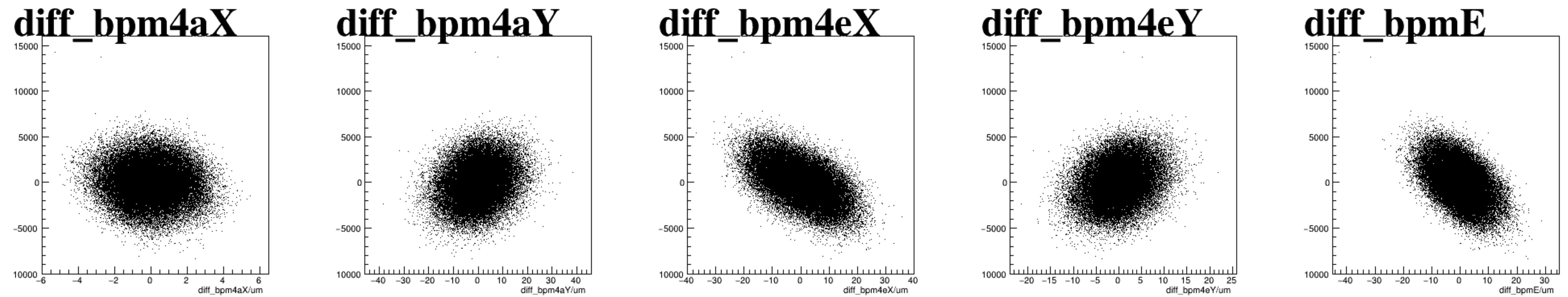
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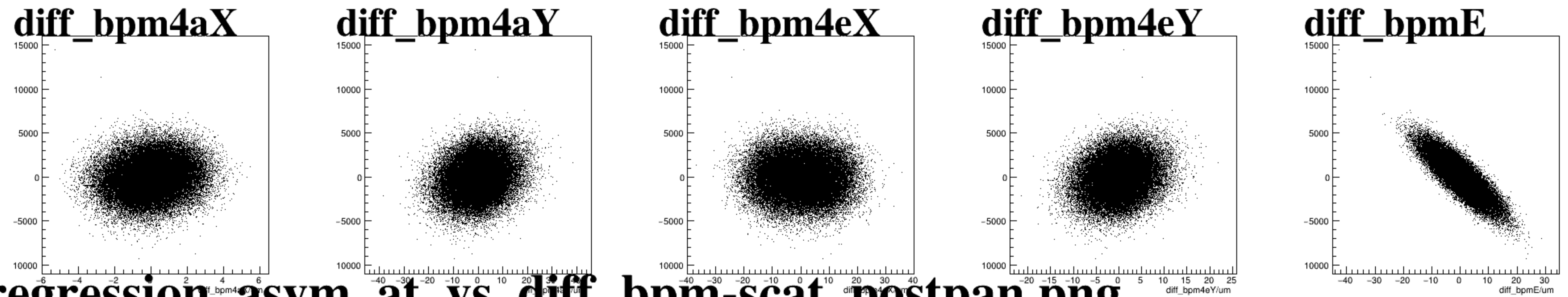
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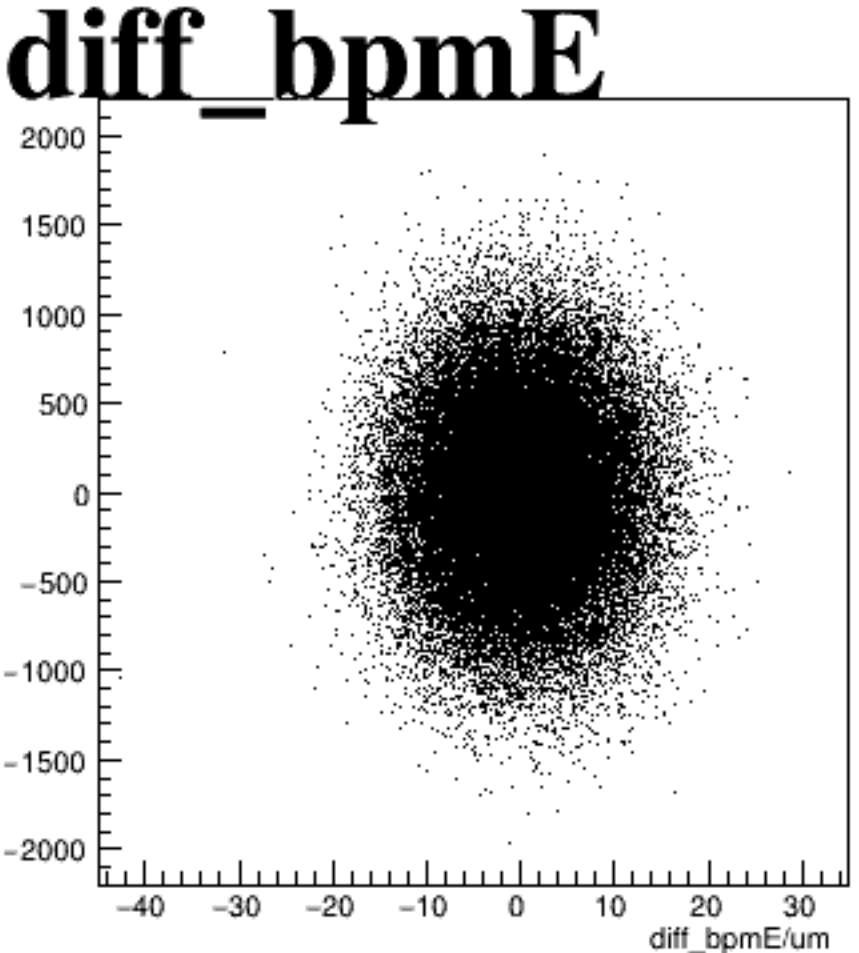
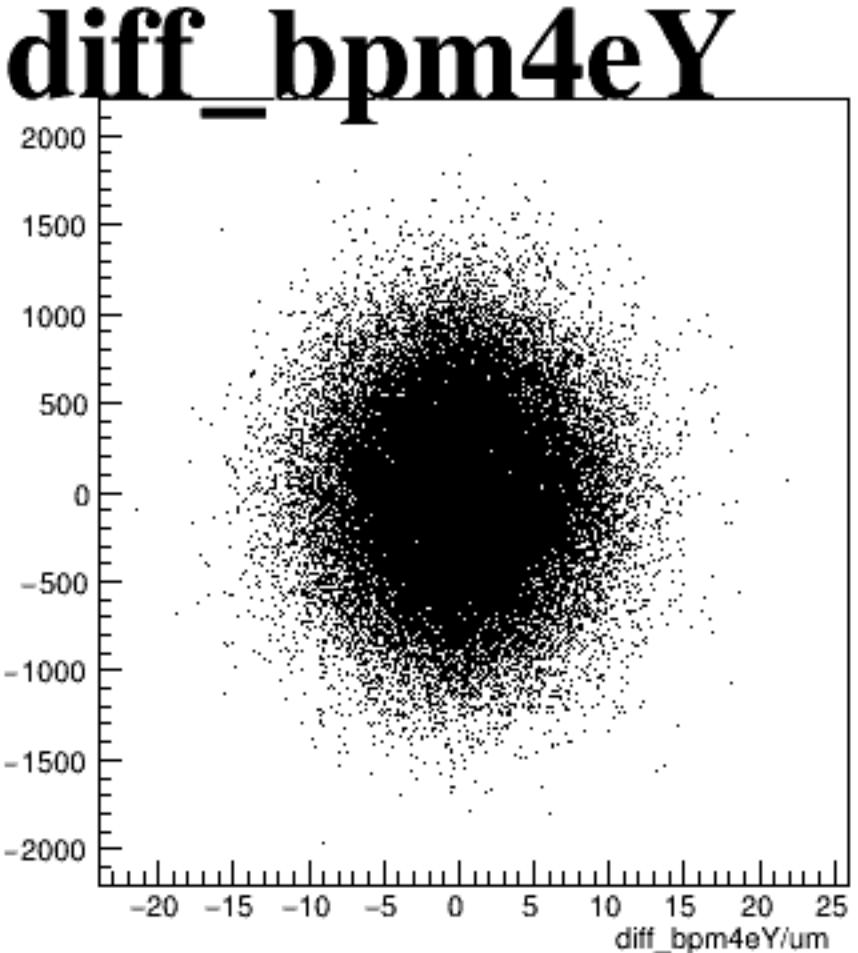
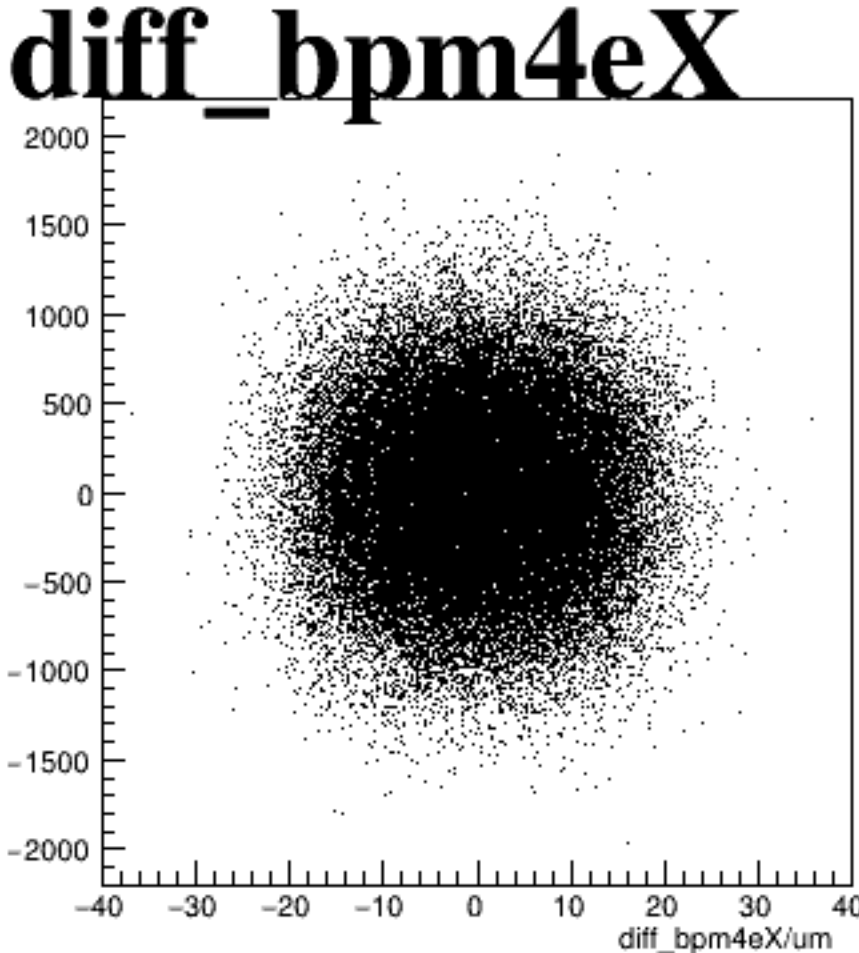
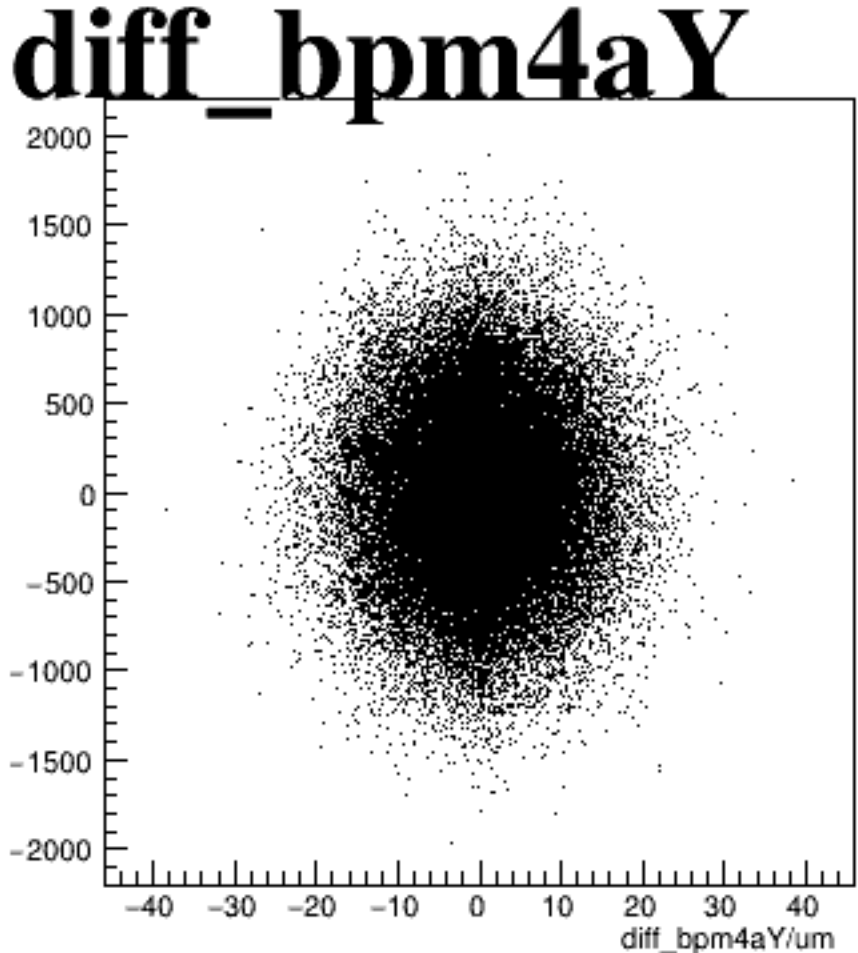
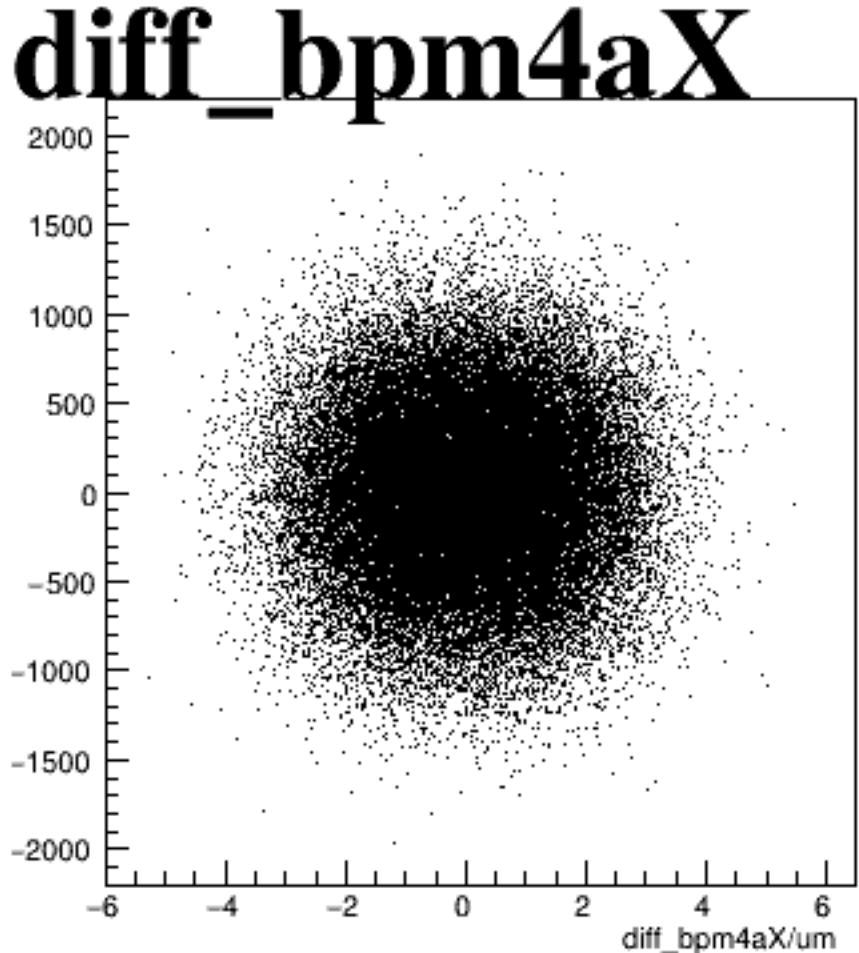
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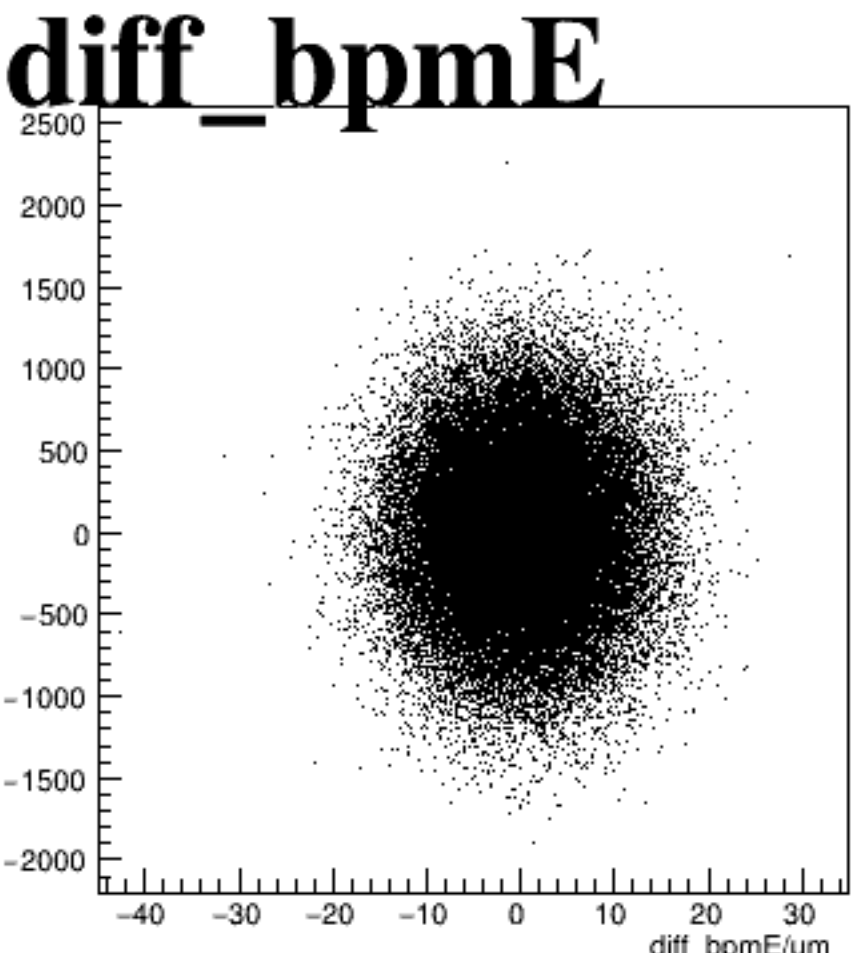
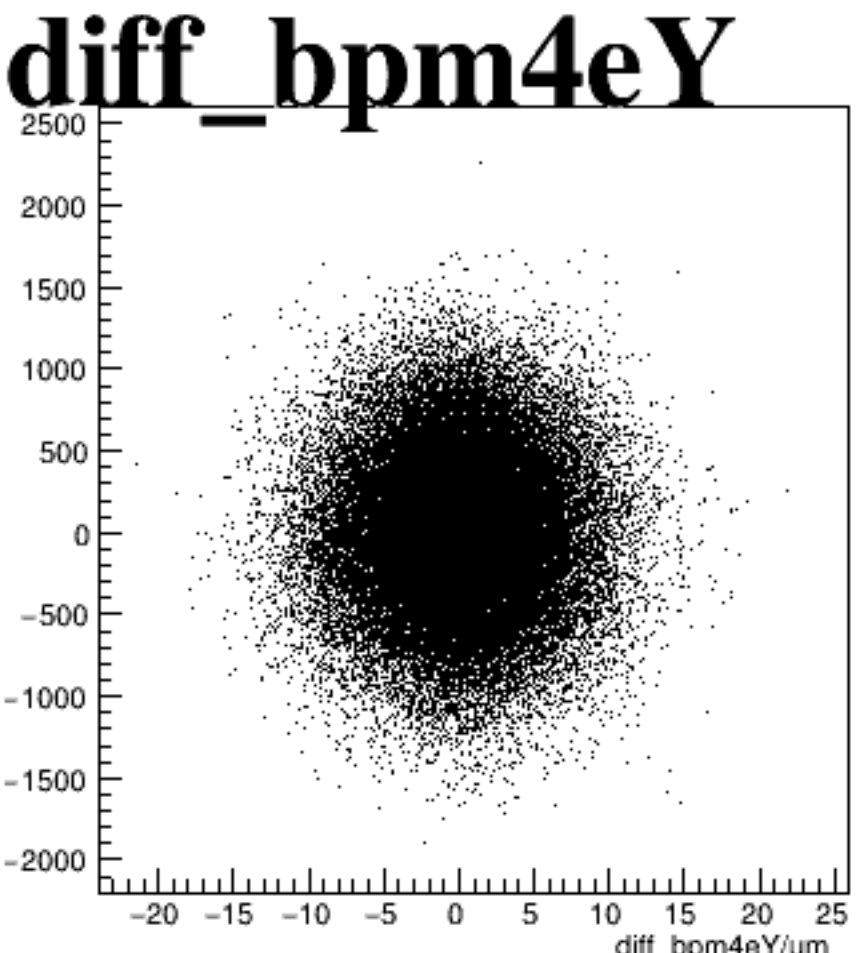
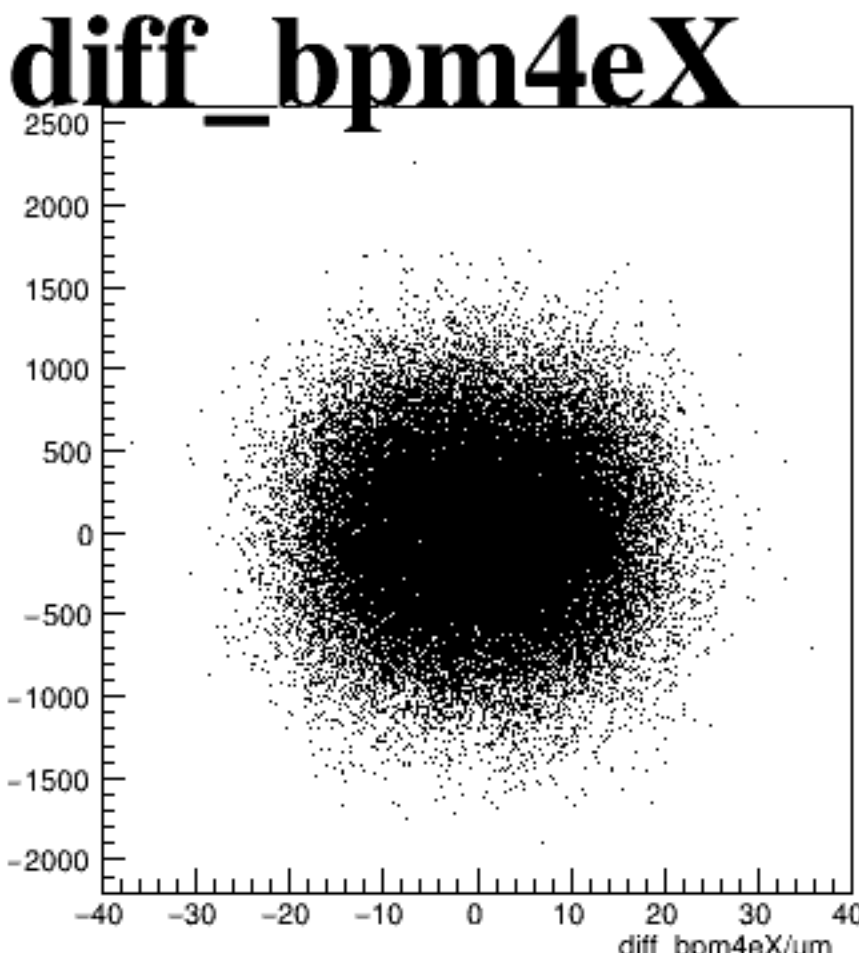
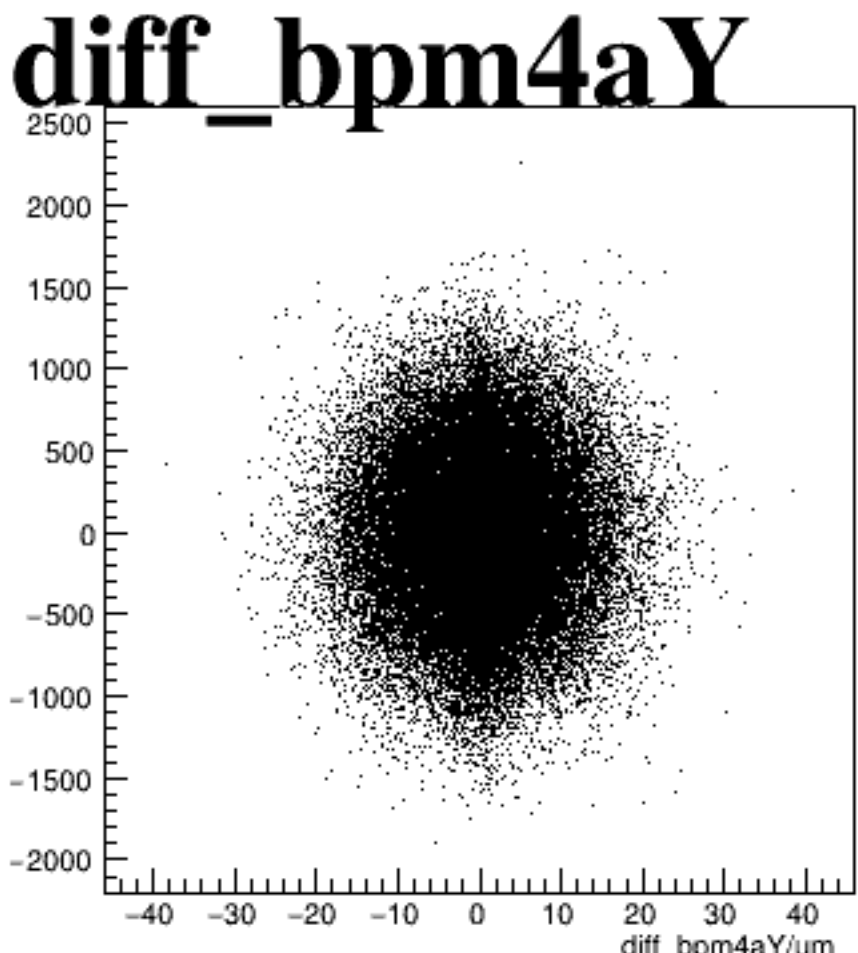
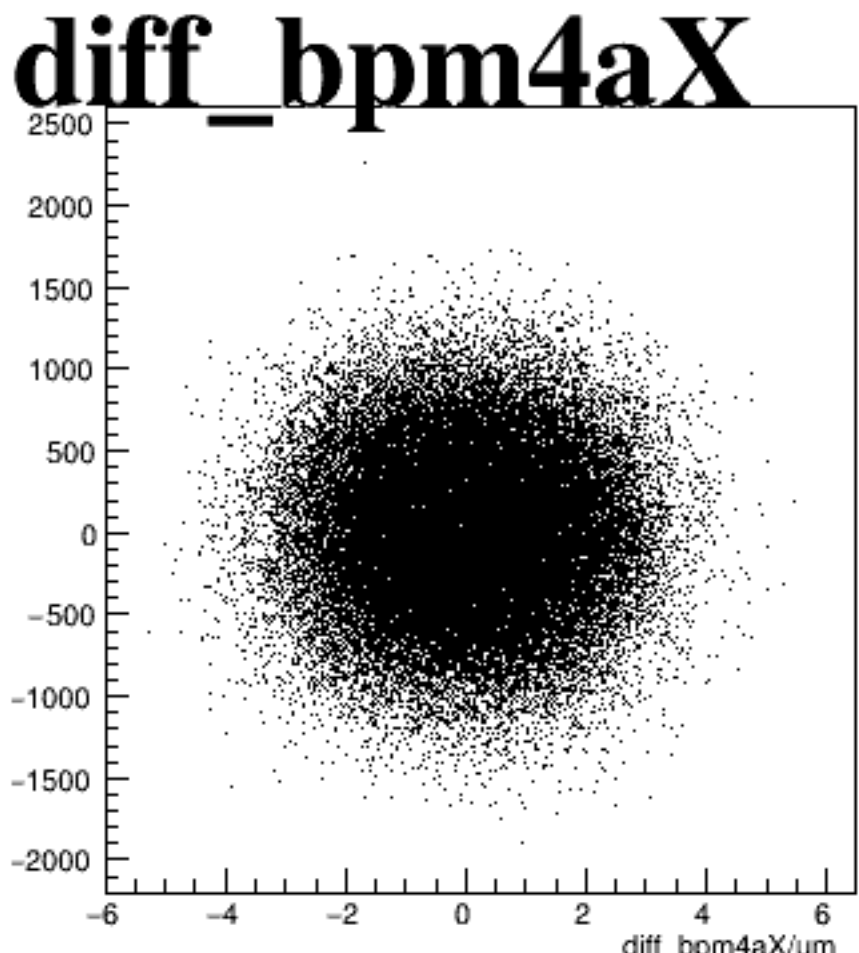
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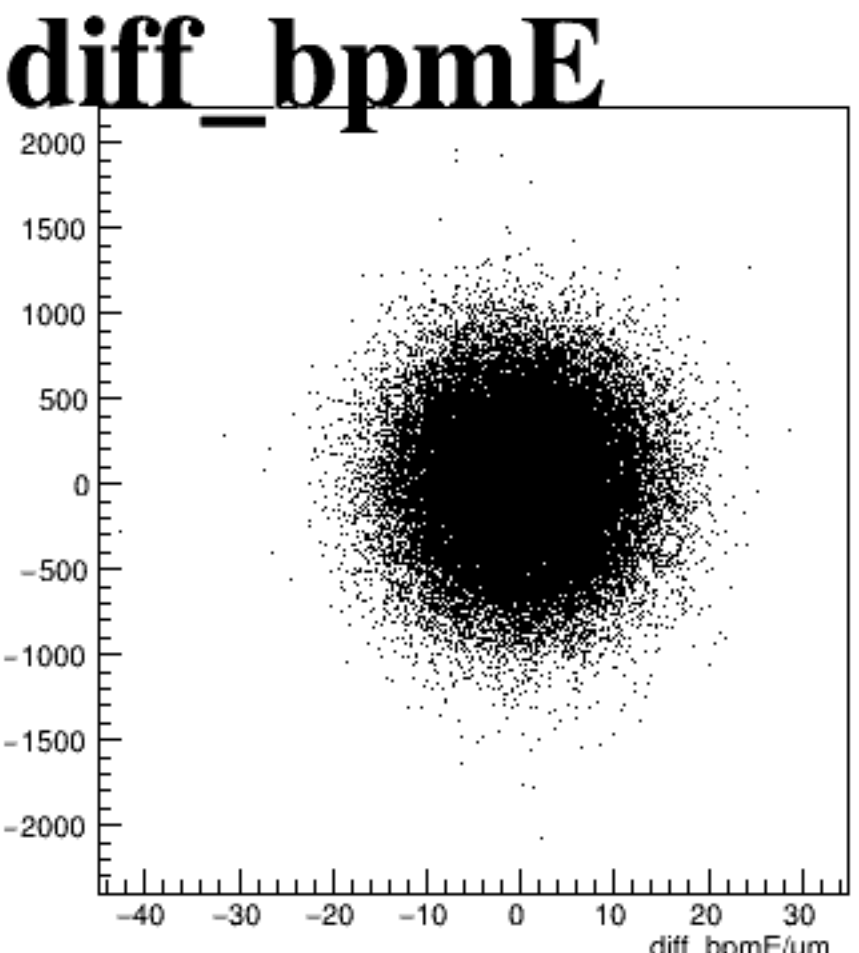
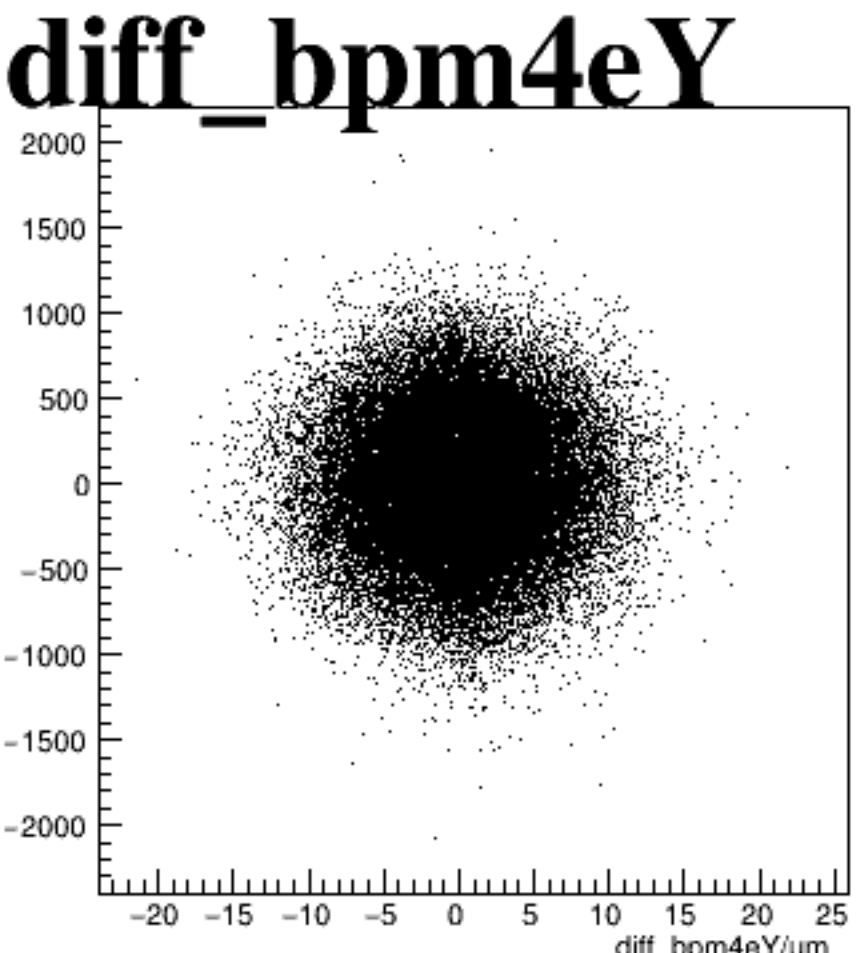
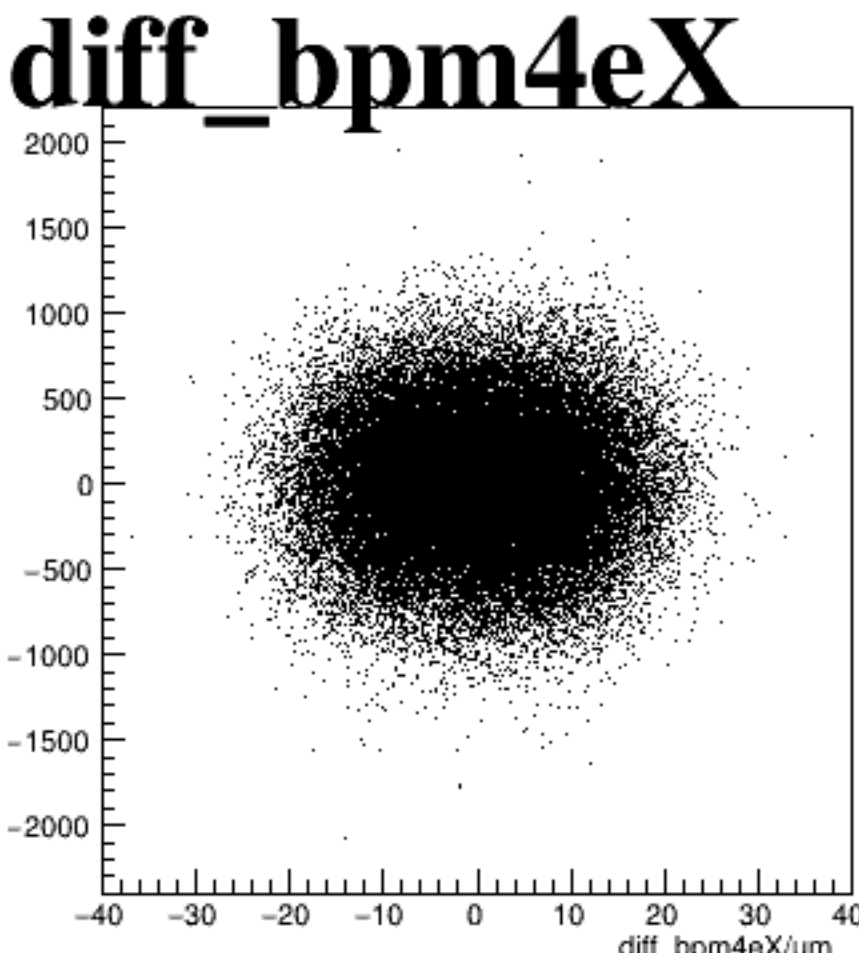
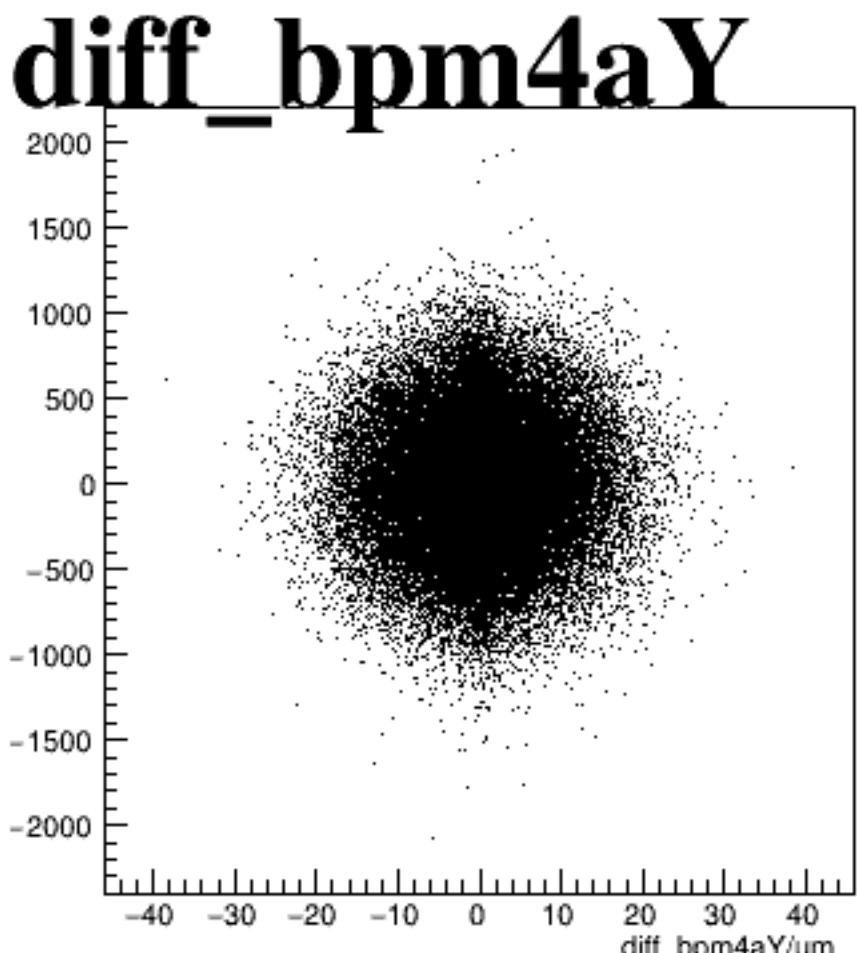
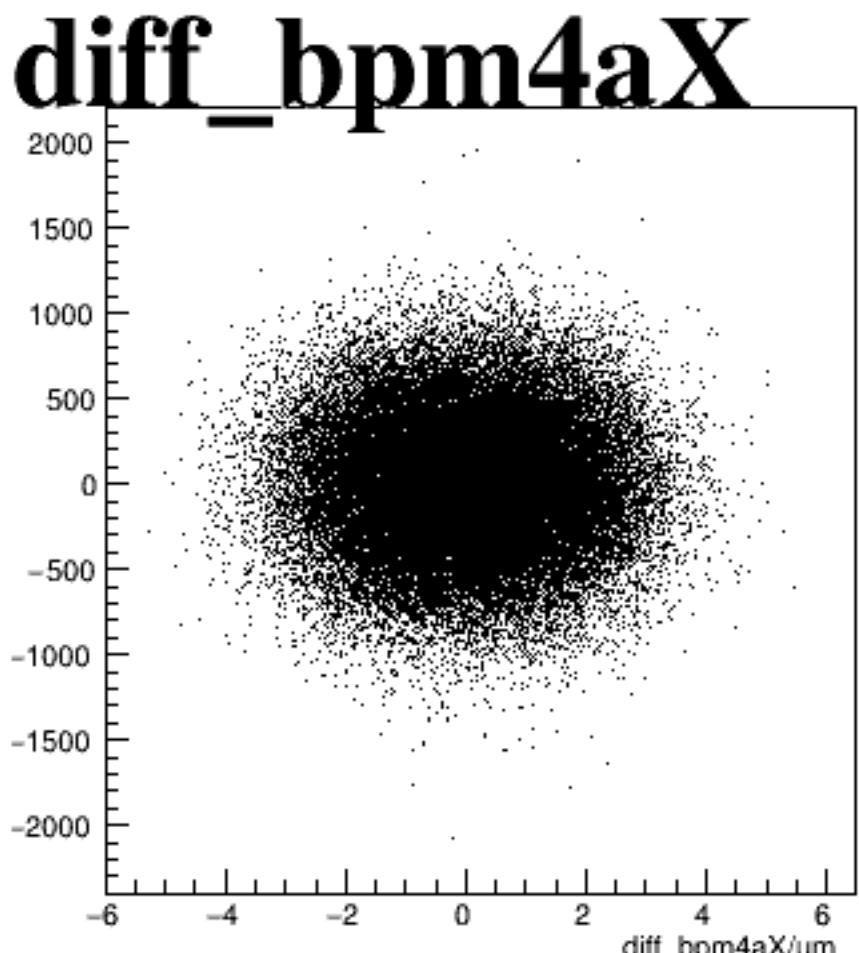
reg_asym_atl1



reg_asym_atr1



reg_asym_atl2



reg_asym_atr2

