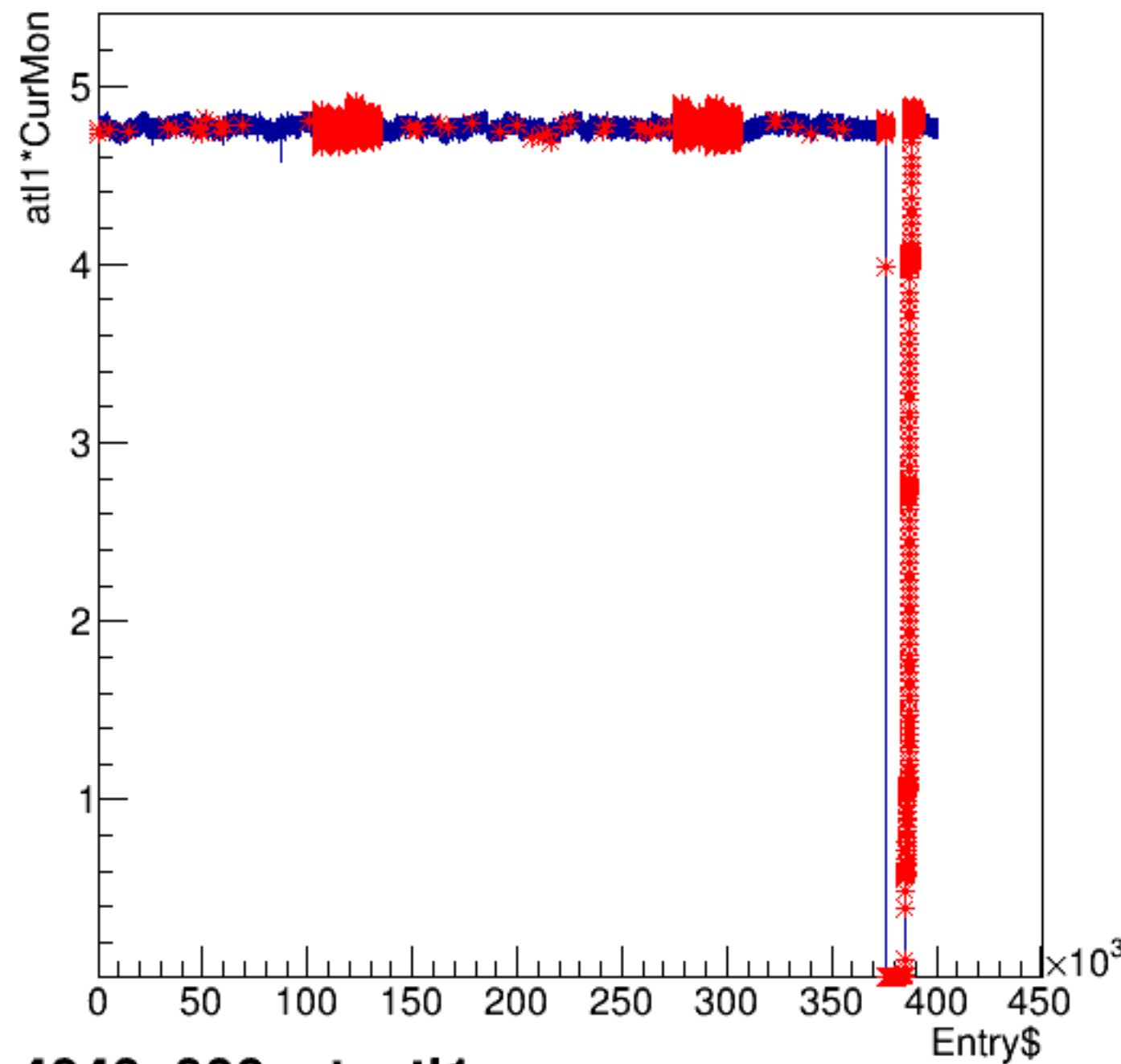
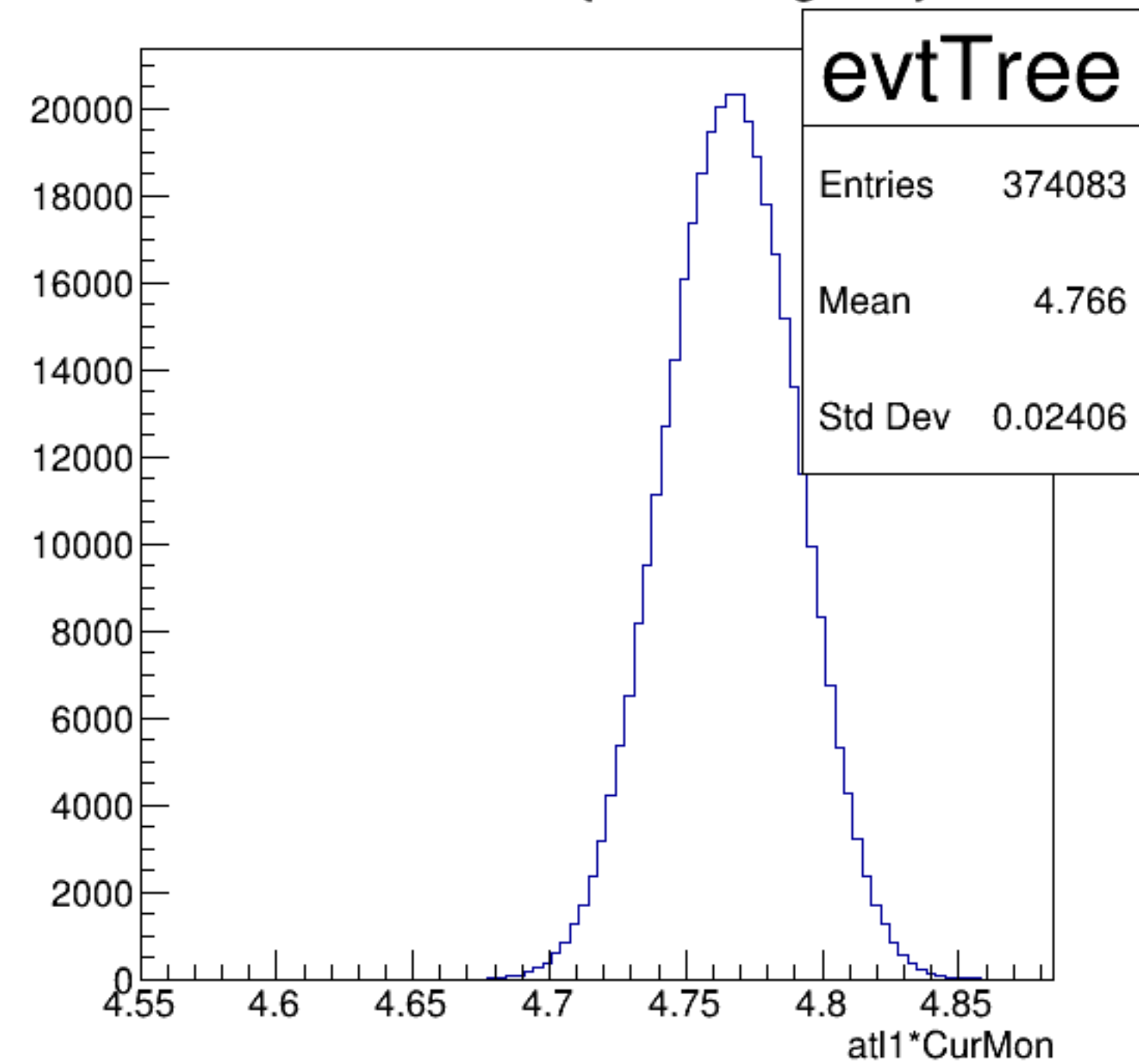


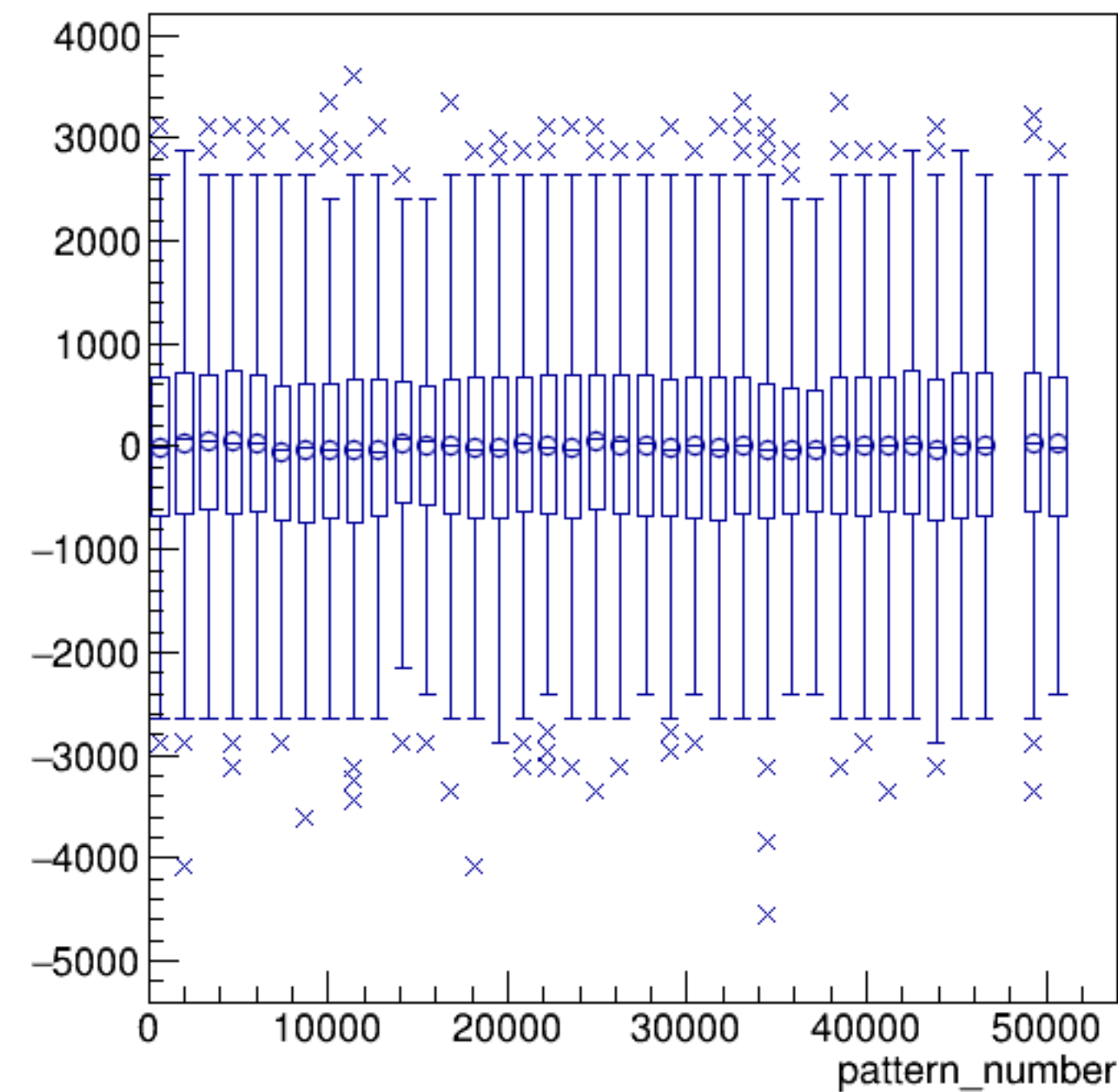
atl1\*CurMon:Entry\$



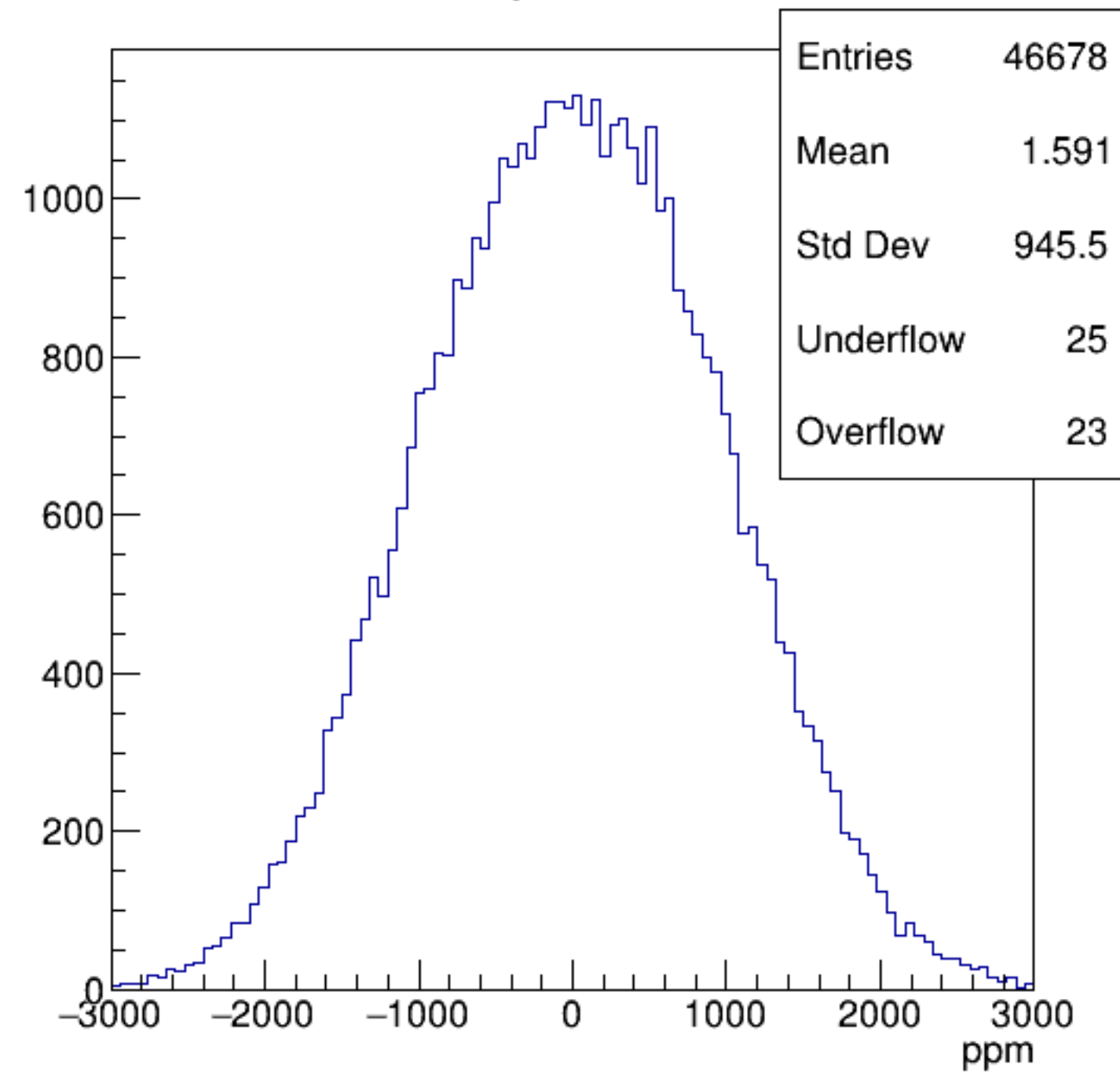
atl1\*CurMon {ErrorFlag==0}



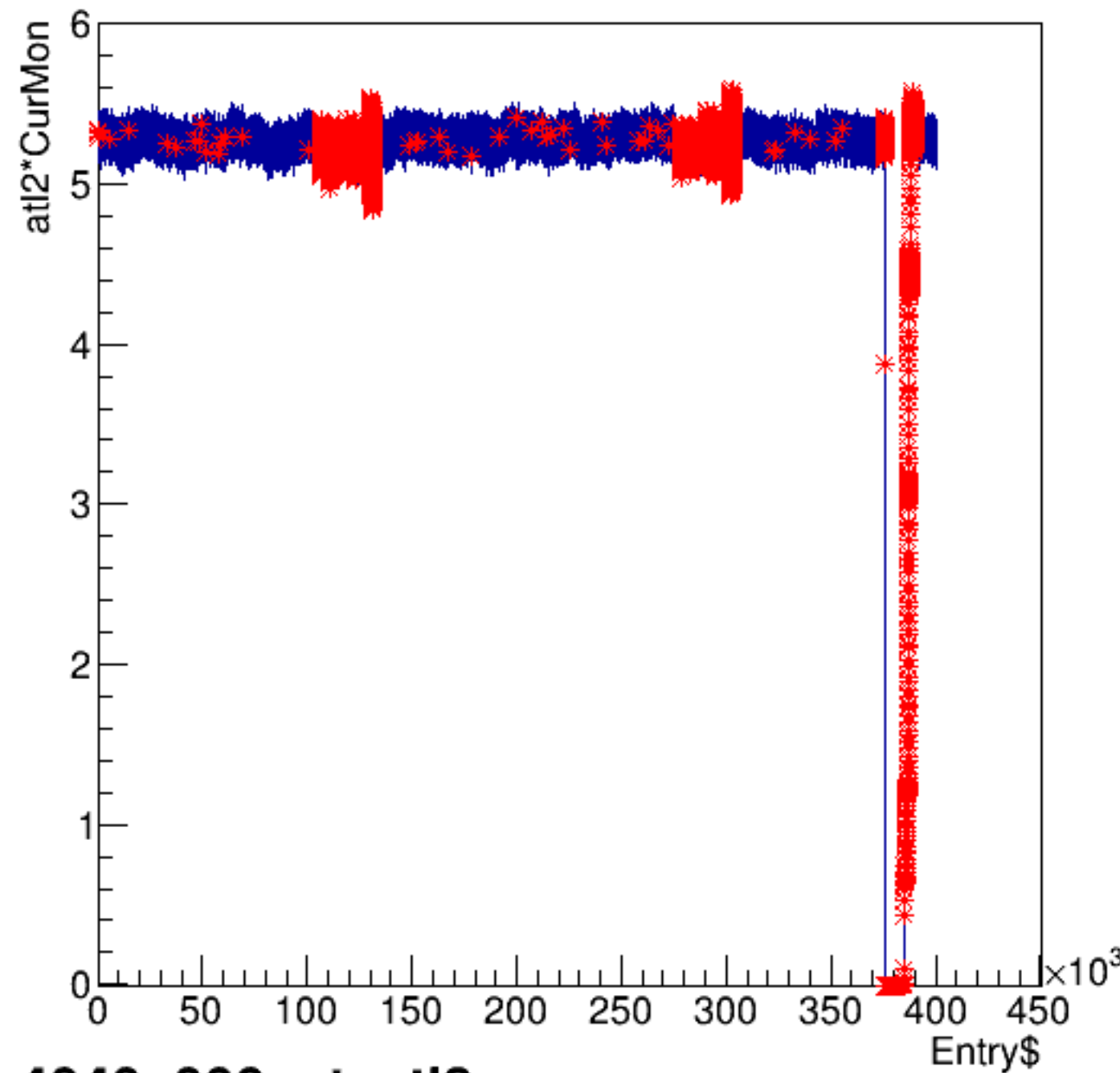
asym\_atl1/ppm:pattern\_number {ErrorFlag==0}



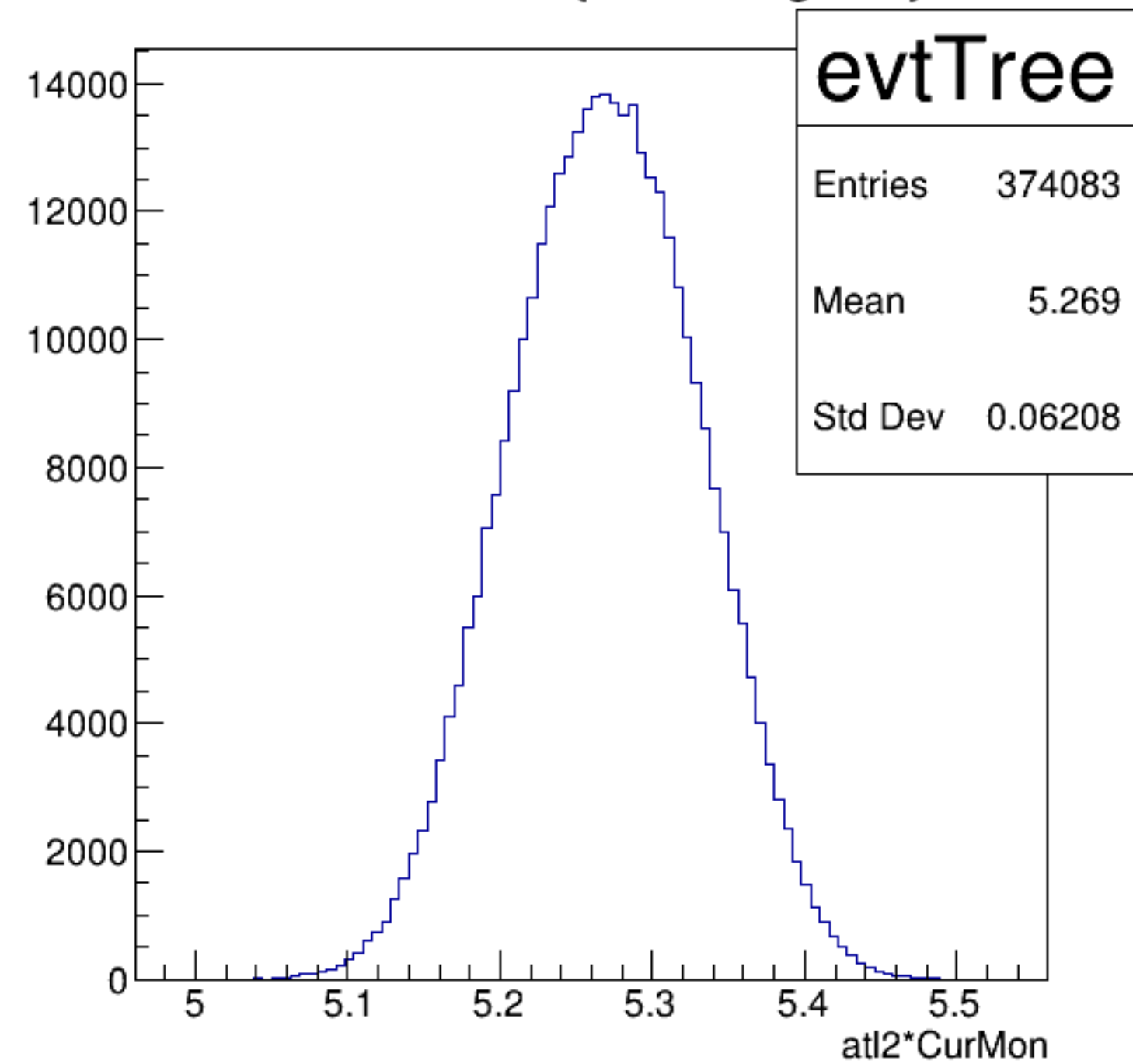
asym\_atl1



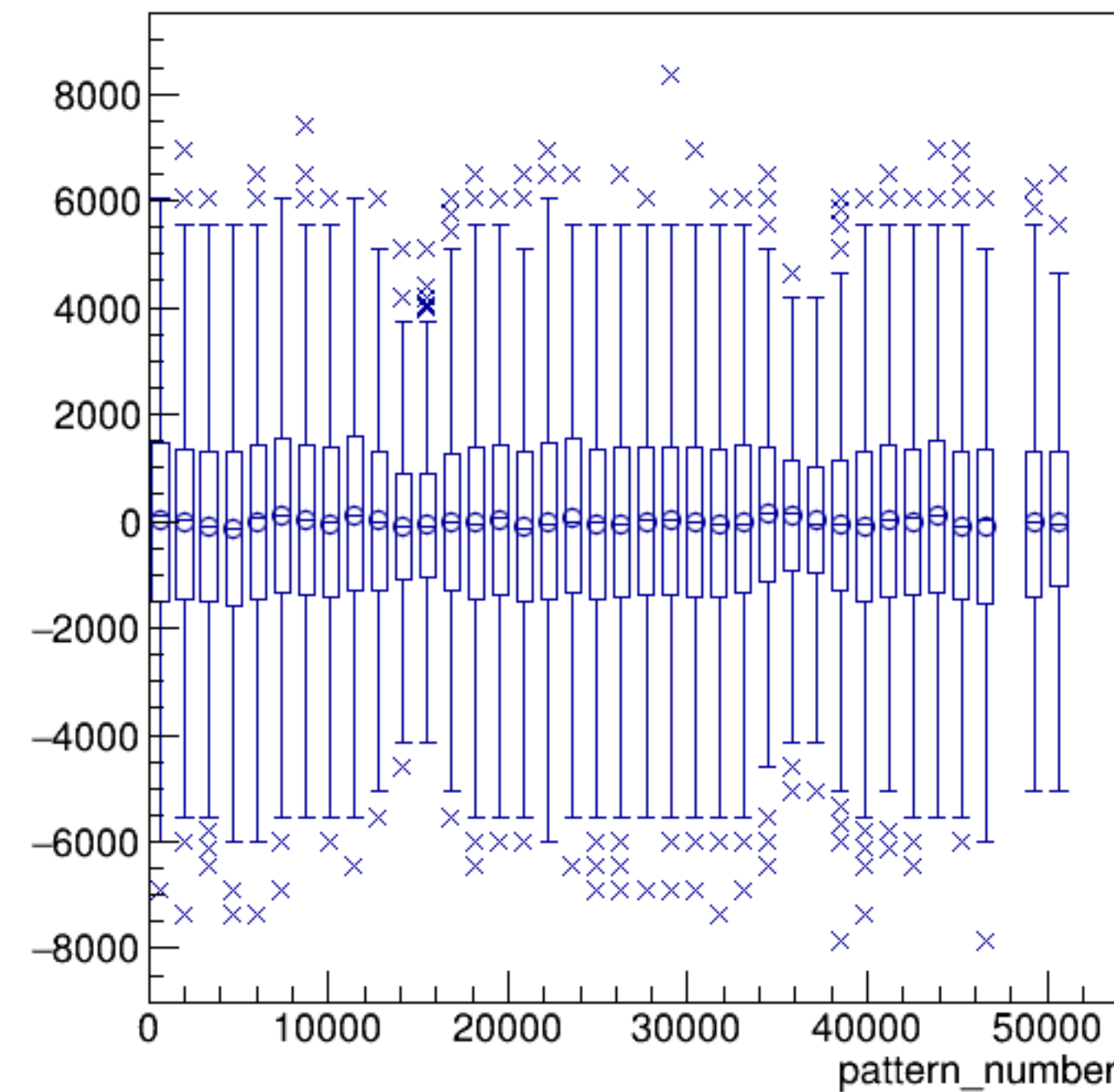
atl2\*CurMon:Entry\$



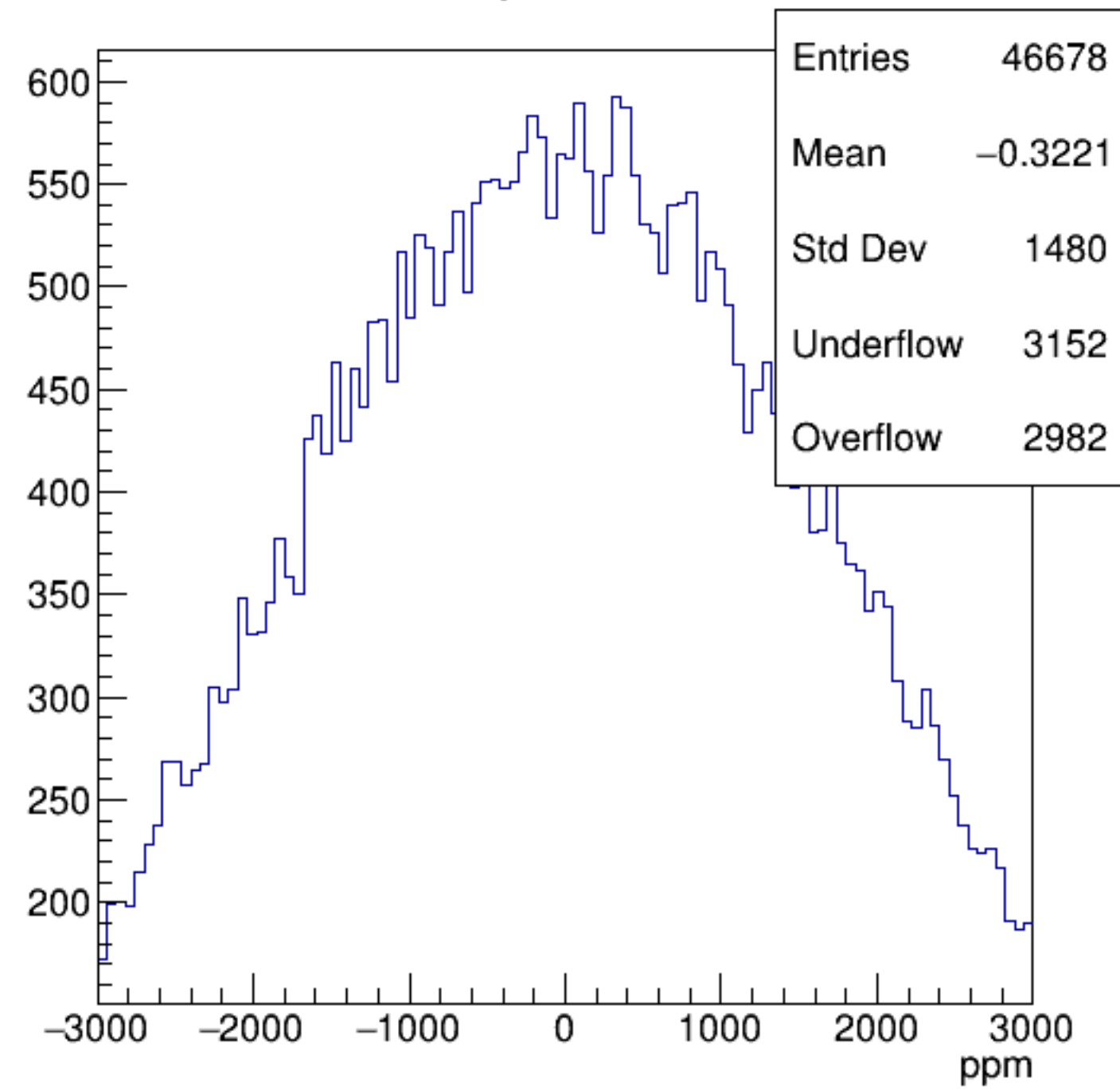
atl2\*CurMon {ErrorFlag==0}



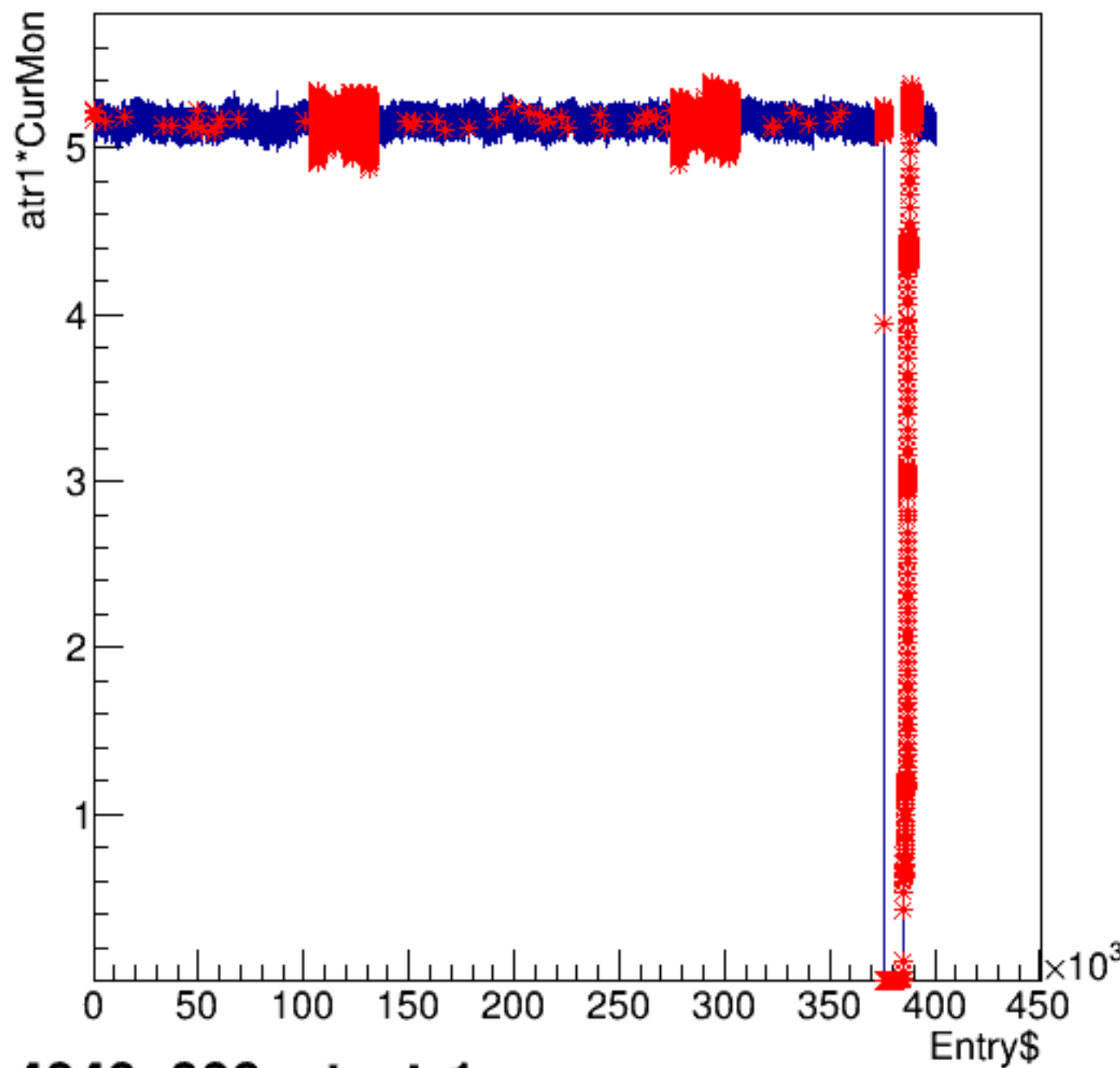
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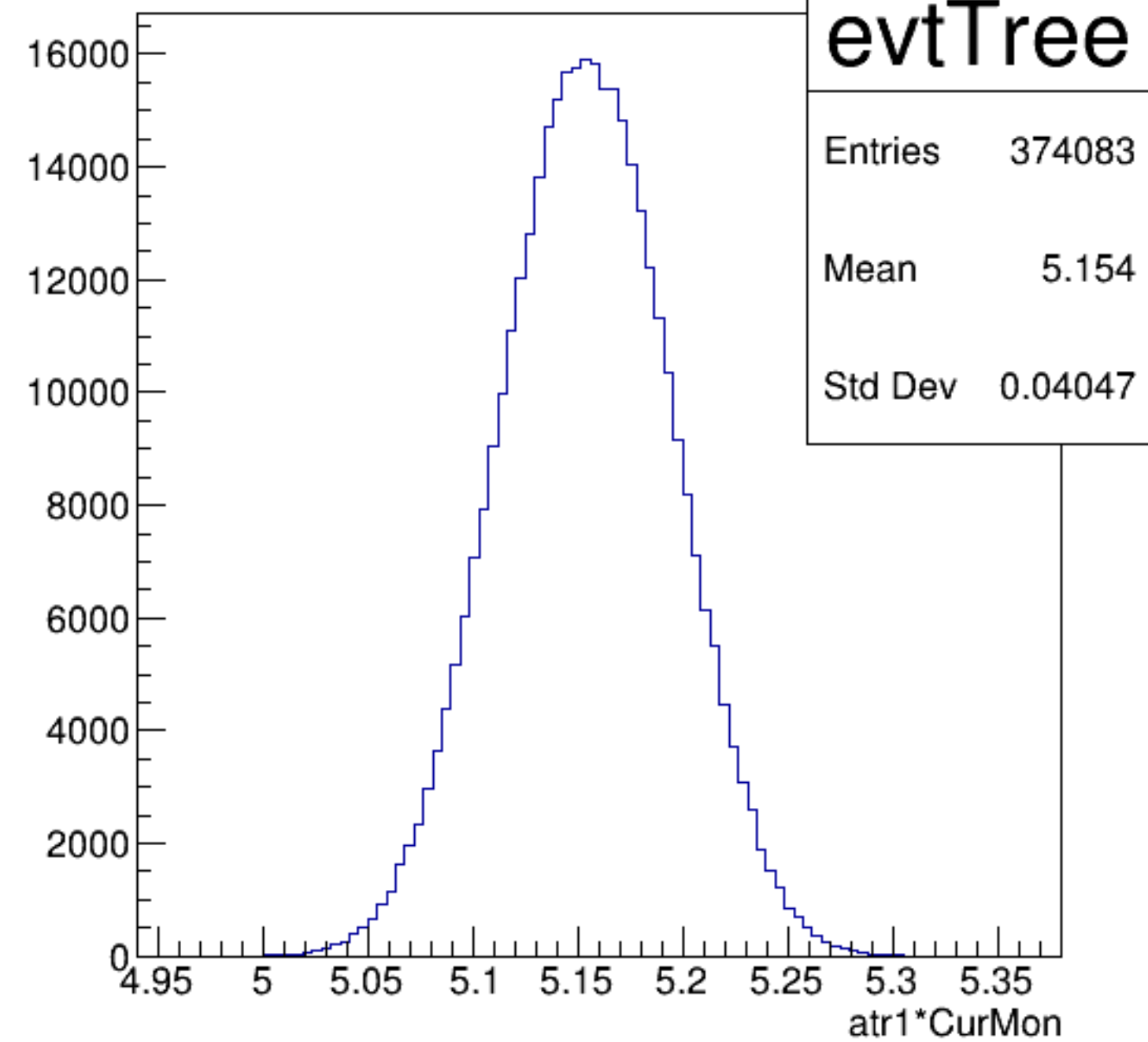
asym\_atl2



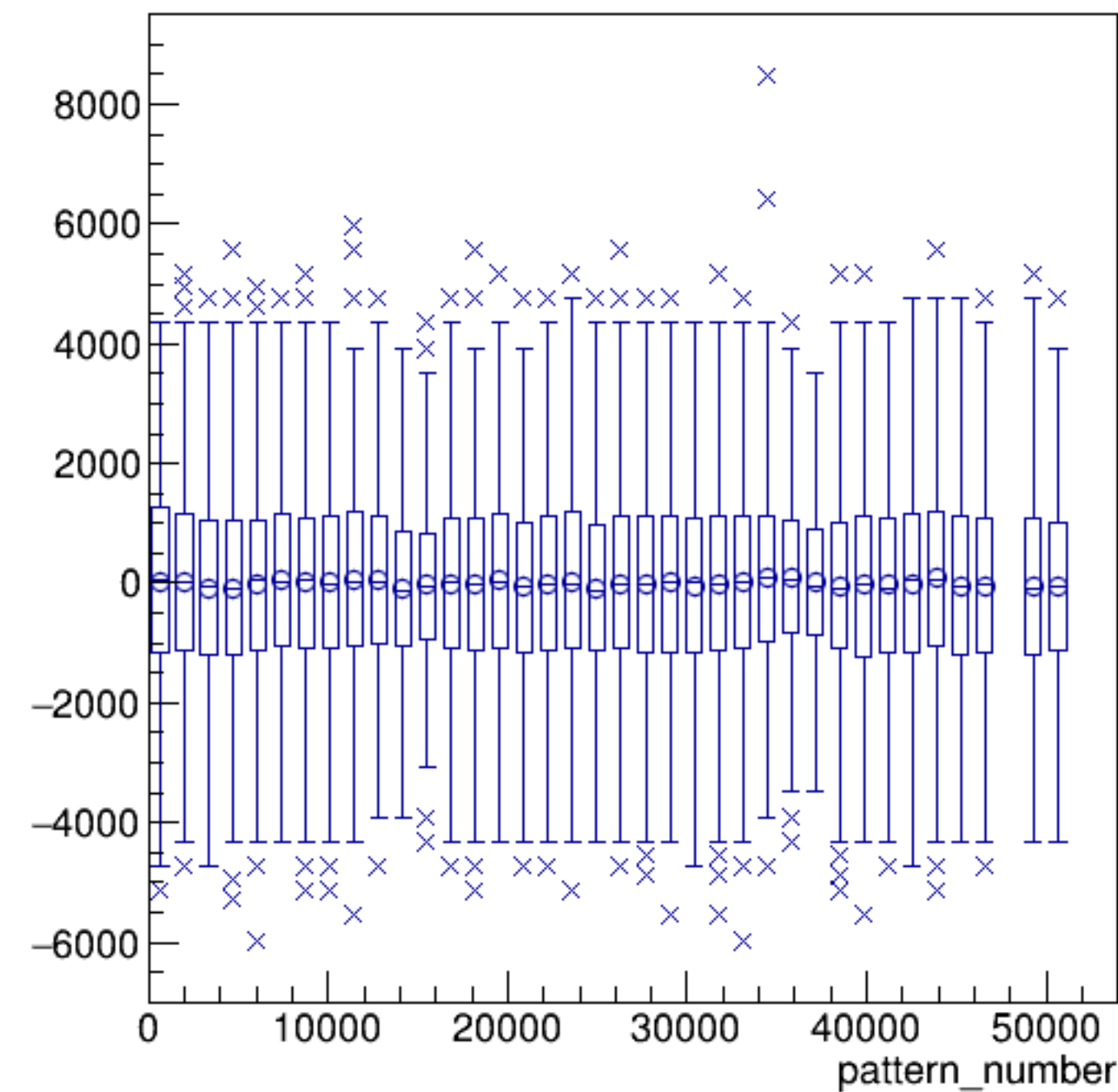
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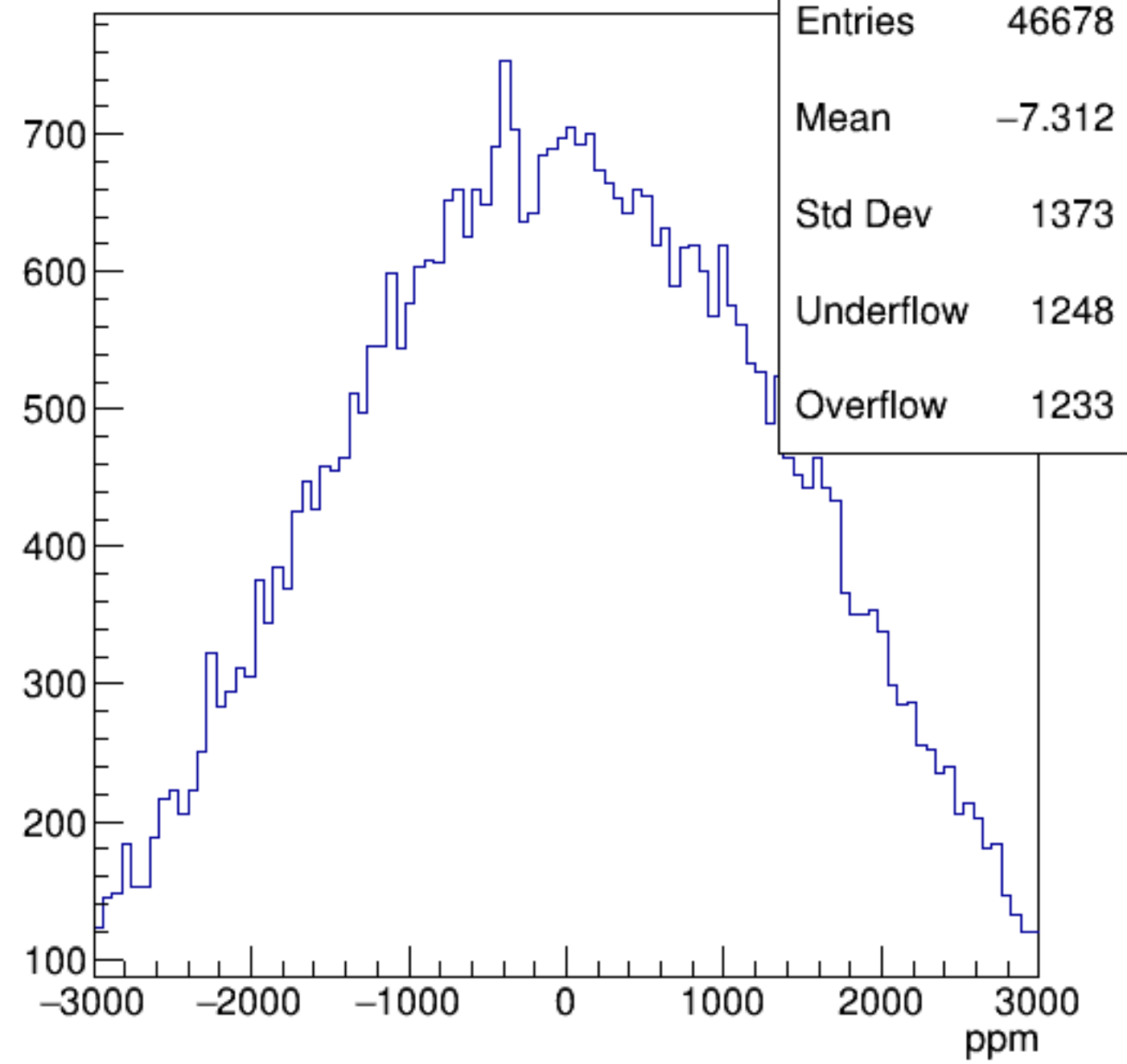
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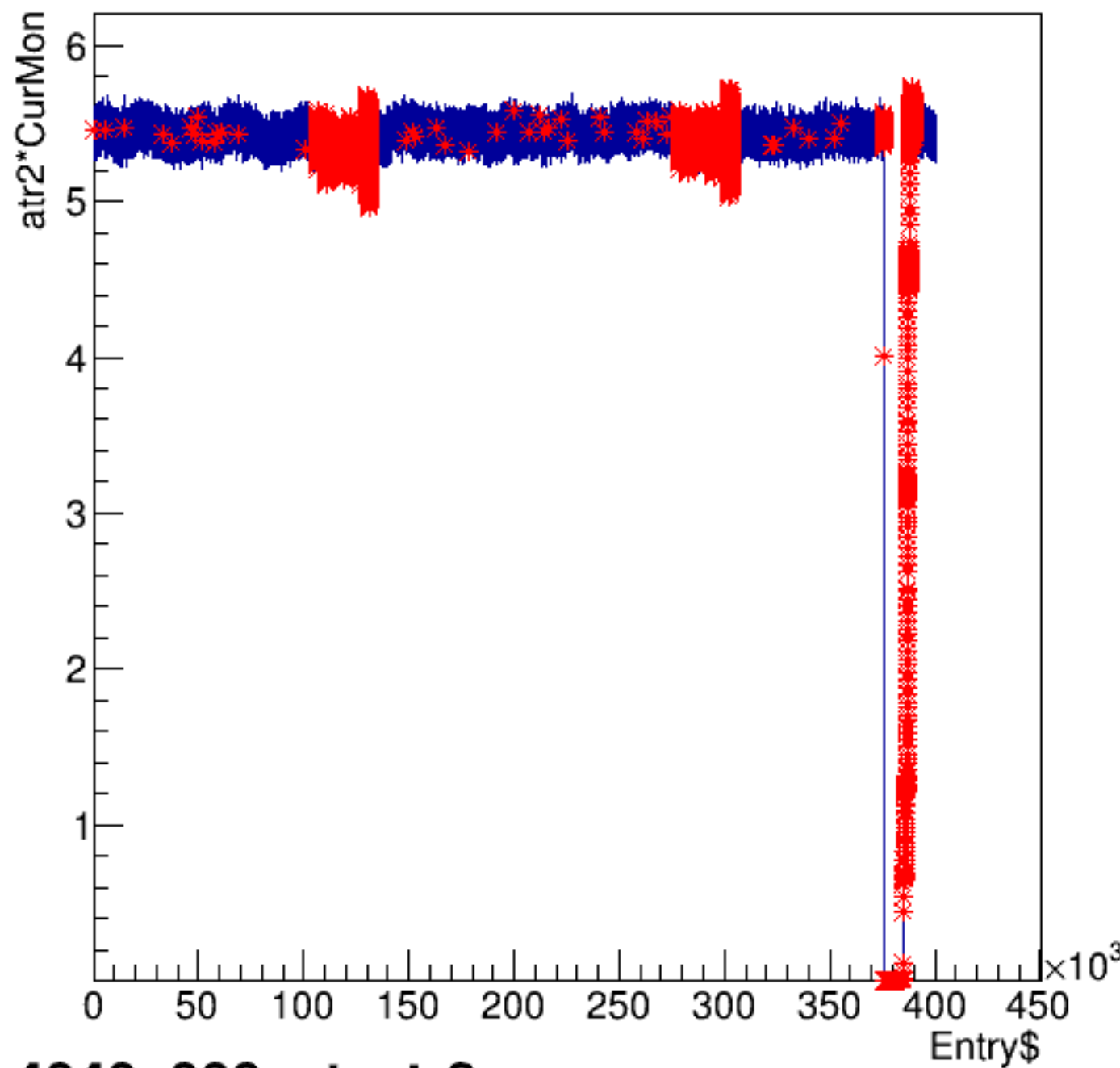
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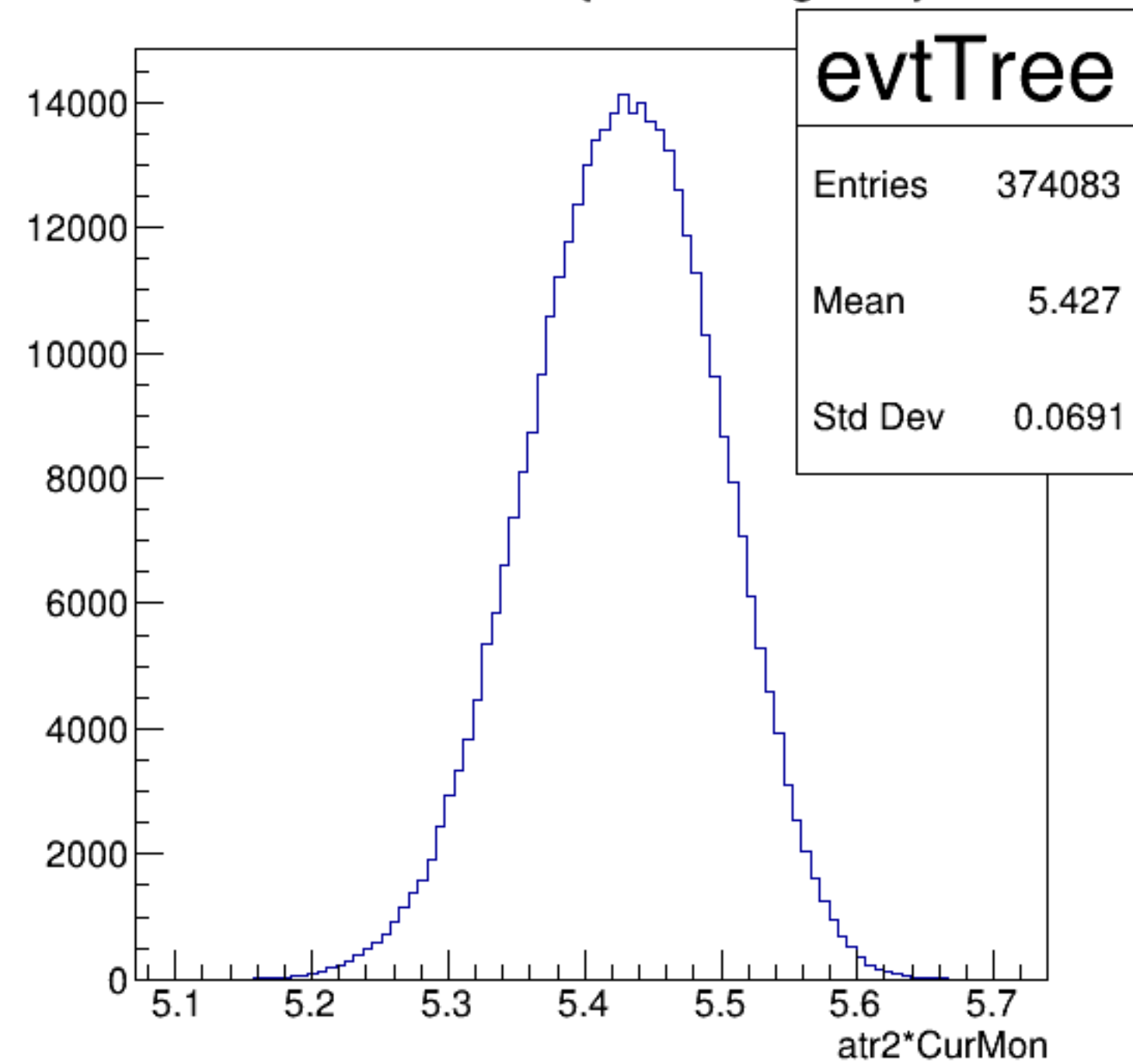
asym\_atr1



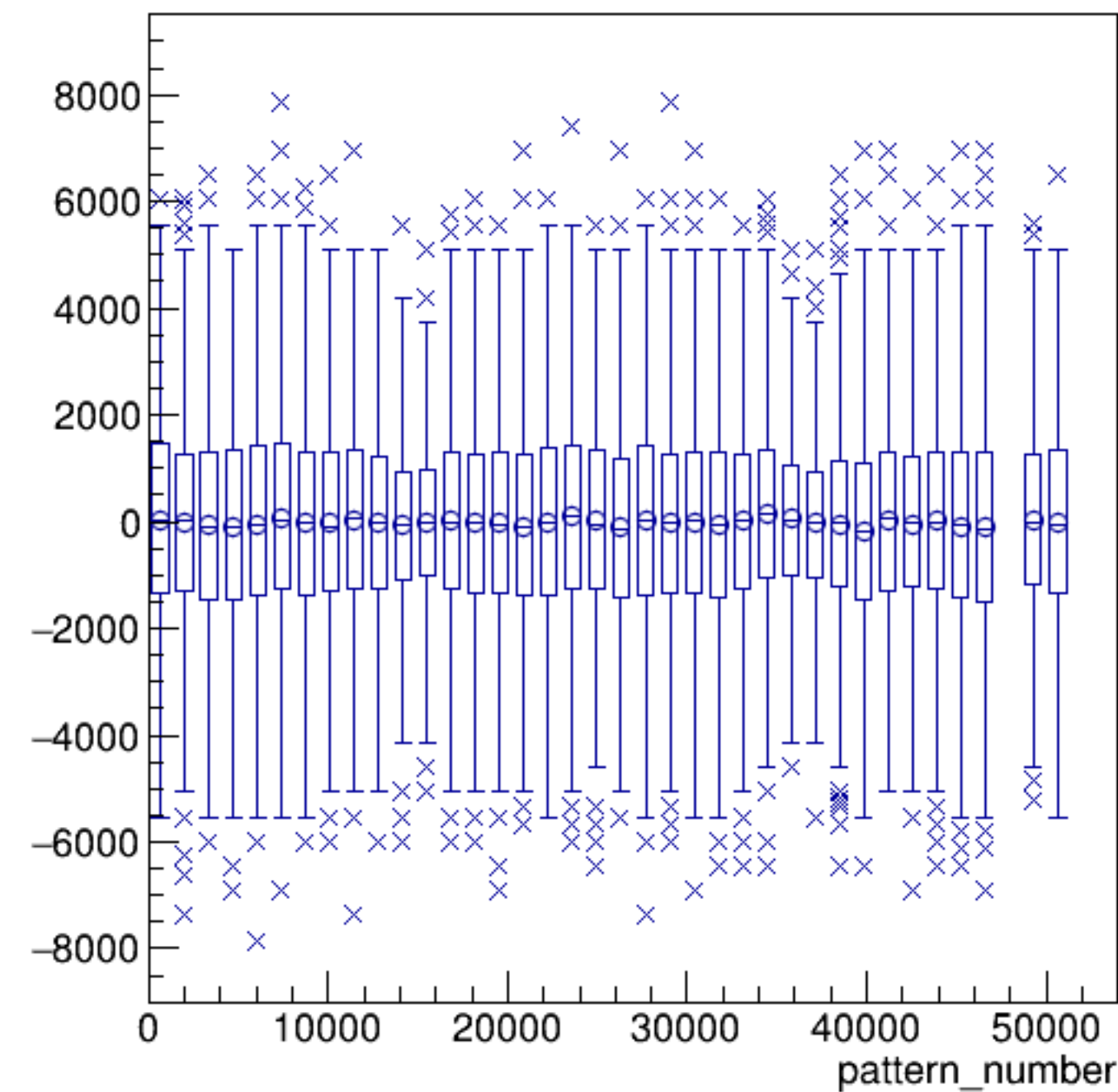
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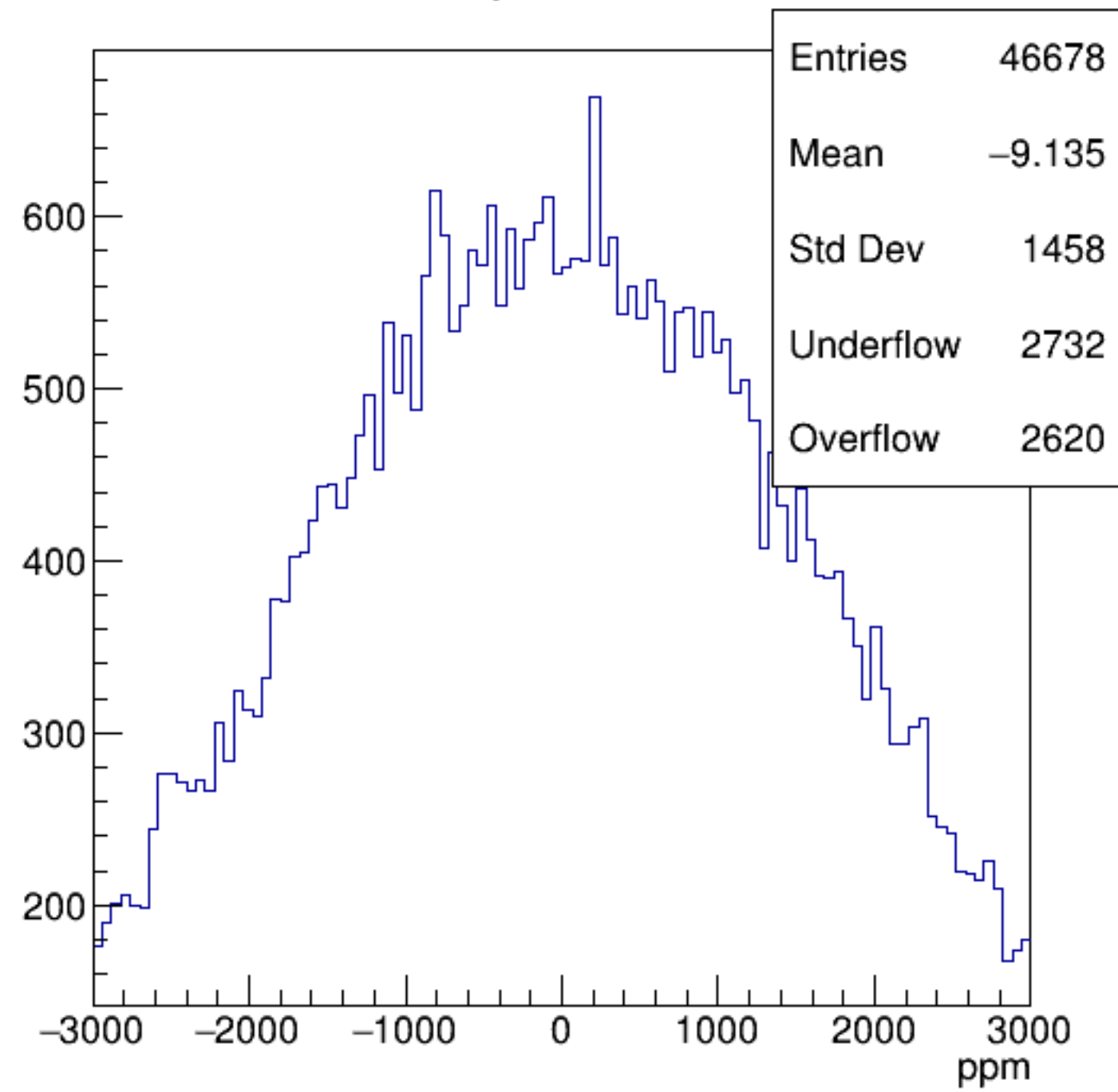
atr2\*CurMon {ErrorFlag==0}



asym\_atr2/ppm:pattern\_number {ErrorFlag==0}

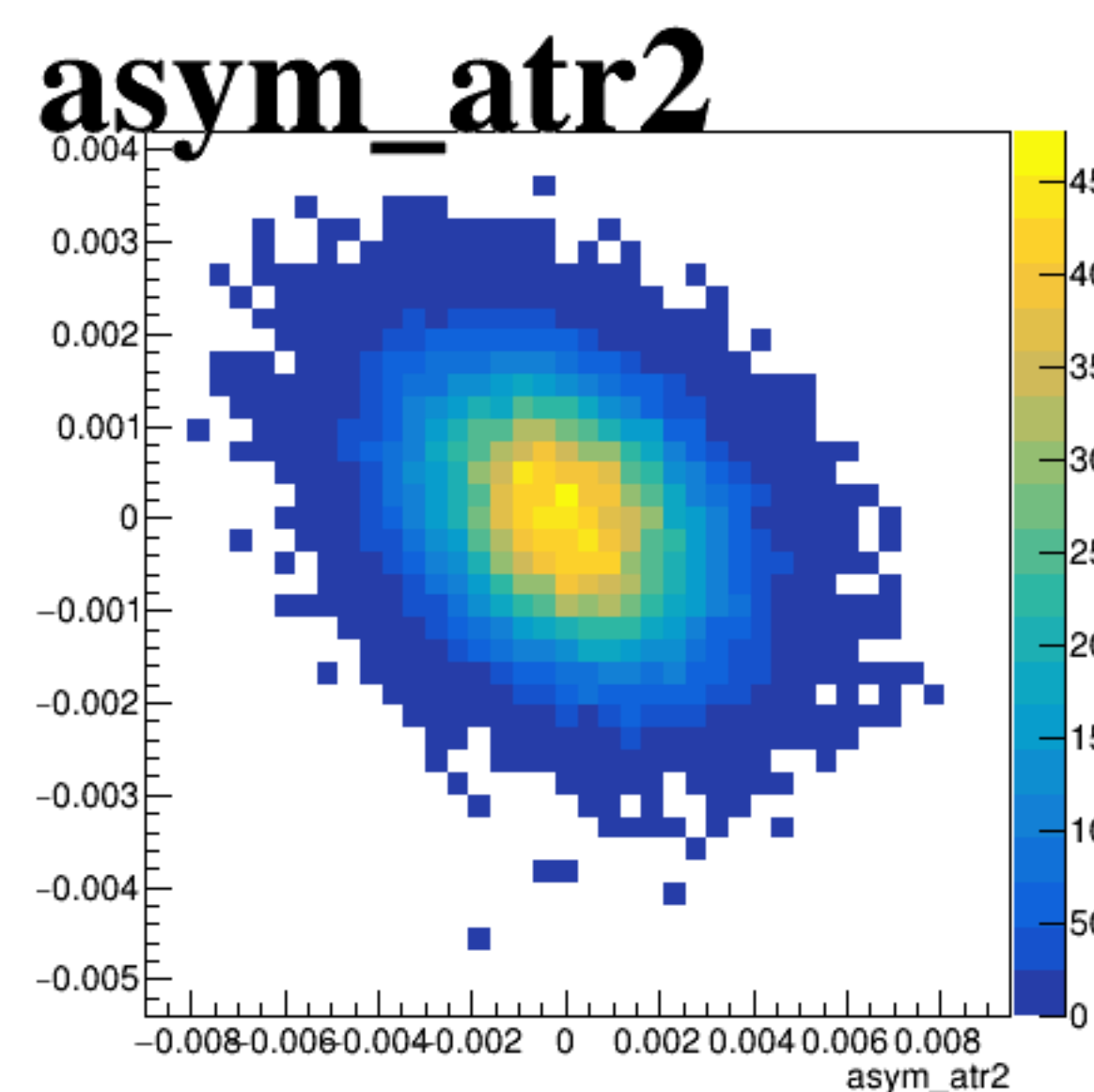
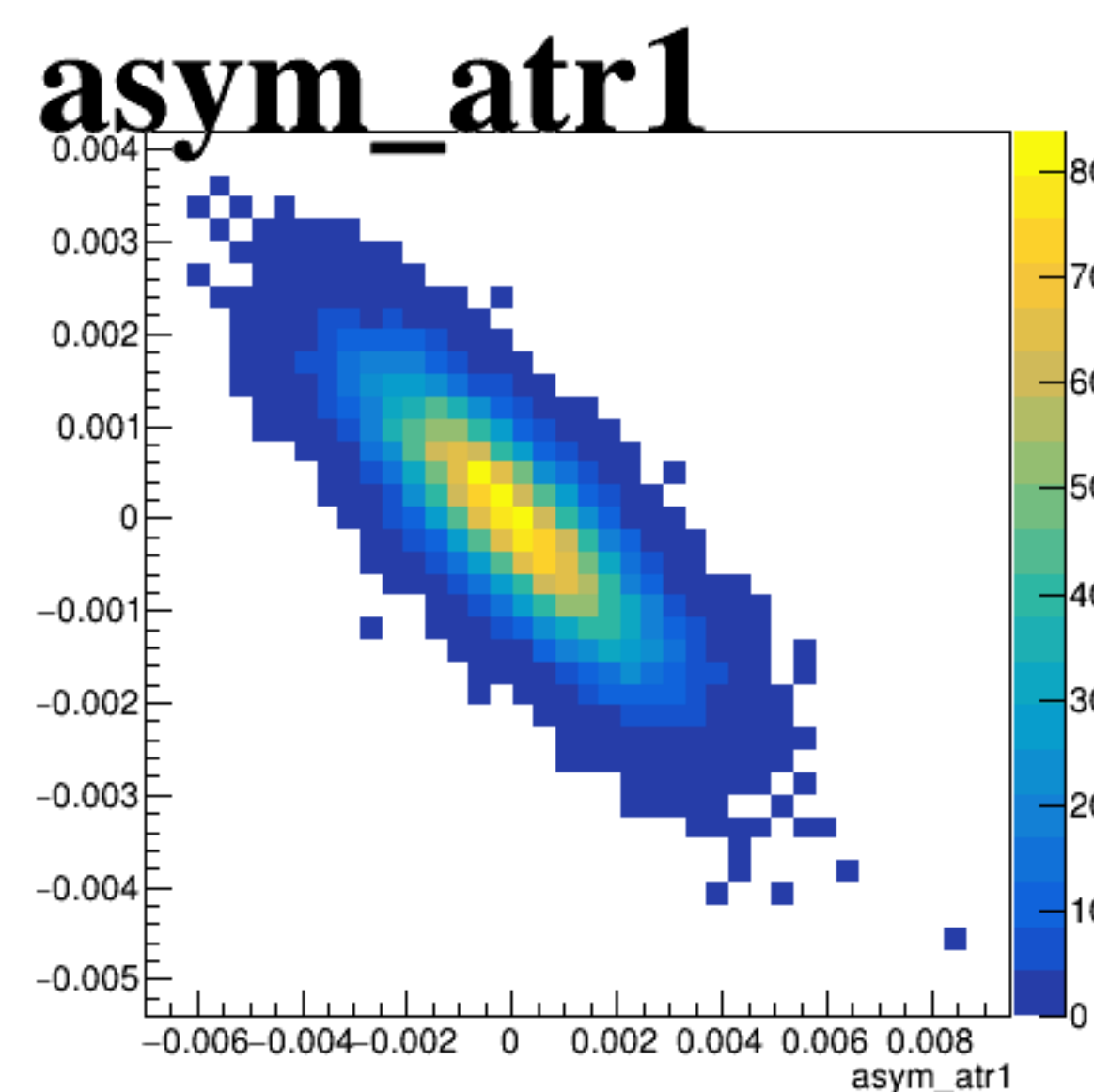
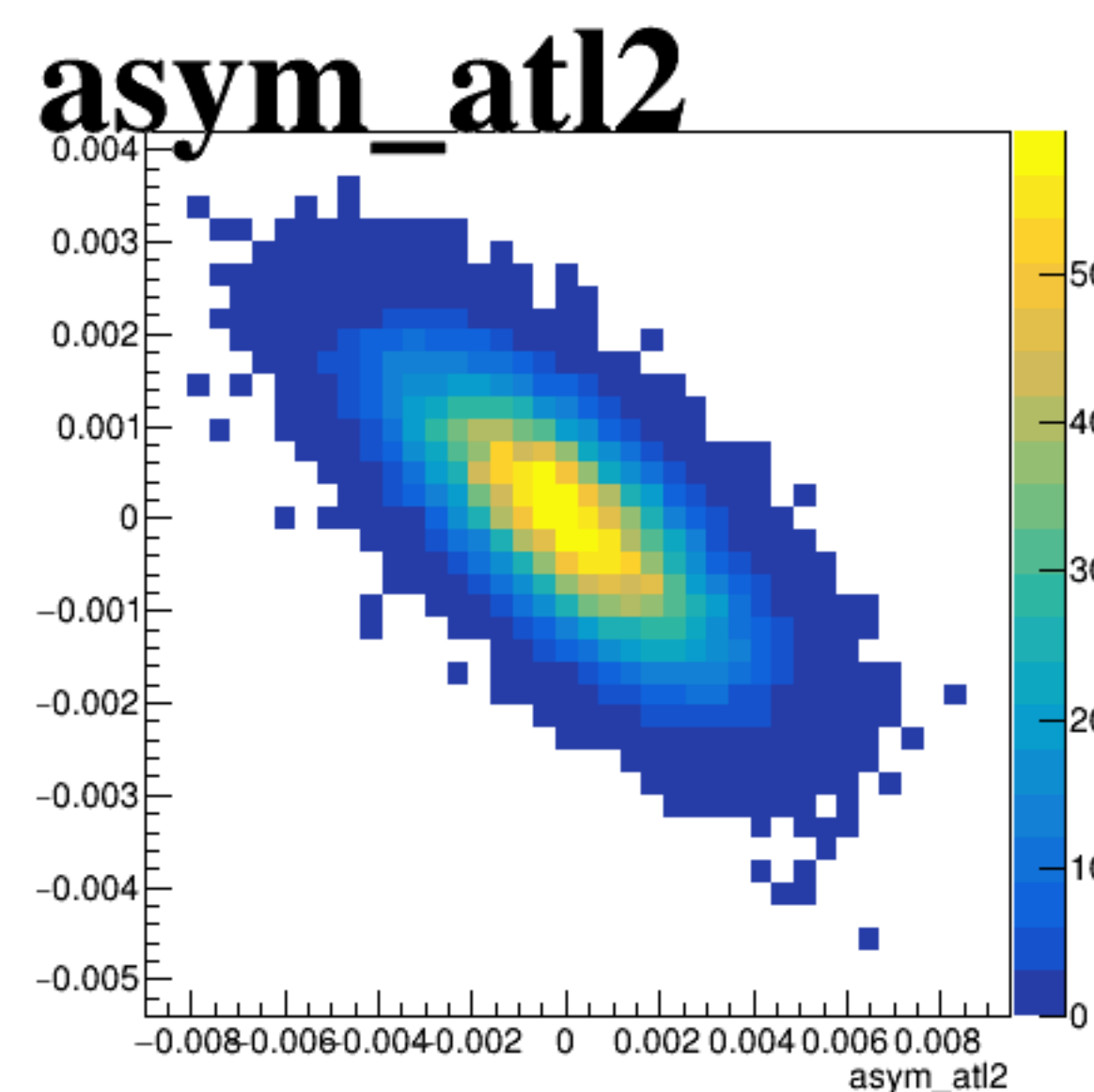
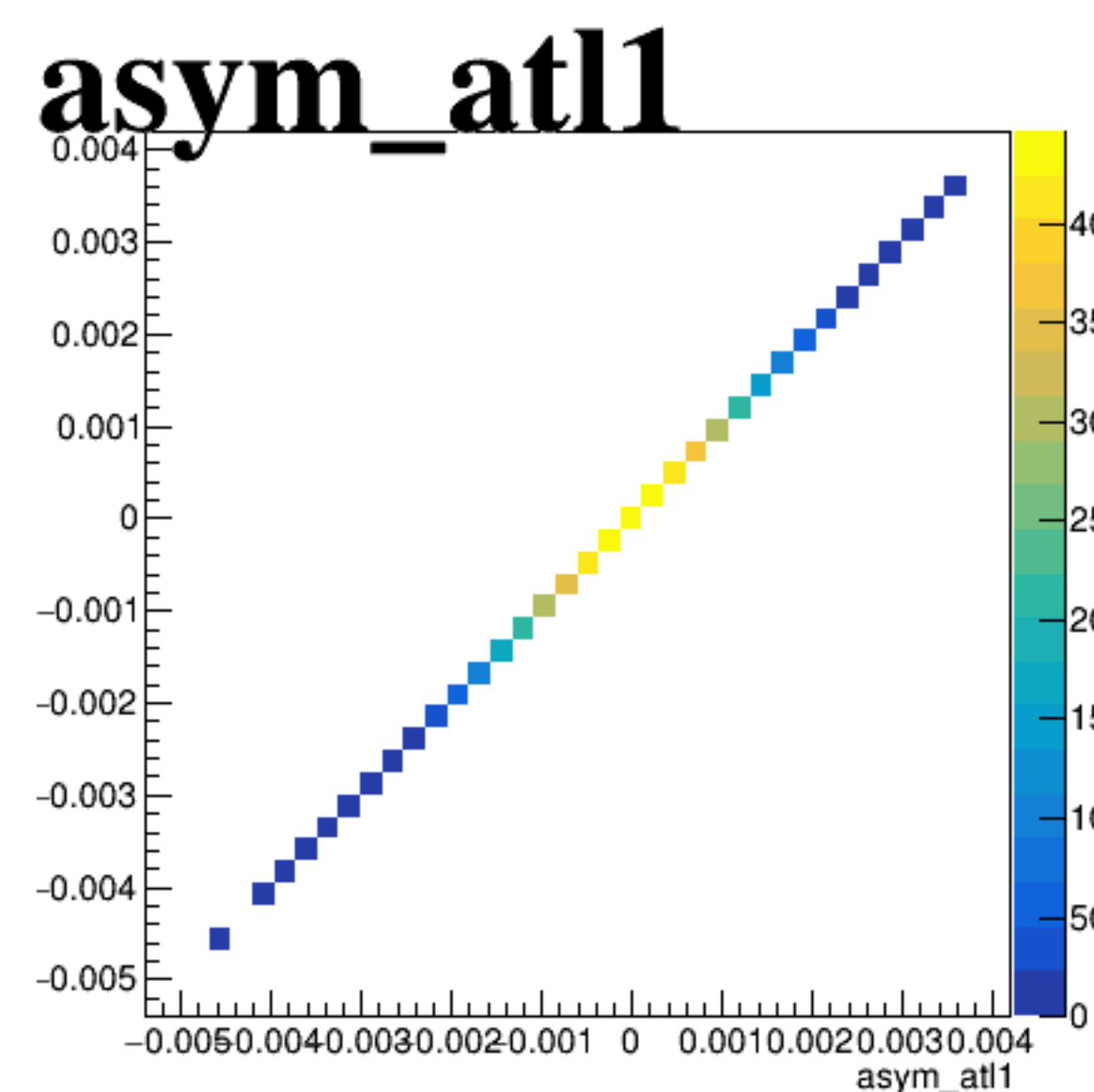


asym\_atr2

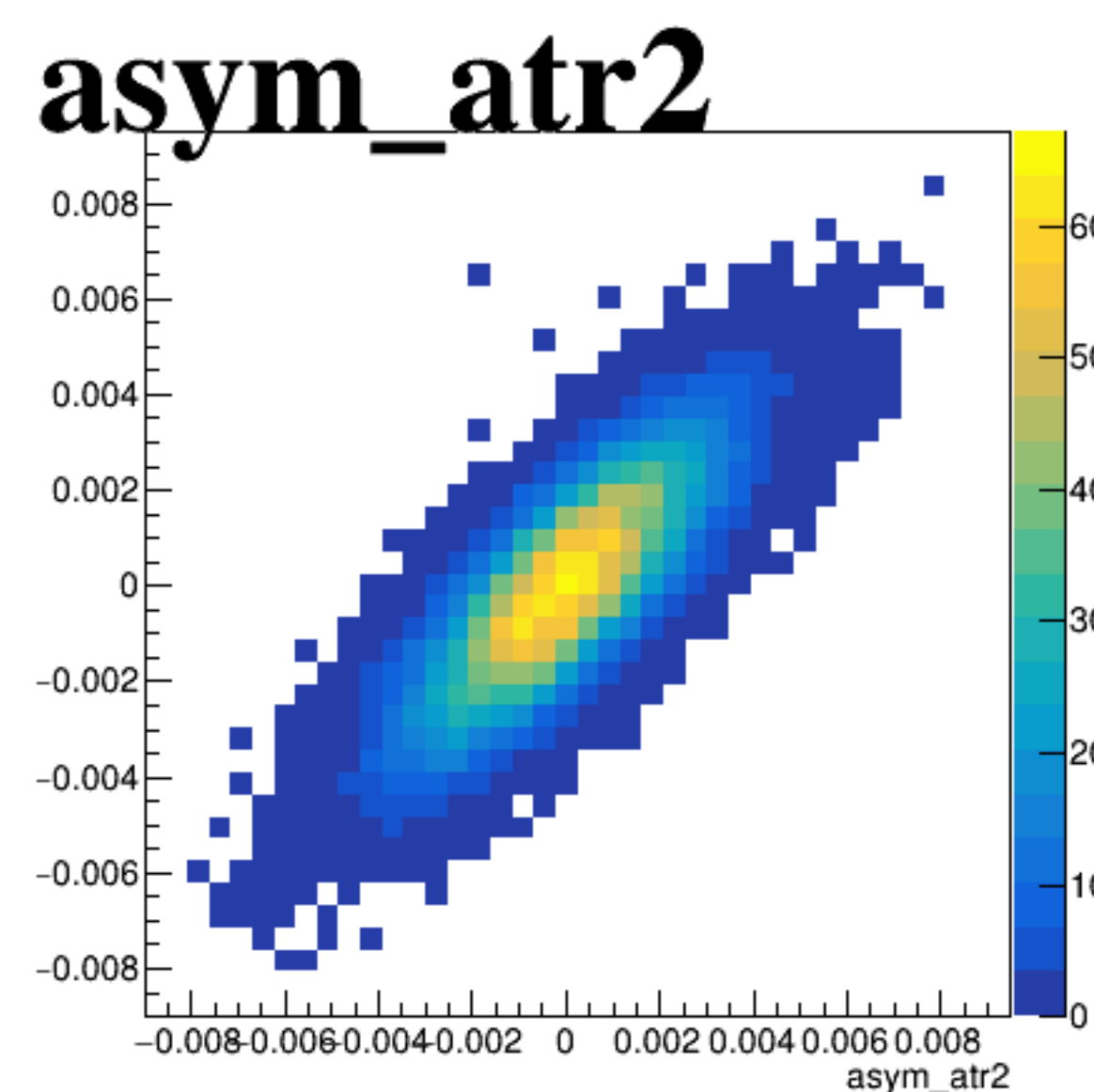
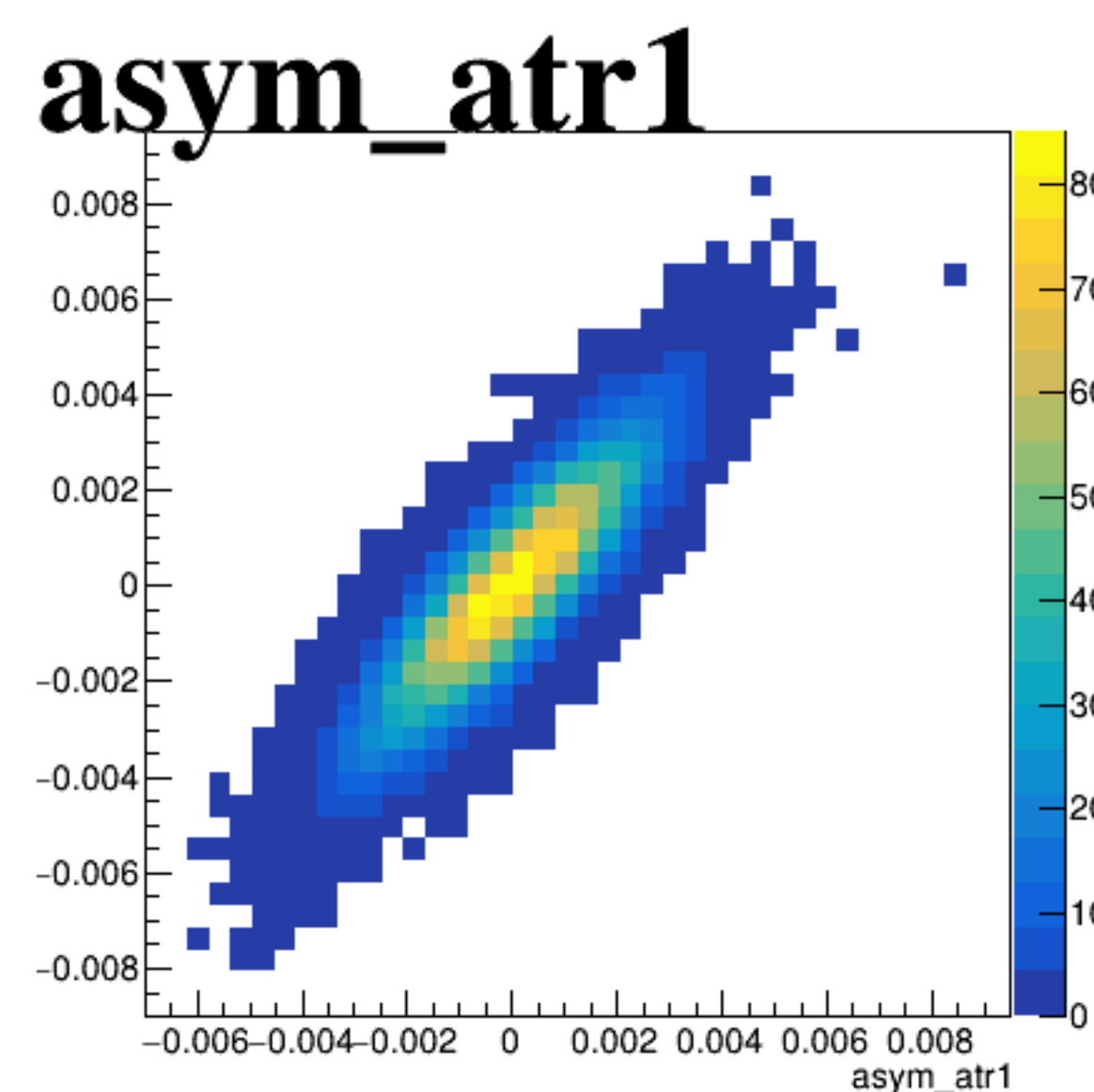
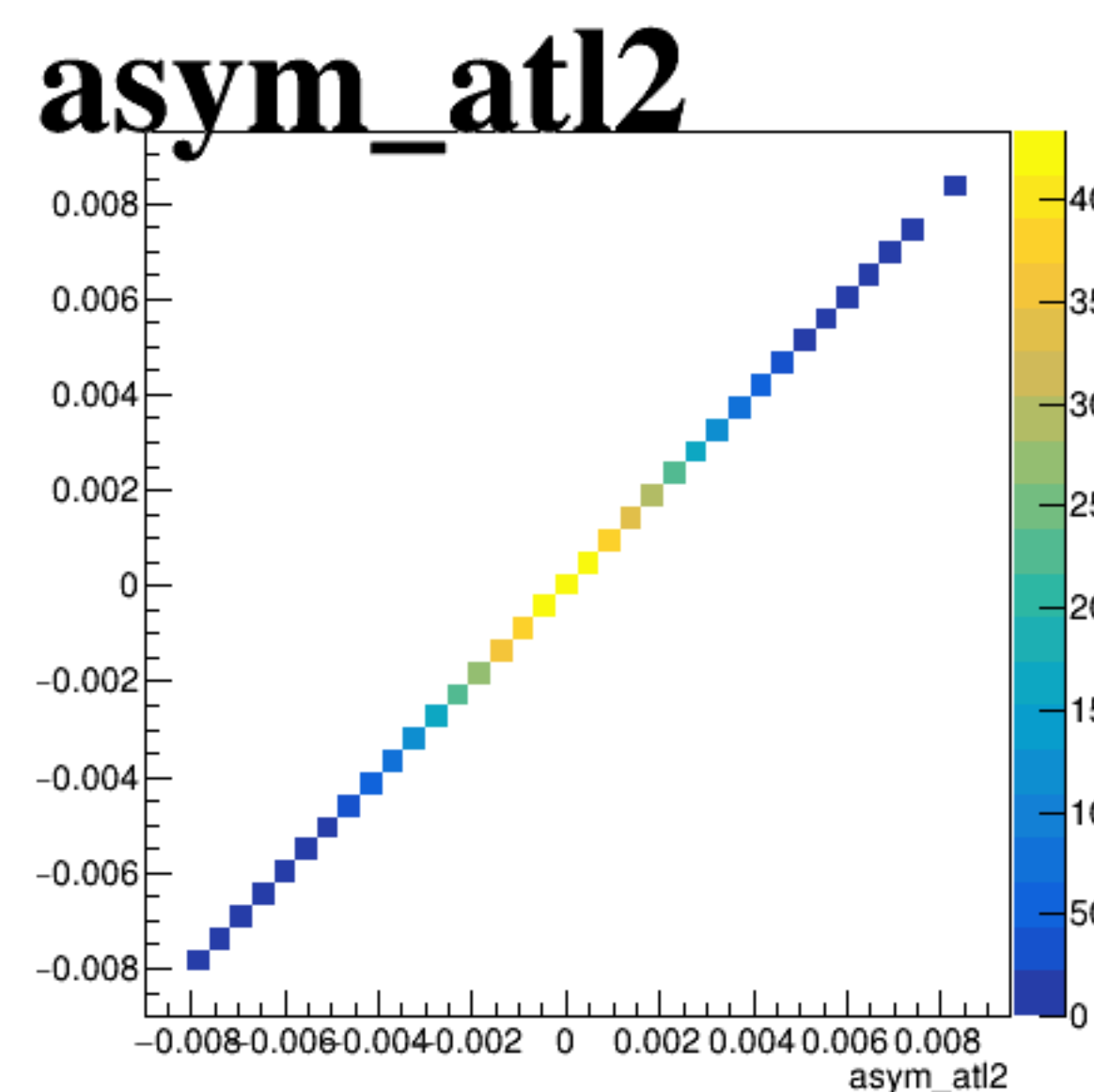
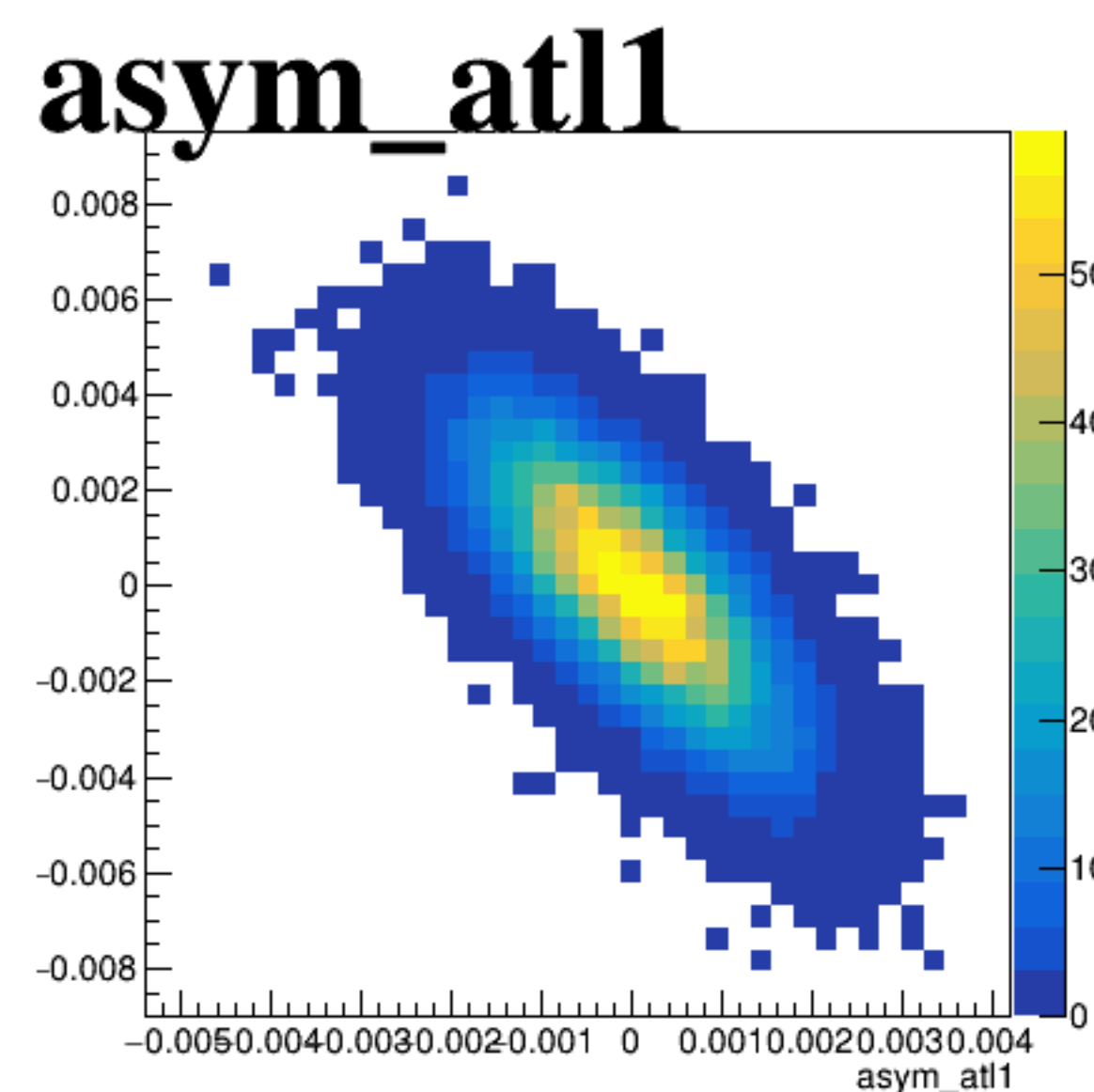
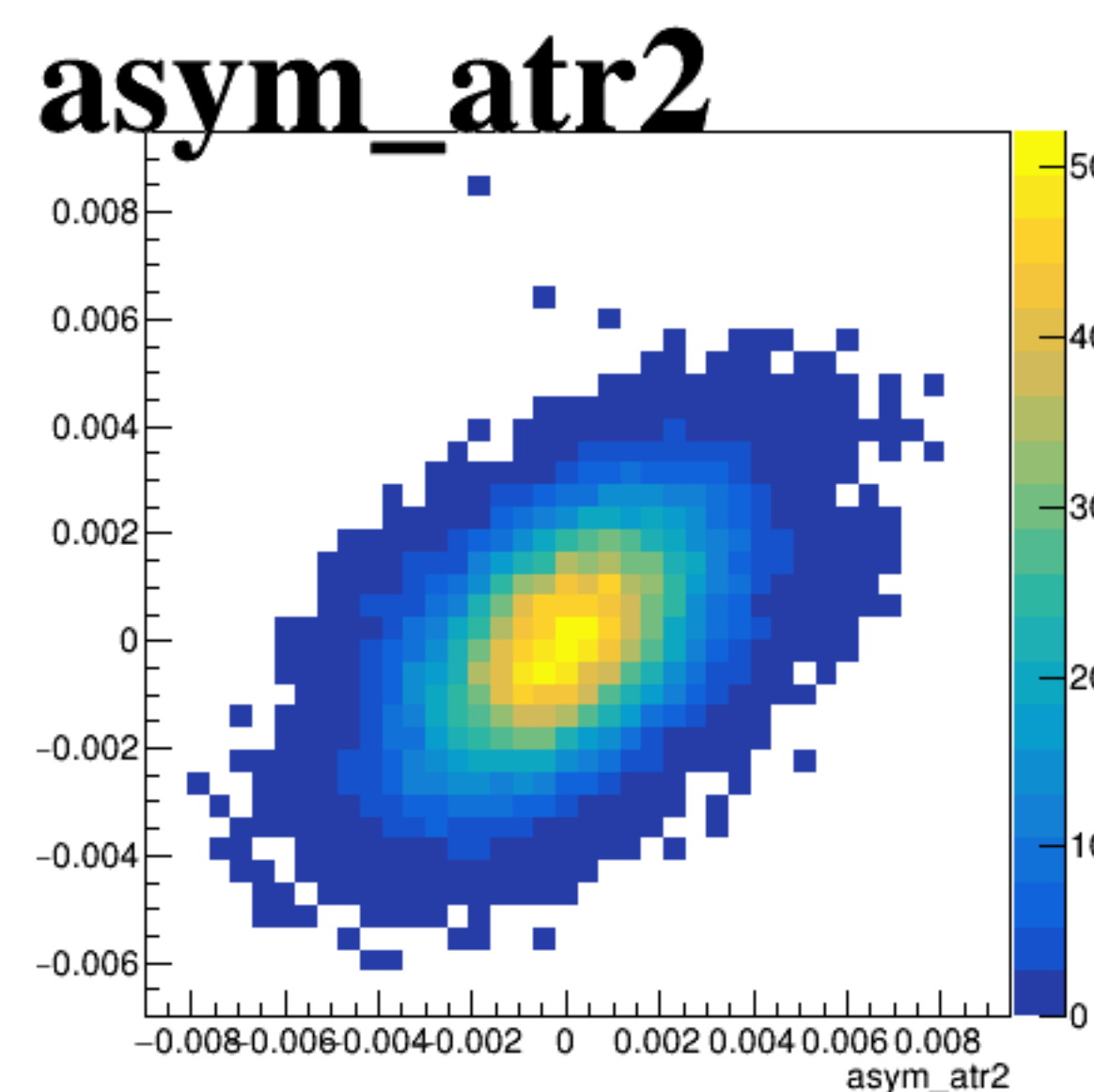
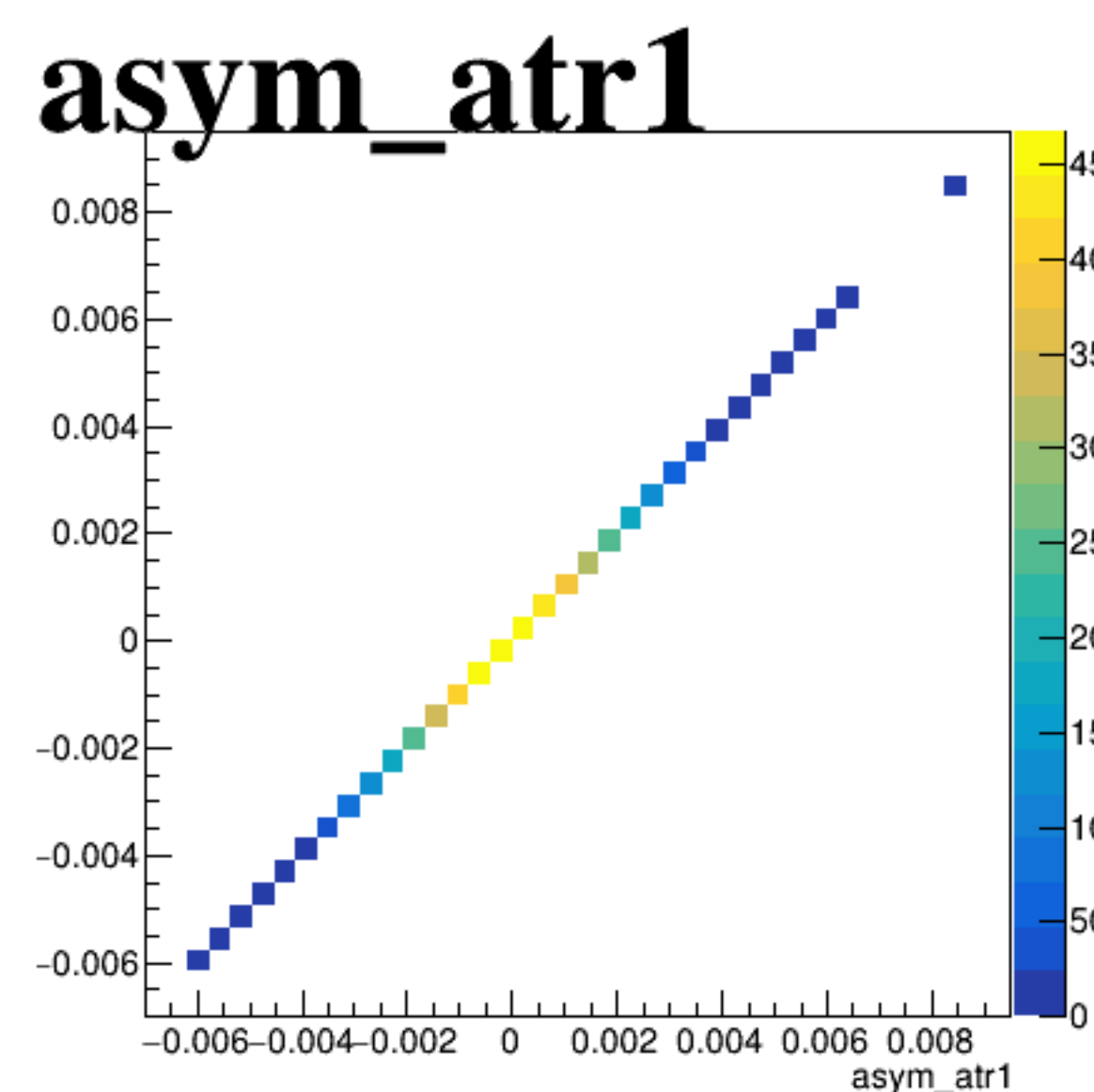
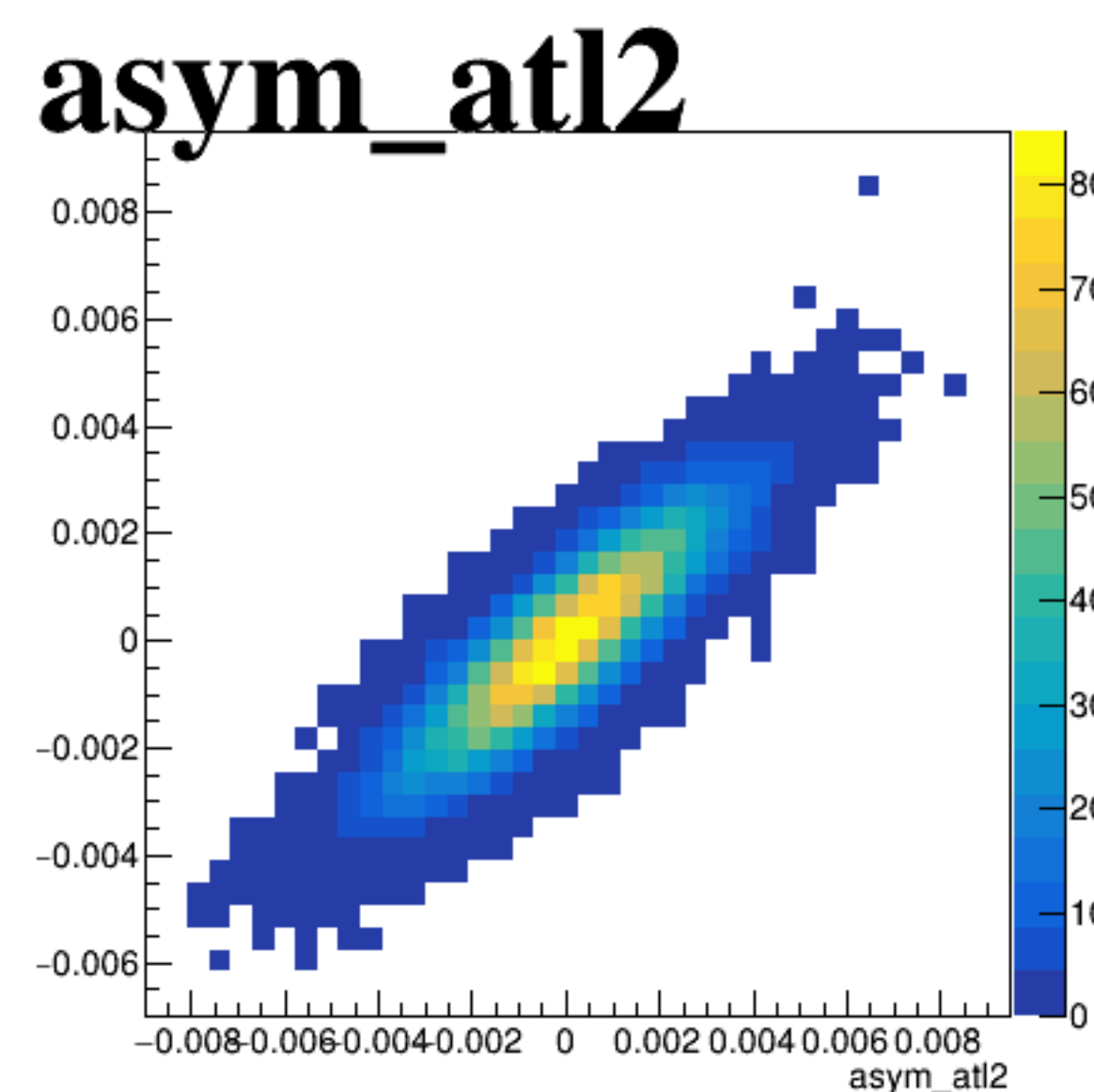
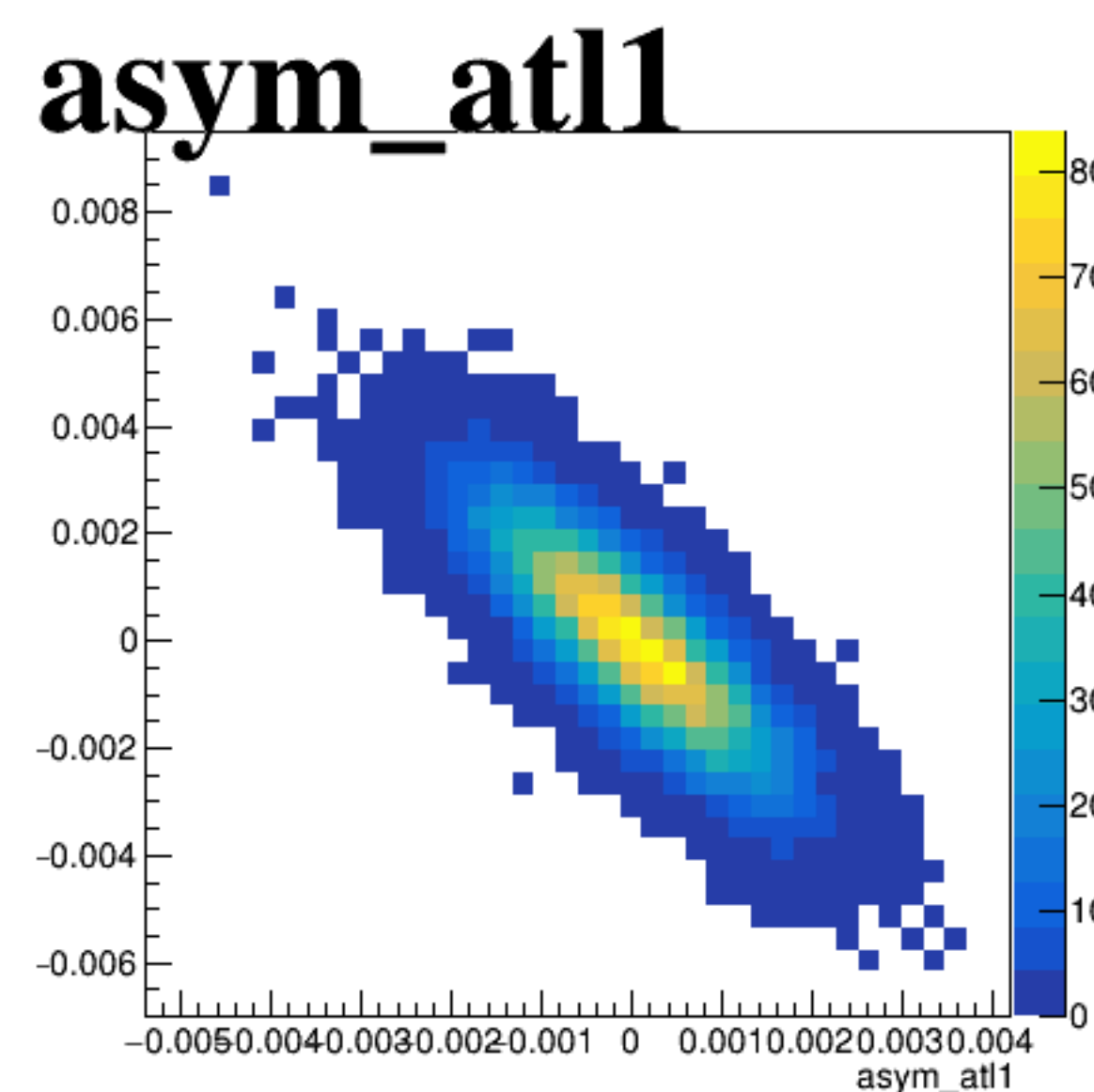




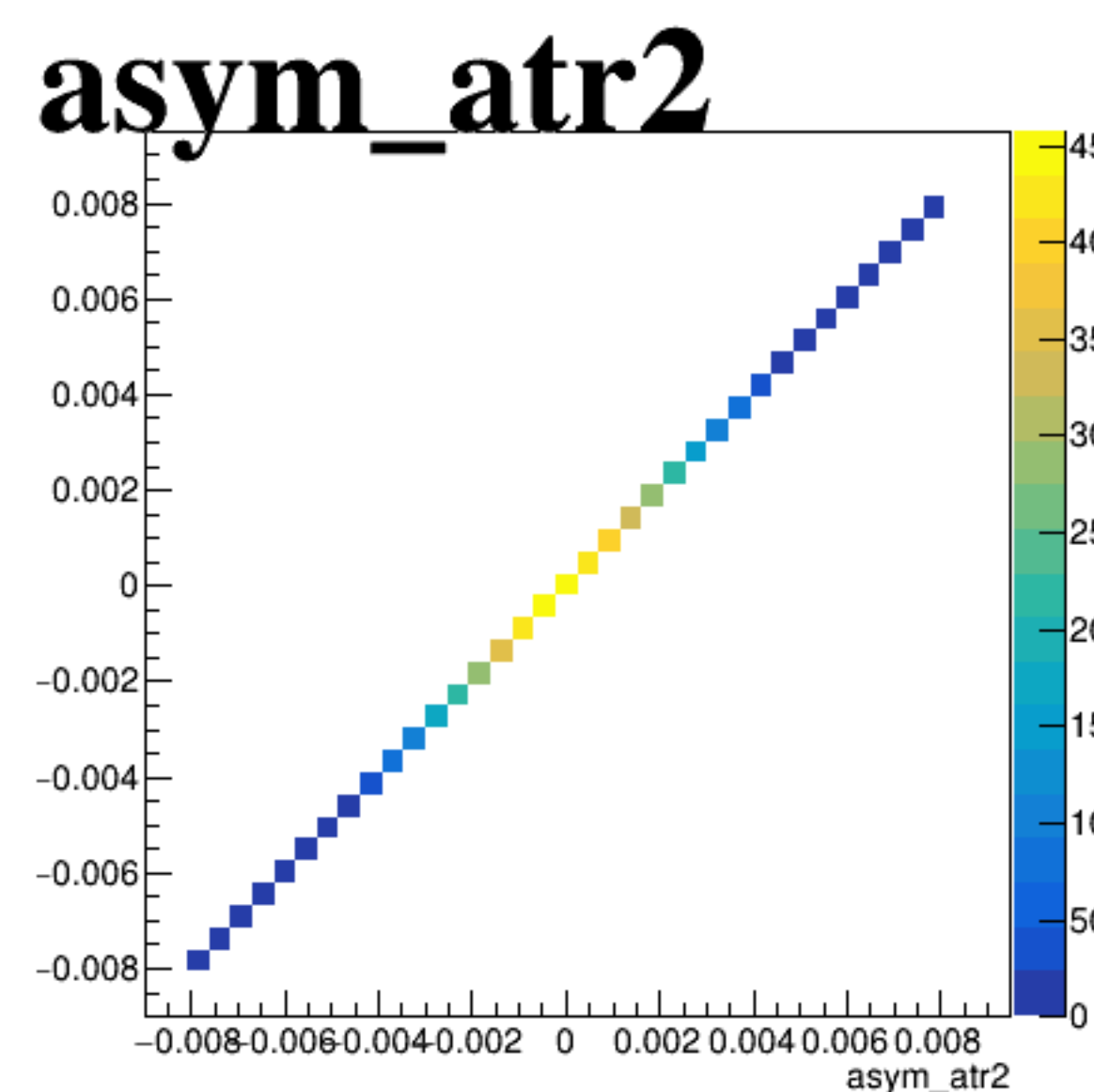
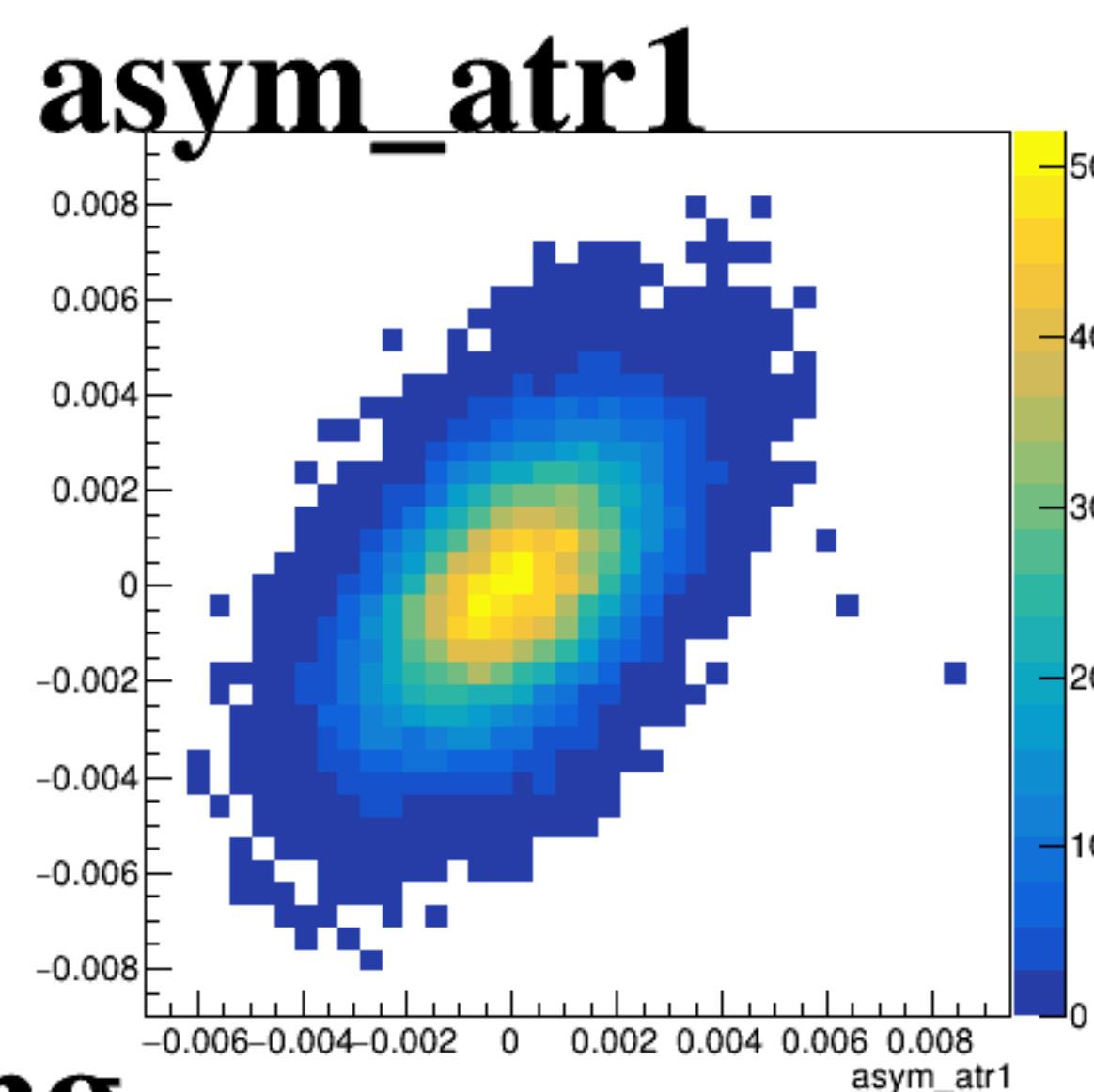
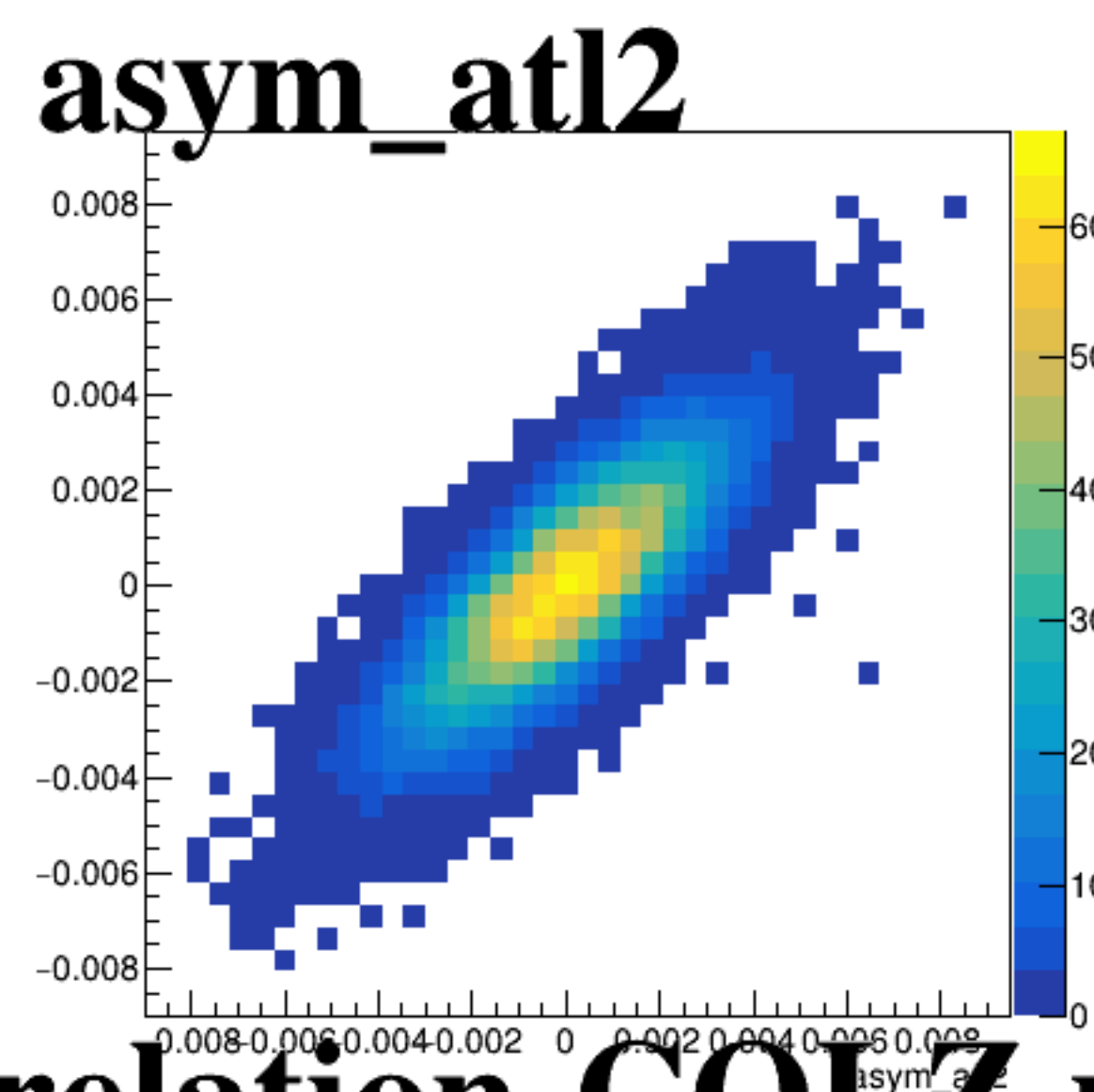
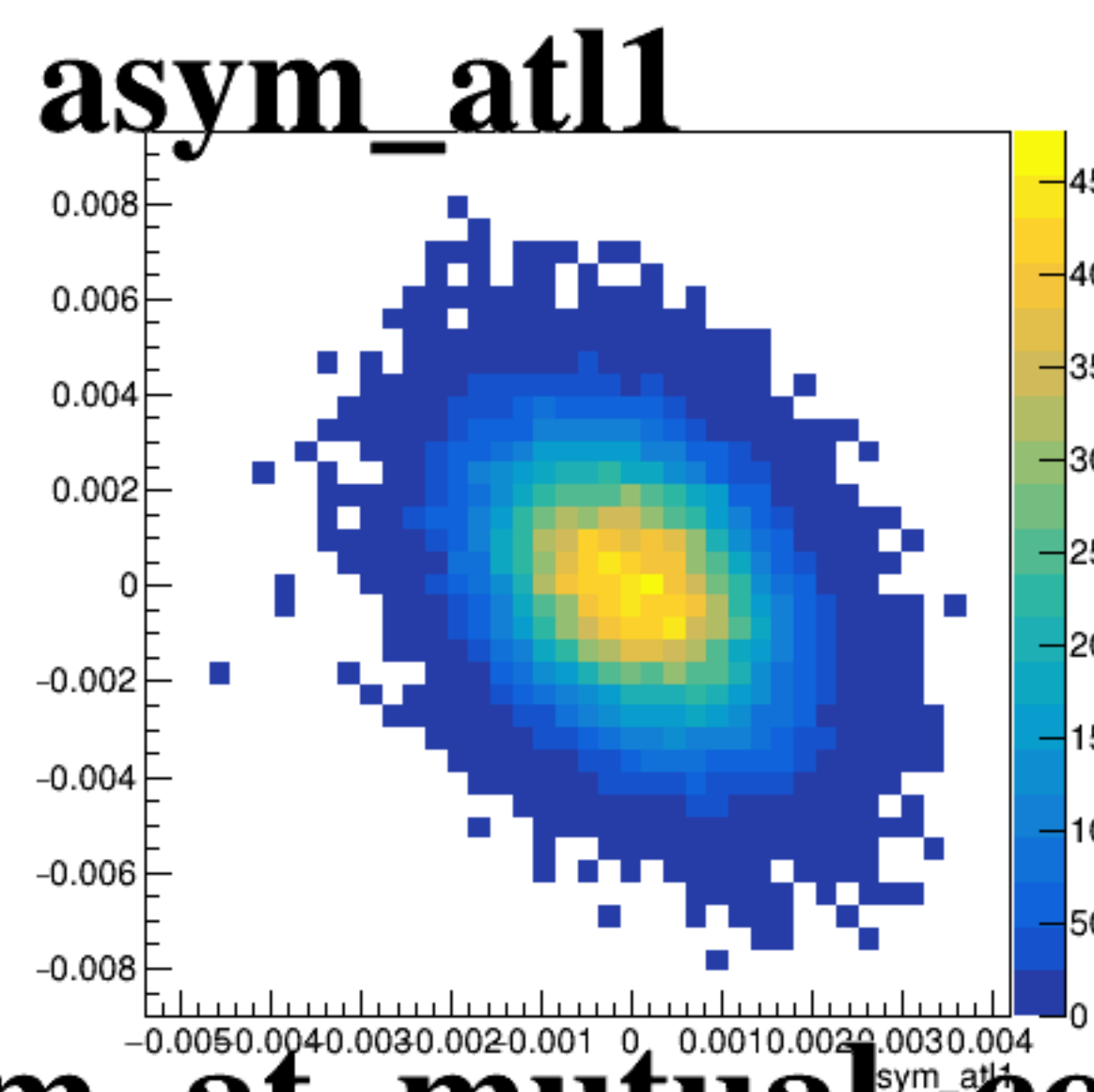
## asym\_atl1



## asym\_atl2

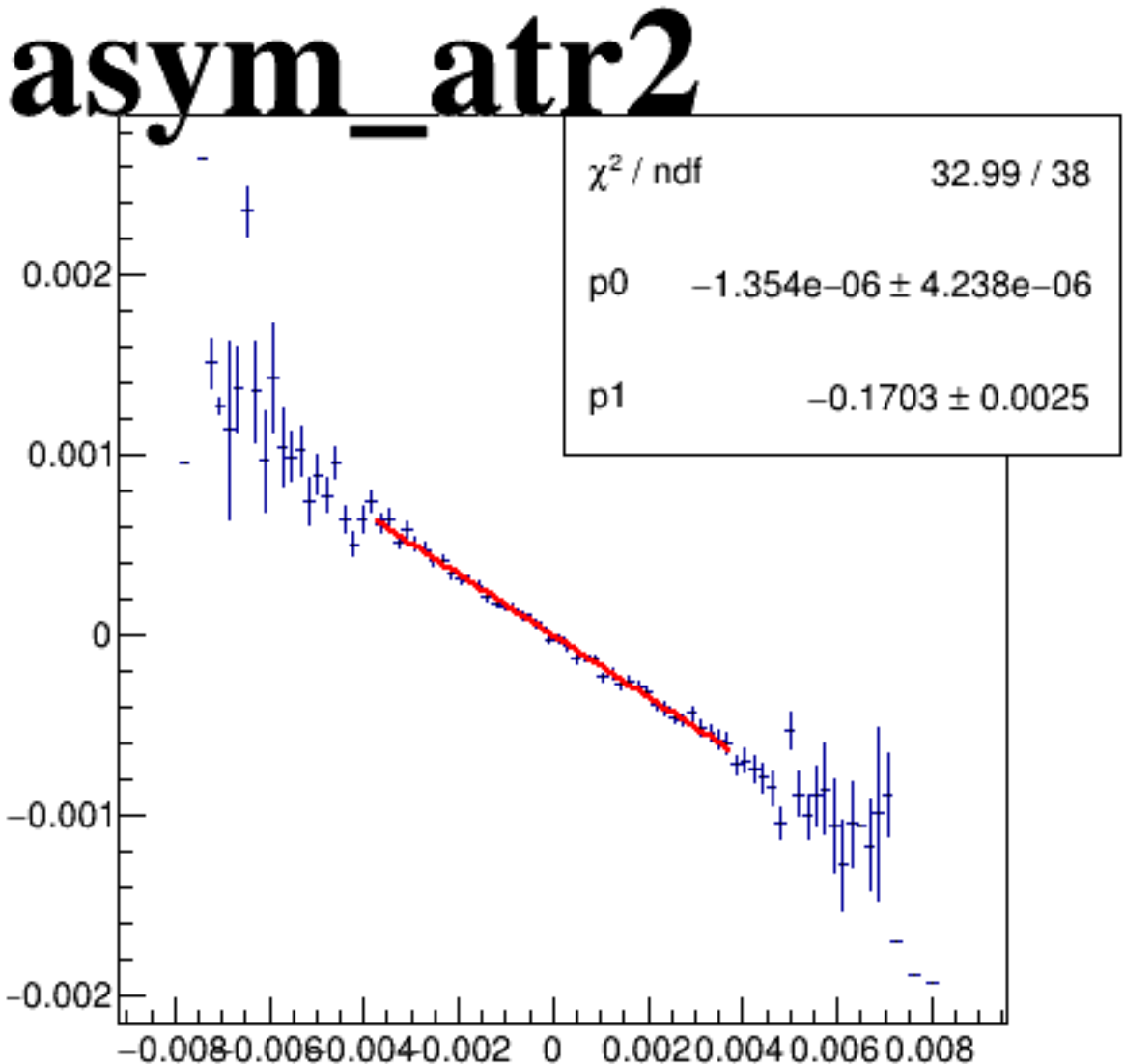
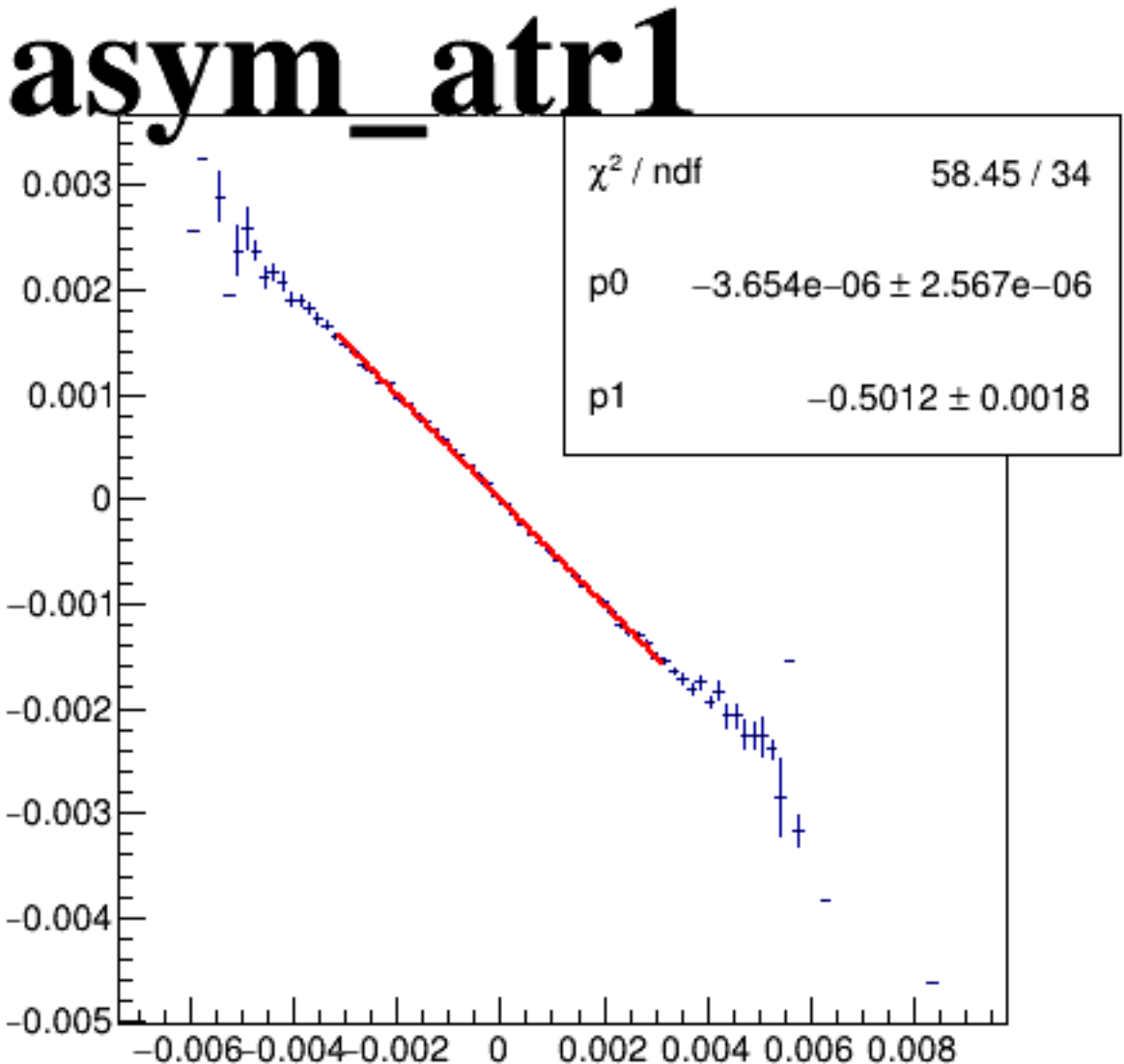
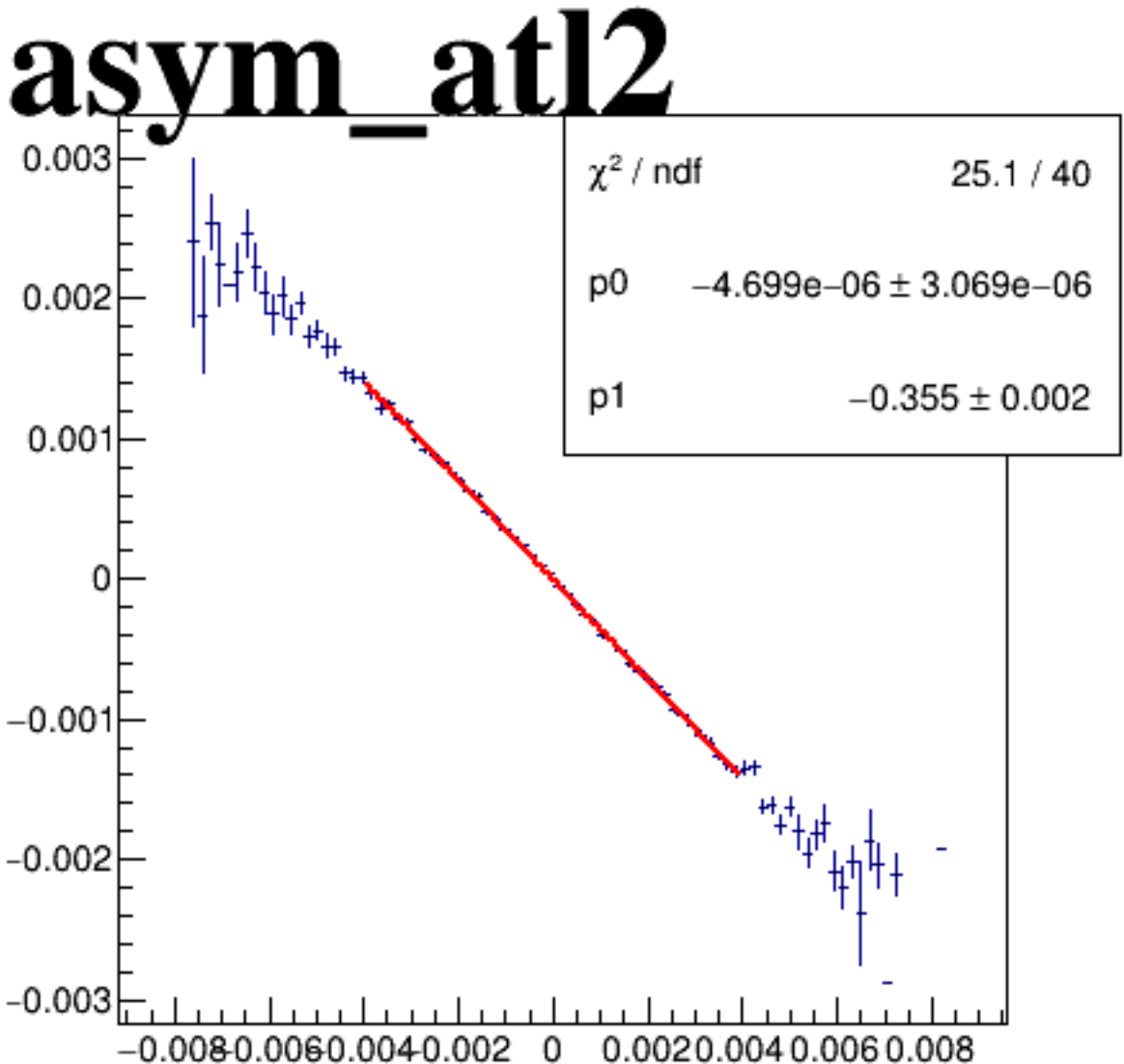
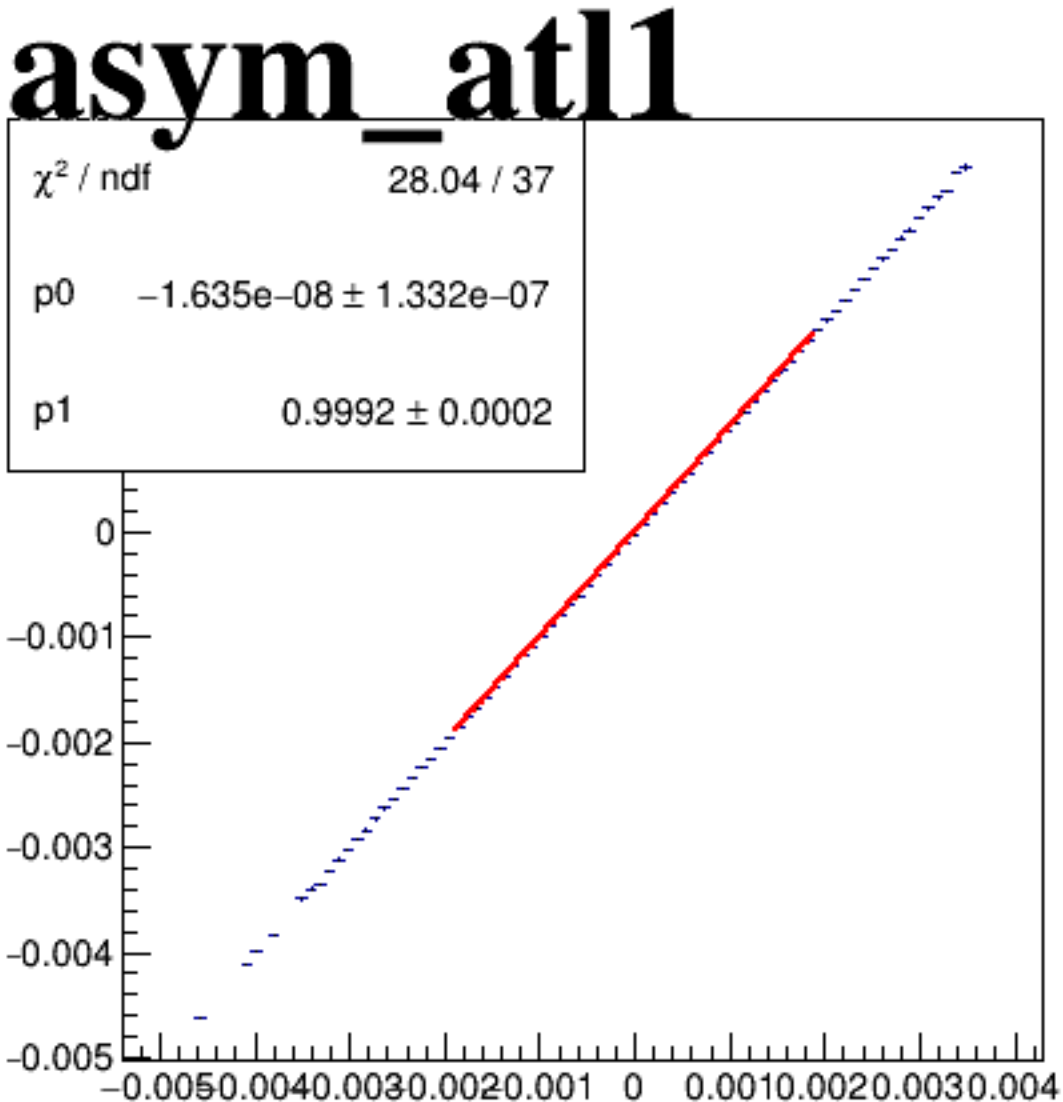
**asym\_atr1**

## asym\_atr2

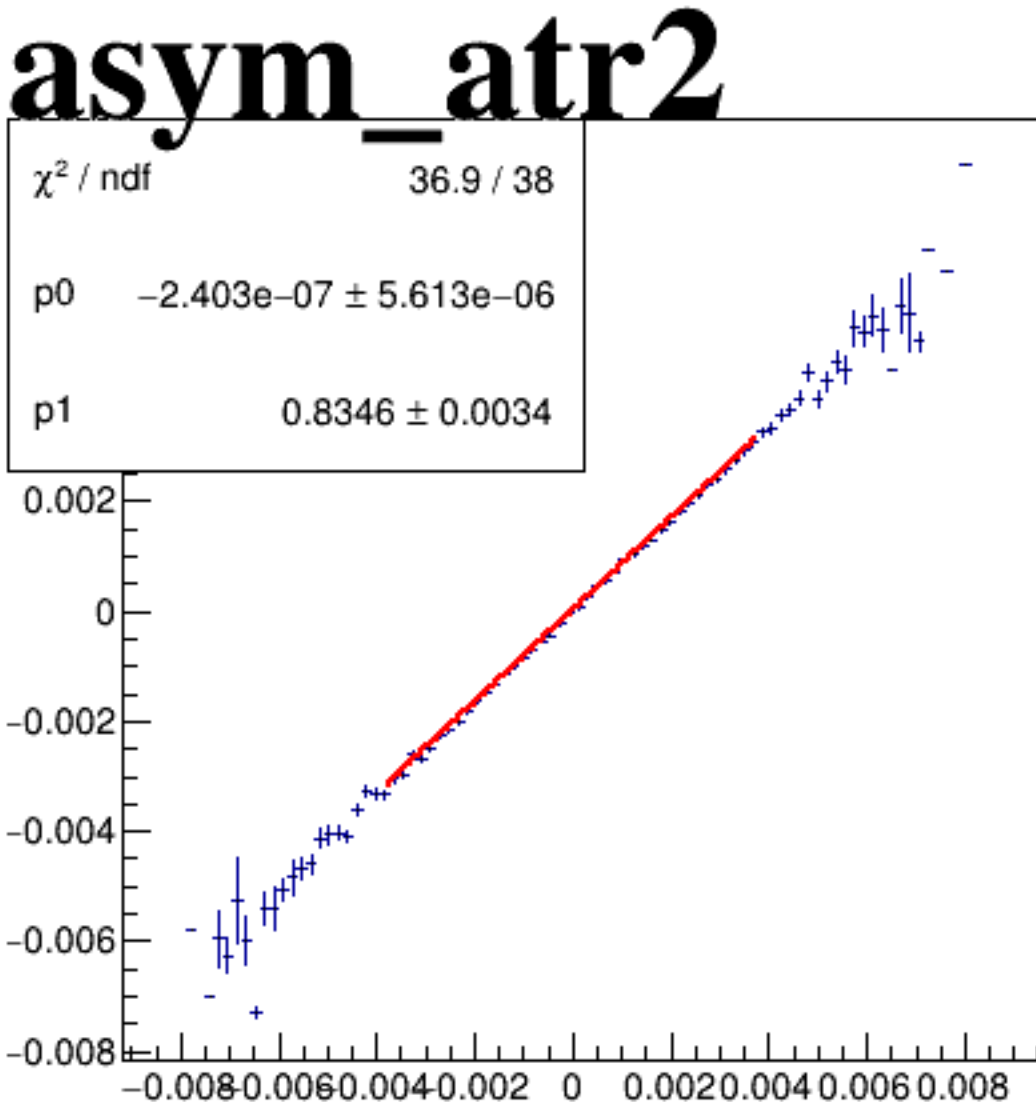
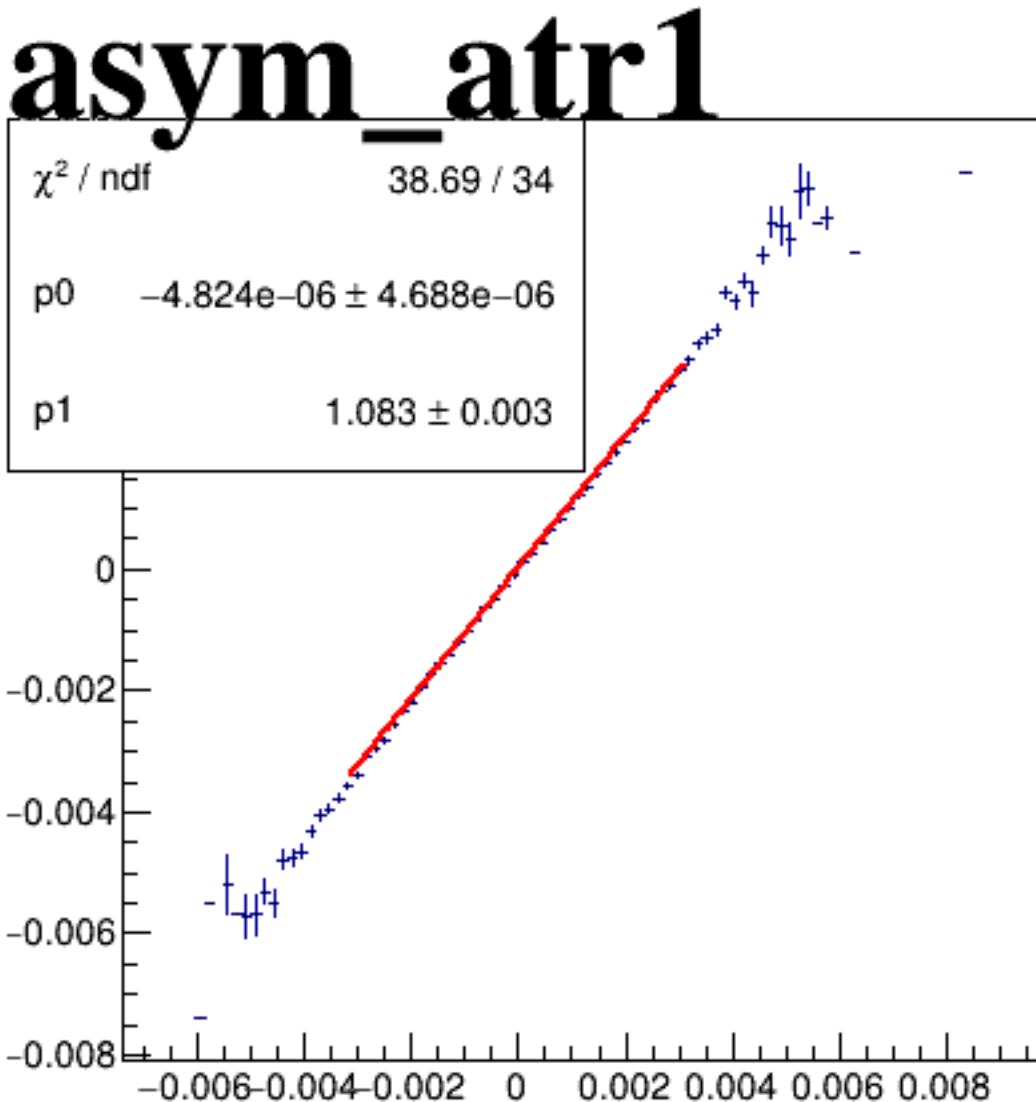
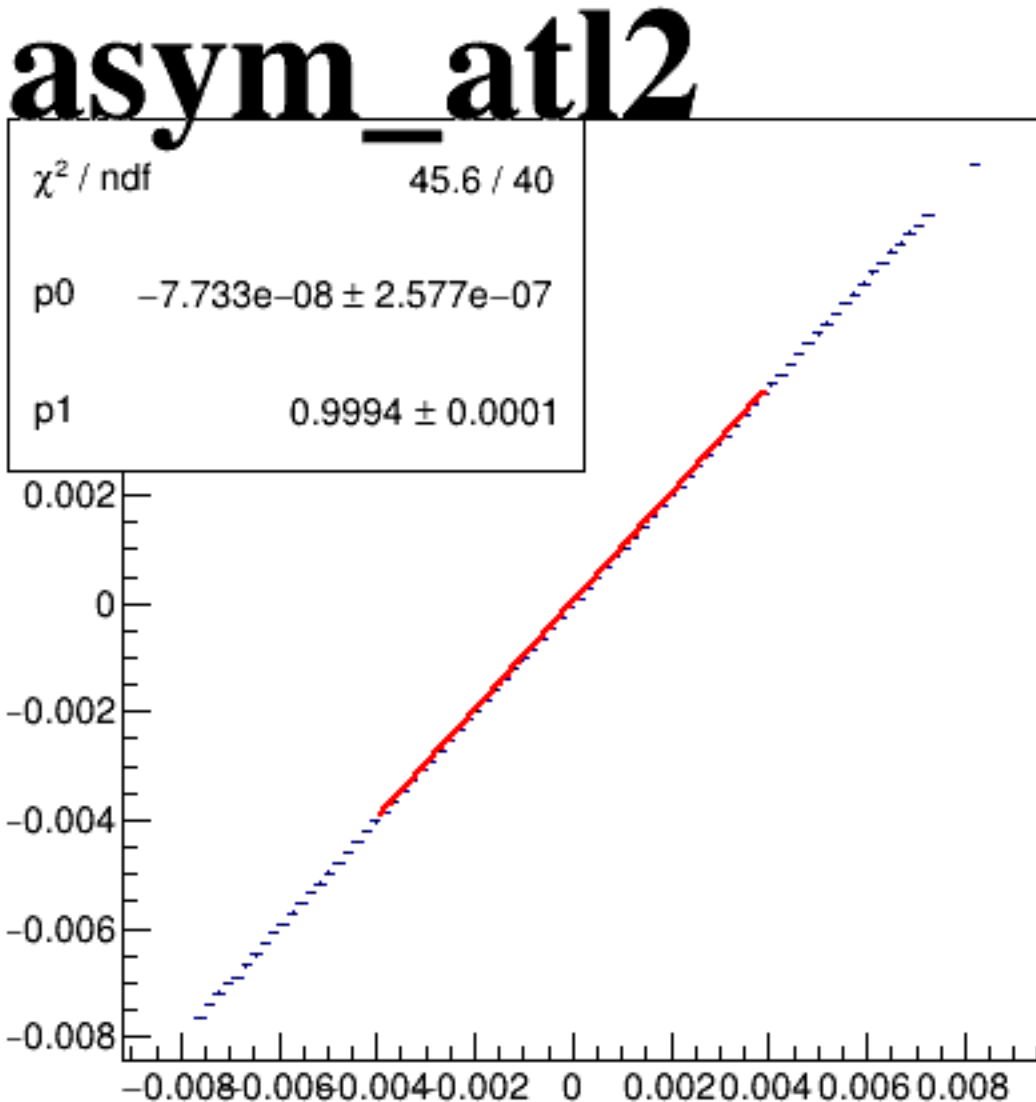
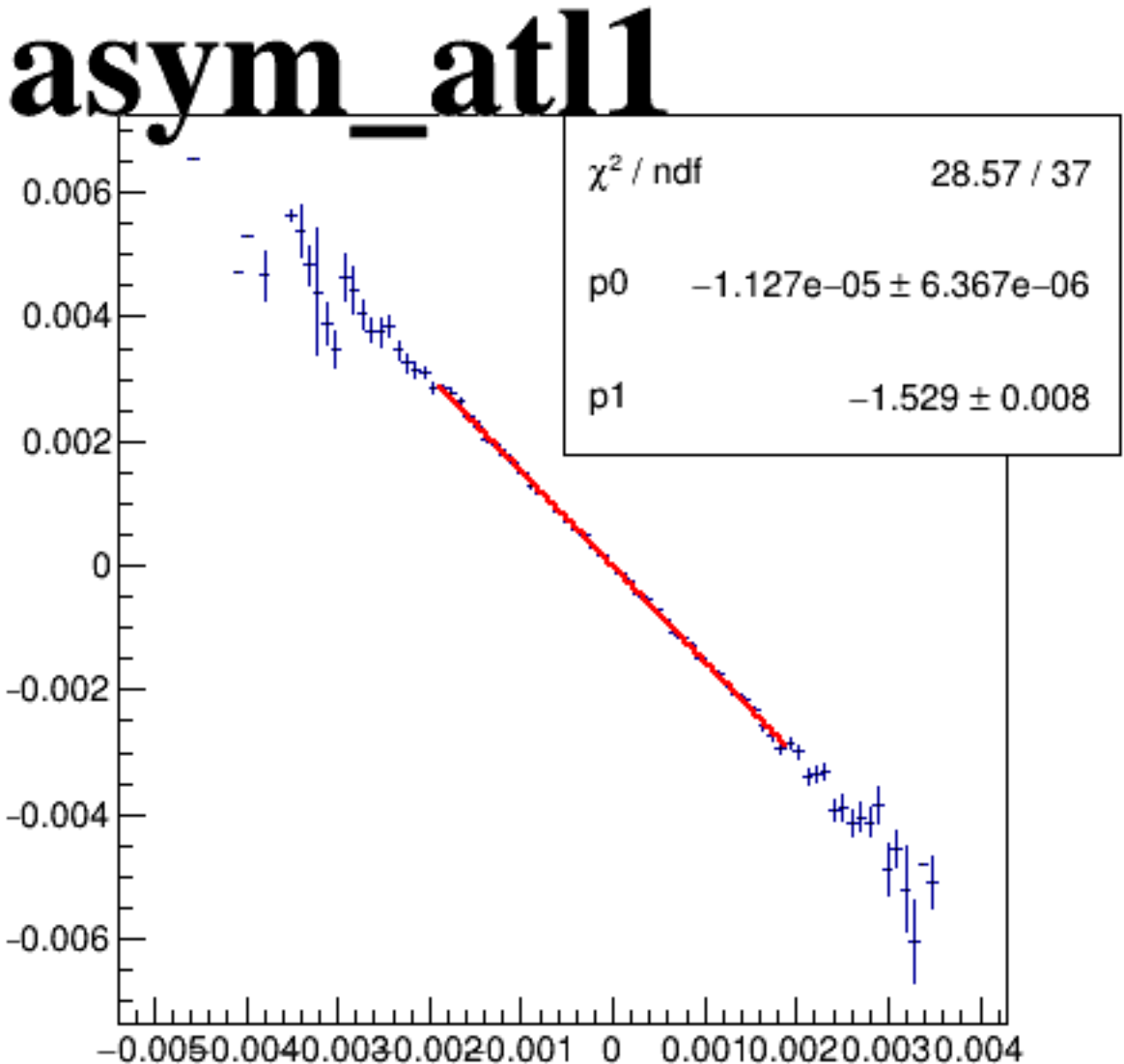




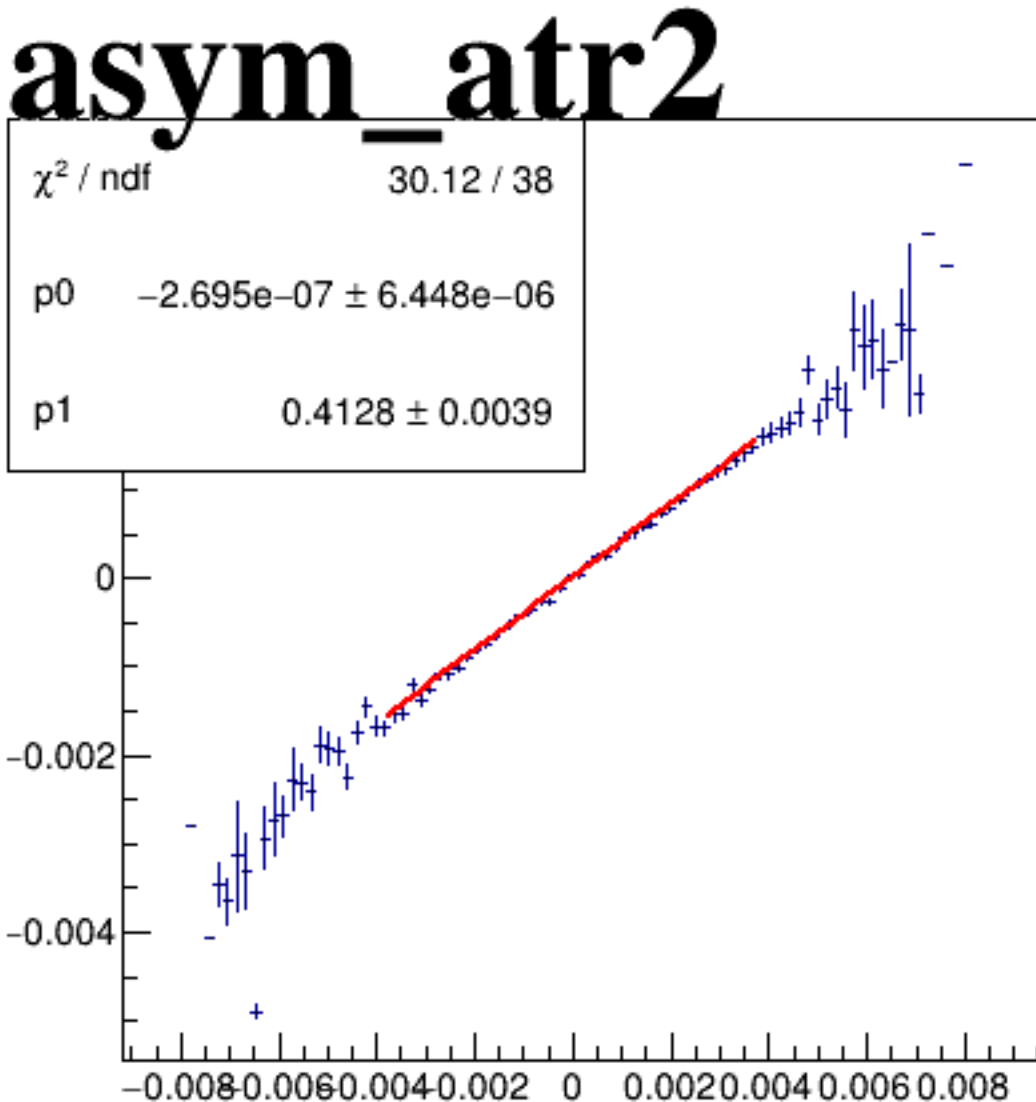
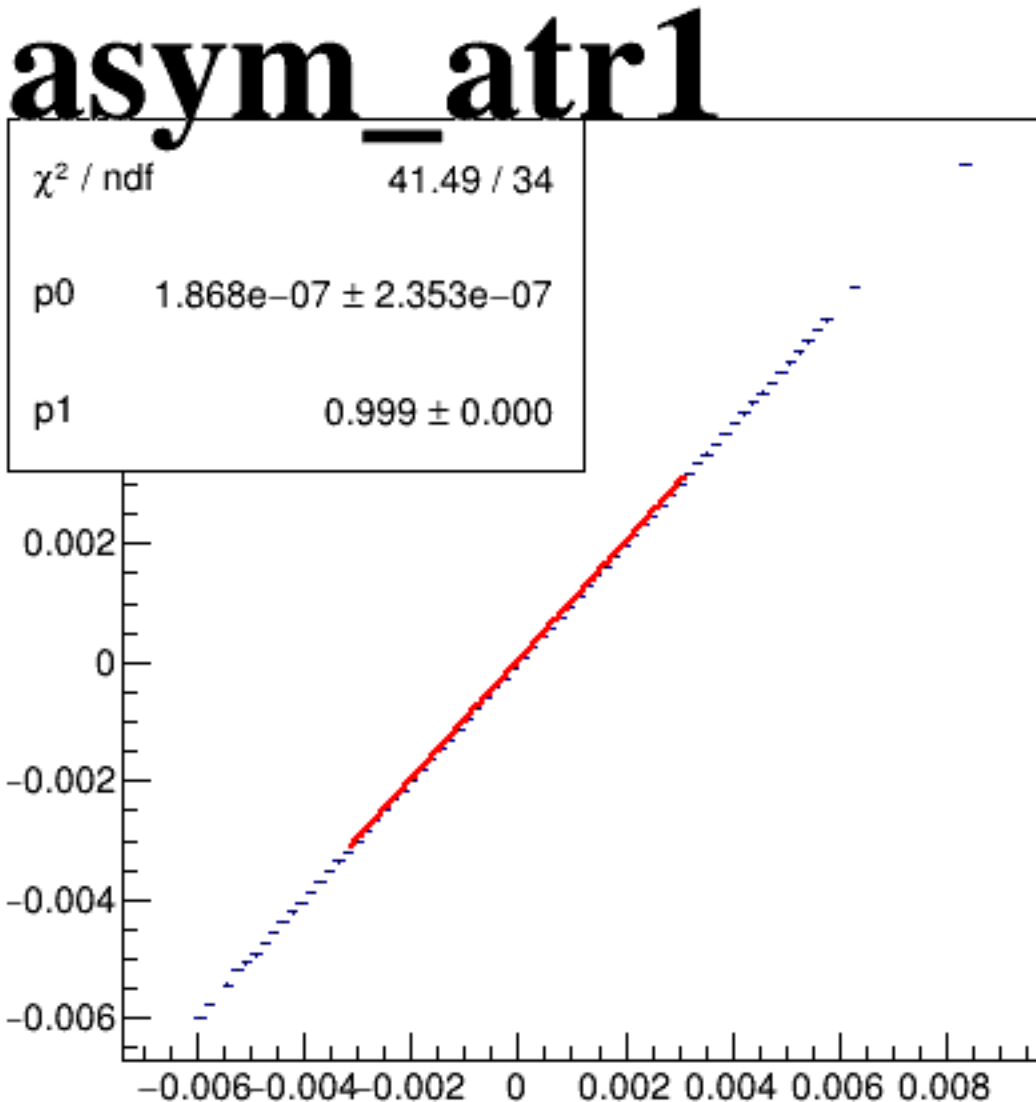
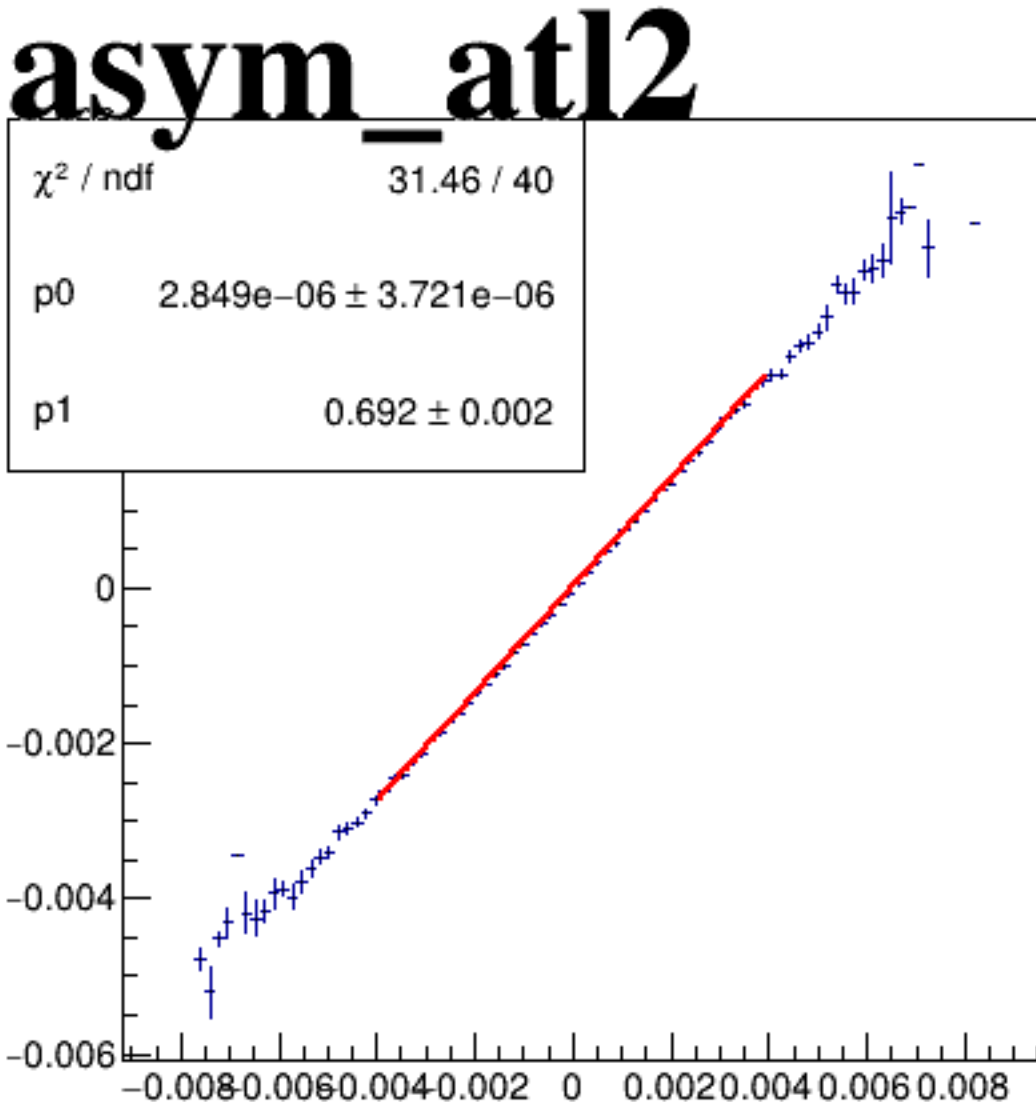
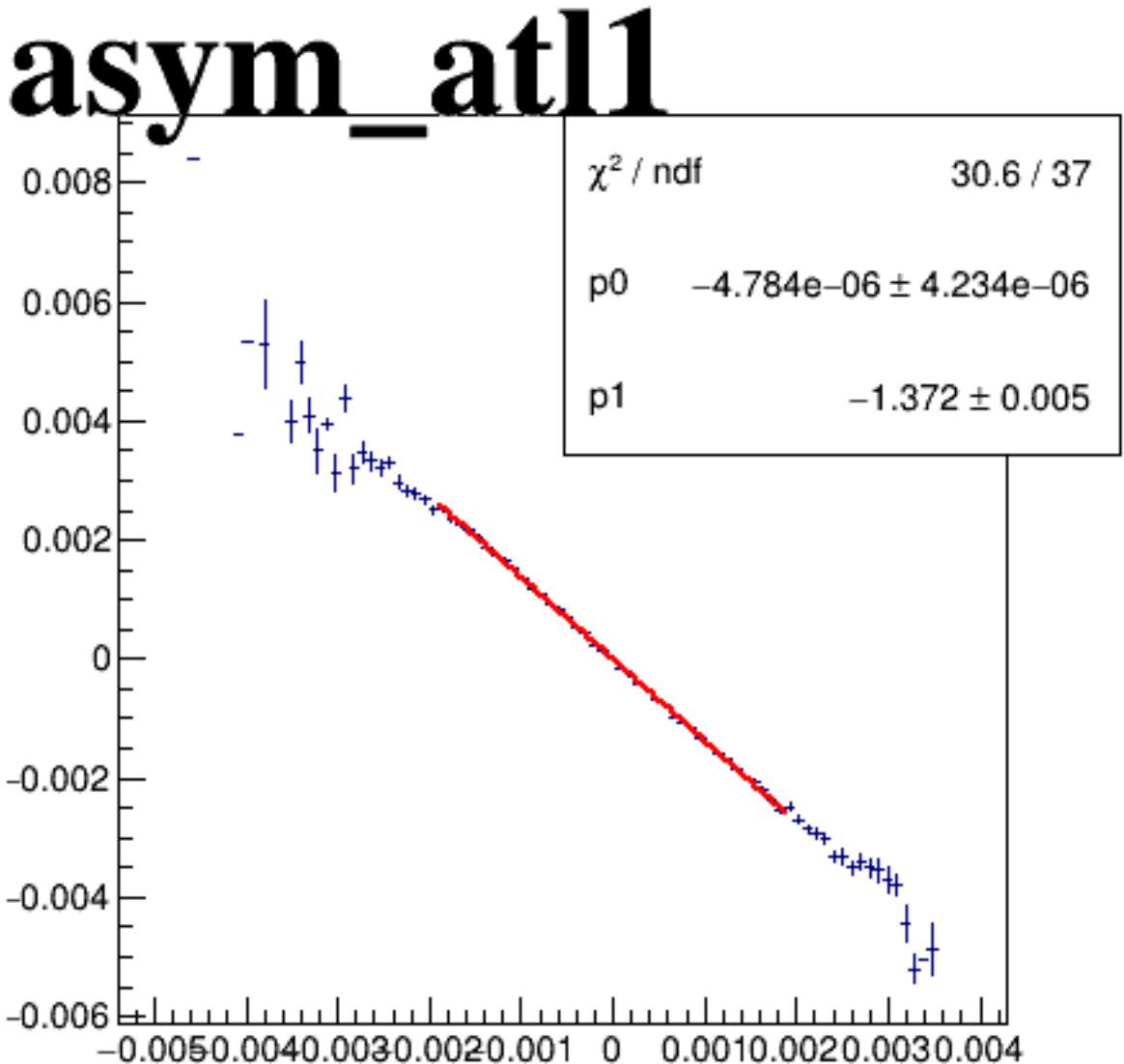
asym\_atl1



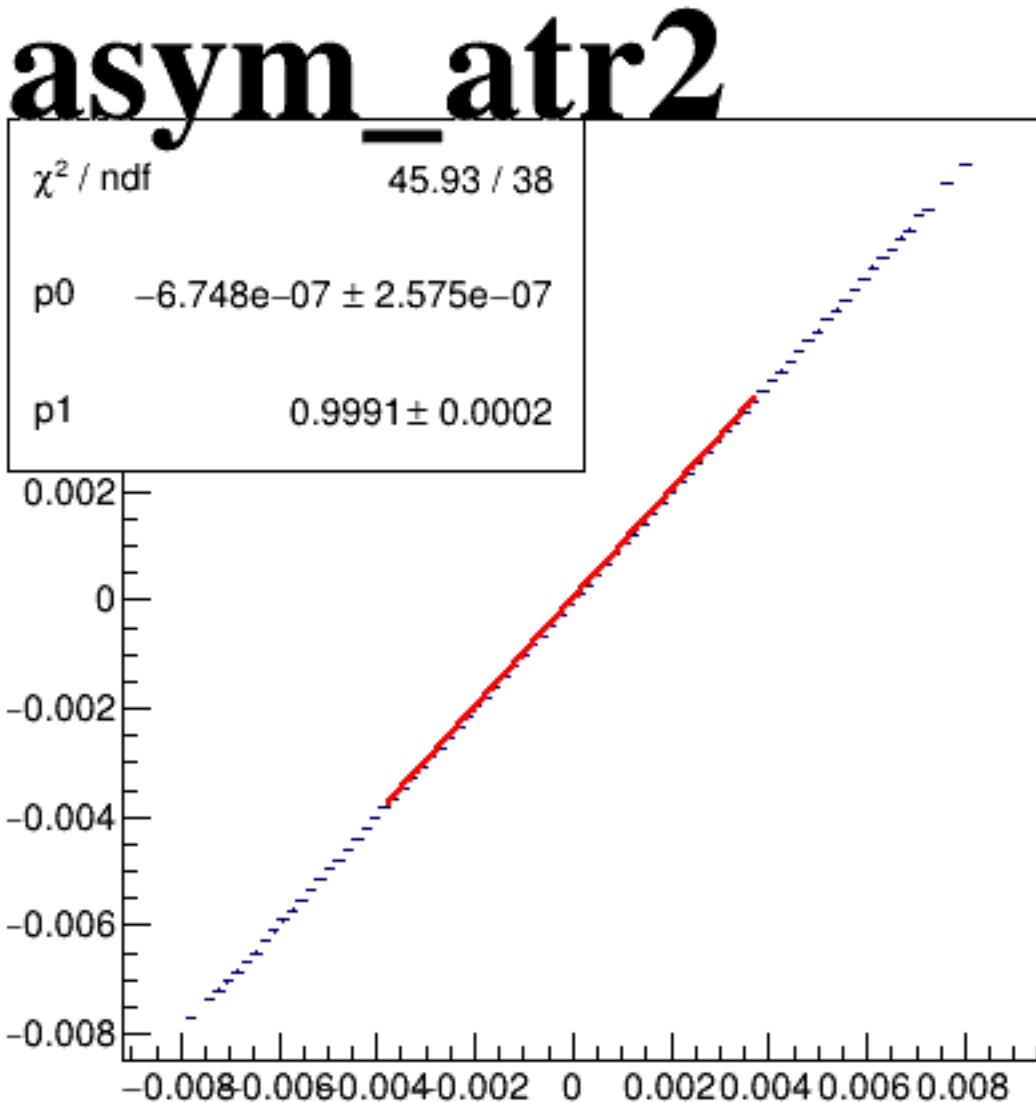
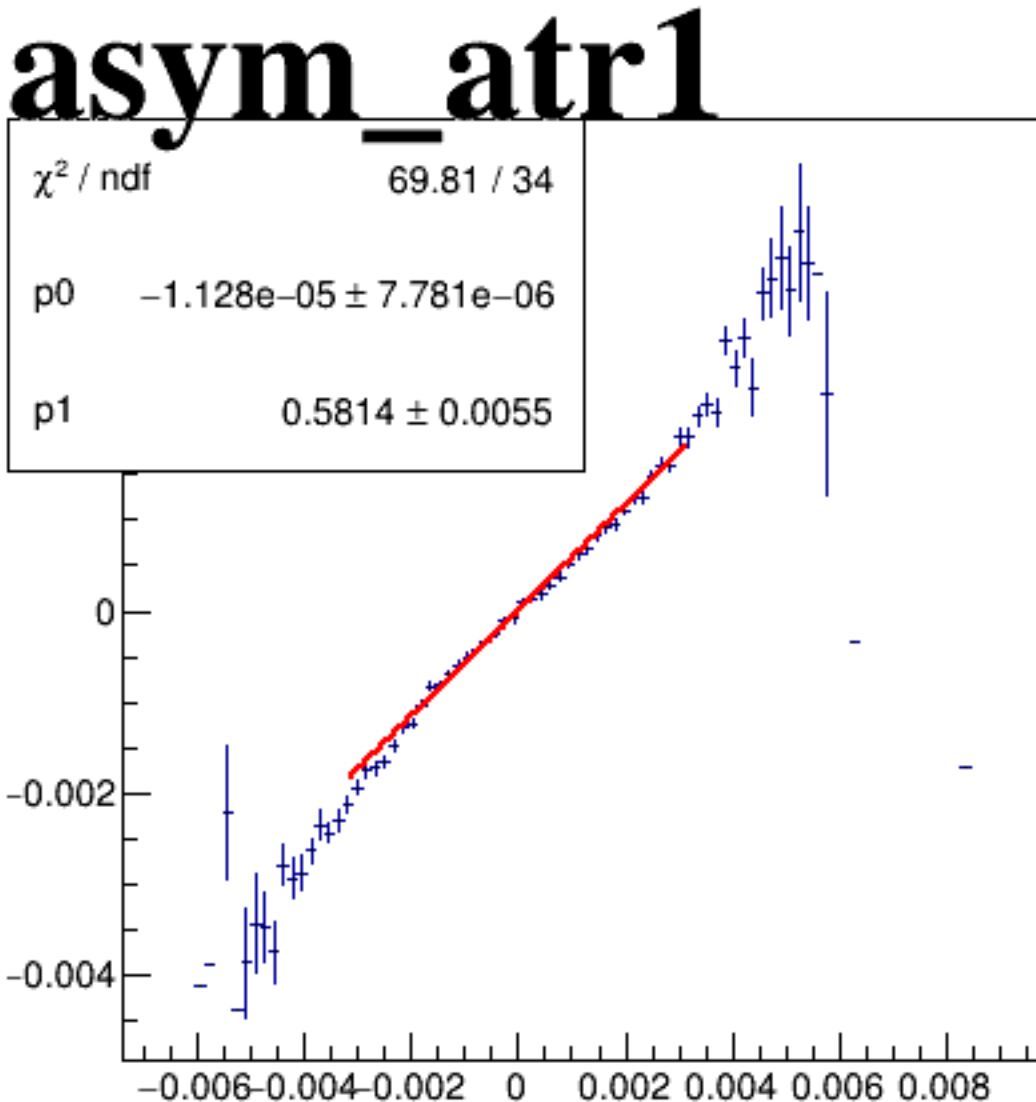
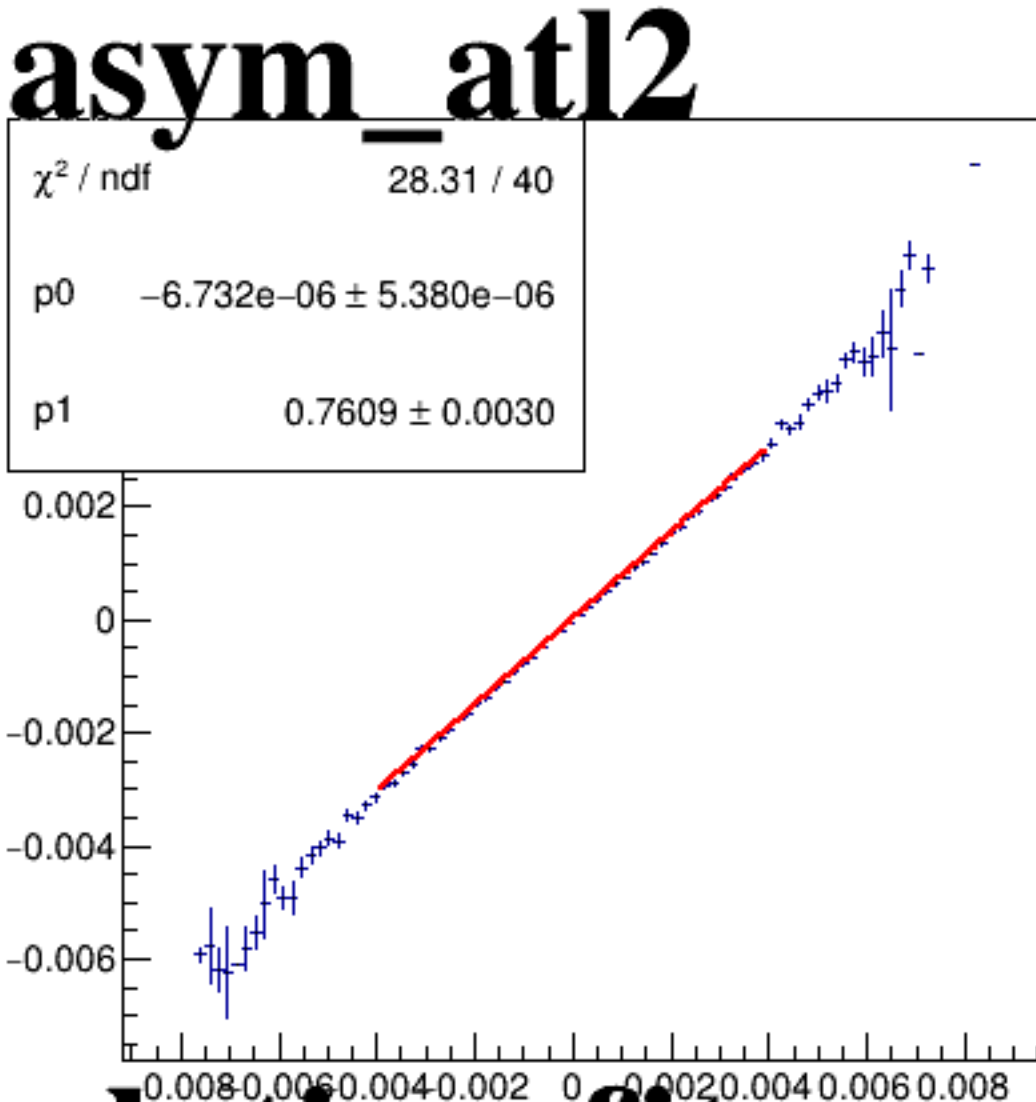
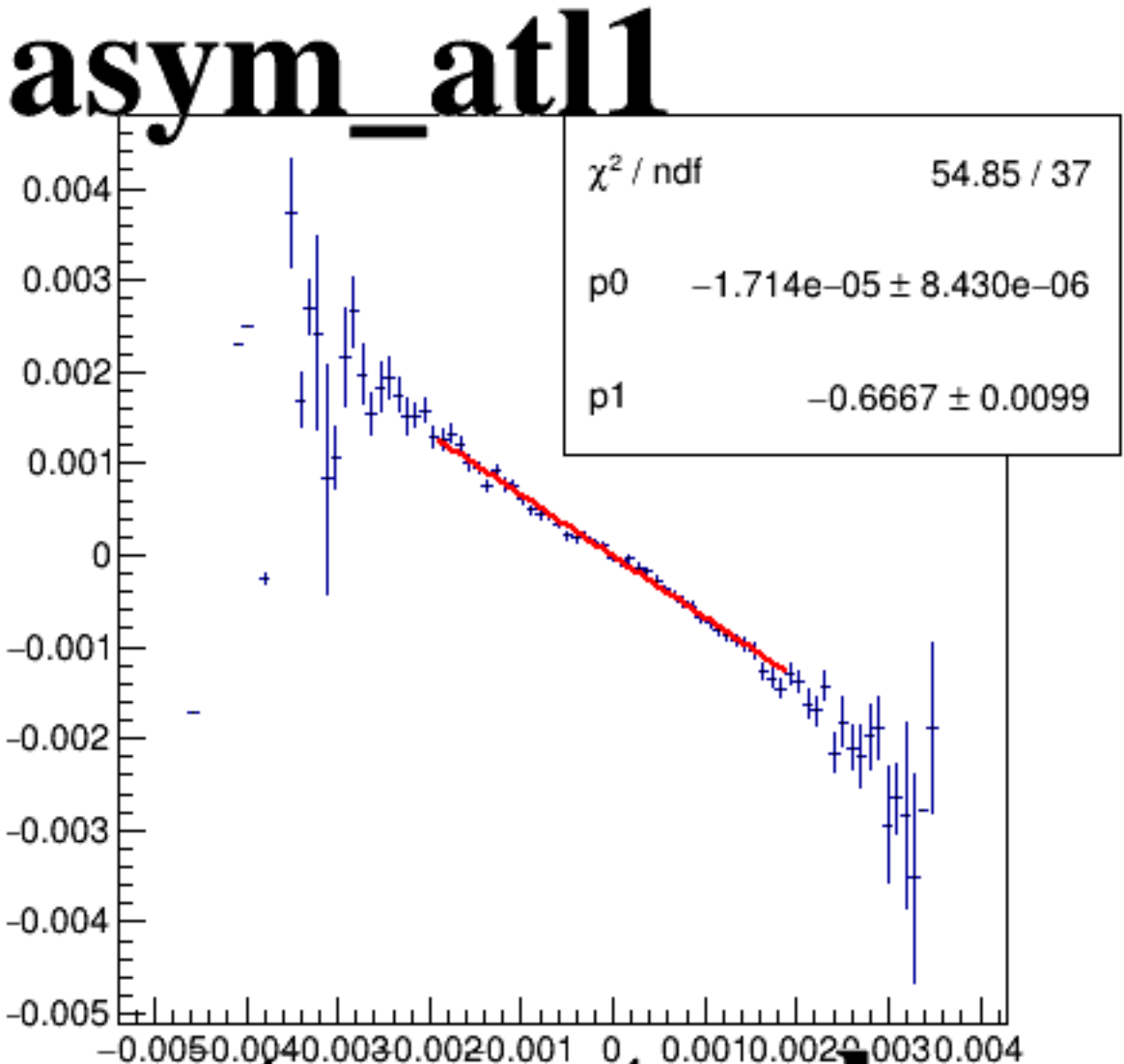
asym\_atl2



asym\_atr1

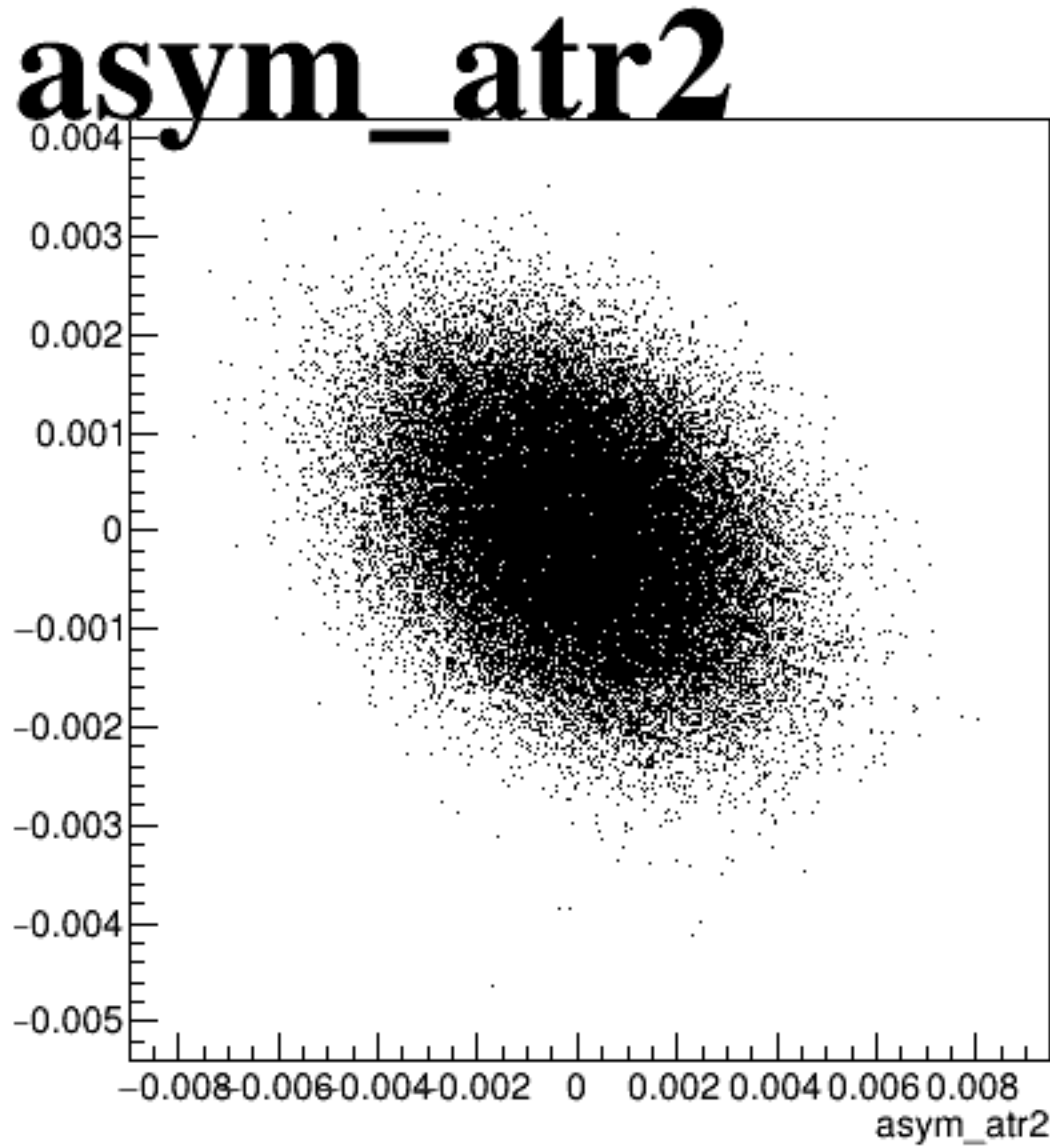
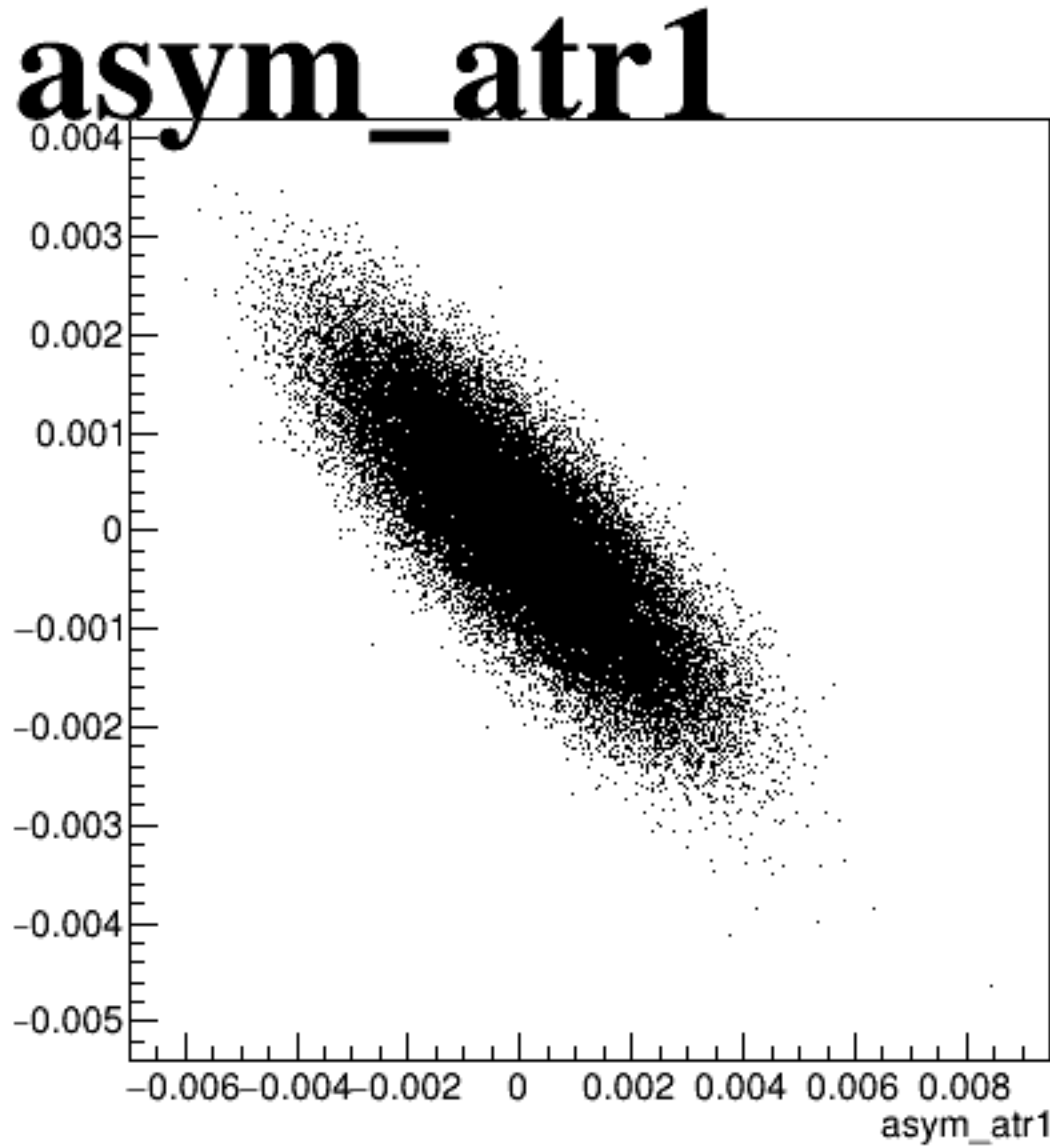
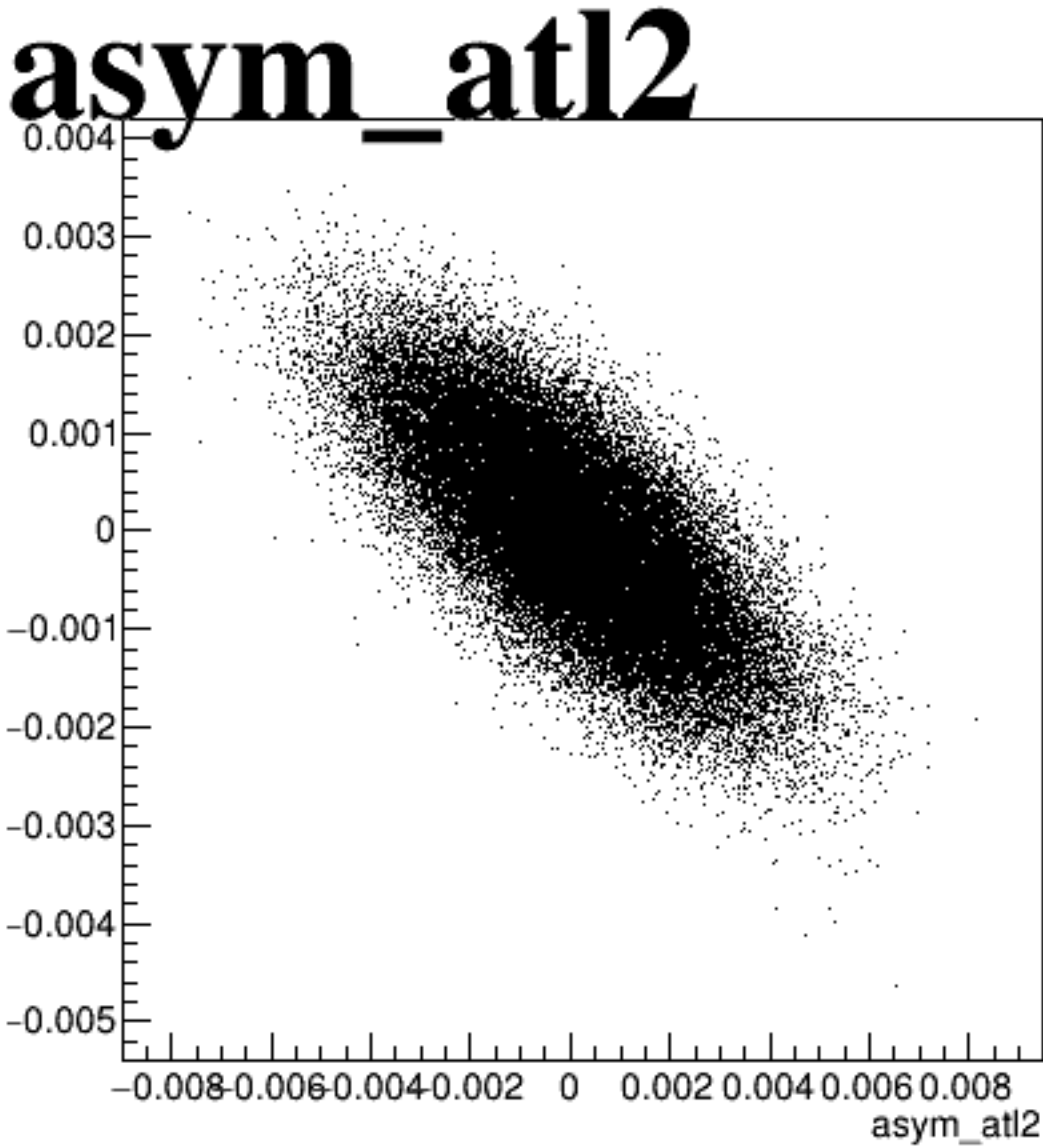
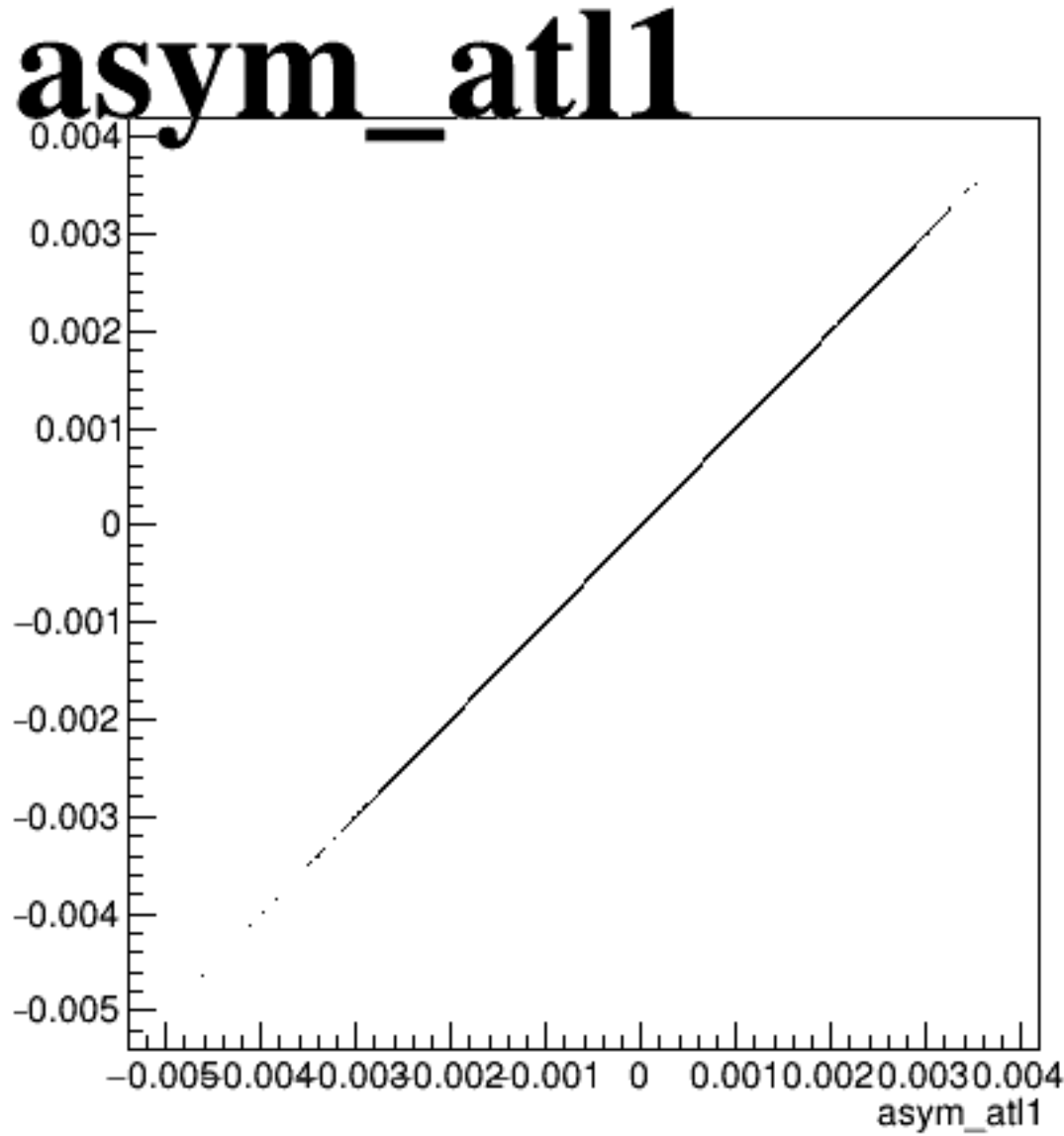


asym\_atr2

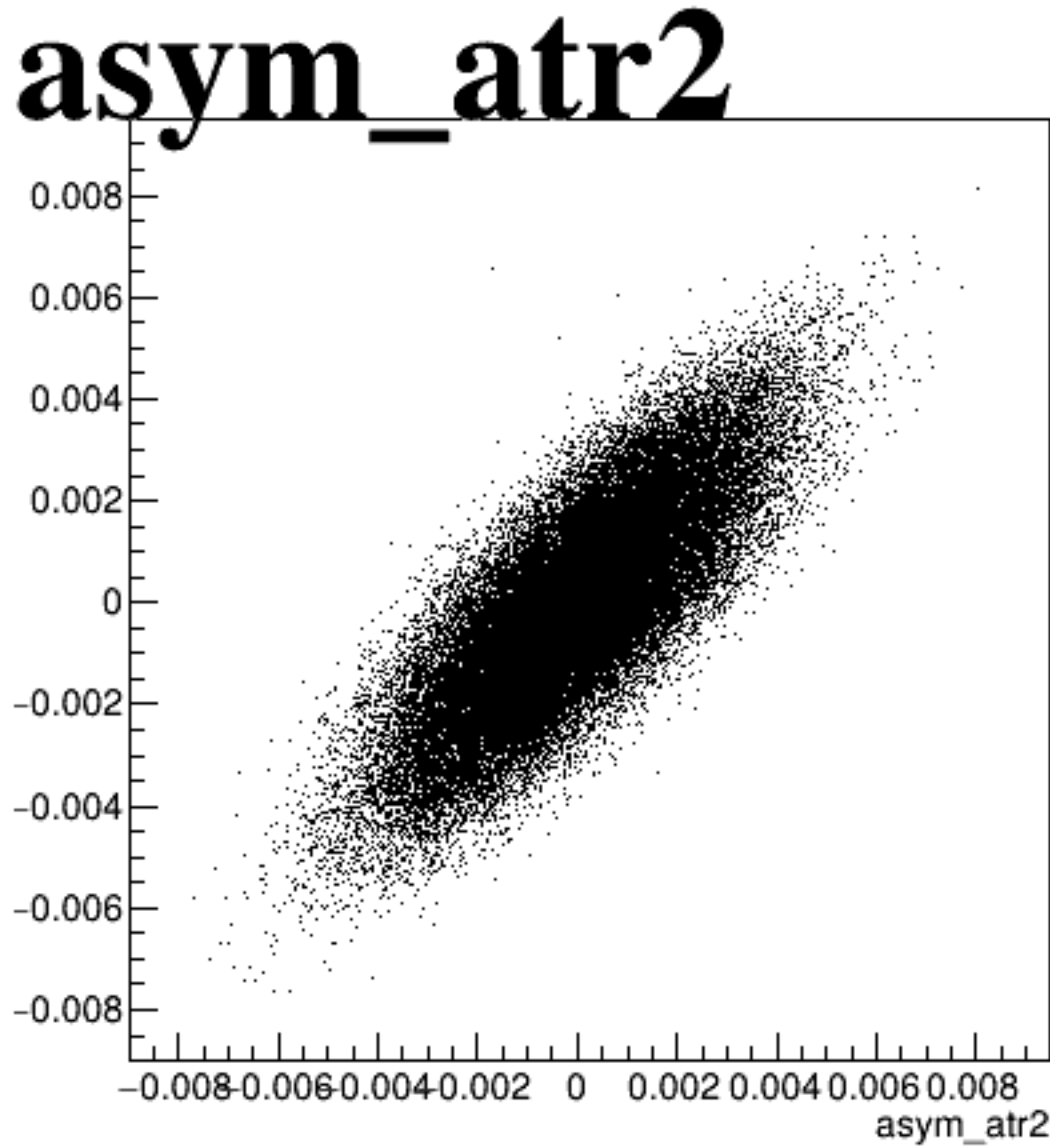
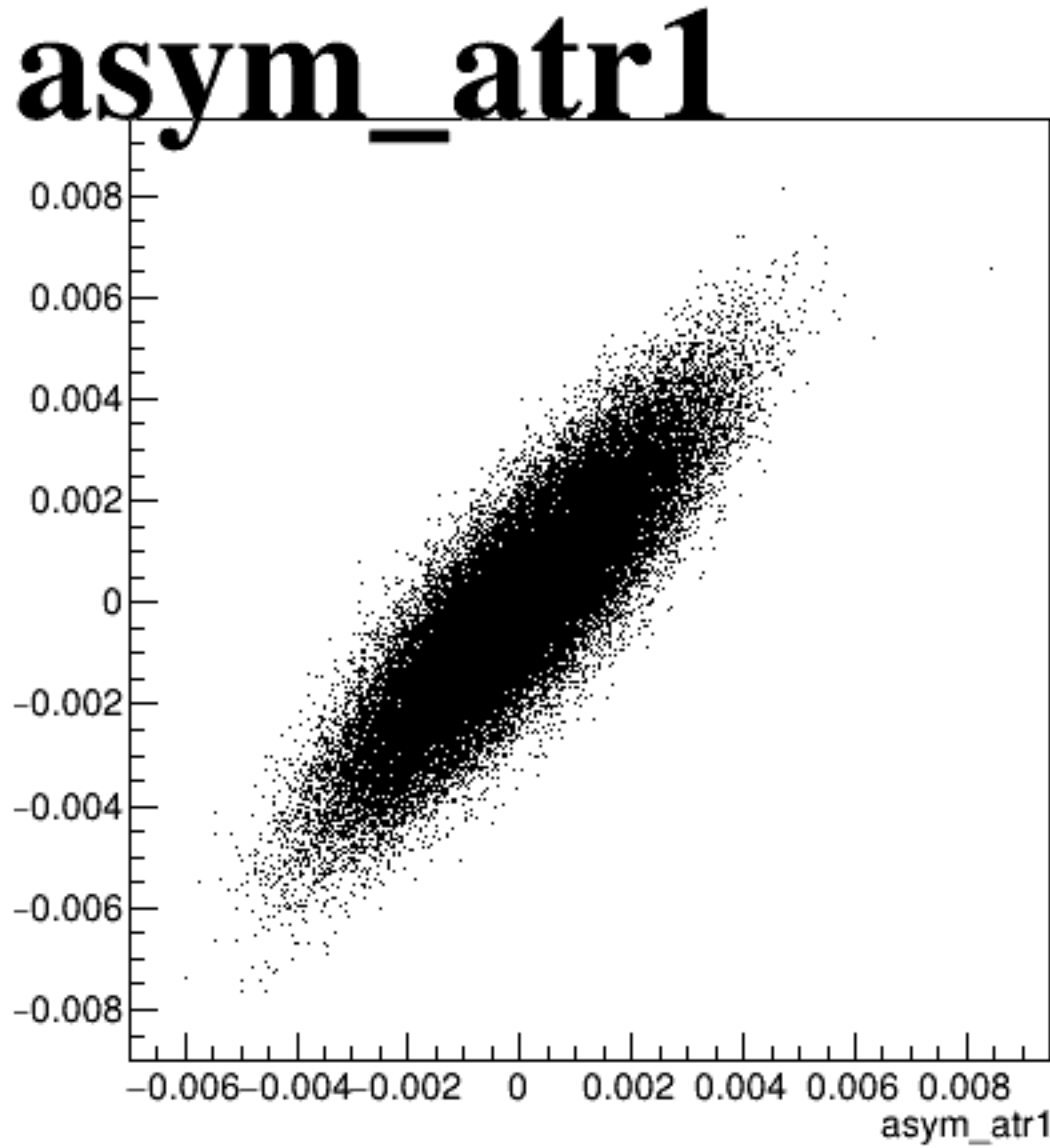
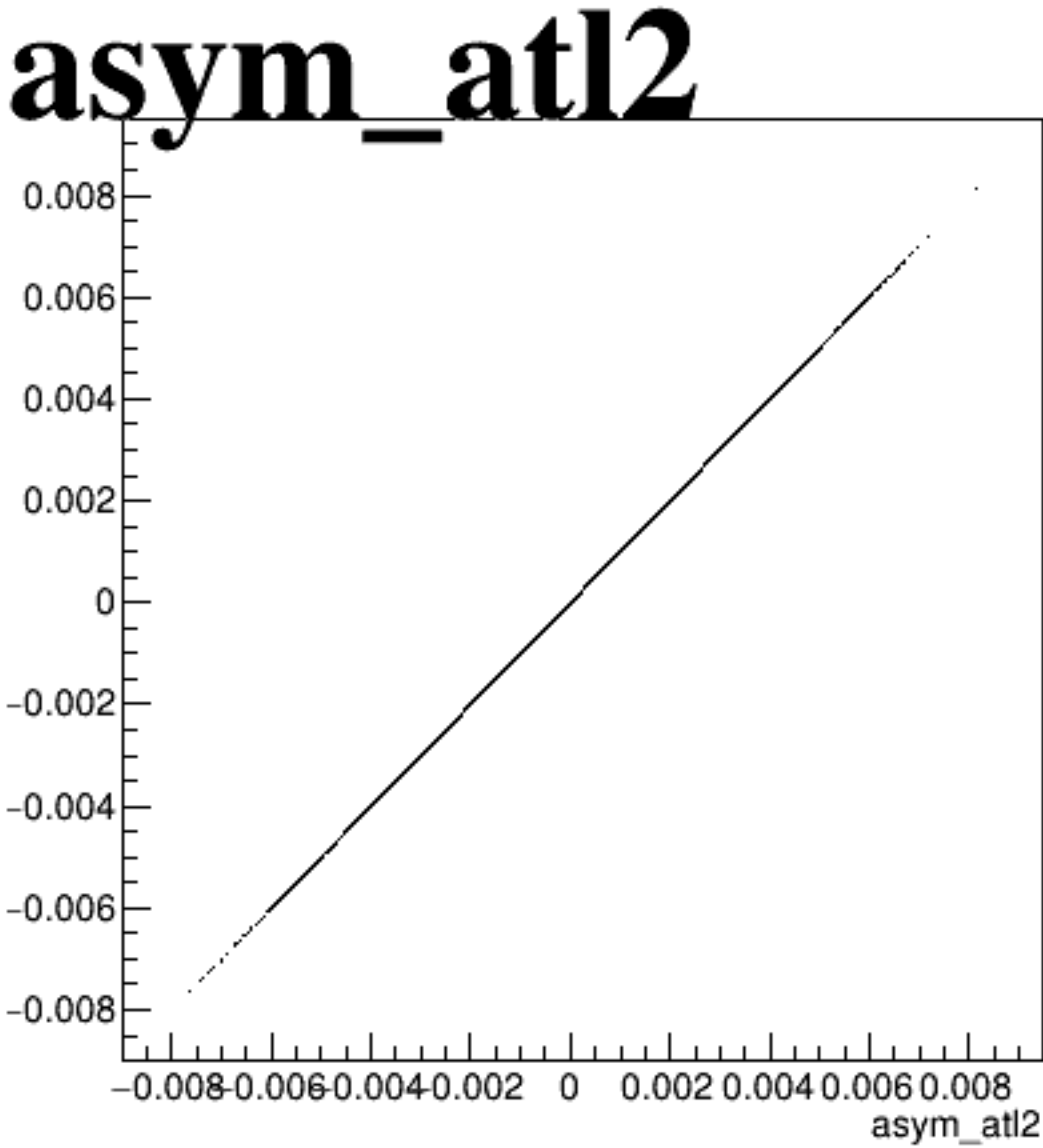
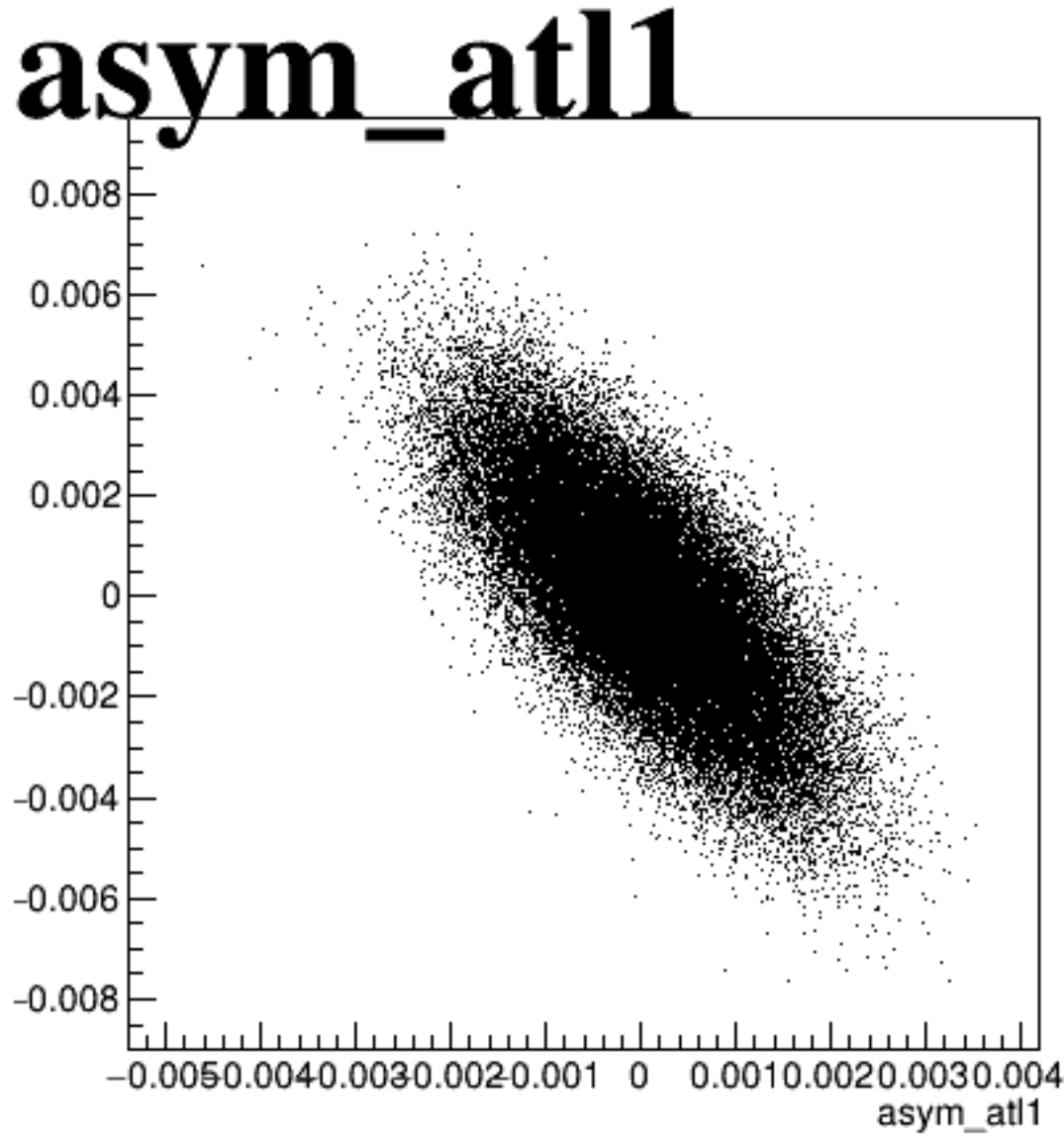




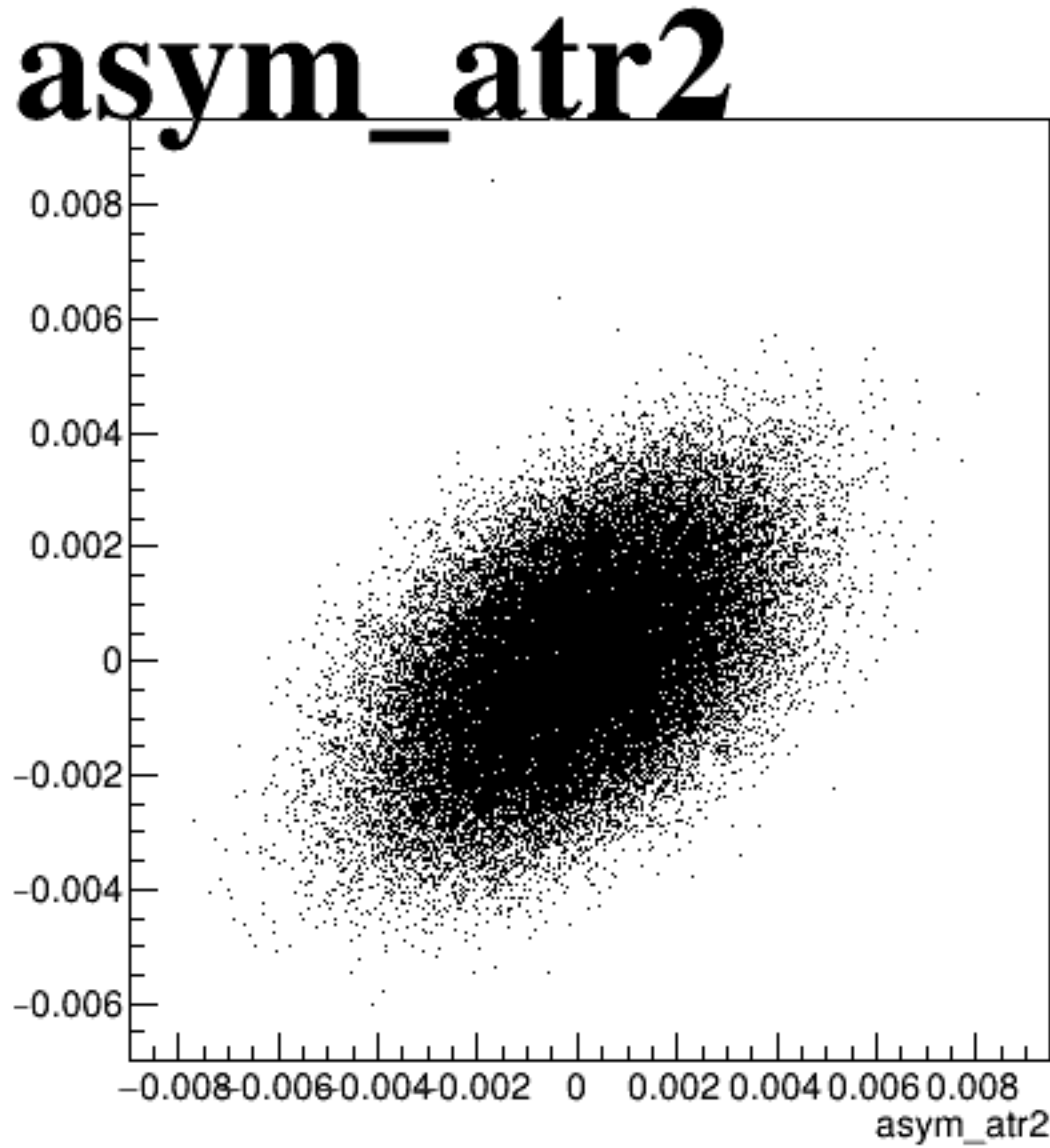
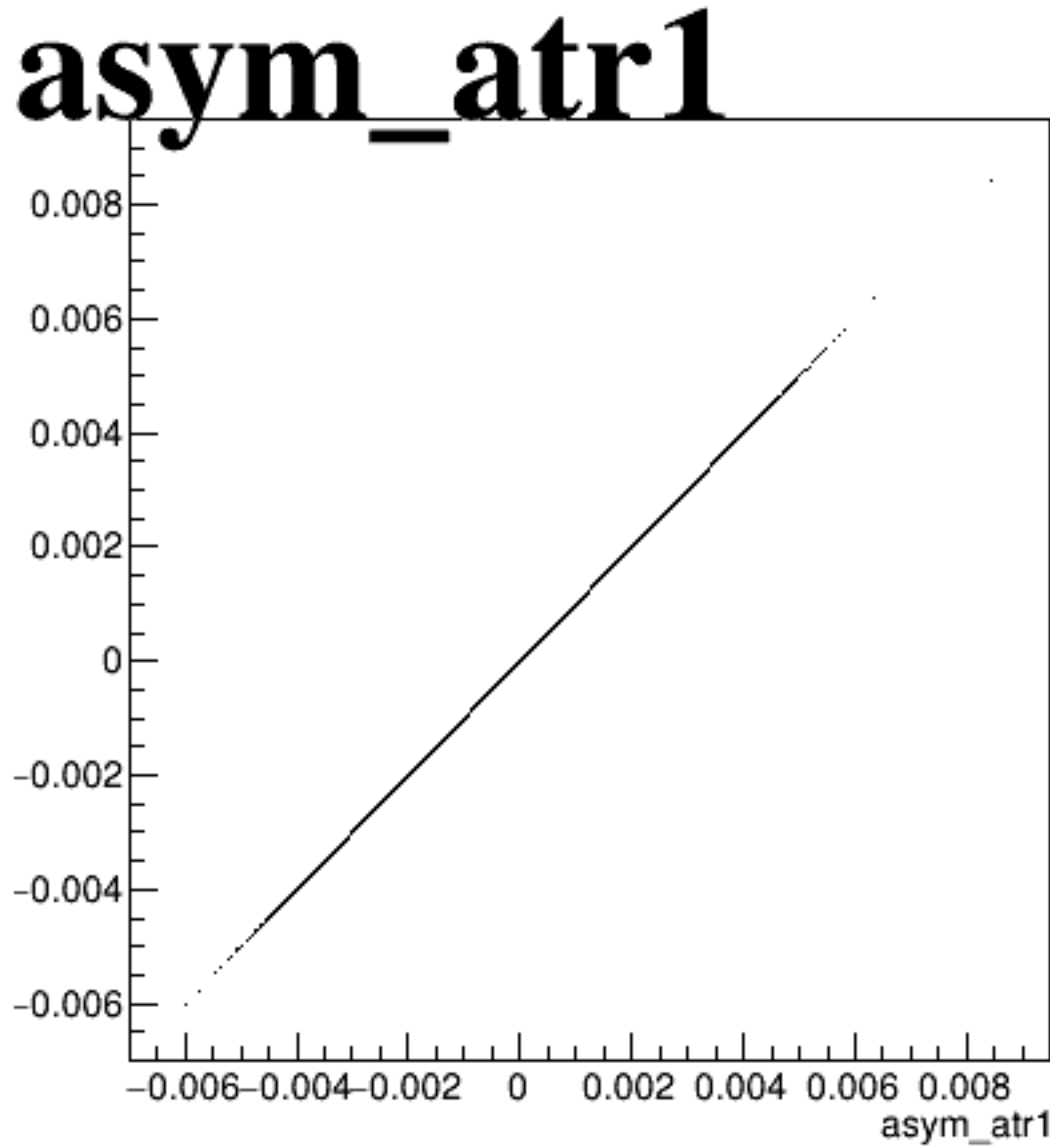
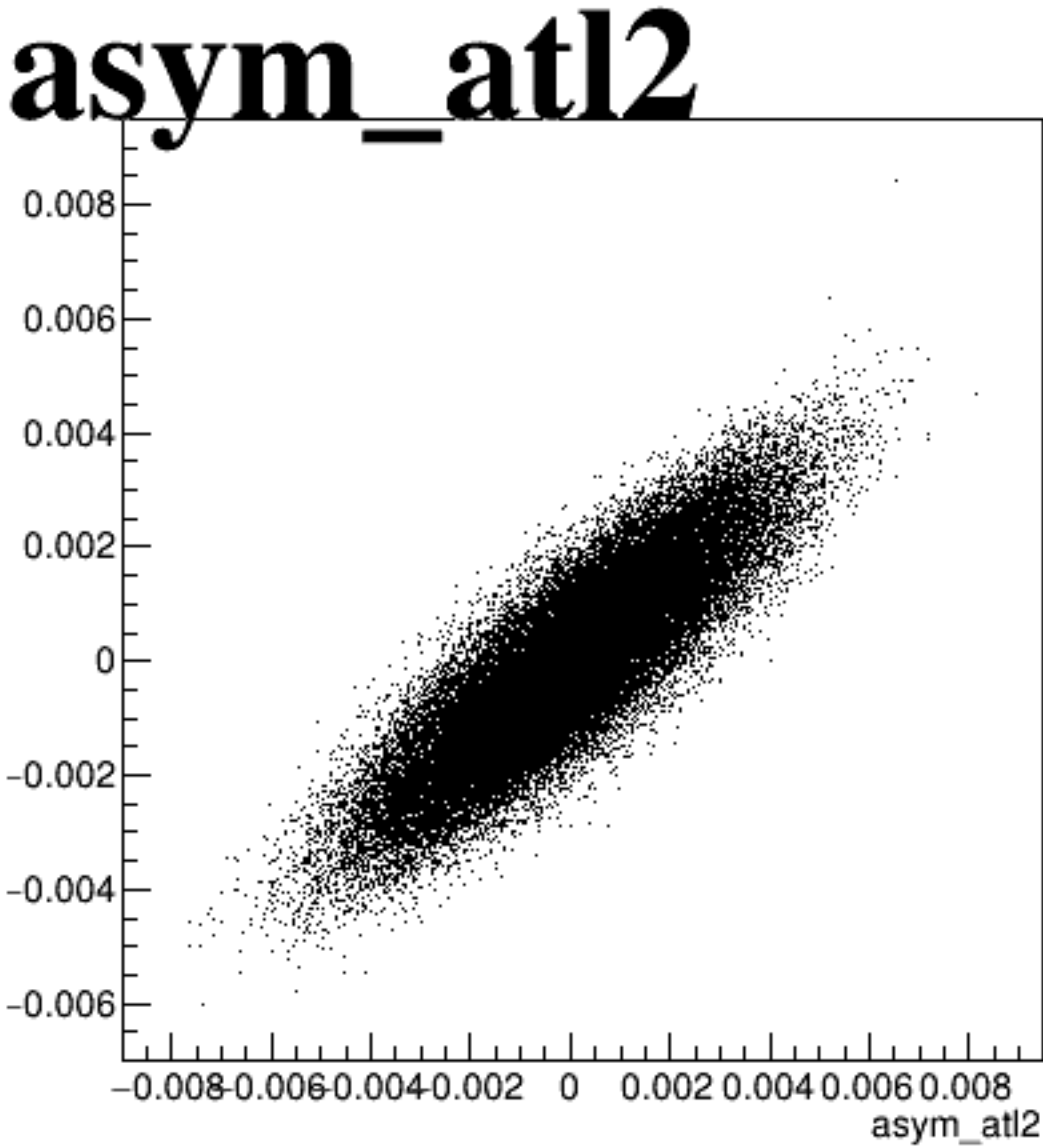
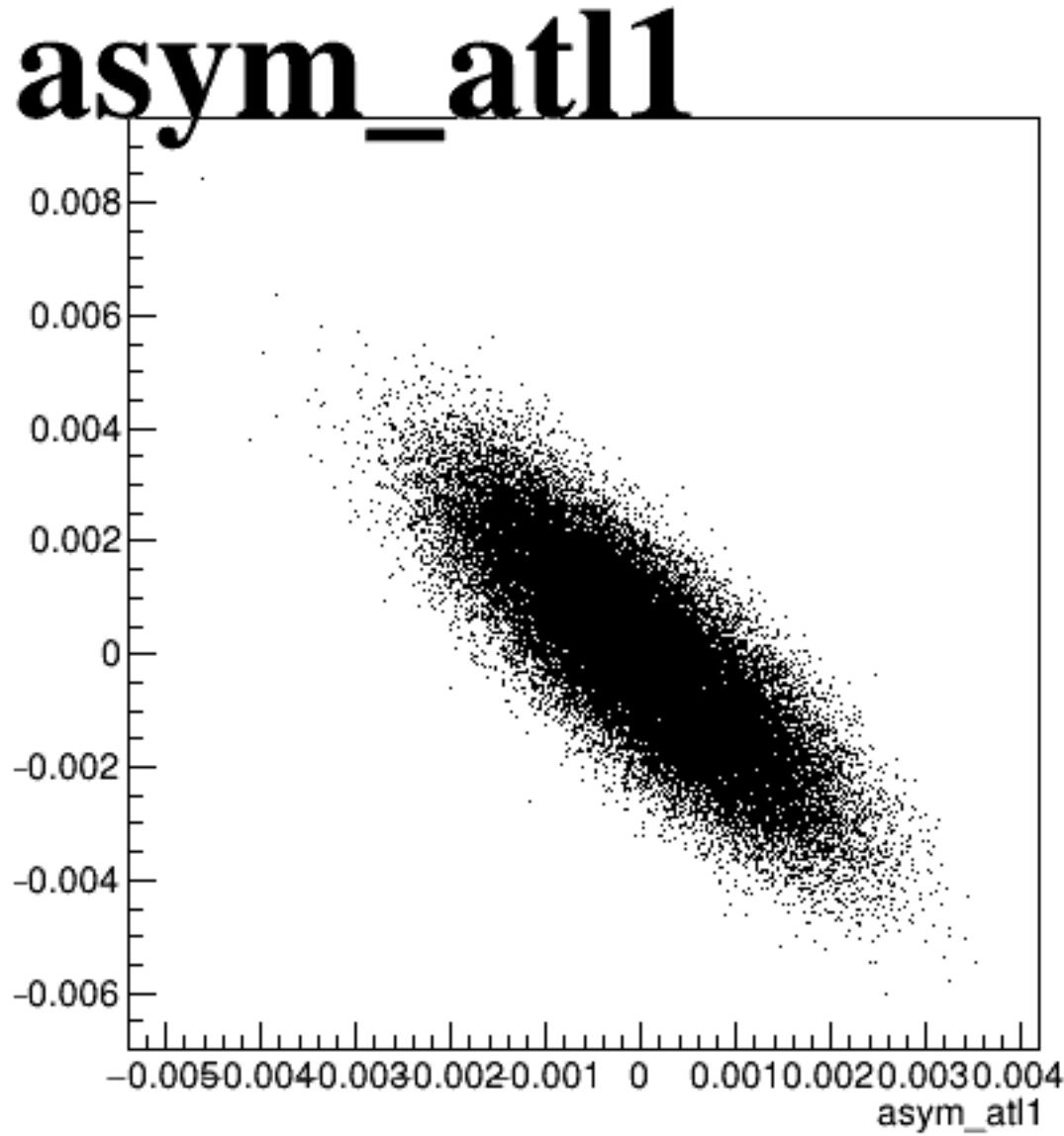
asym\_atl1



asym\_atl2



asym\_atr1



asym\_atr2

