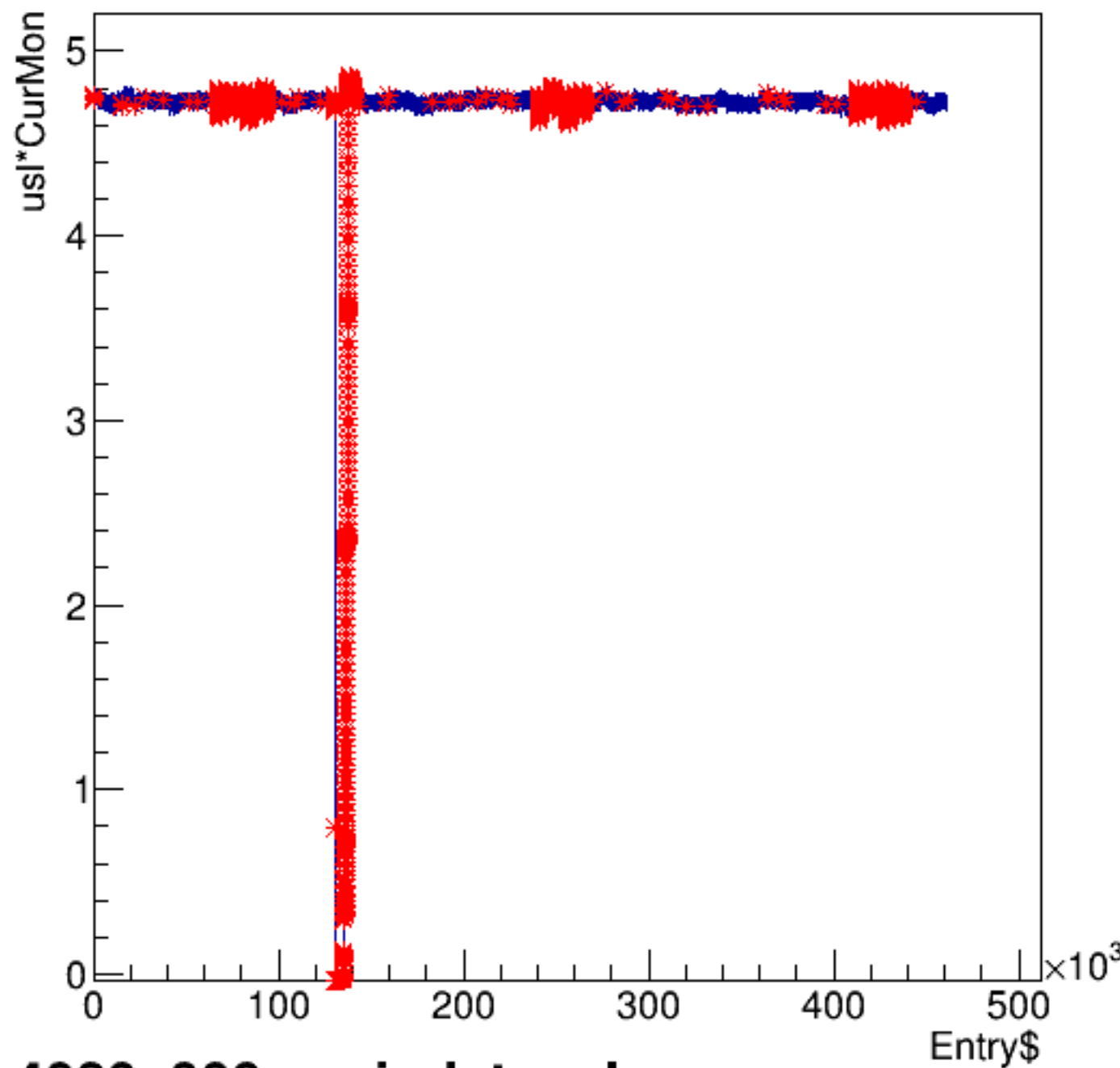
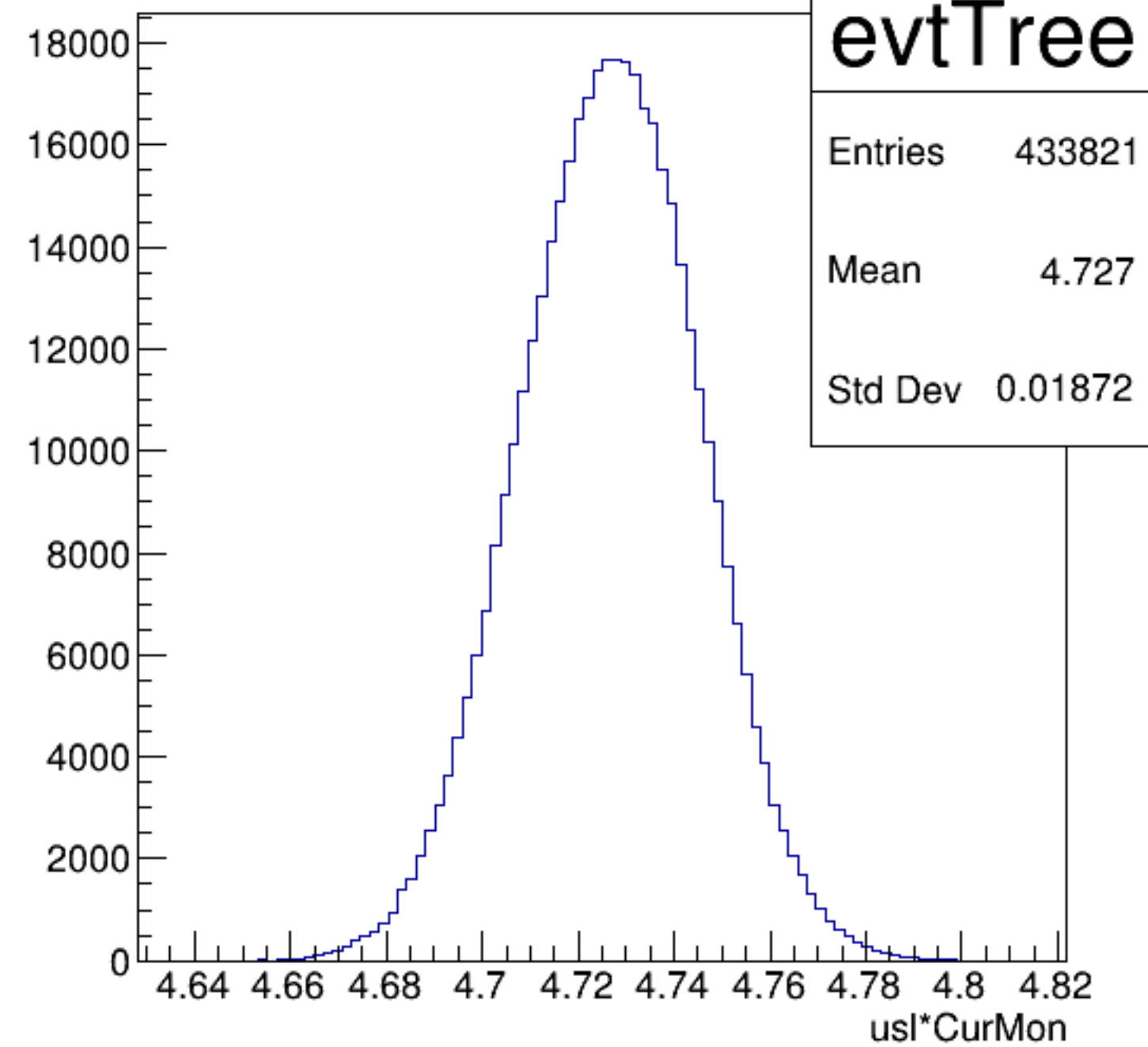


usl\*CurMon:Entry\$

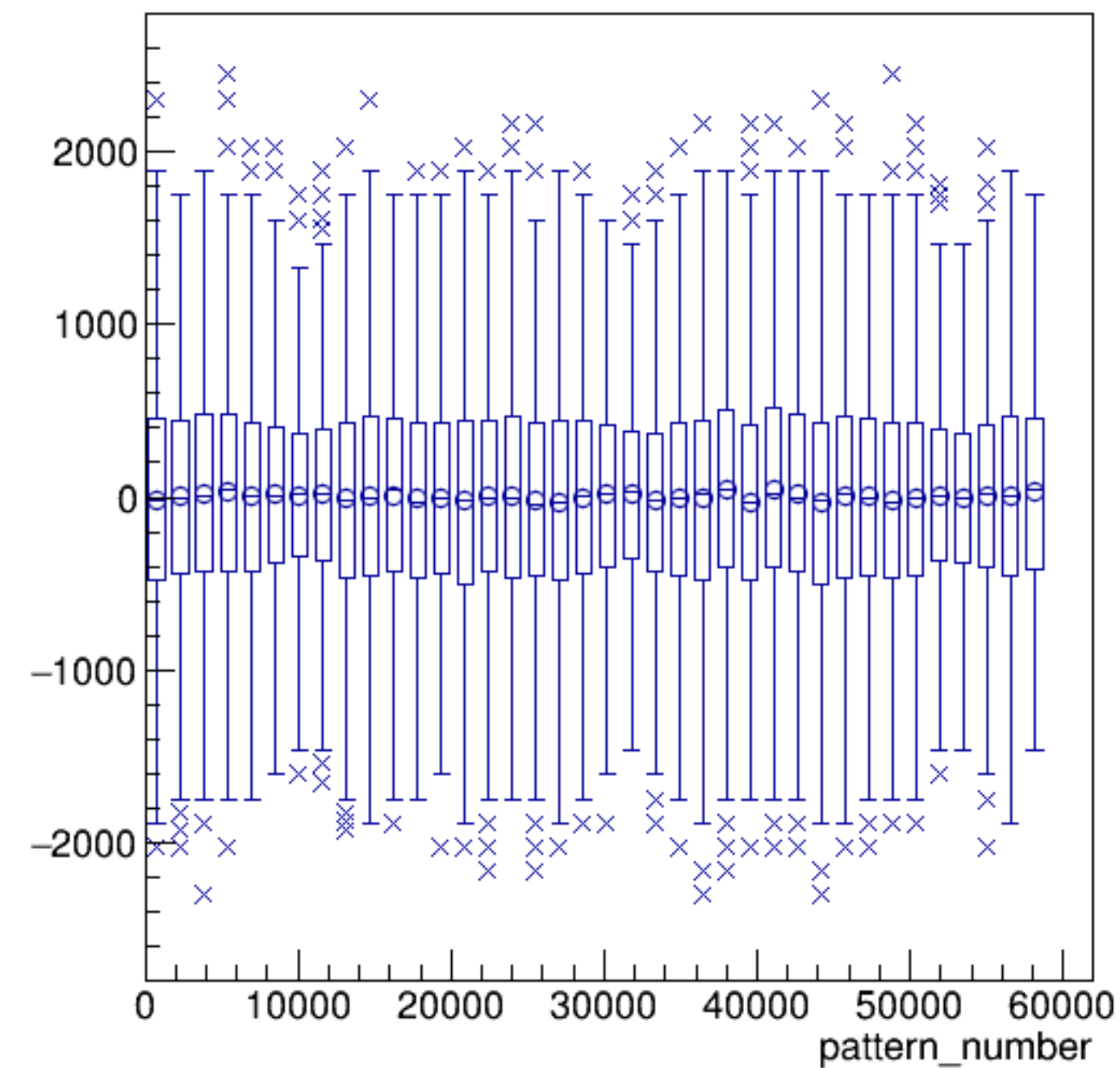


run4080\_000\_maindet\_usl.png

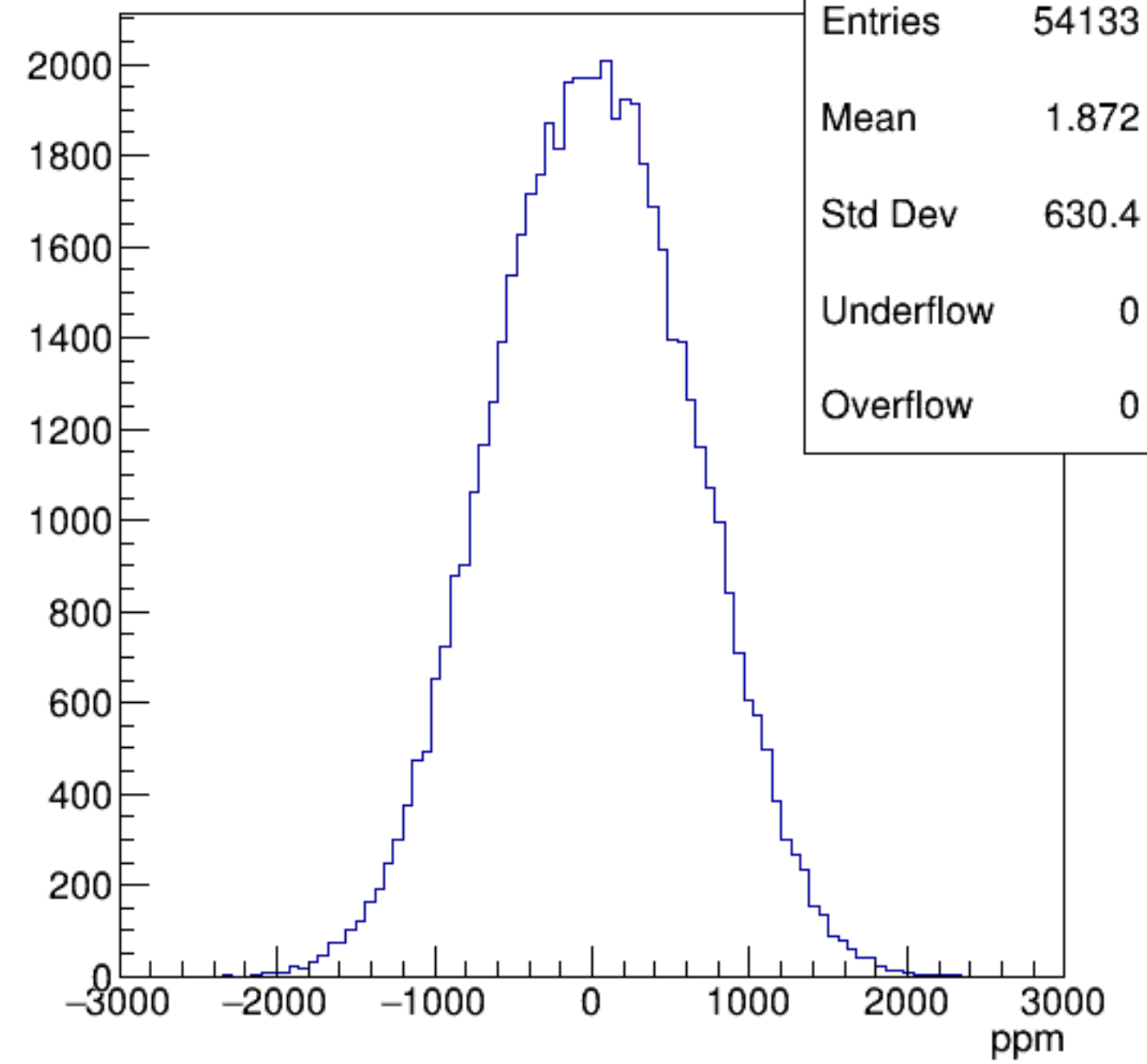
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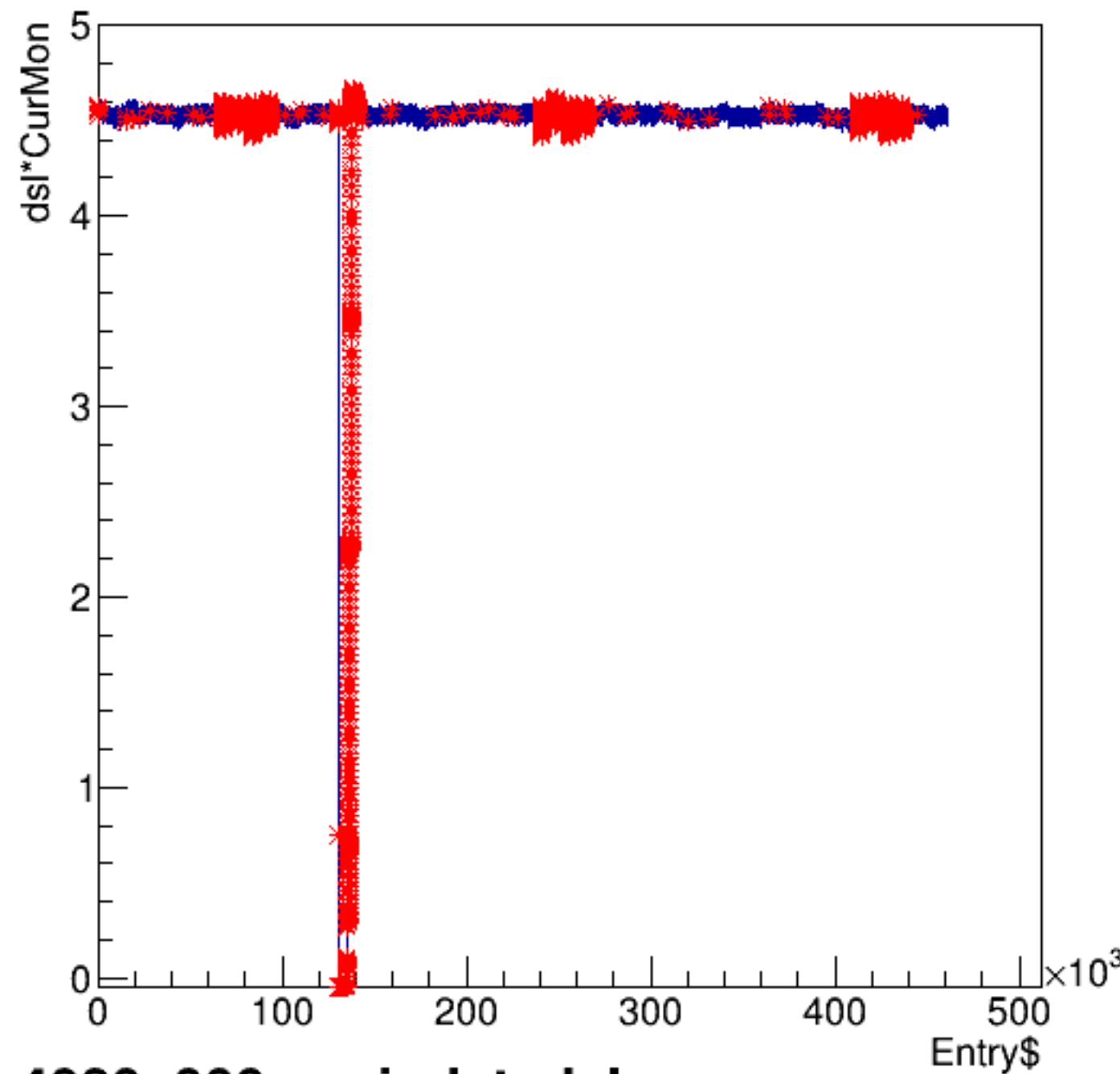
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asym\_usl

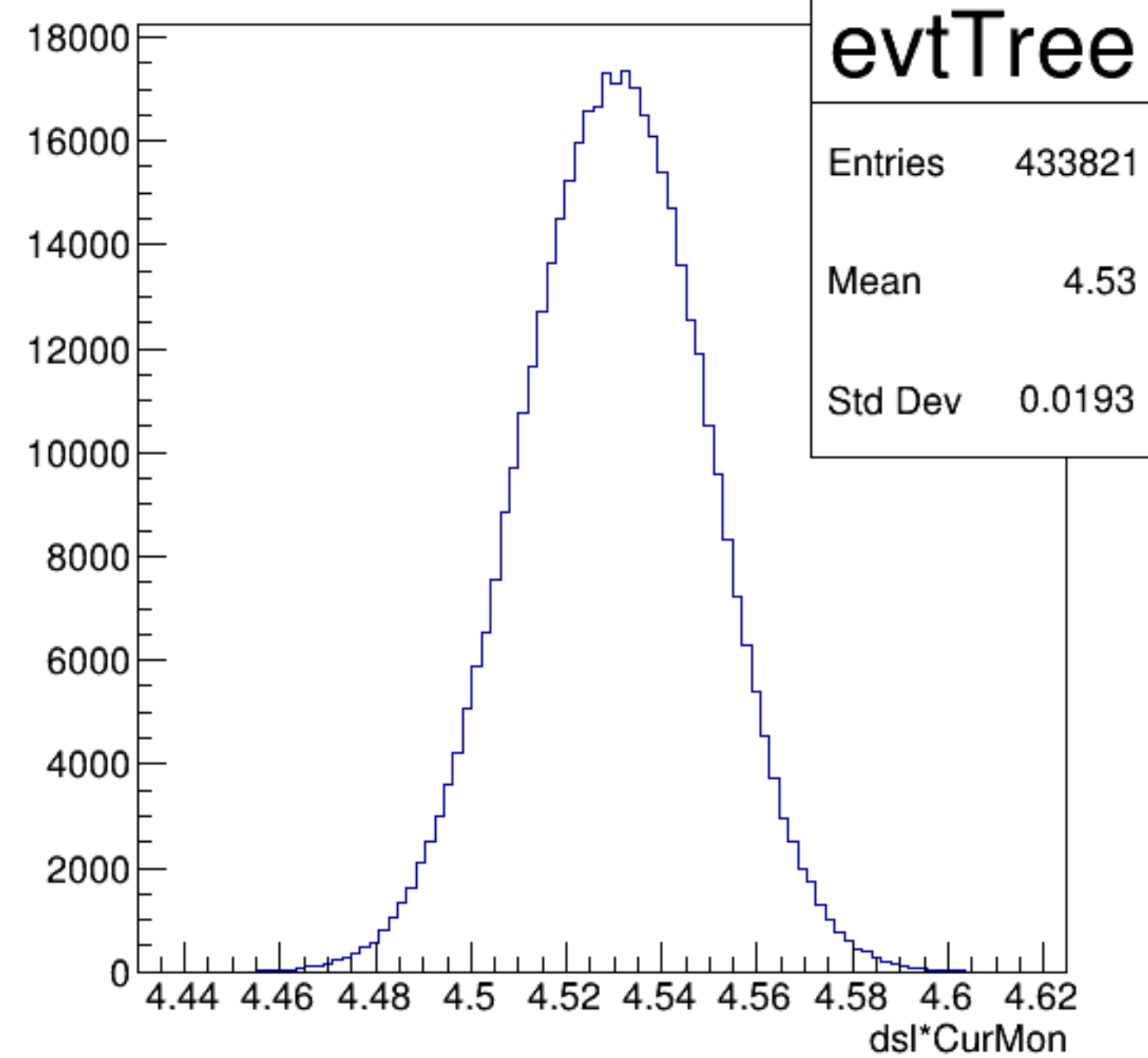


dsl\*CurMon:Entry\$

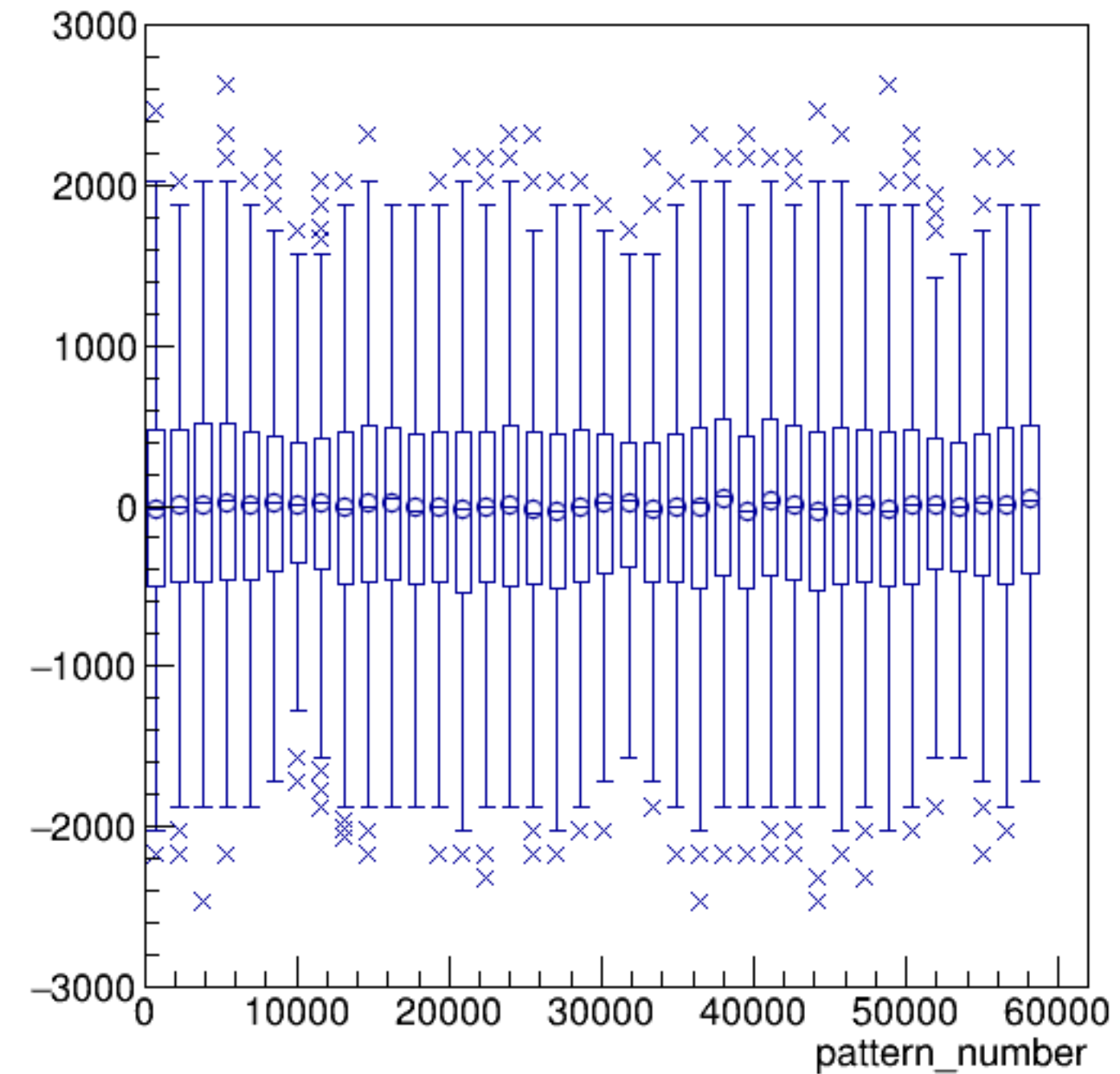


run4080\_000\_maindet\_dsl.png

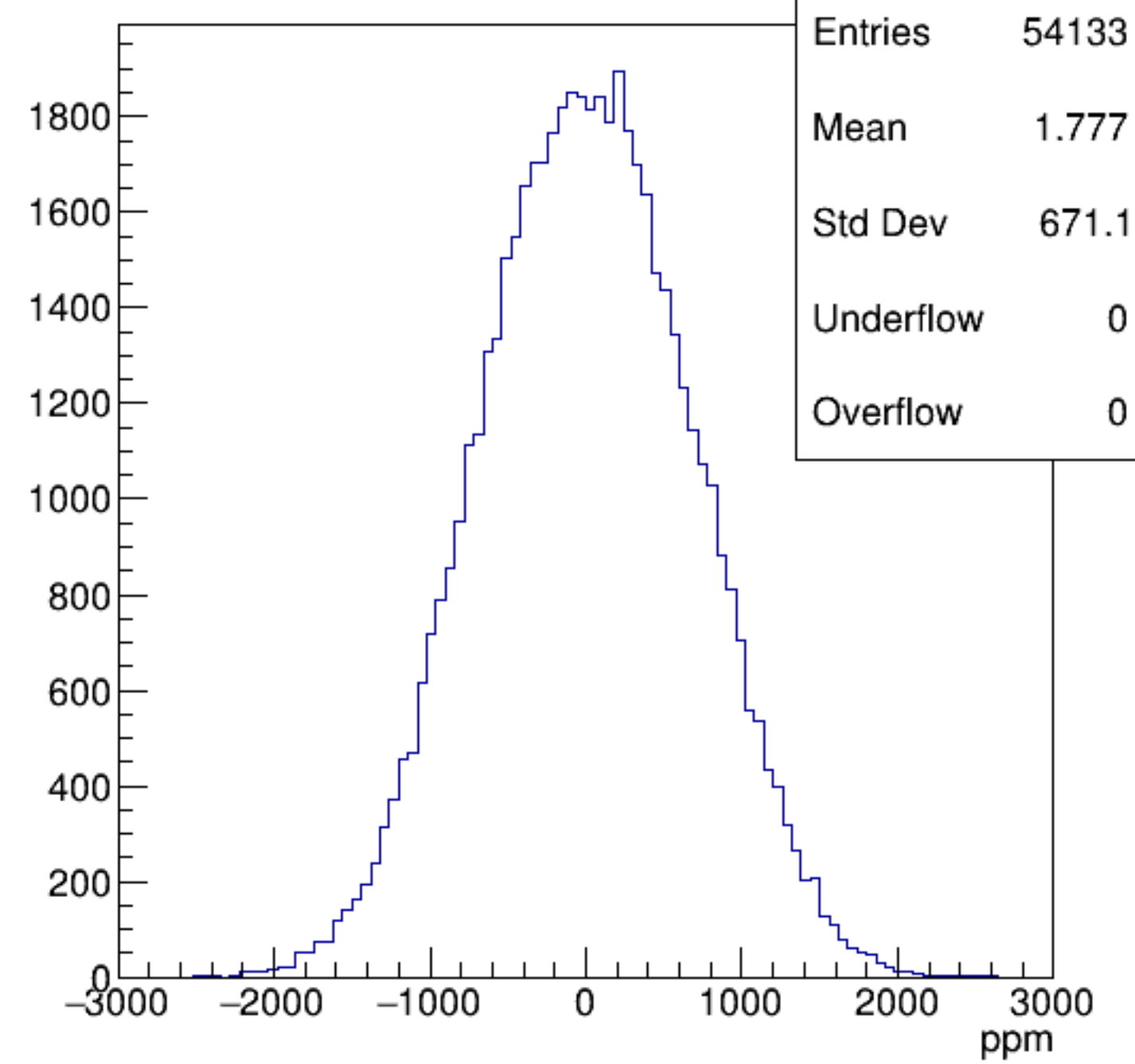
dsl\*CurMon {ErrorFlag==0}



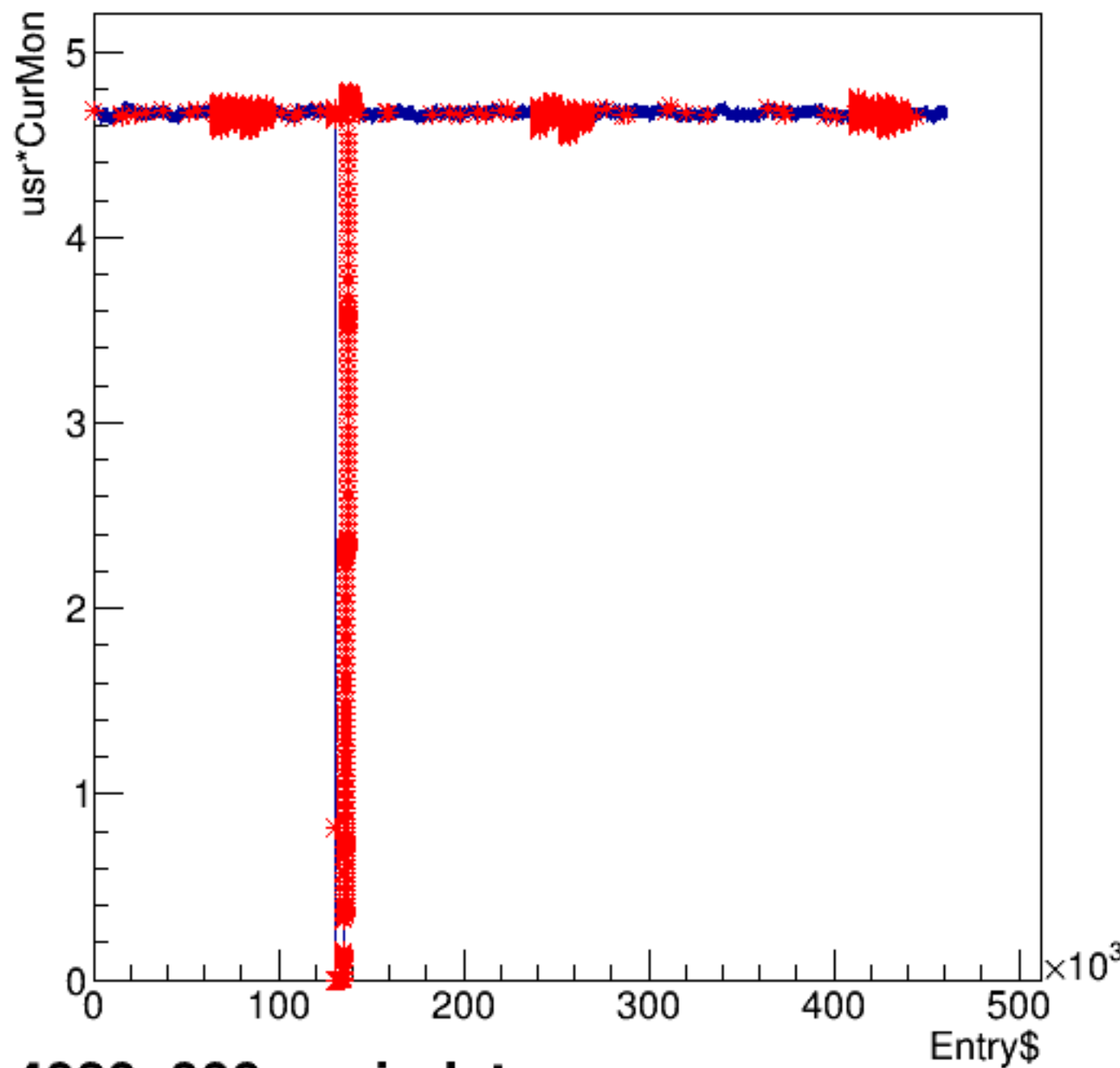
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asym\_dsl

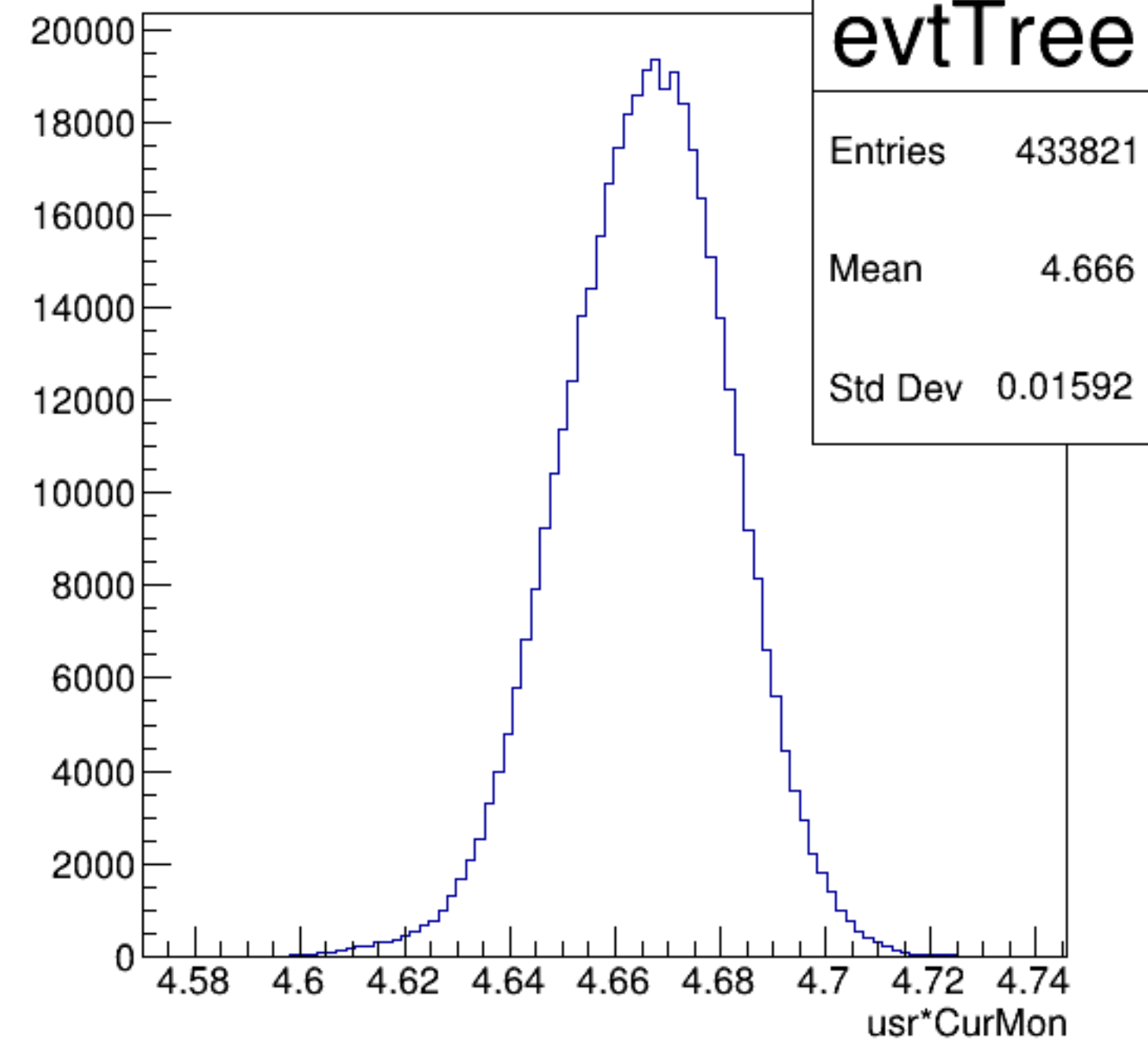


usr\*CurMon:Entry\$

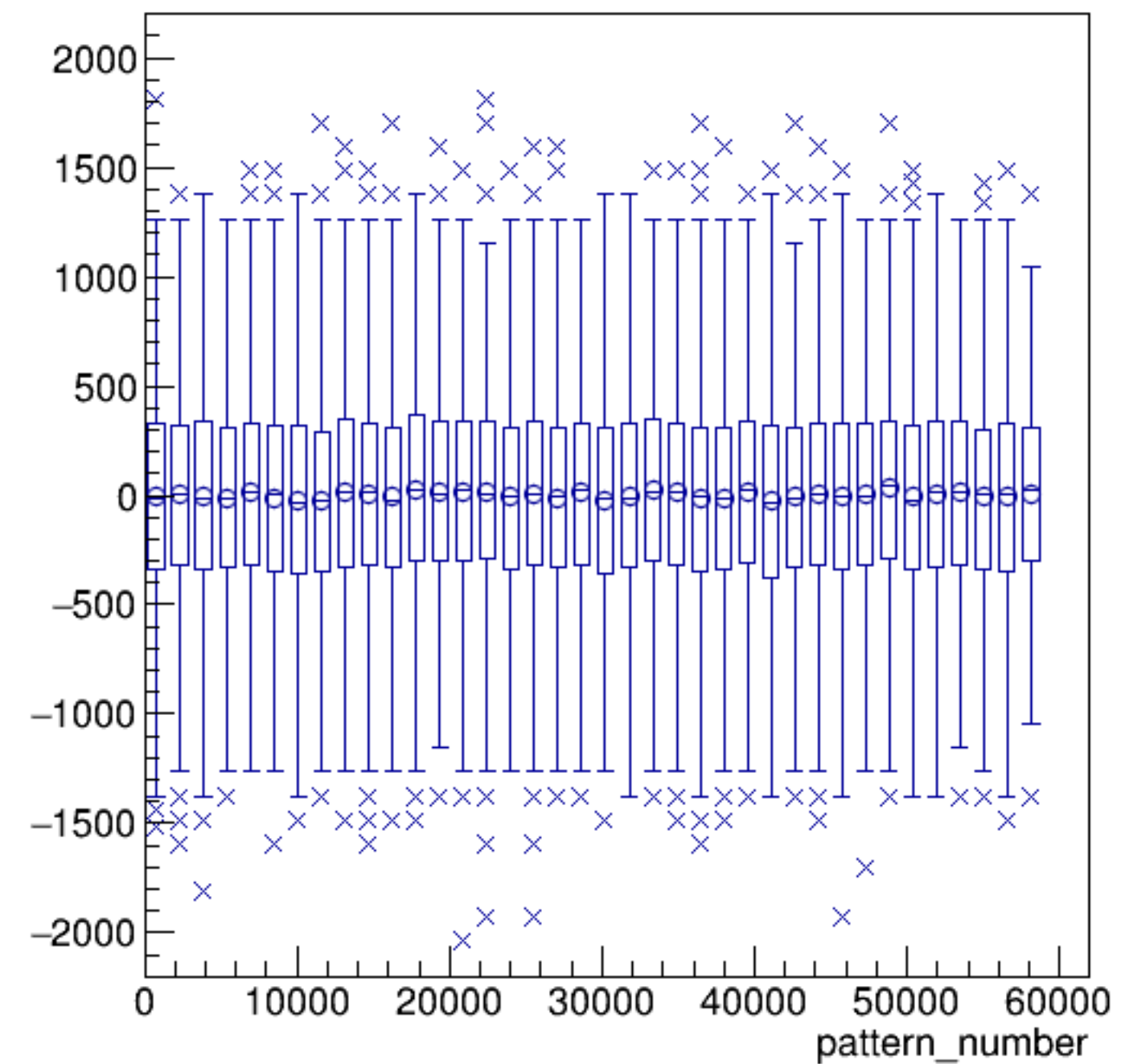


run4080\_000\_maintet\_usr.png

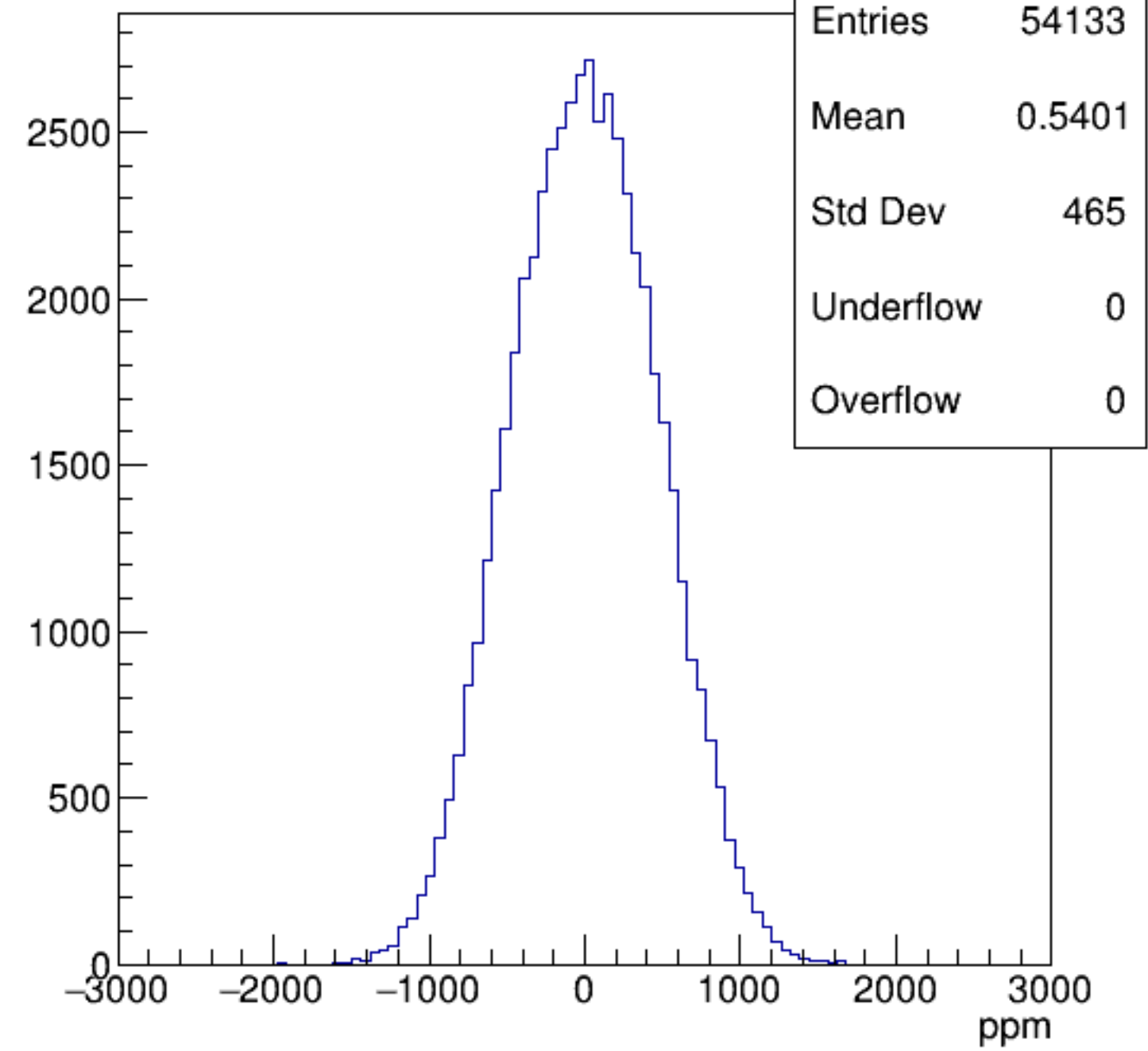
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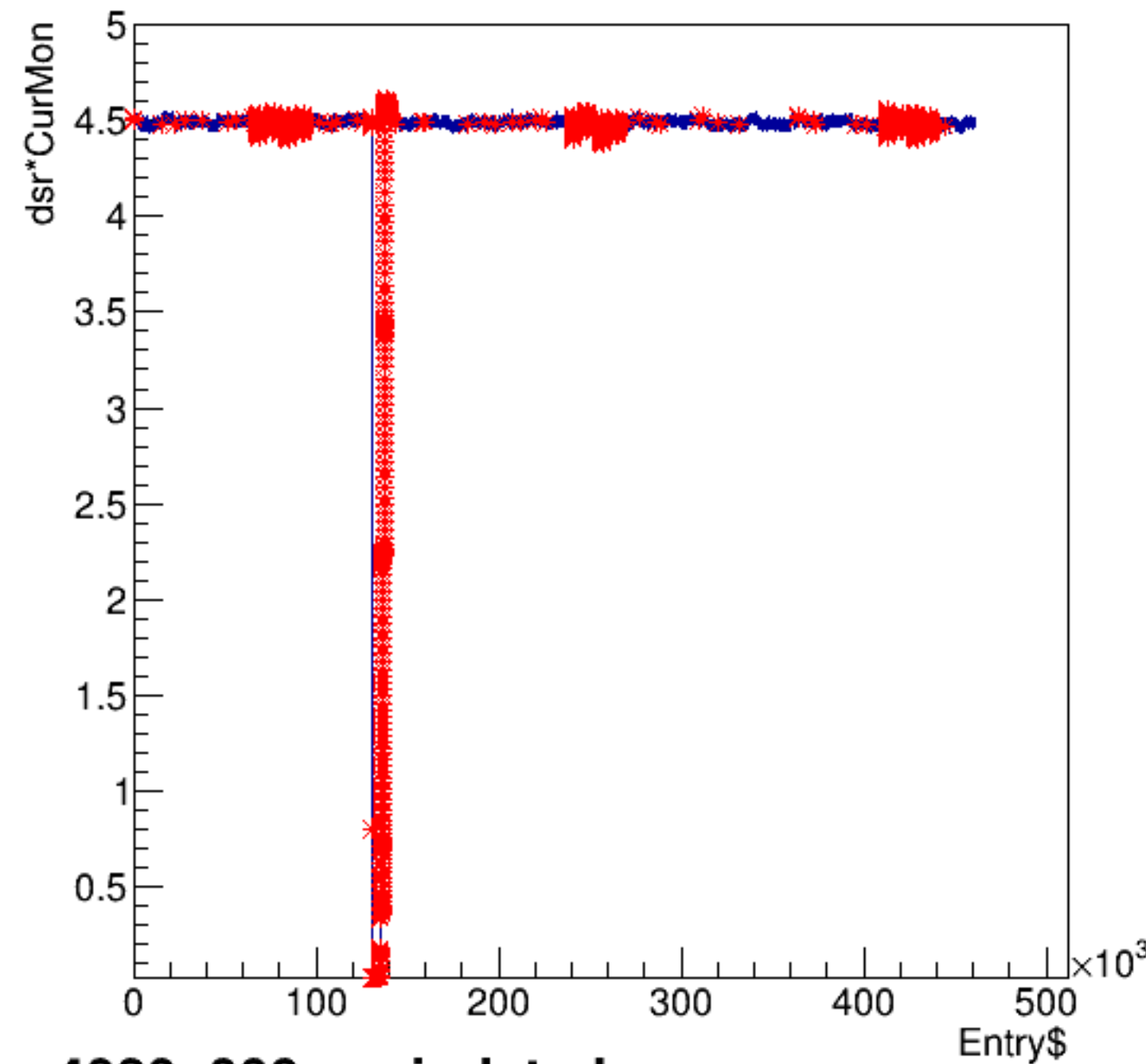
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asym\_usr

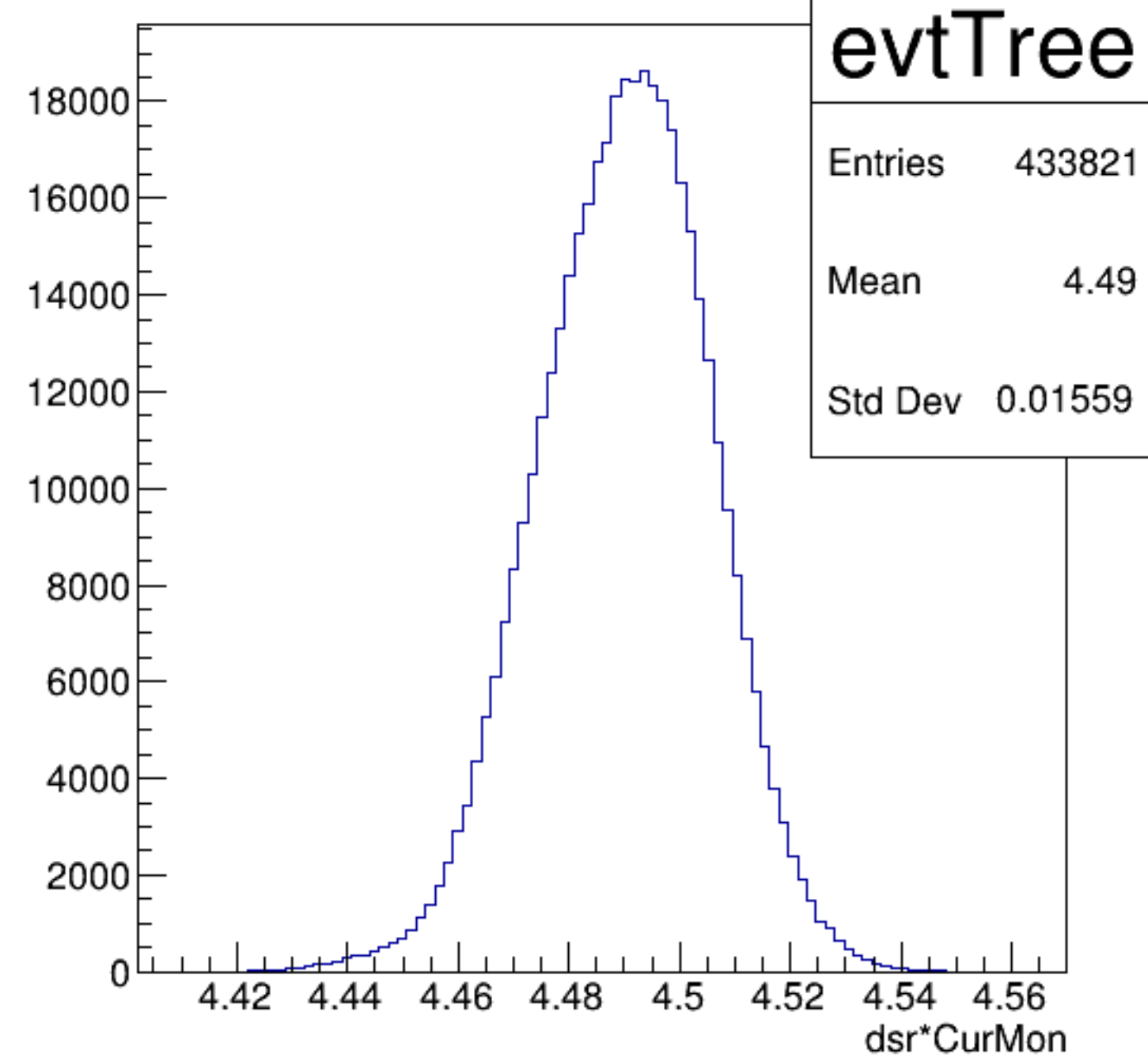


dsr\*CurMon:Entry\$

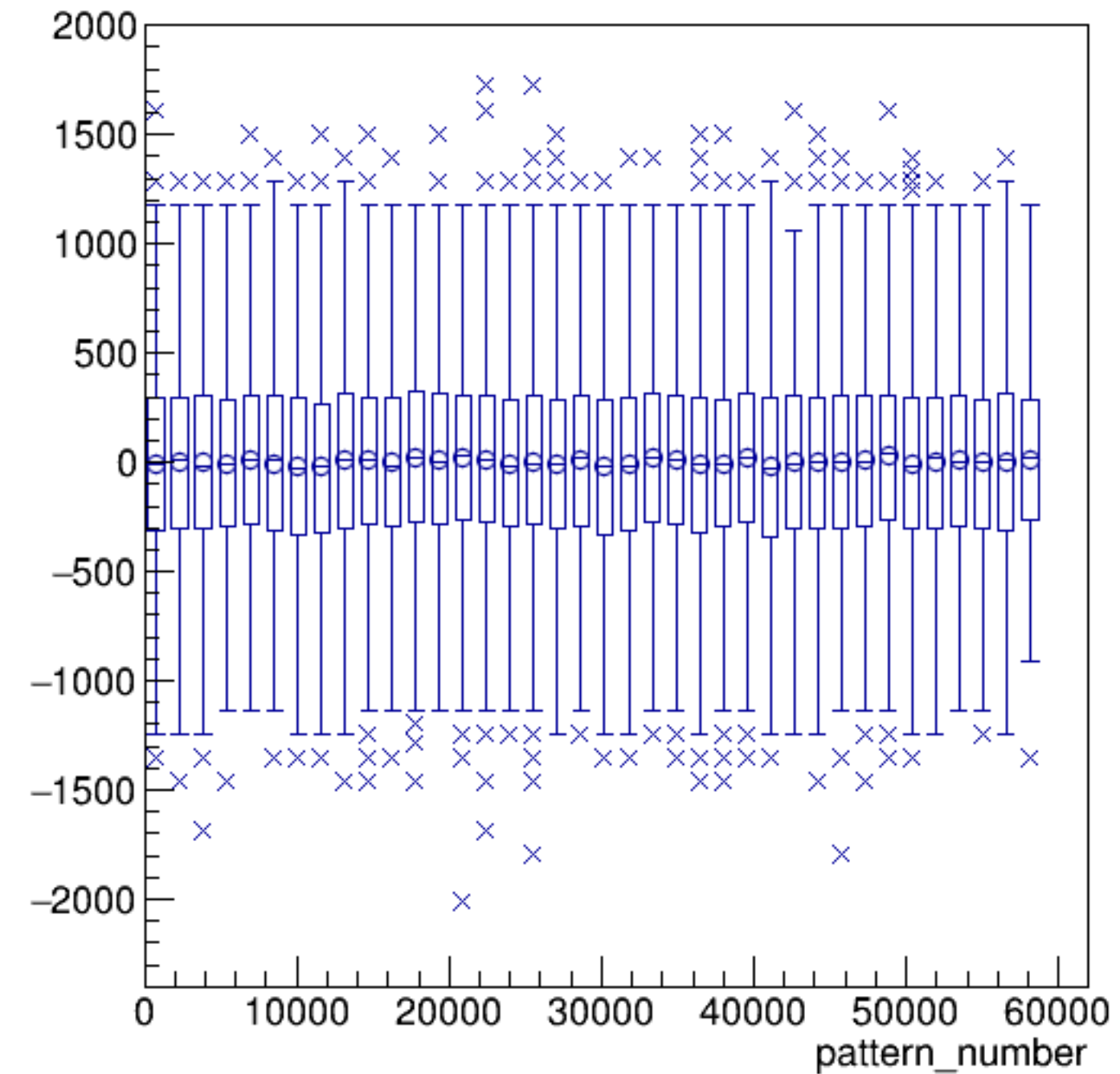


run4080\_000\_maindet\_dsr.png

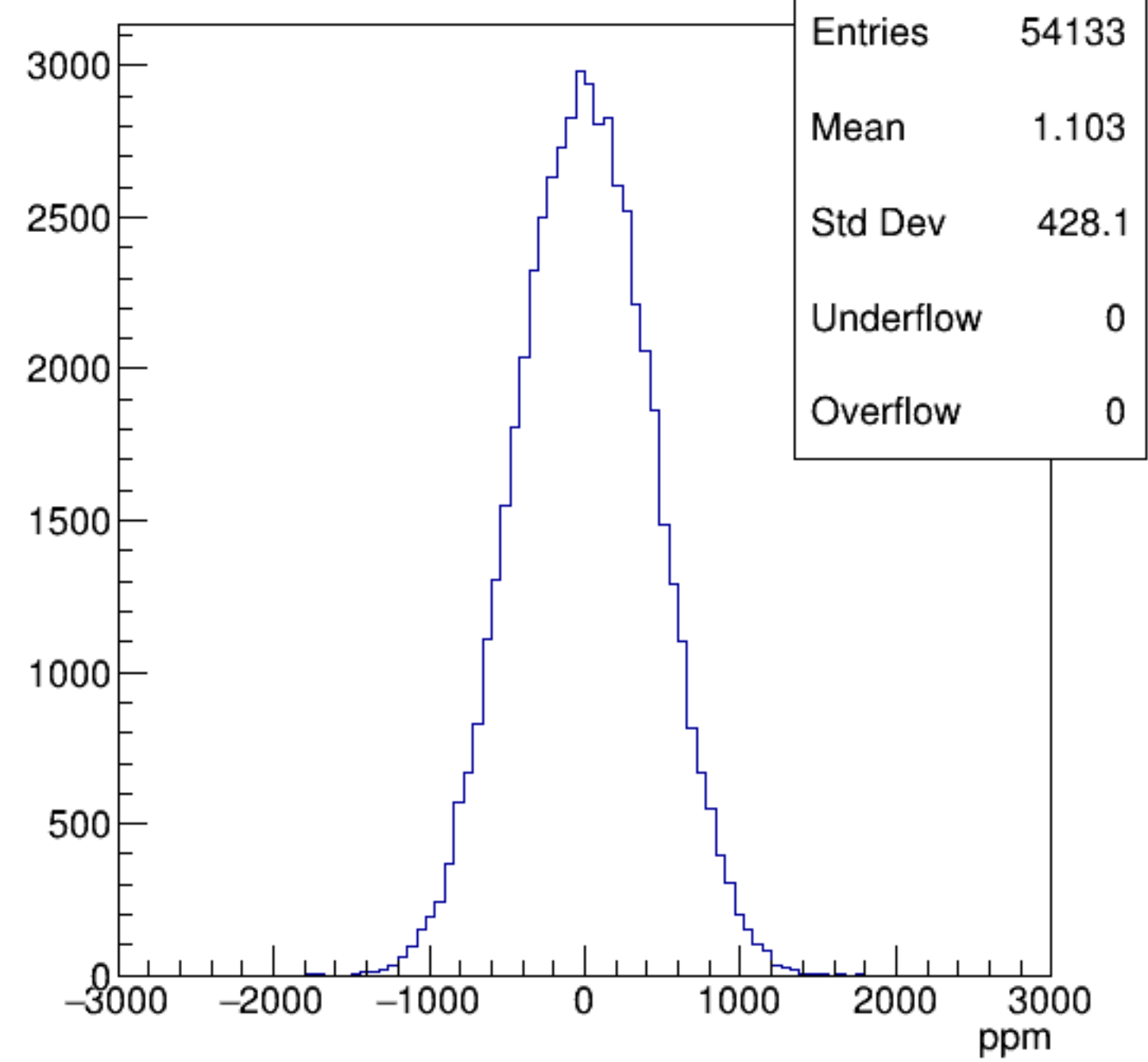
dsr\*CurMon {ErrorFlag==0}



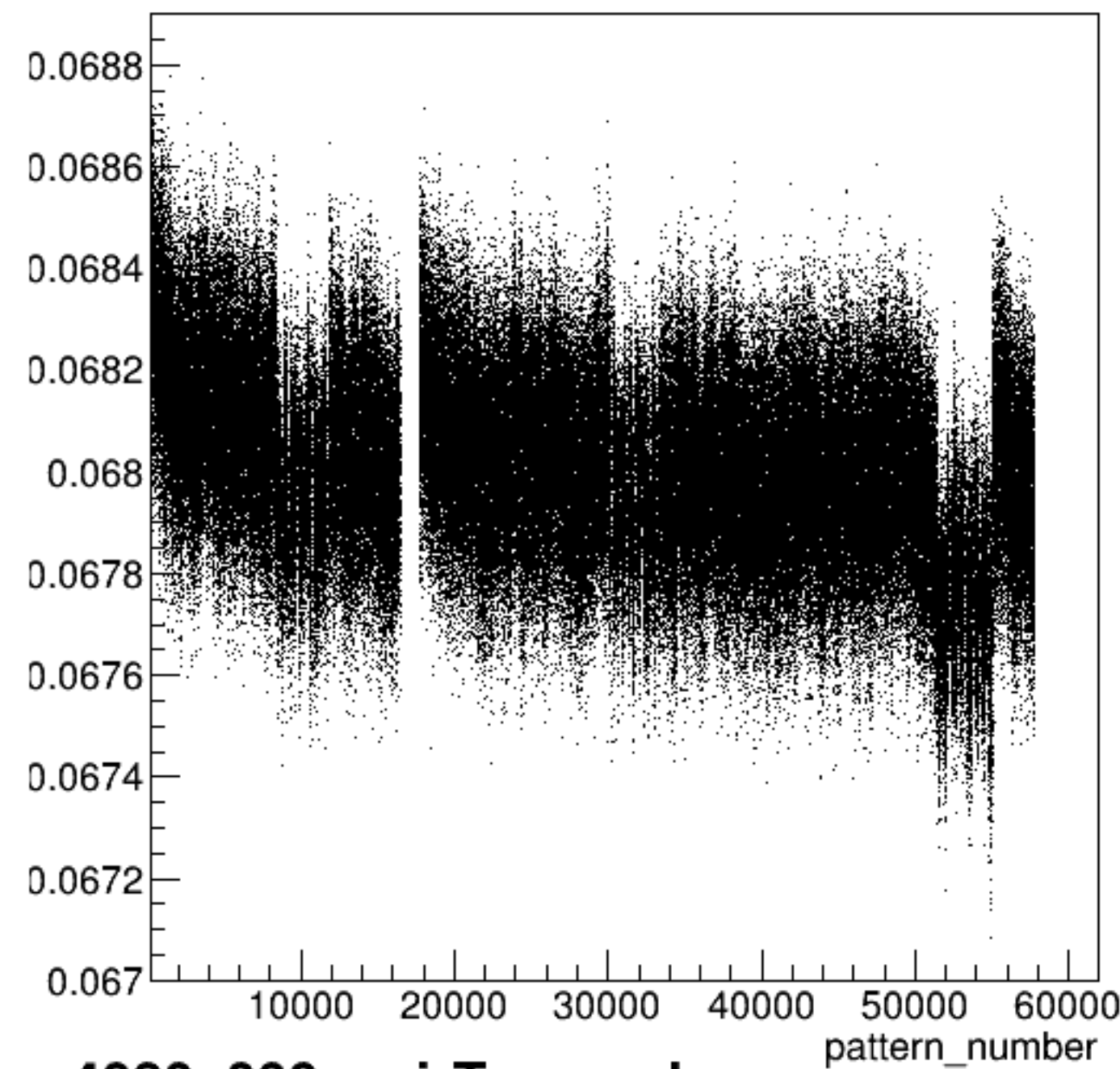
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asym\_dsr

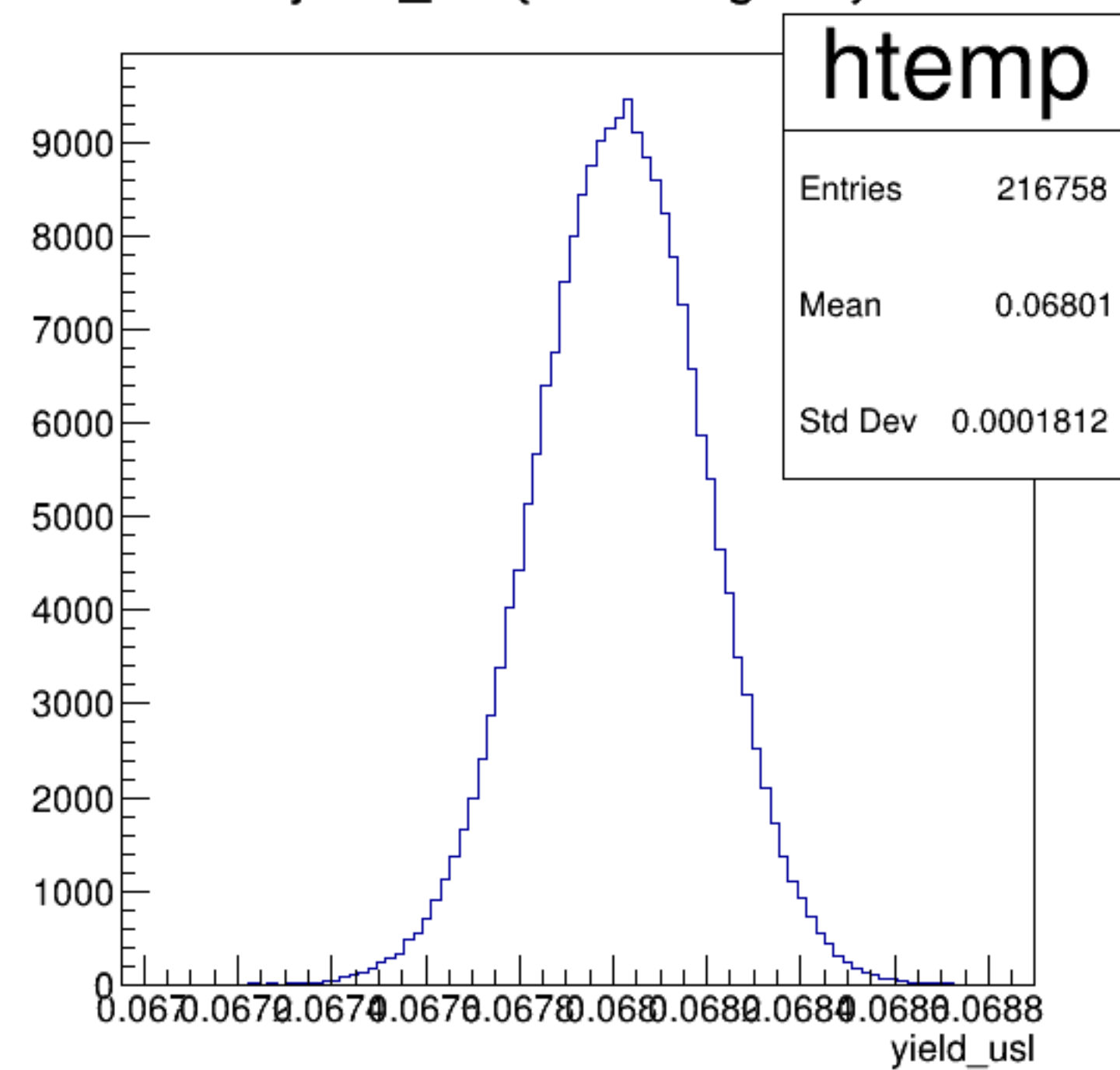


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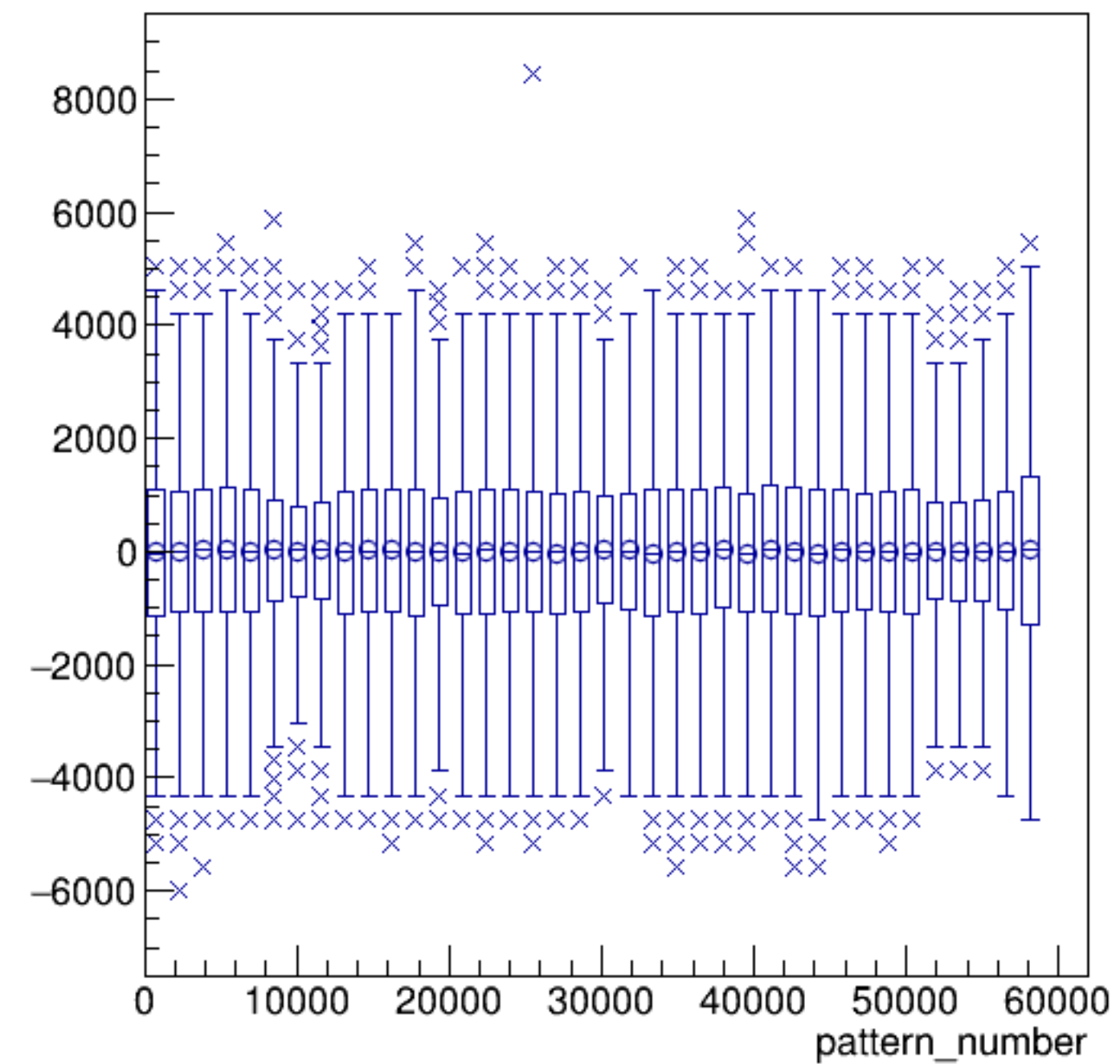


run4080\_000\_pairTree\_usl.png

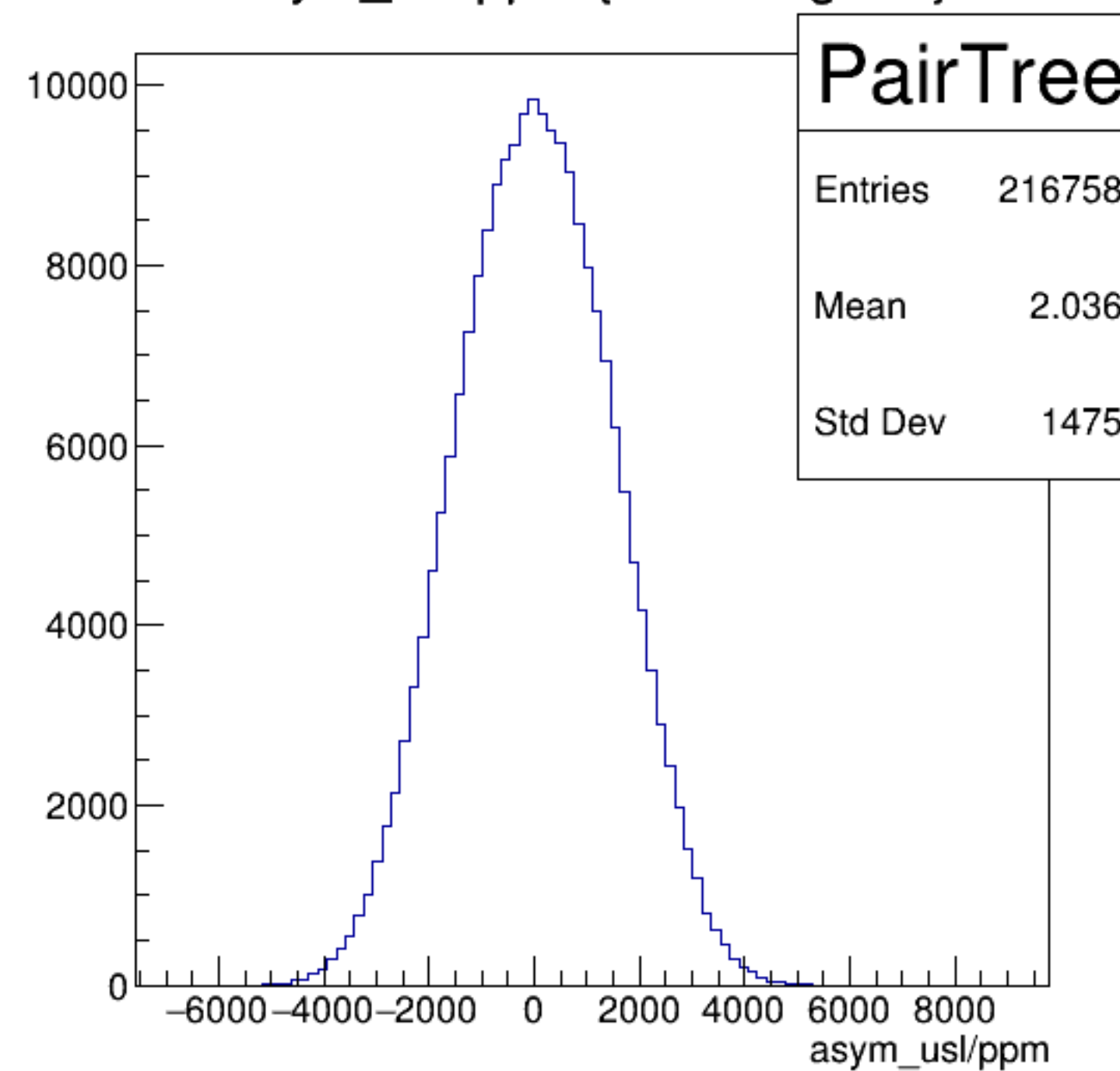
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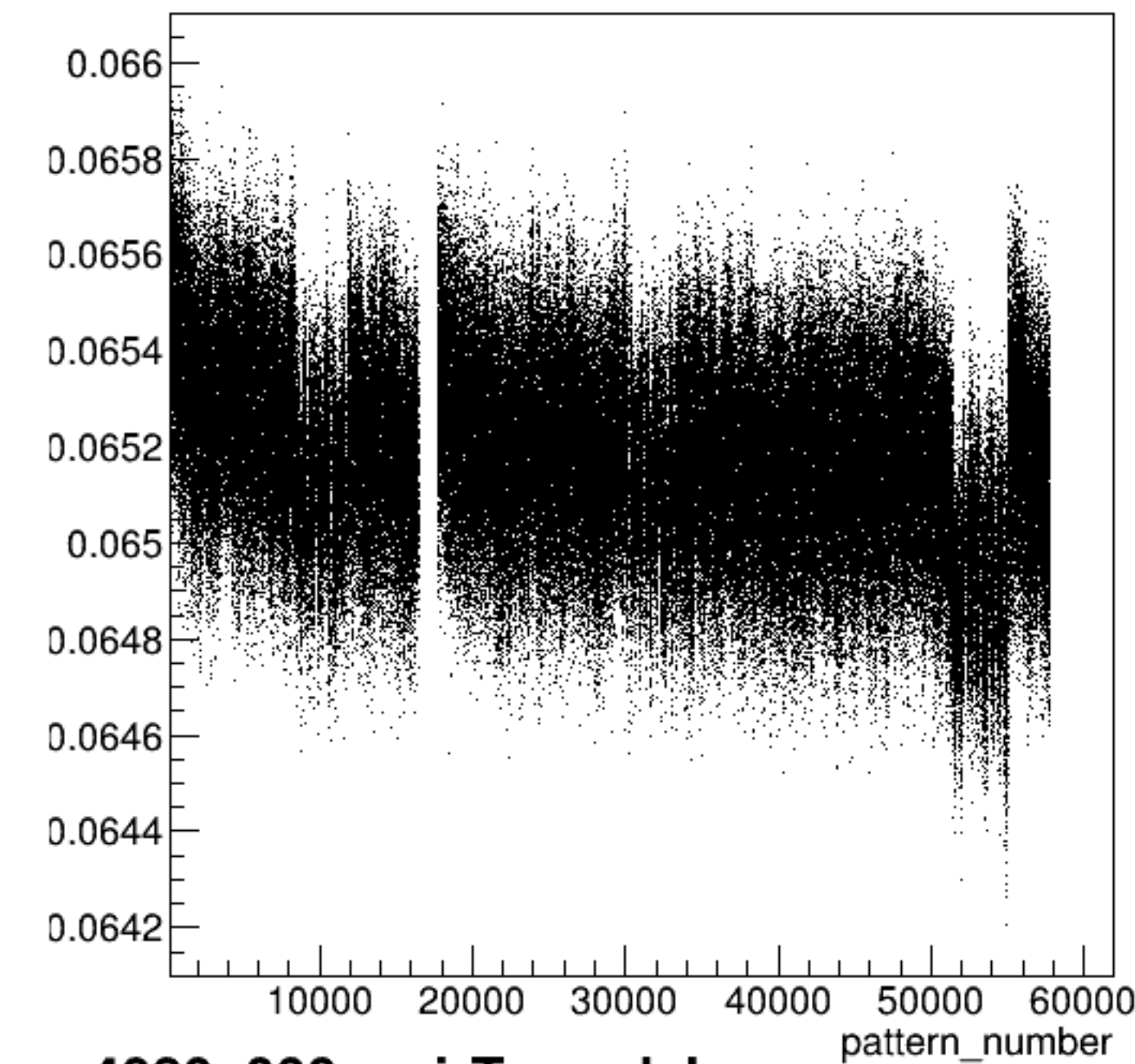
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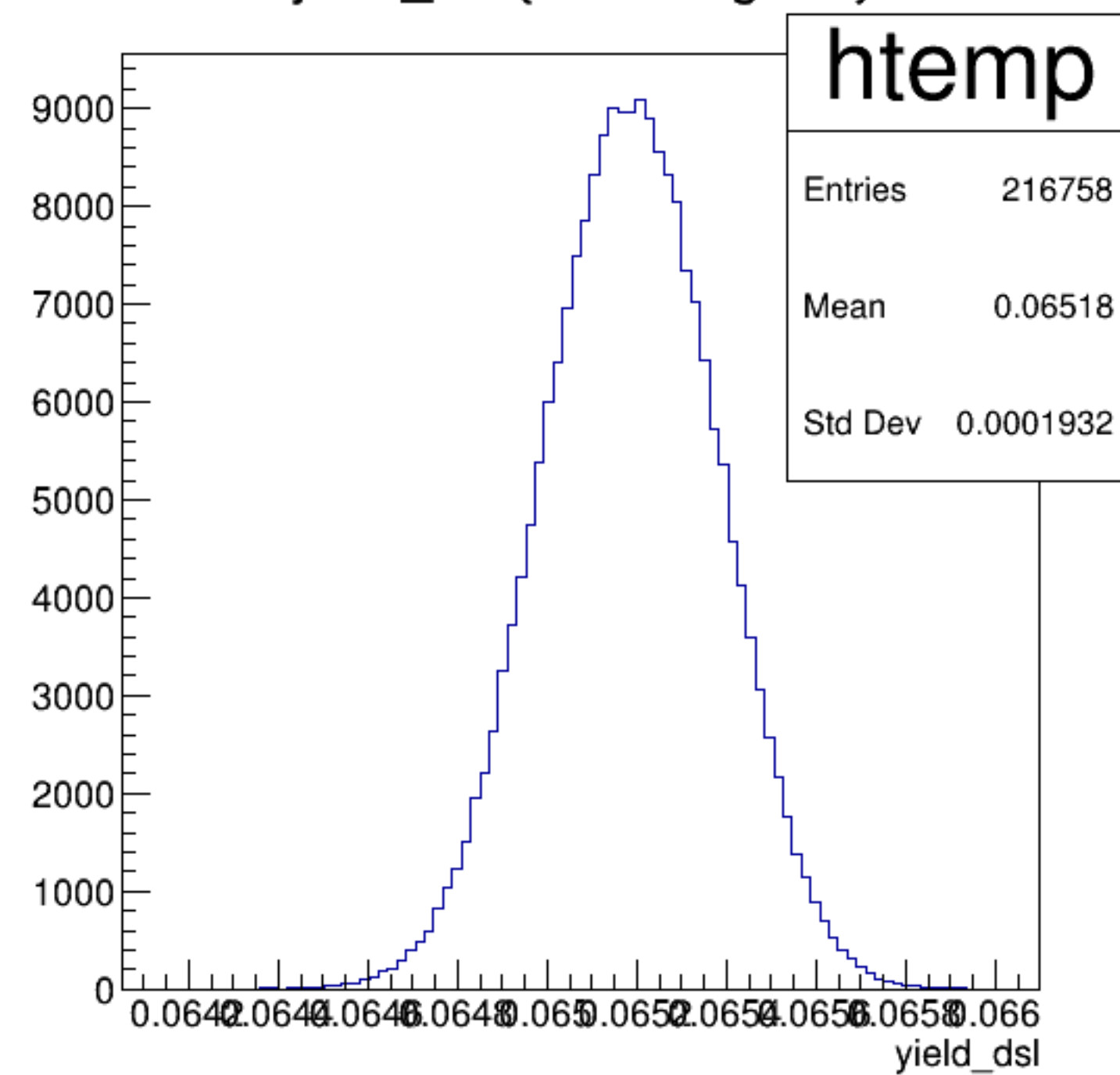
asym\_usl/ppm {ErrorFlag==0}



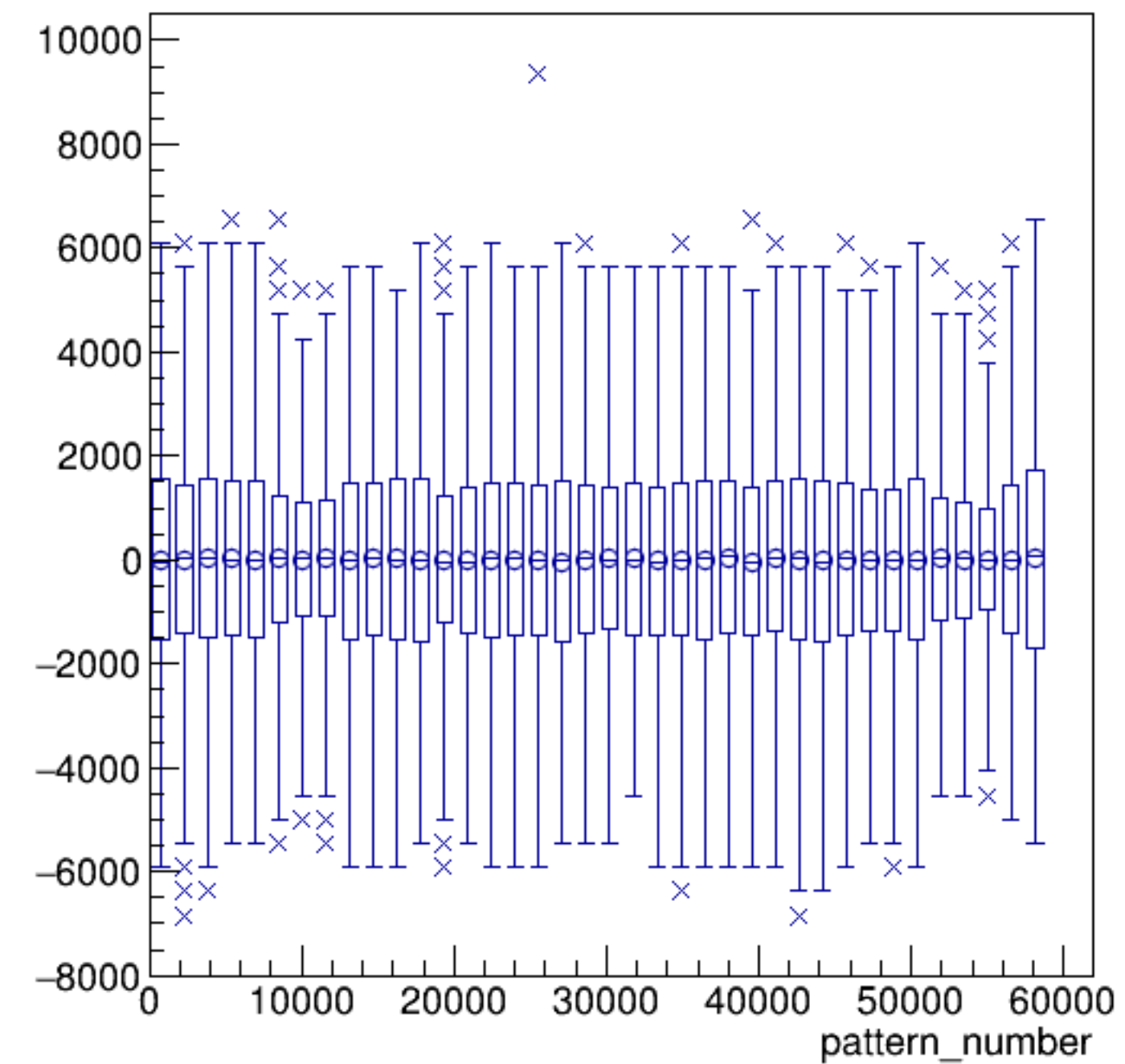
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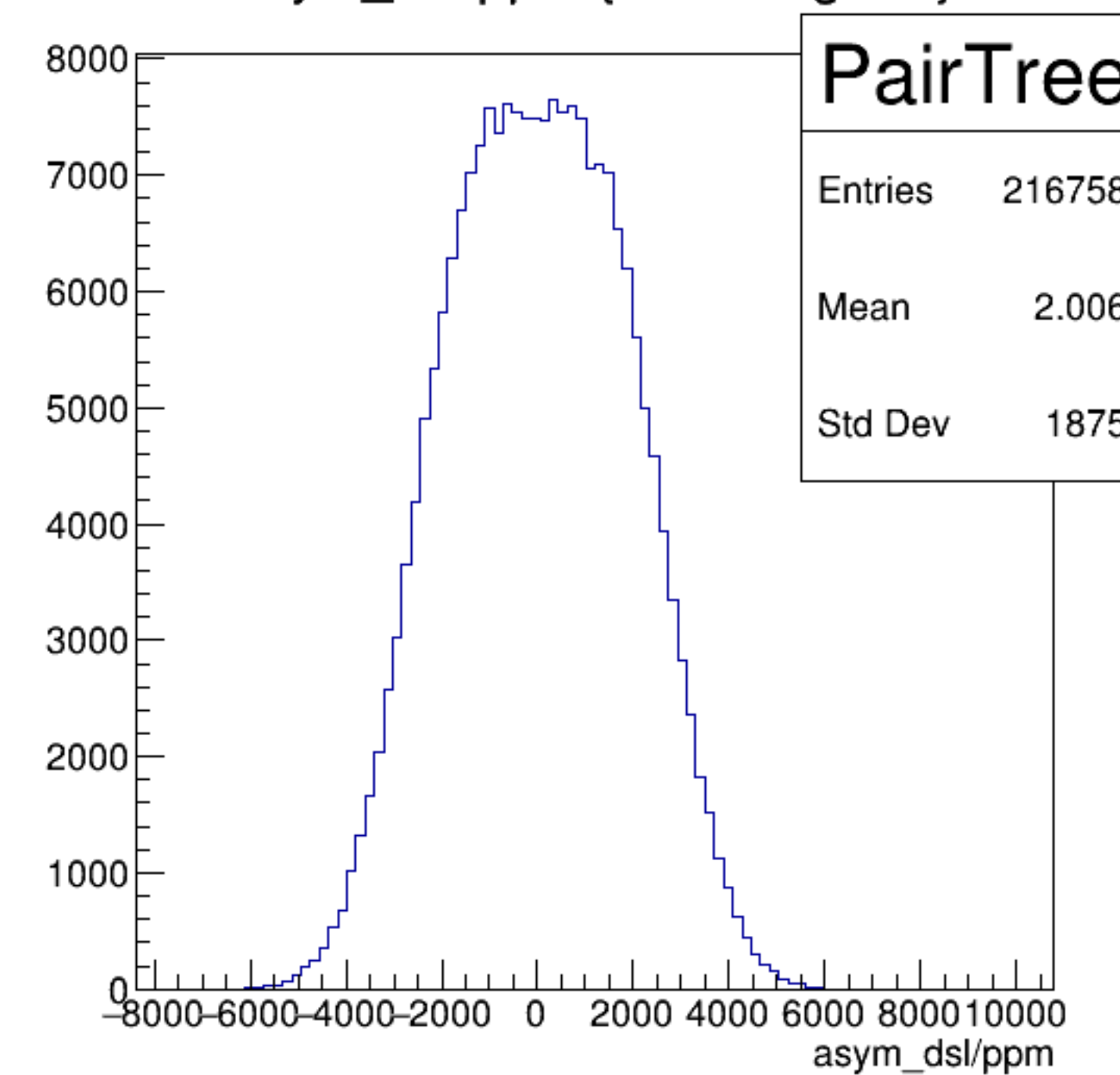
yield\_dsl {ErrorFlag==0}



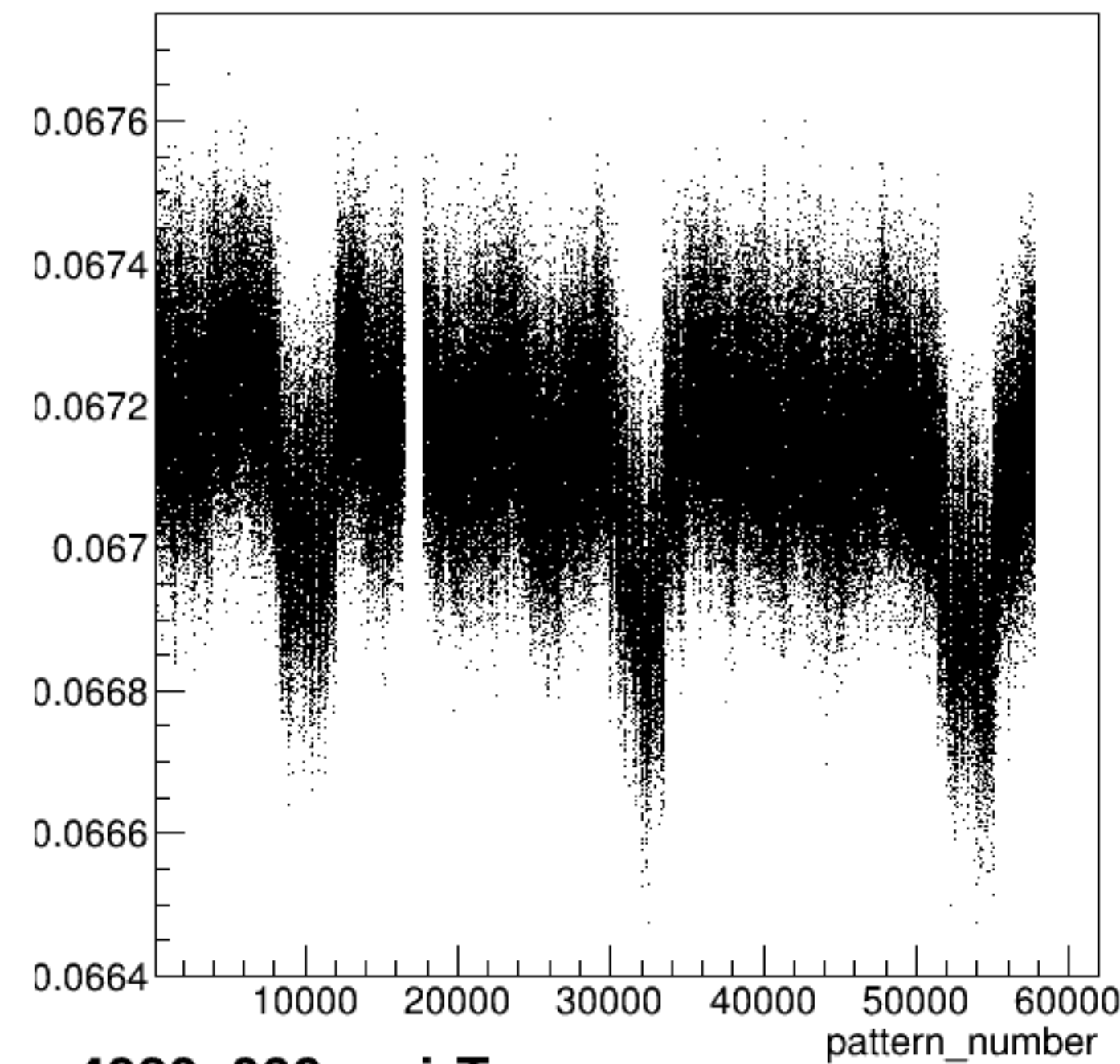
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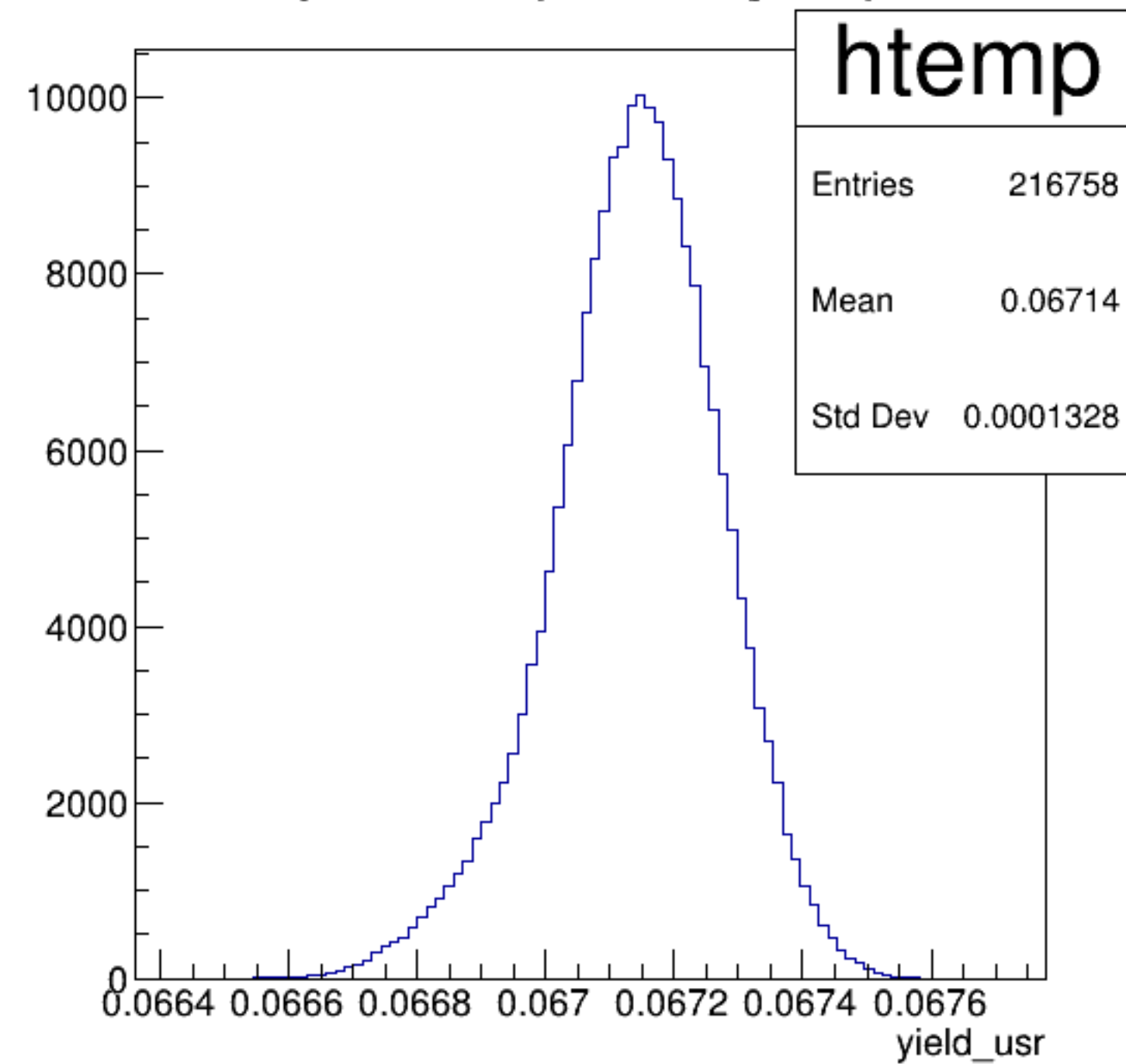
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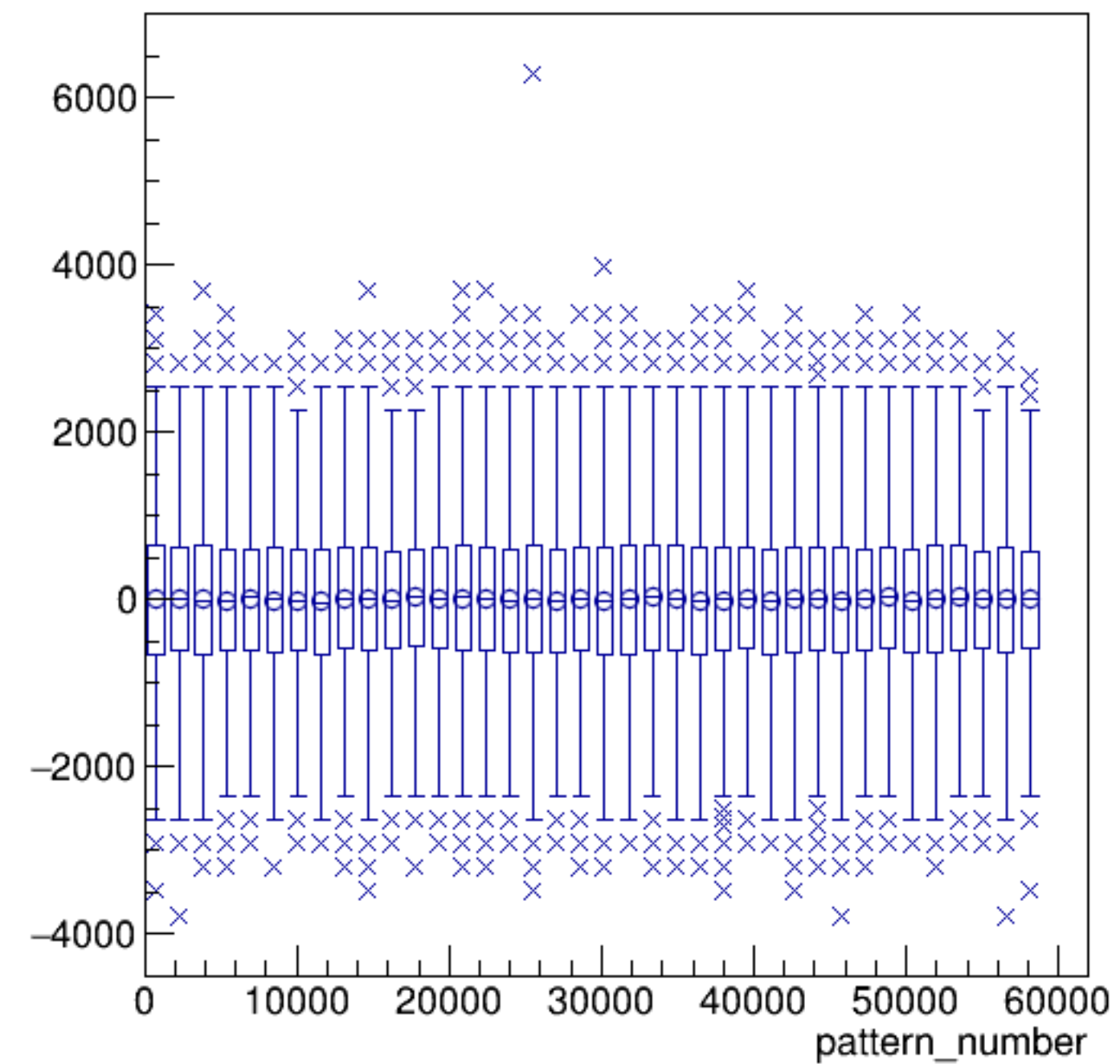
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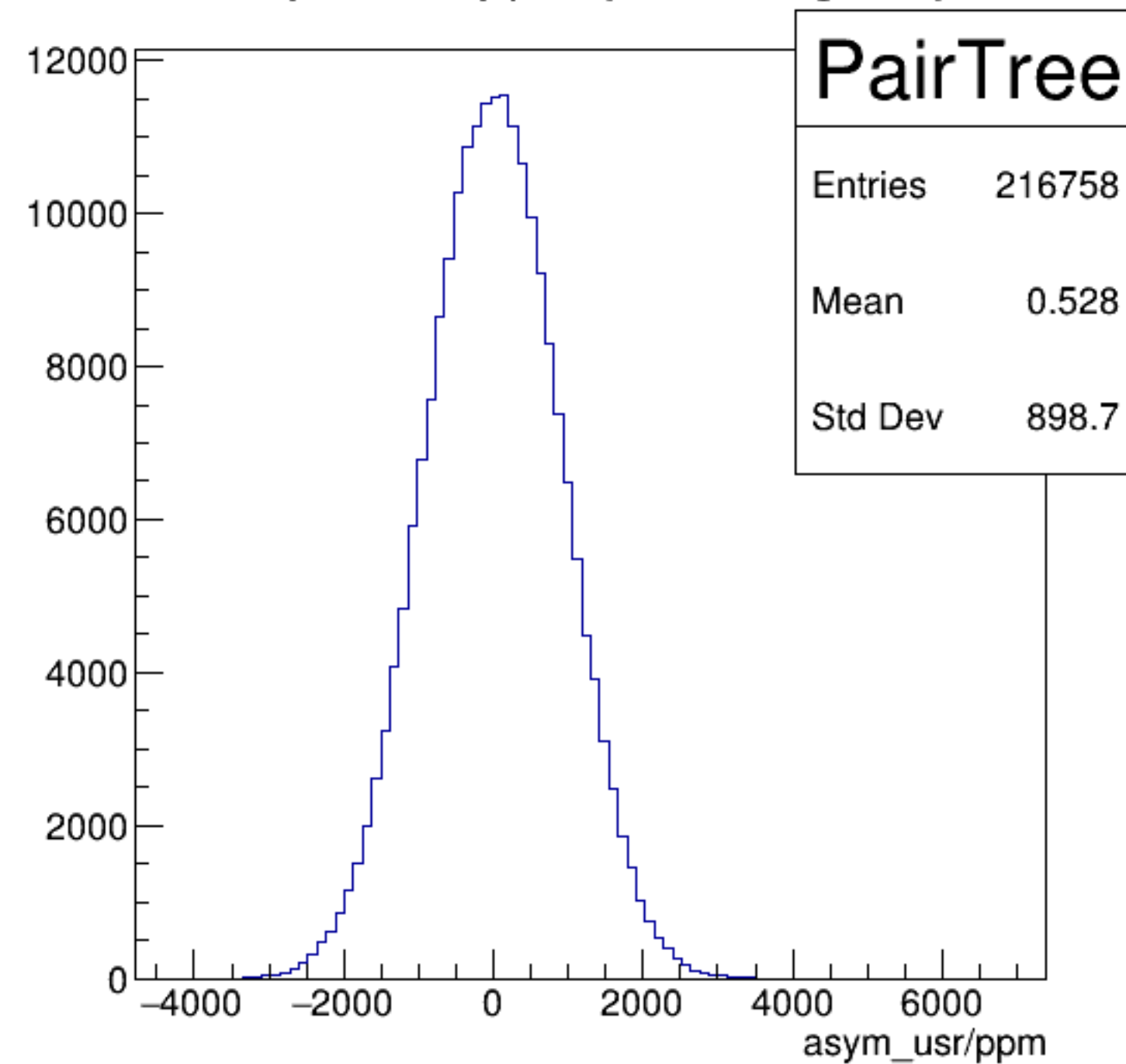
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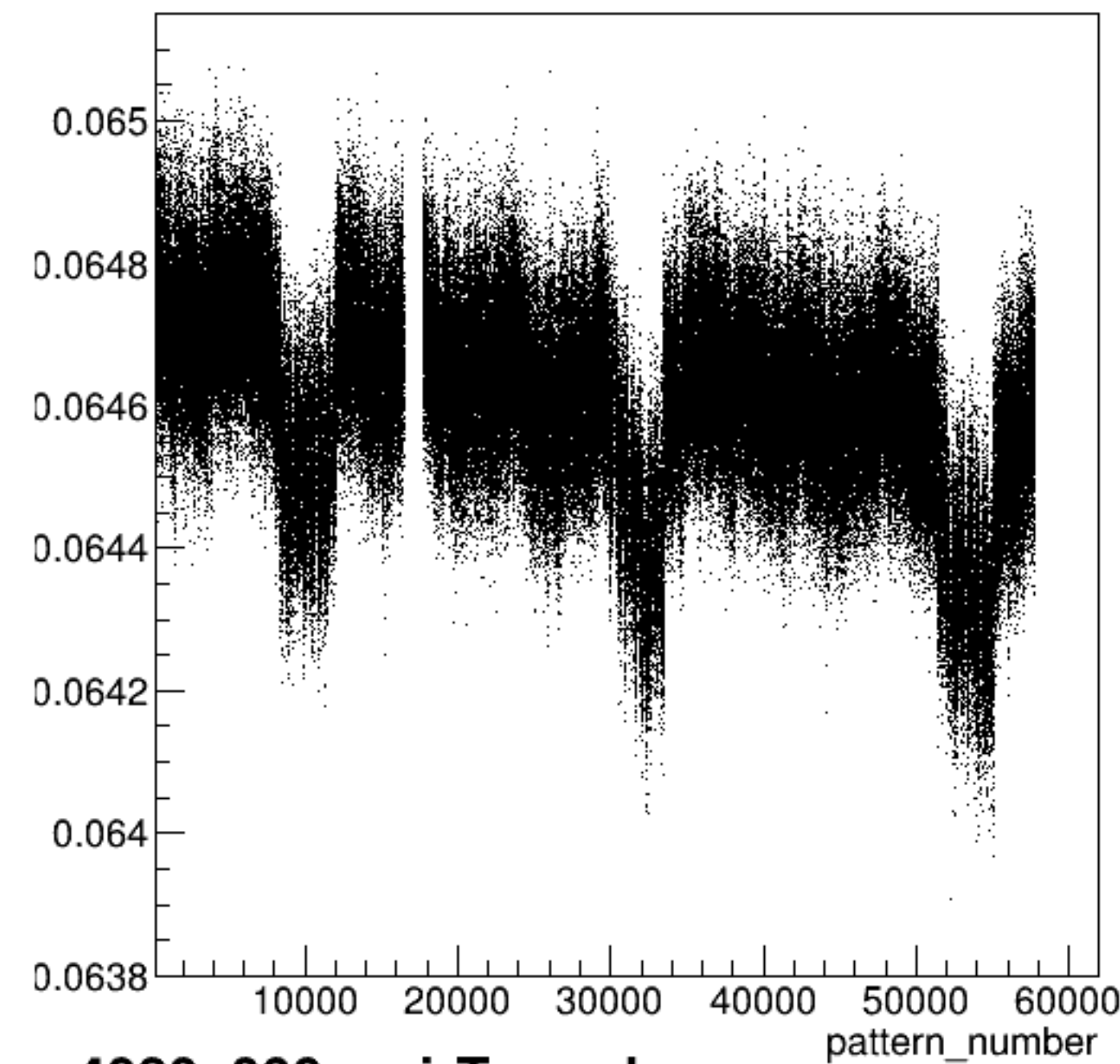
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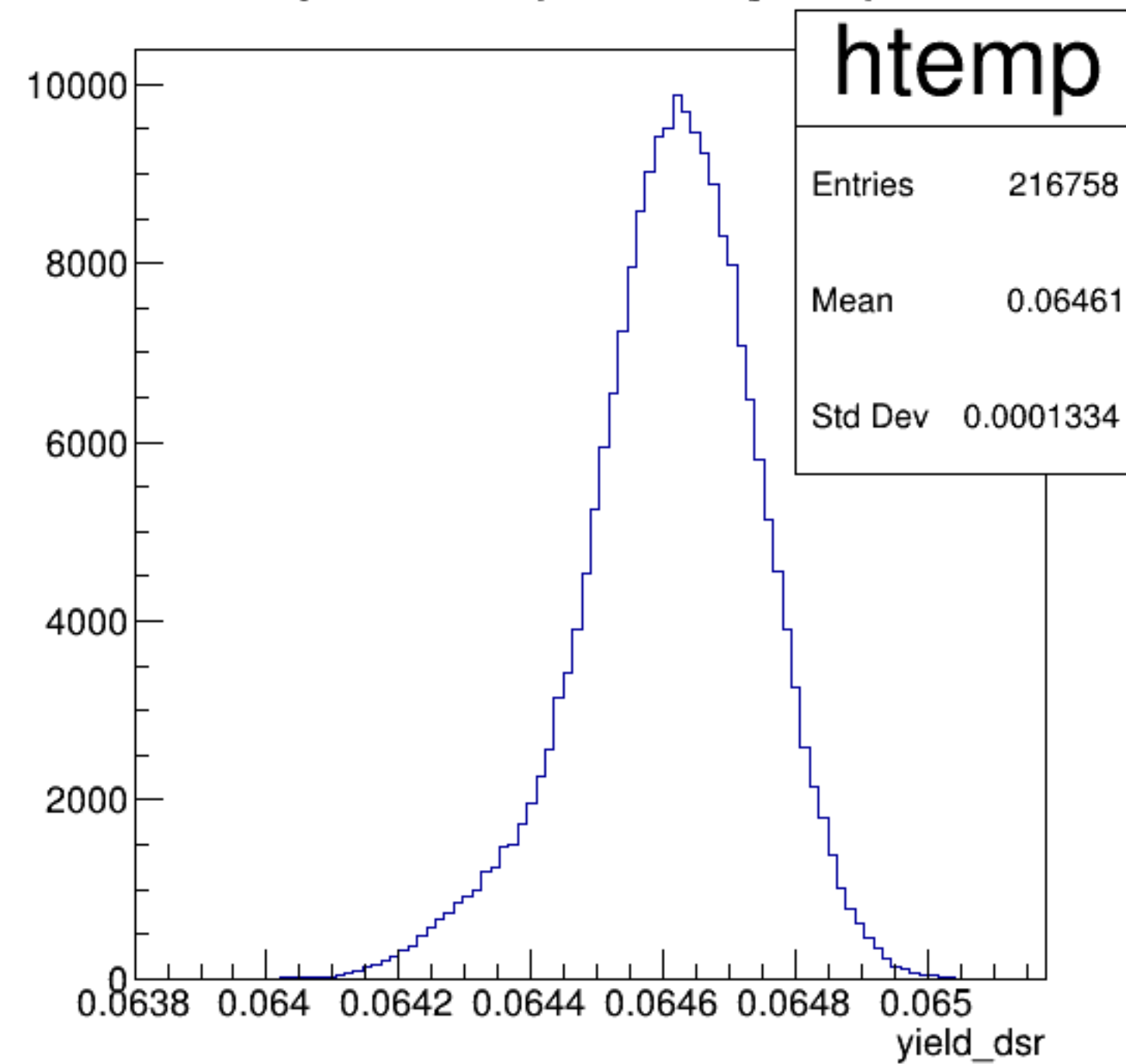
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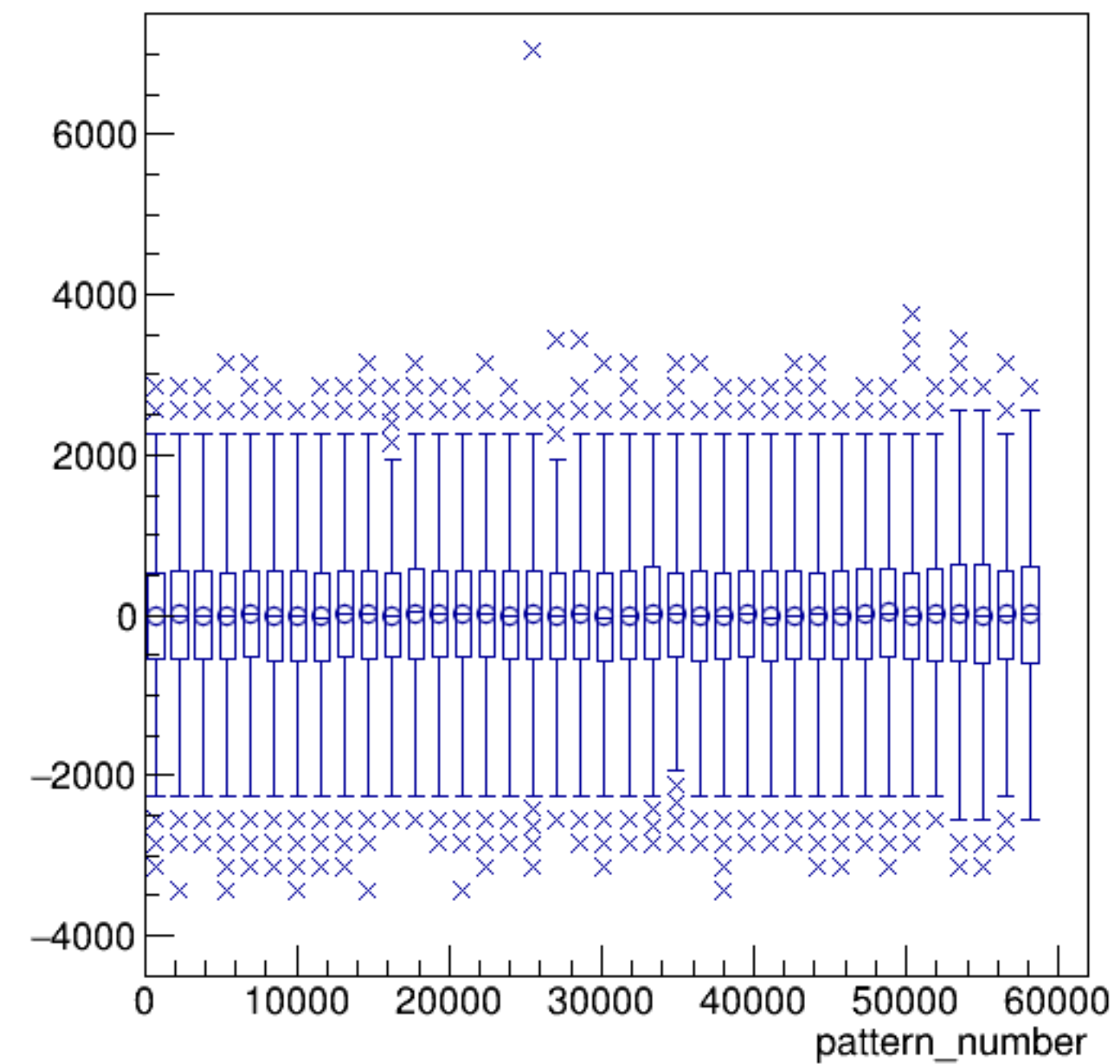
yield\_dsr:pattern\_number {ErrorFlag==0}



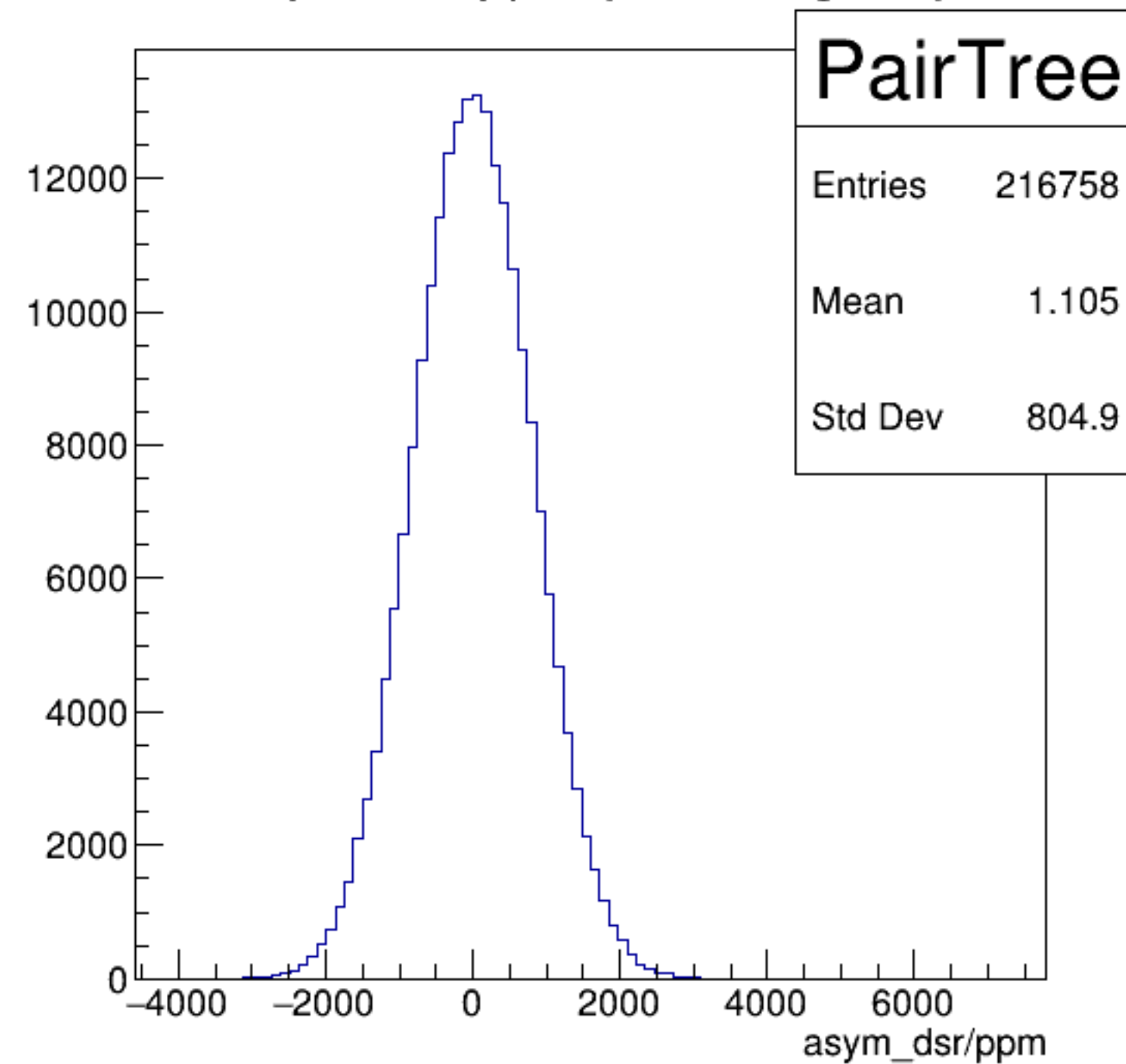
yield\_dsr {ErrorFlag==0}



asym\_dsr/ppm:pattern\_number {ErrorFlag==0}



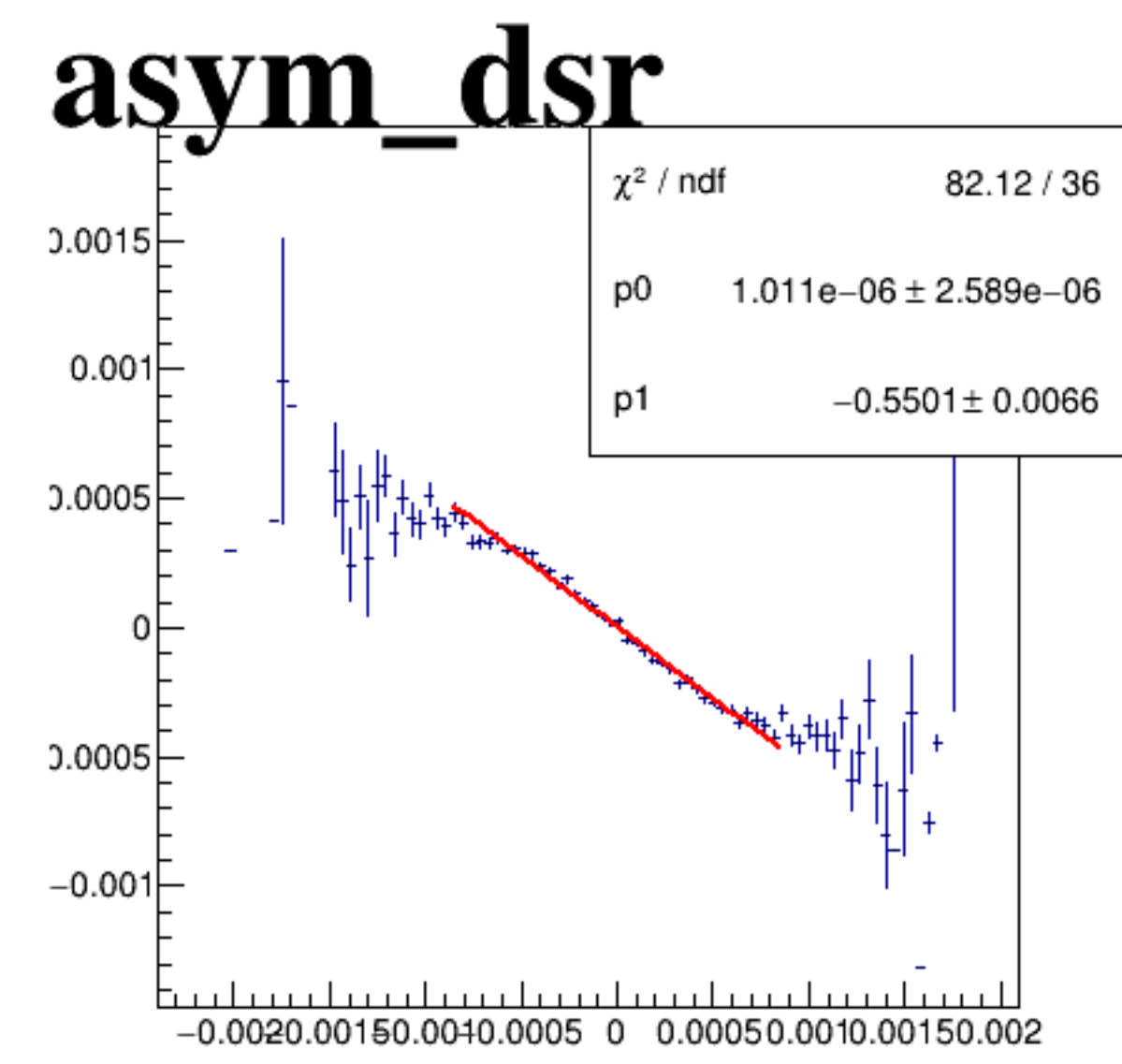
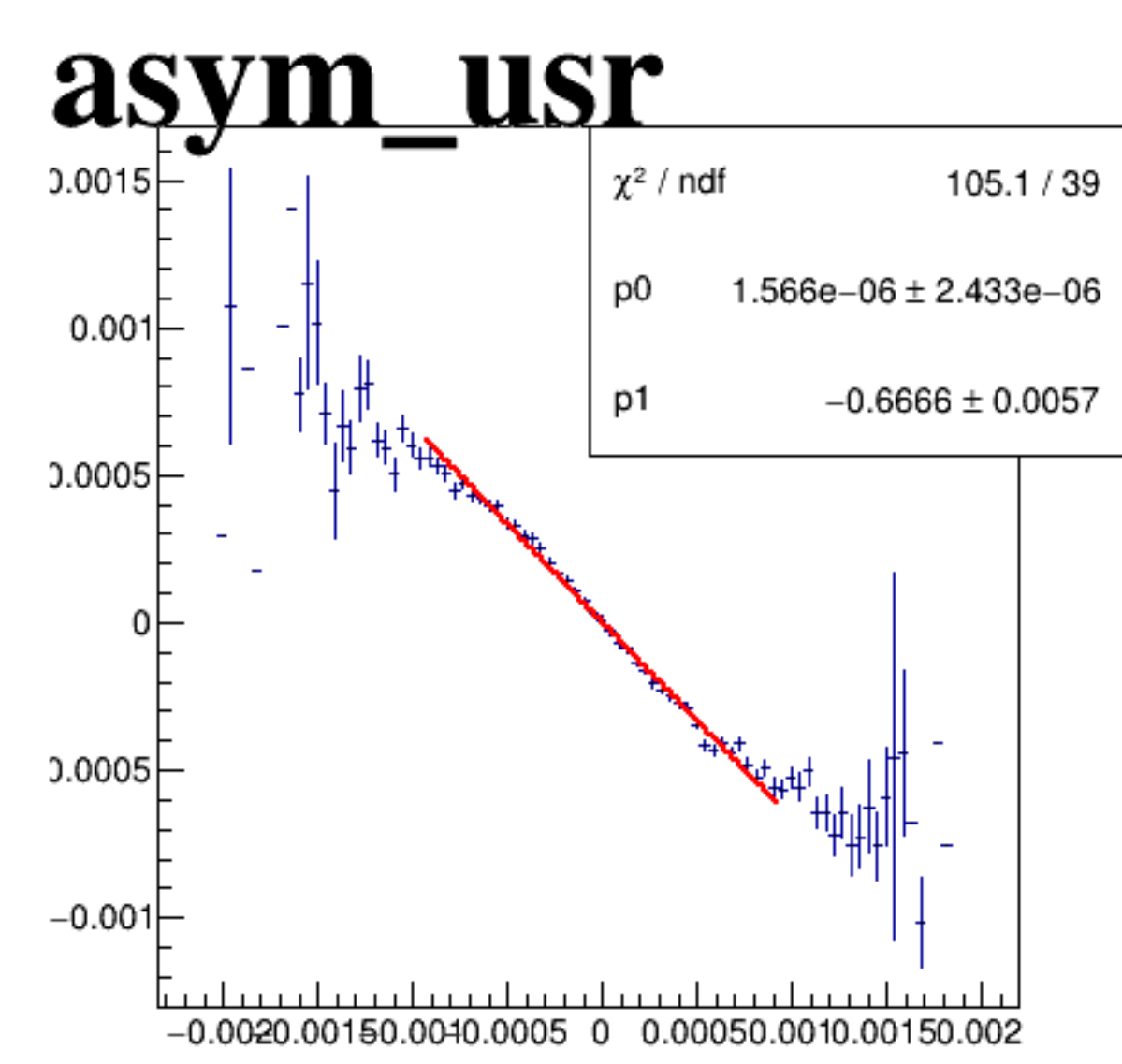
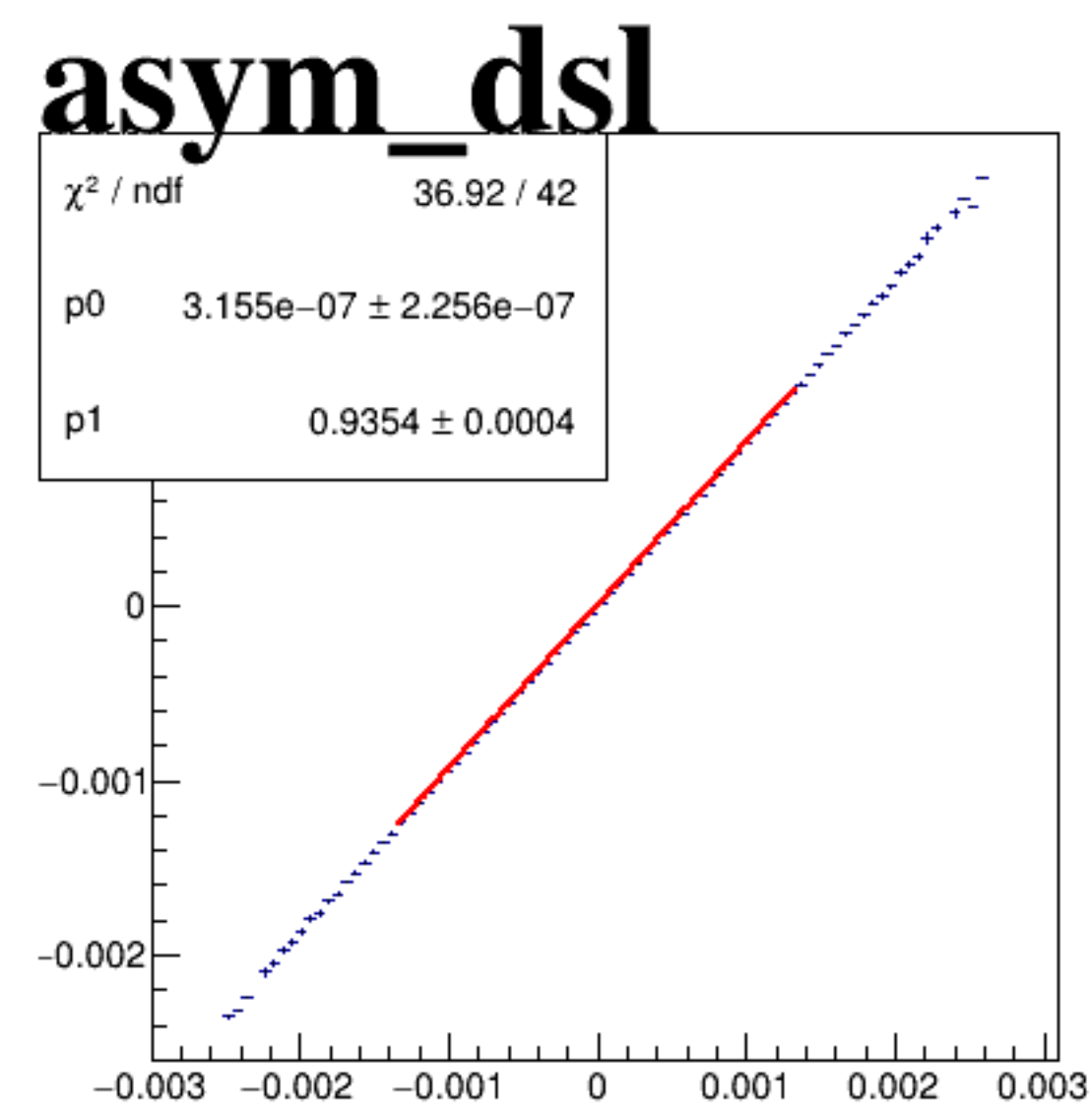
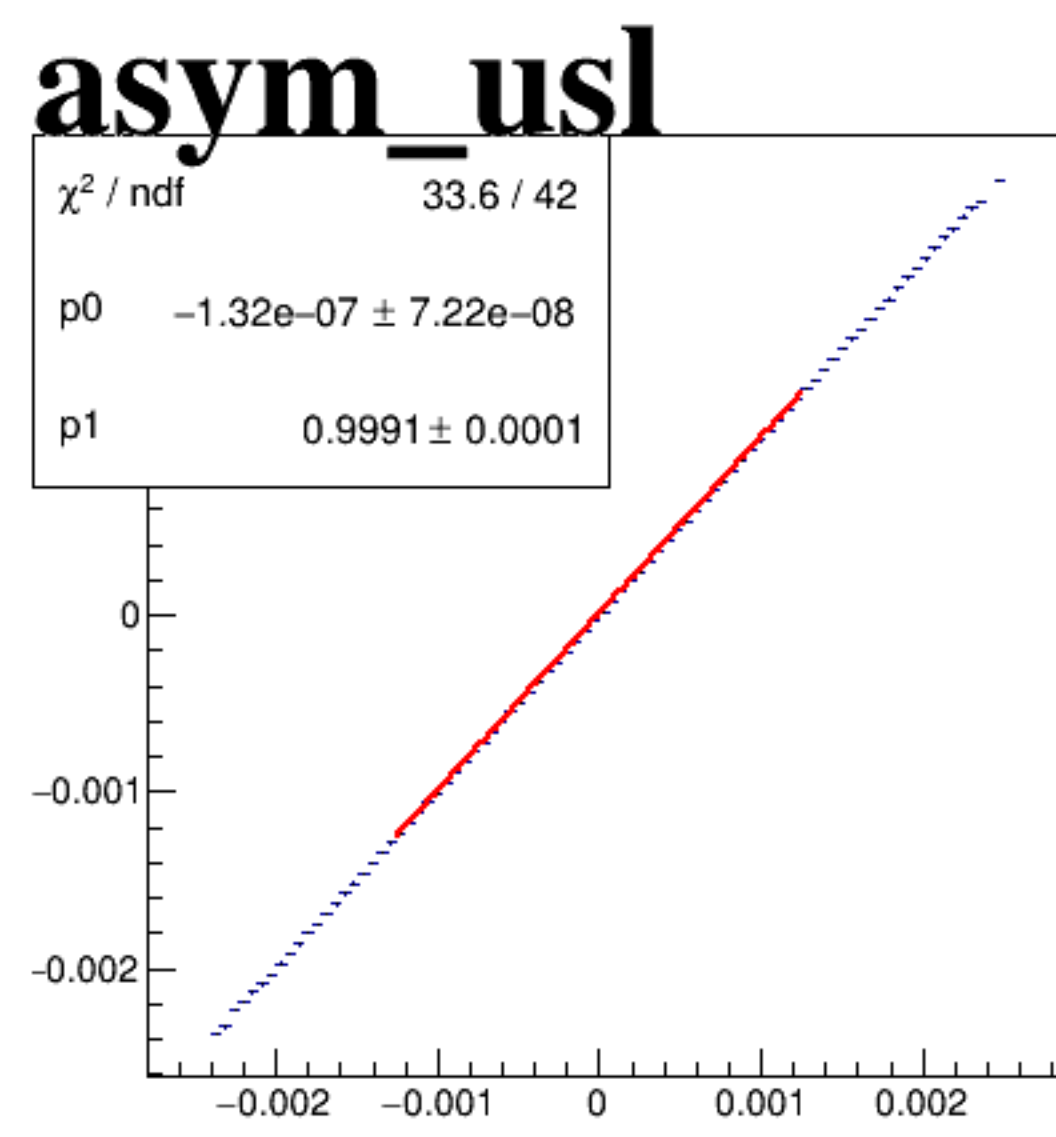
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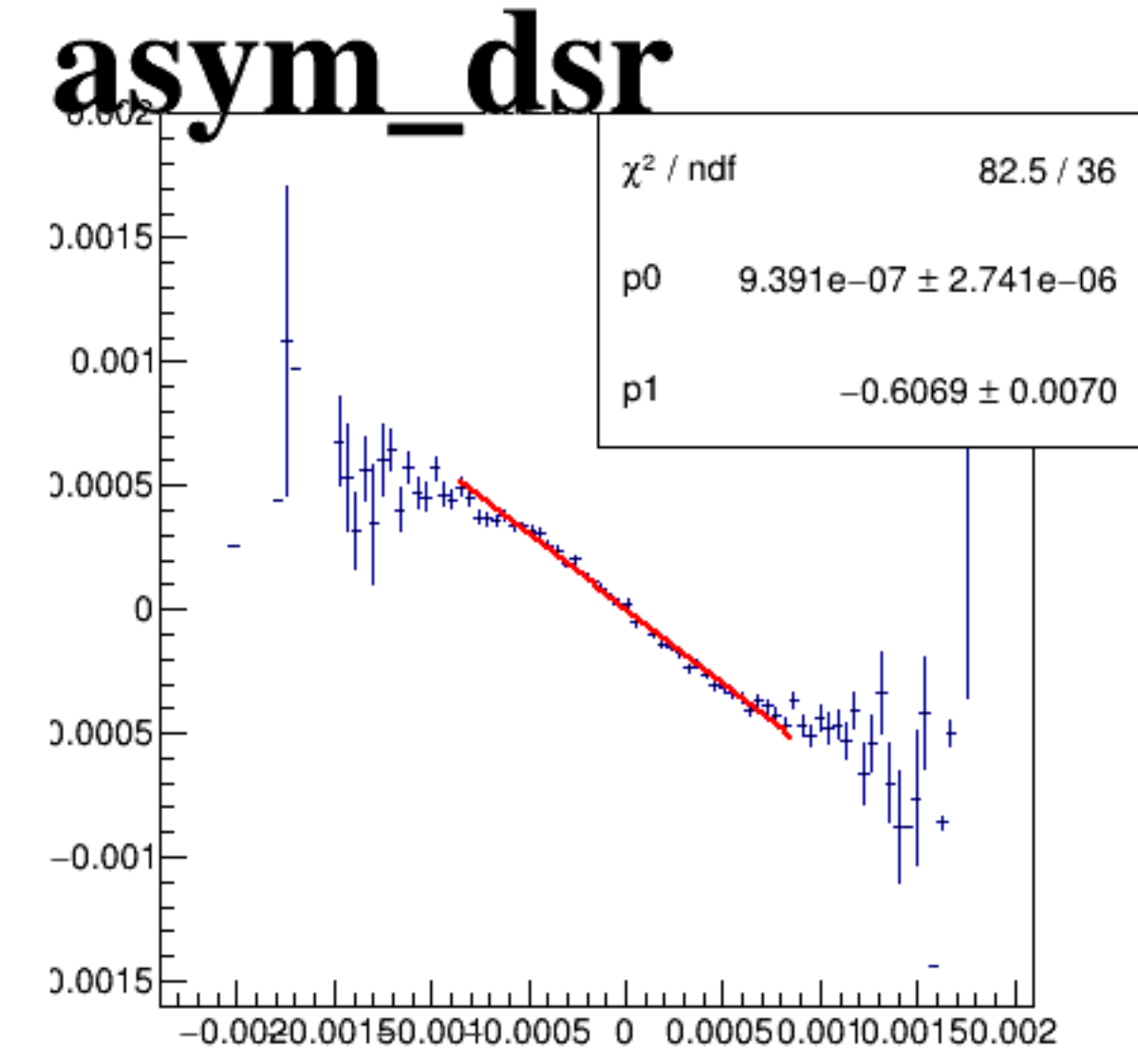
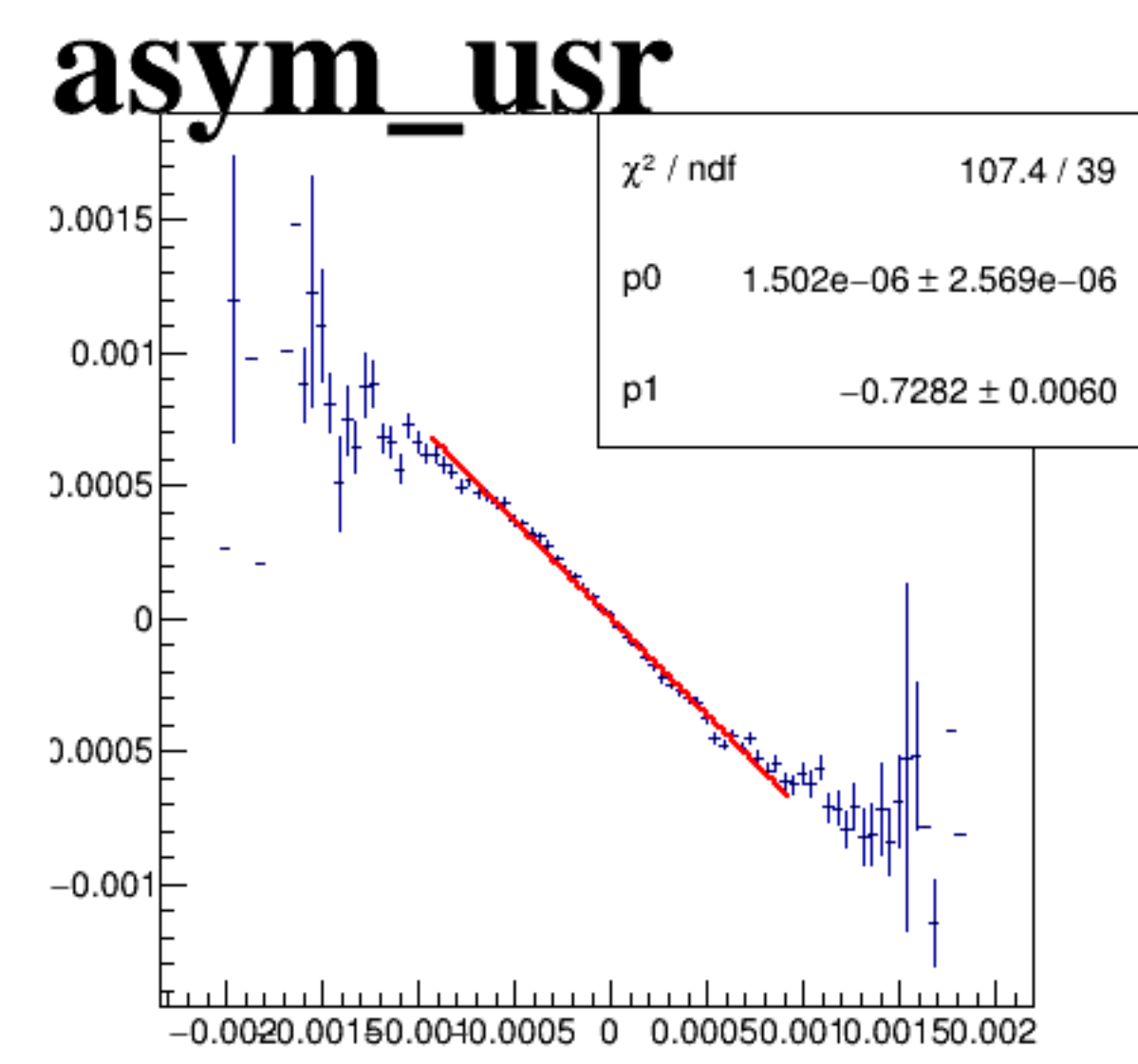
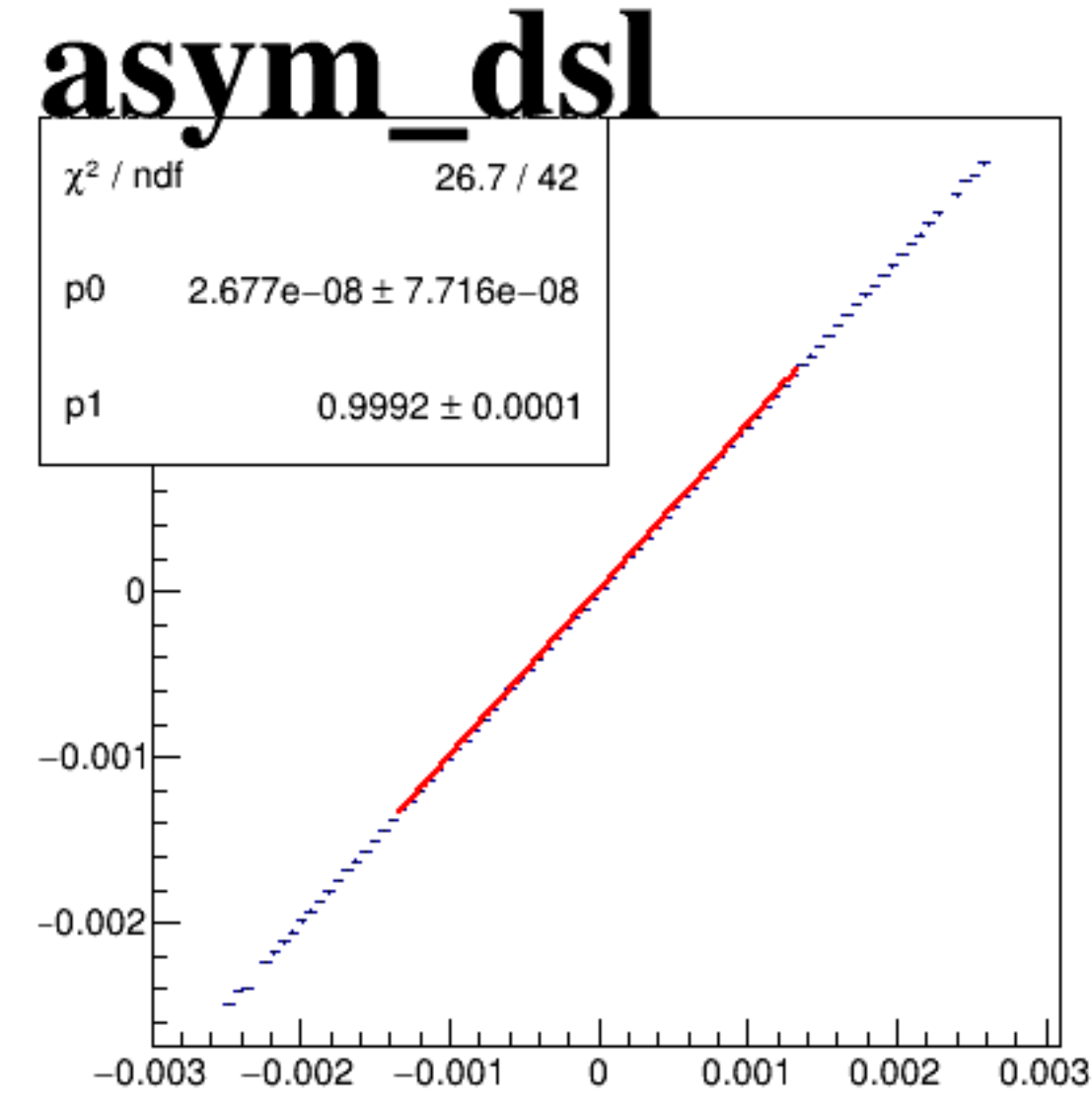
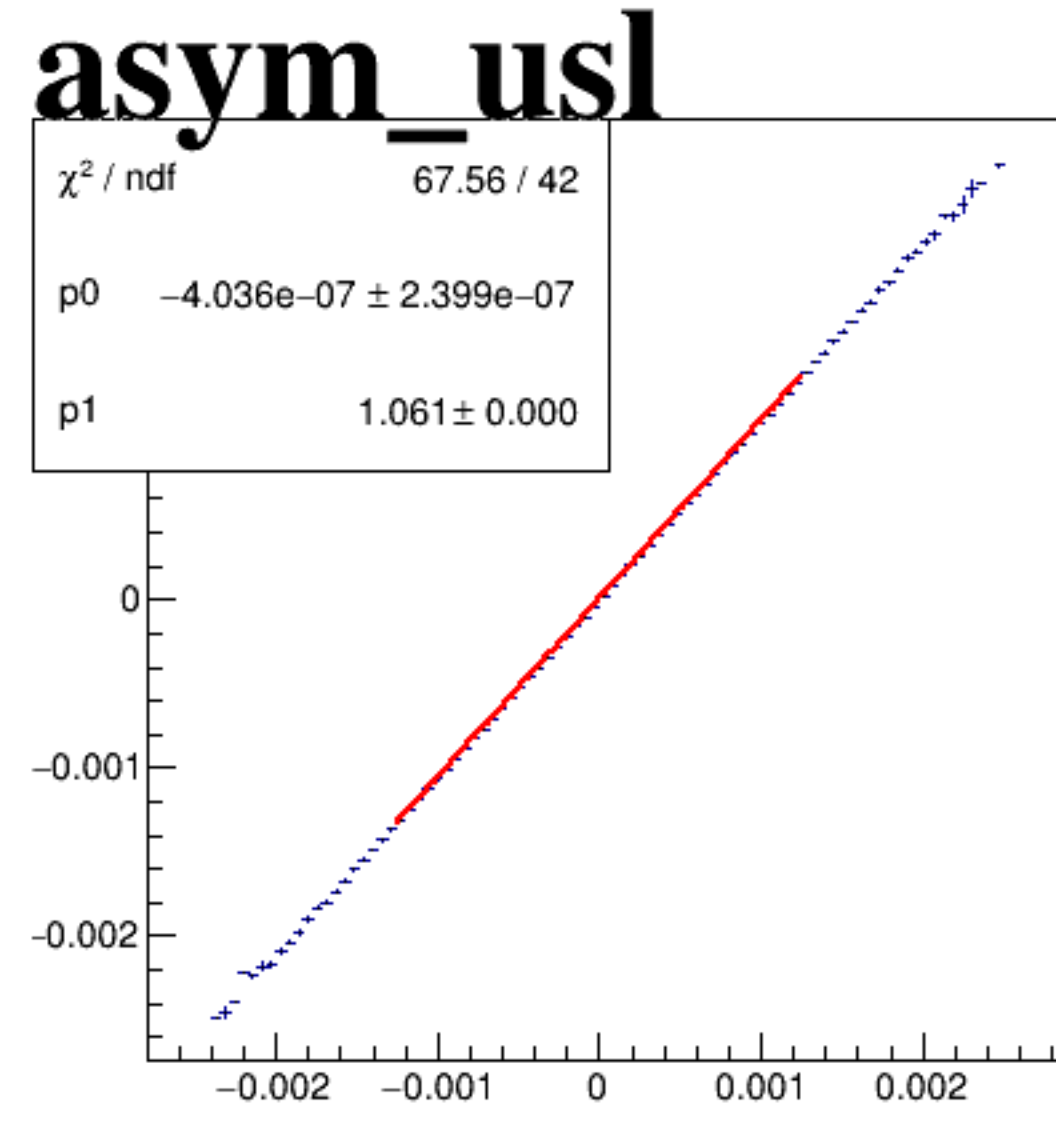




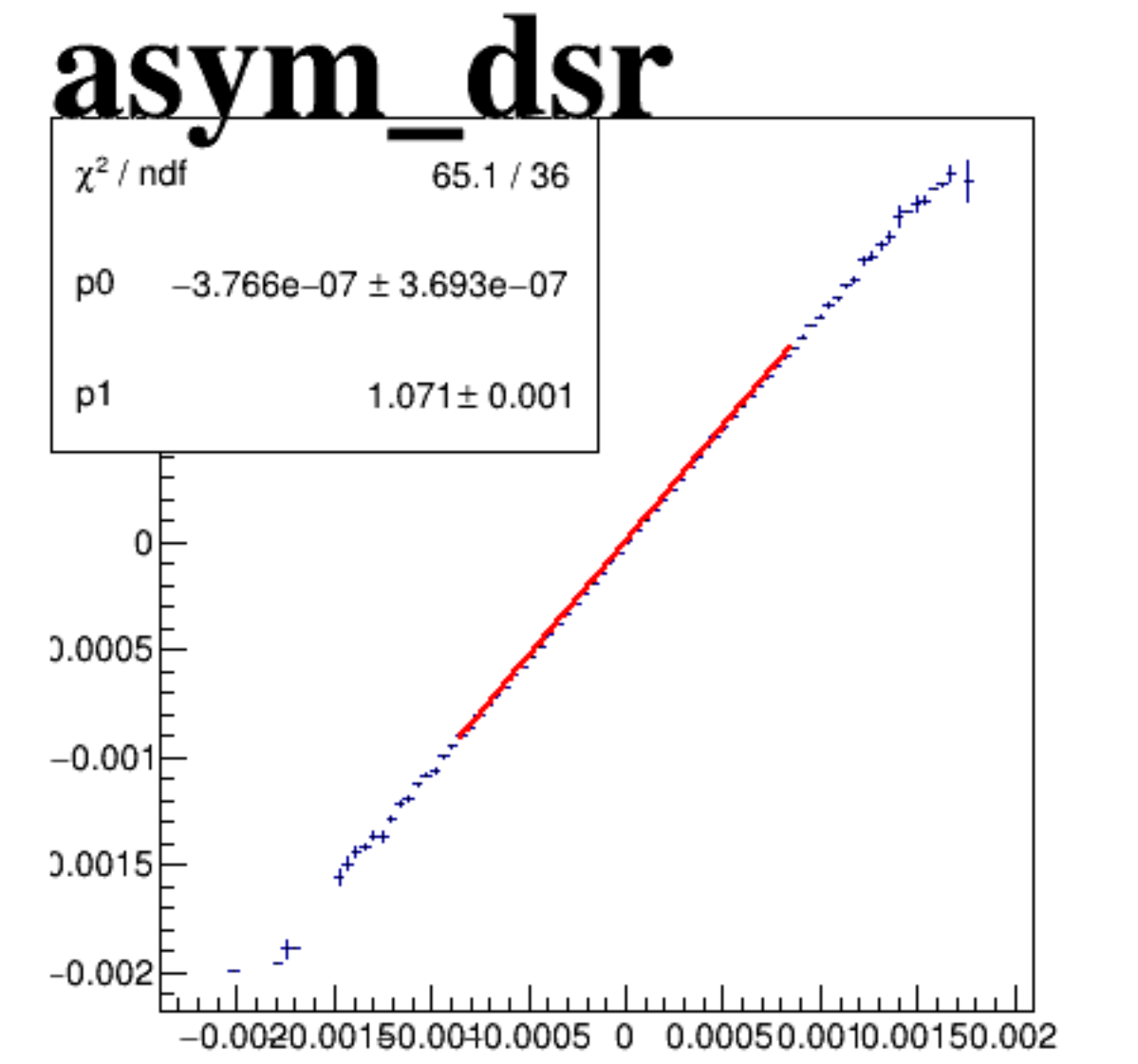
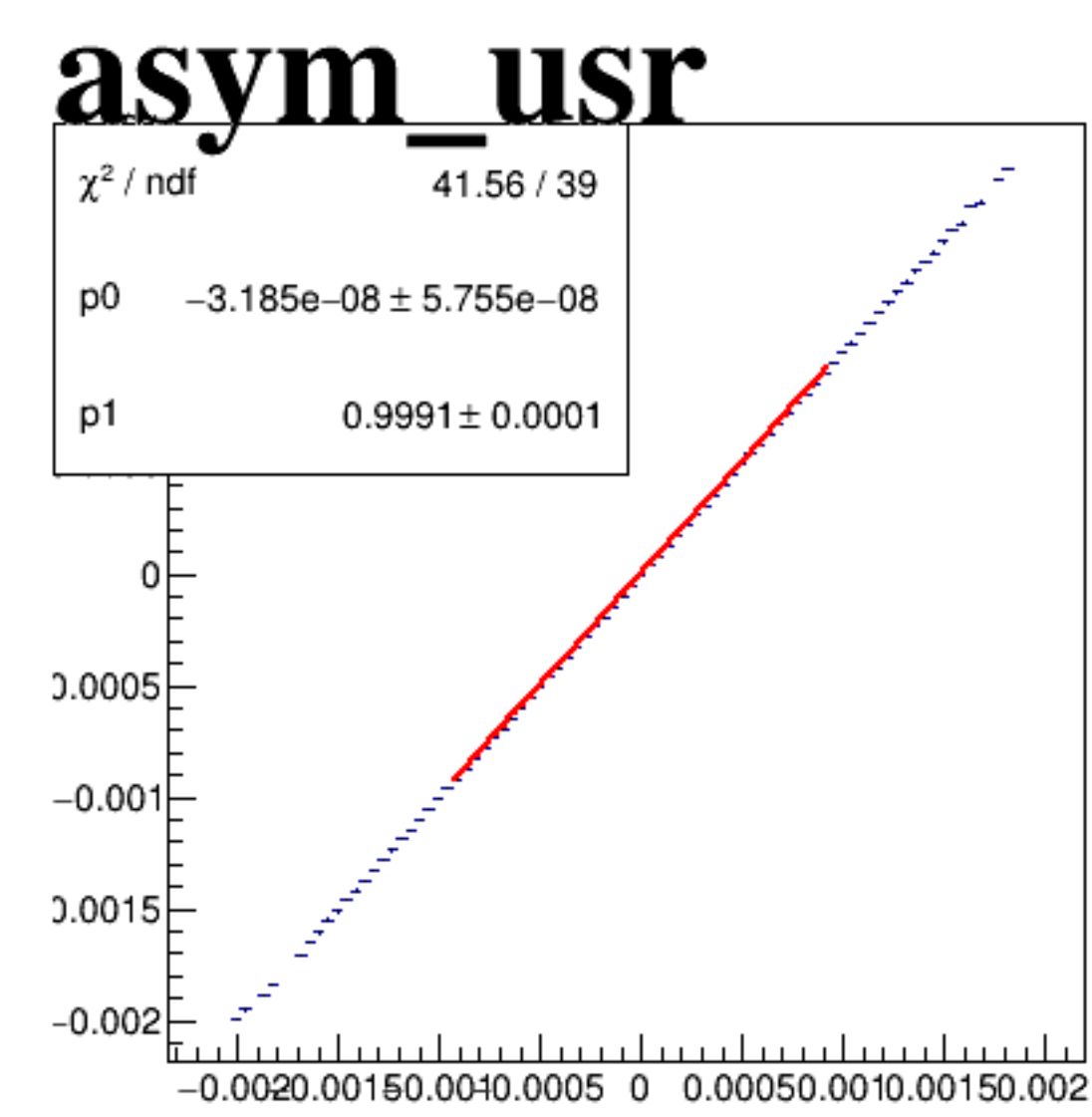
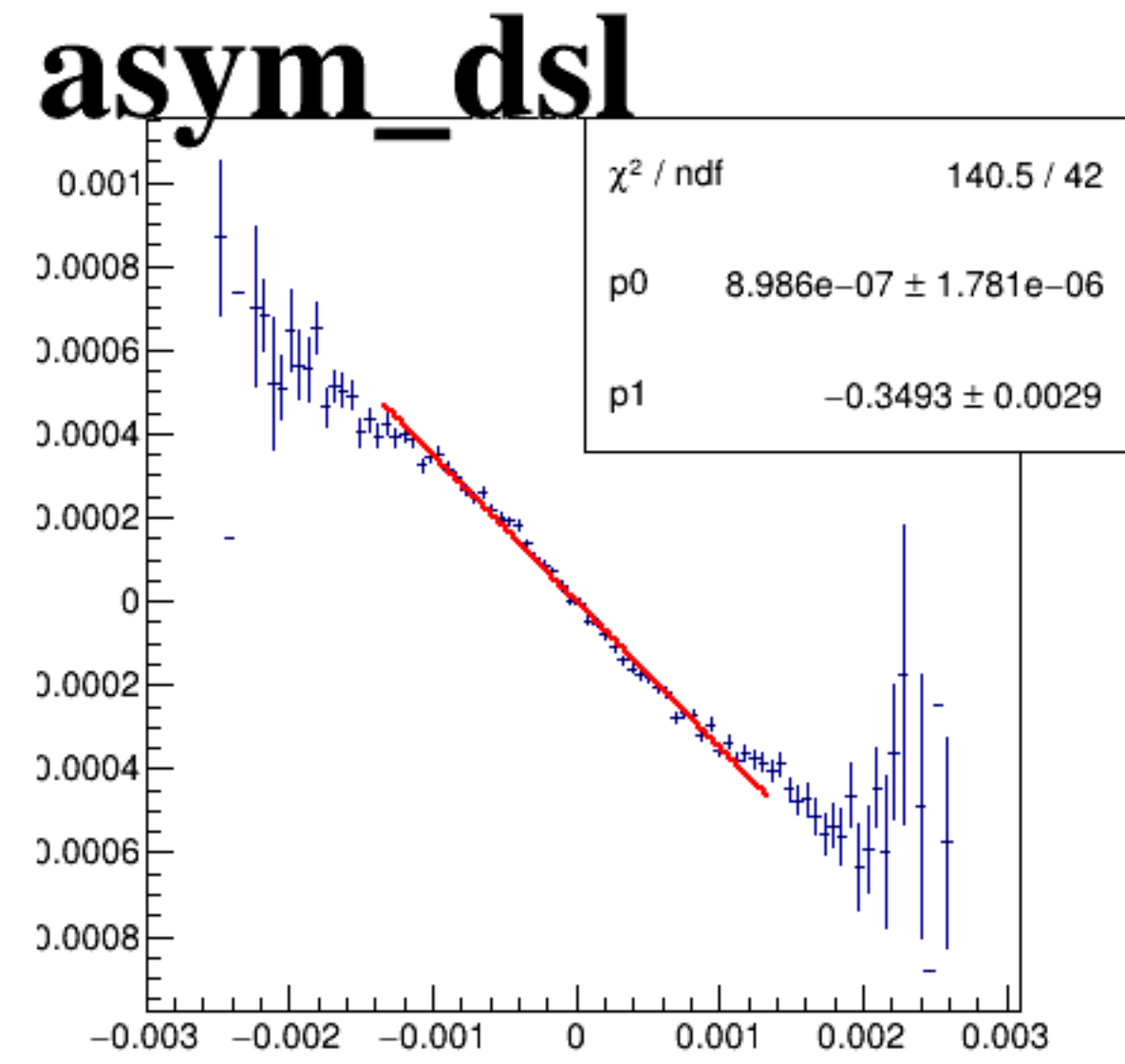
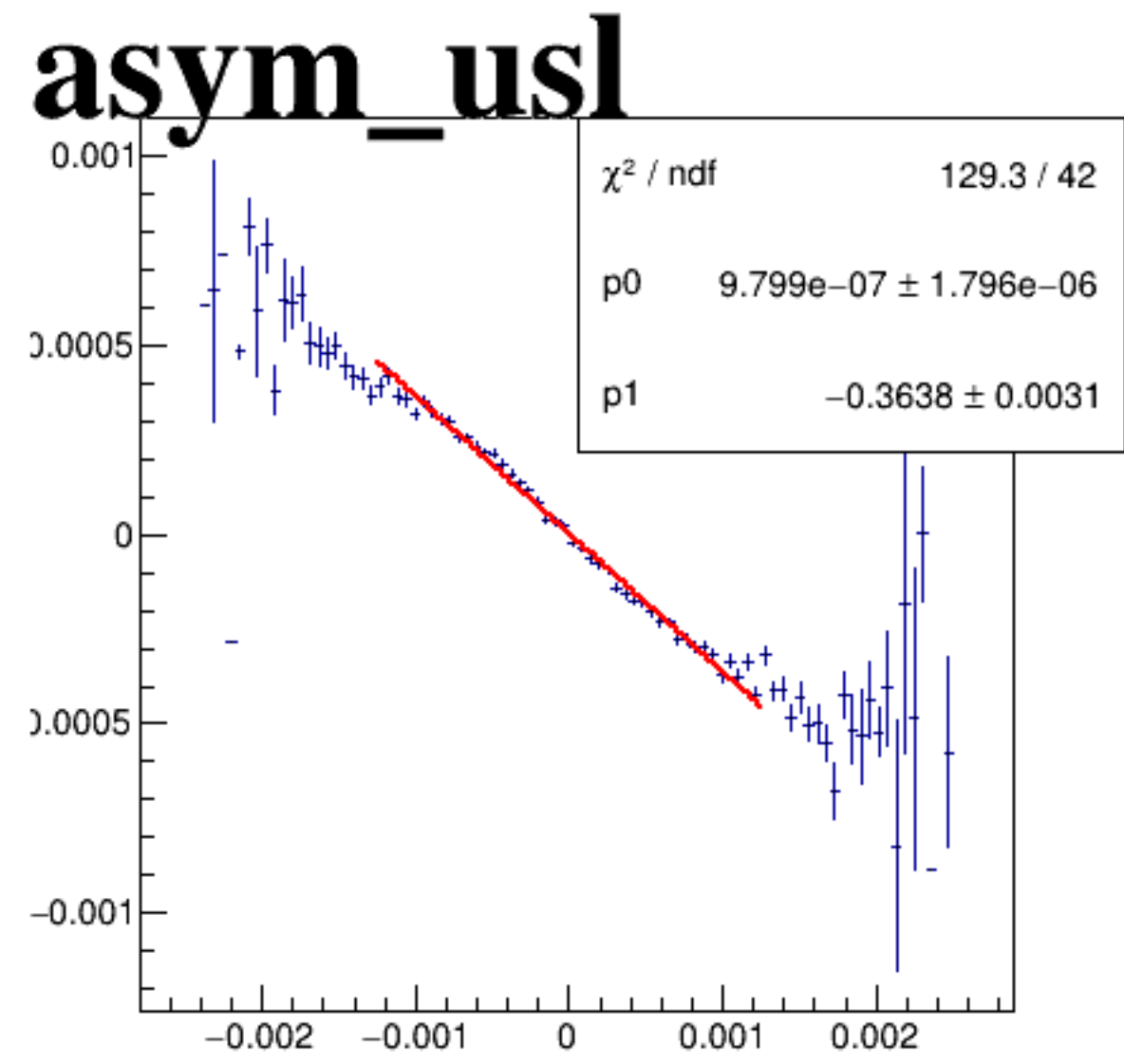
**asym\_usl**



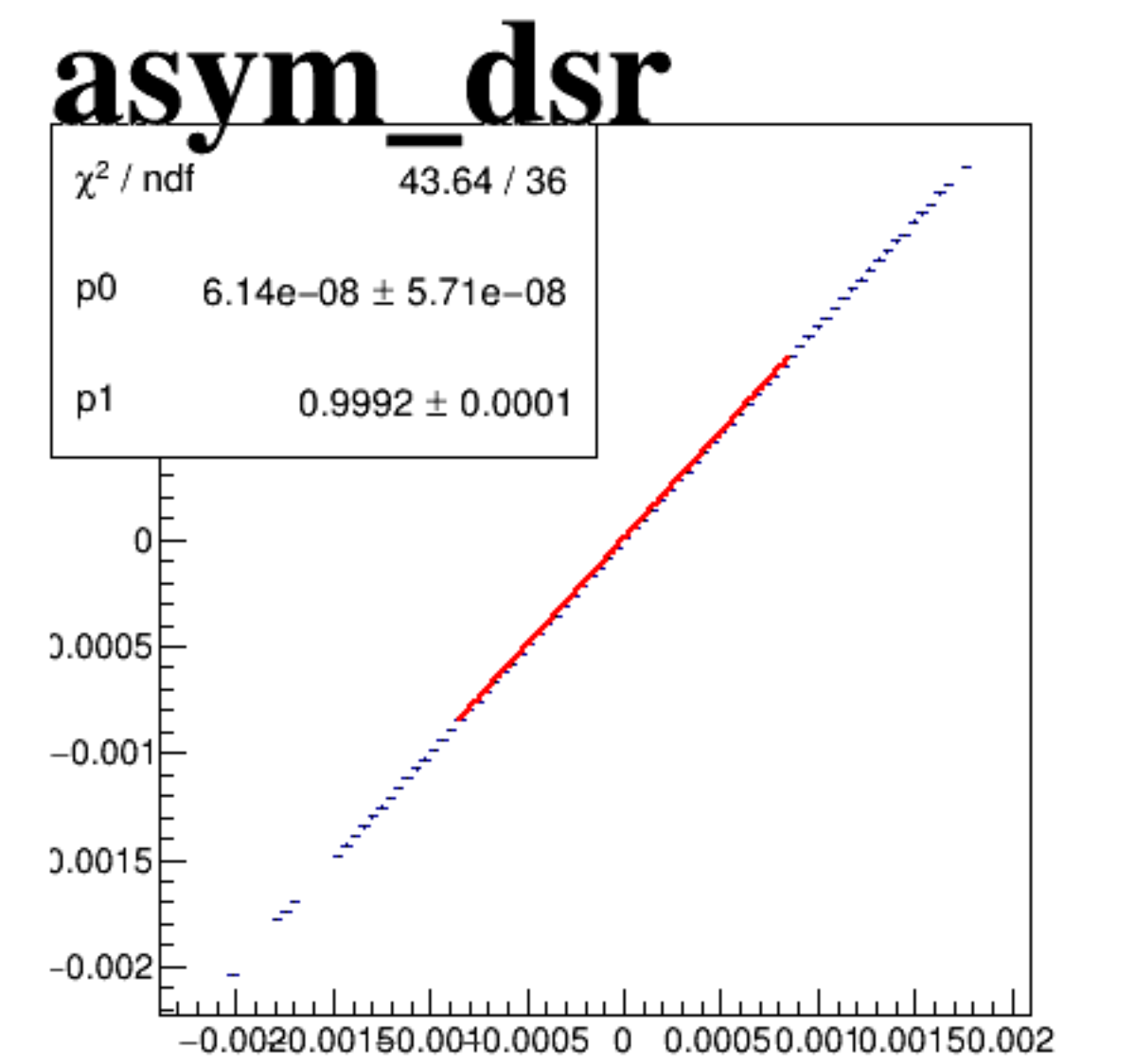
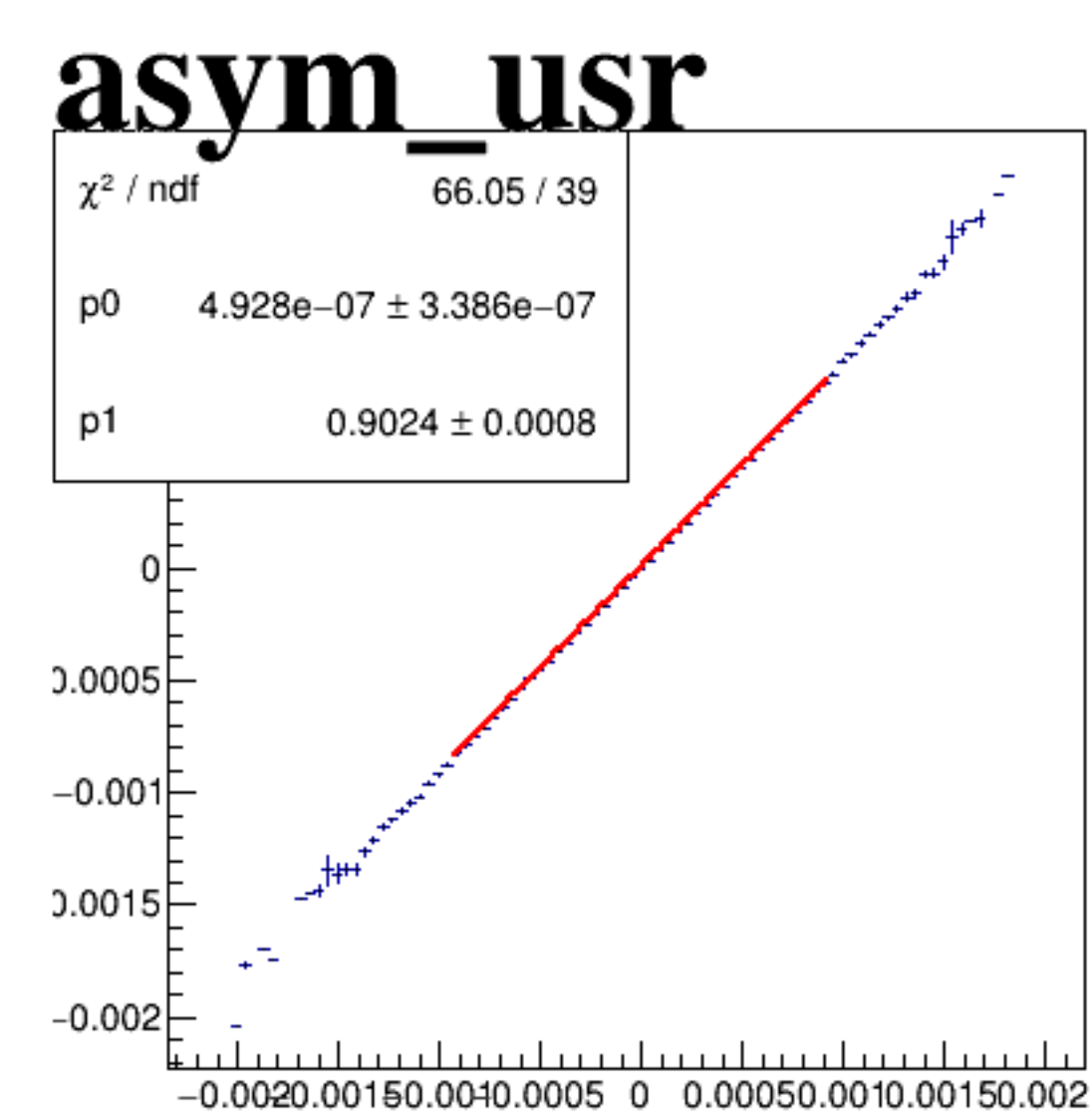
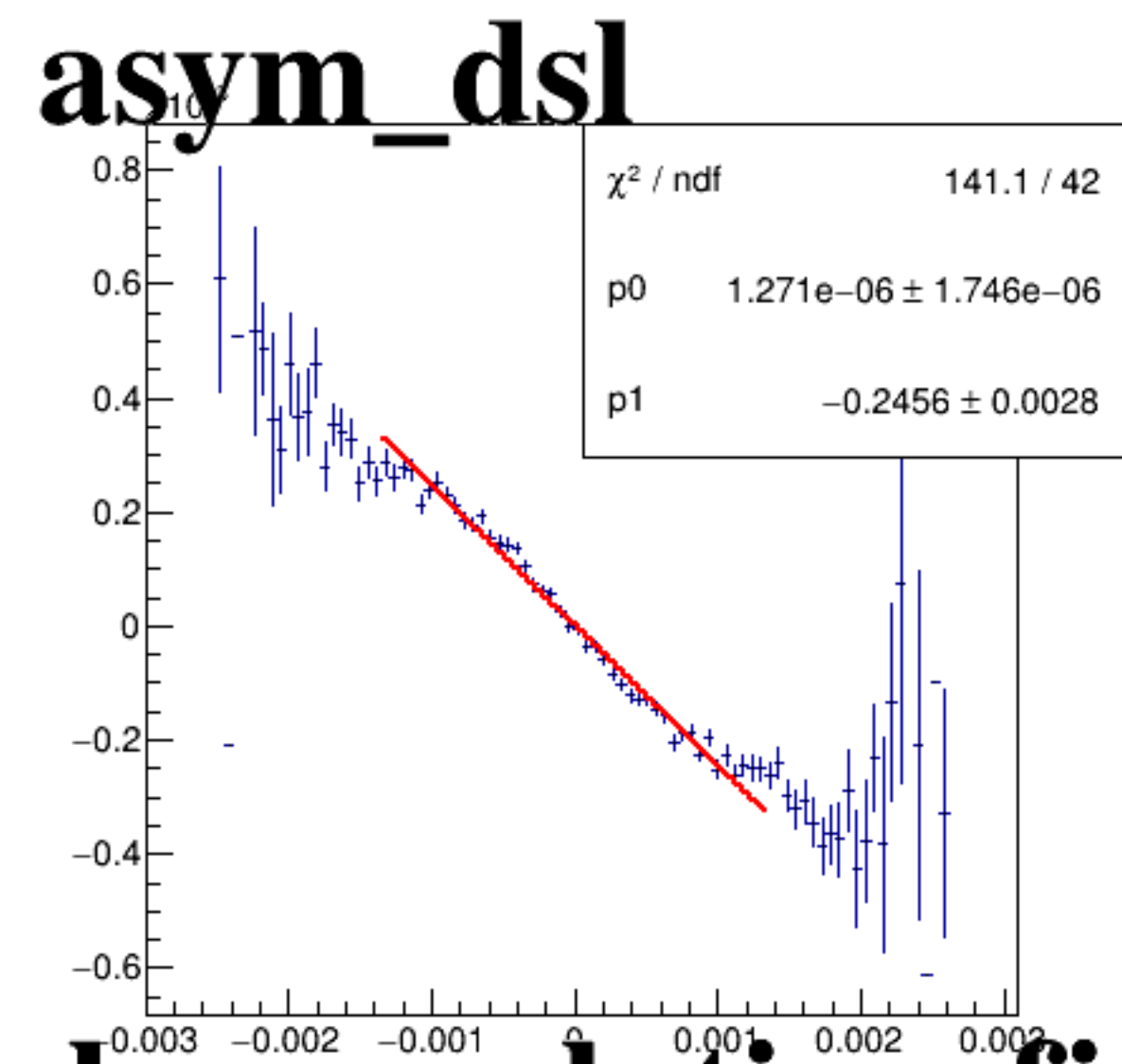
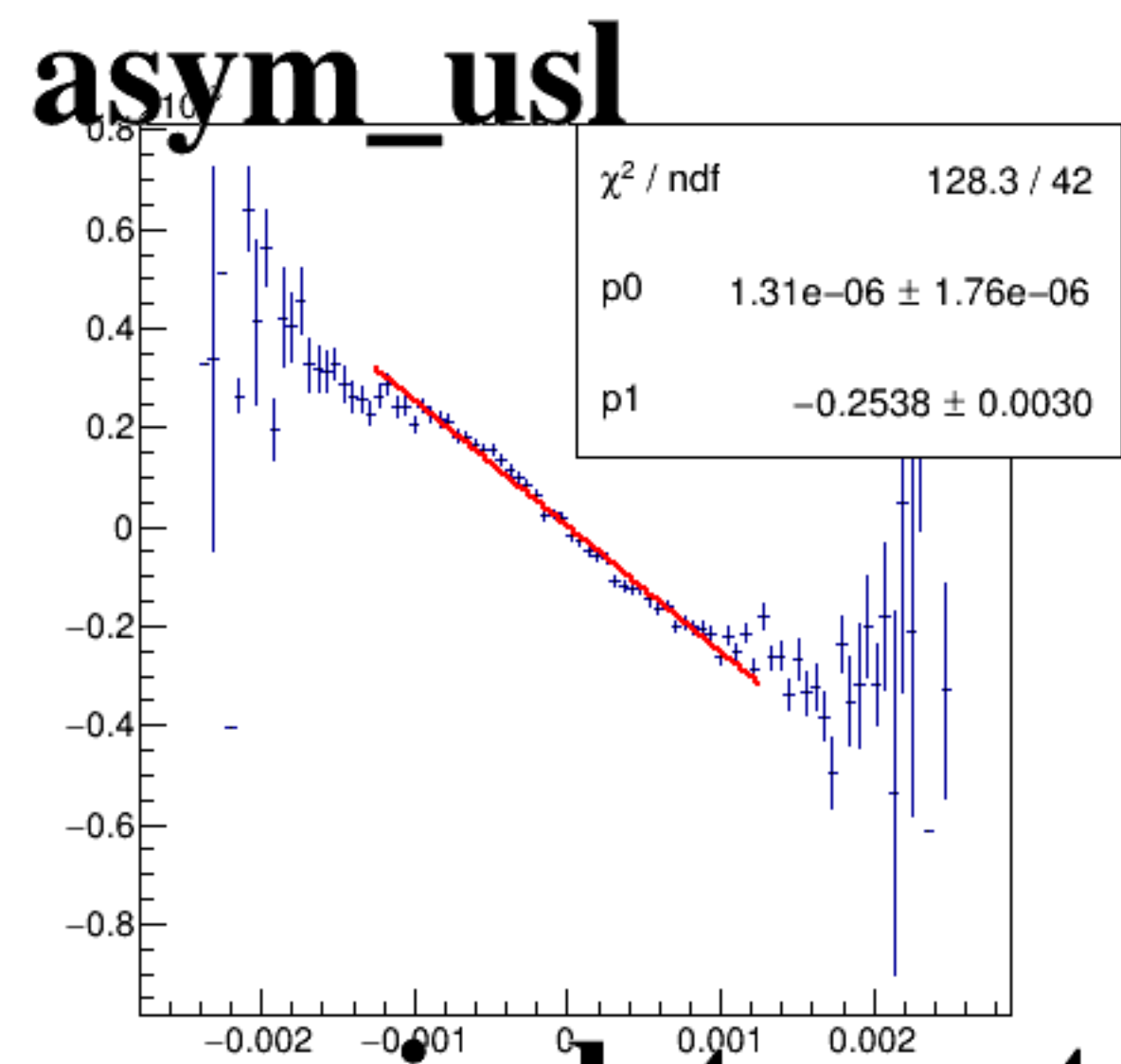
**asym\_dsl**



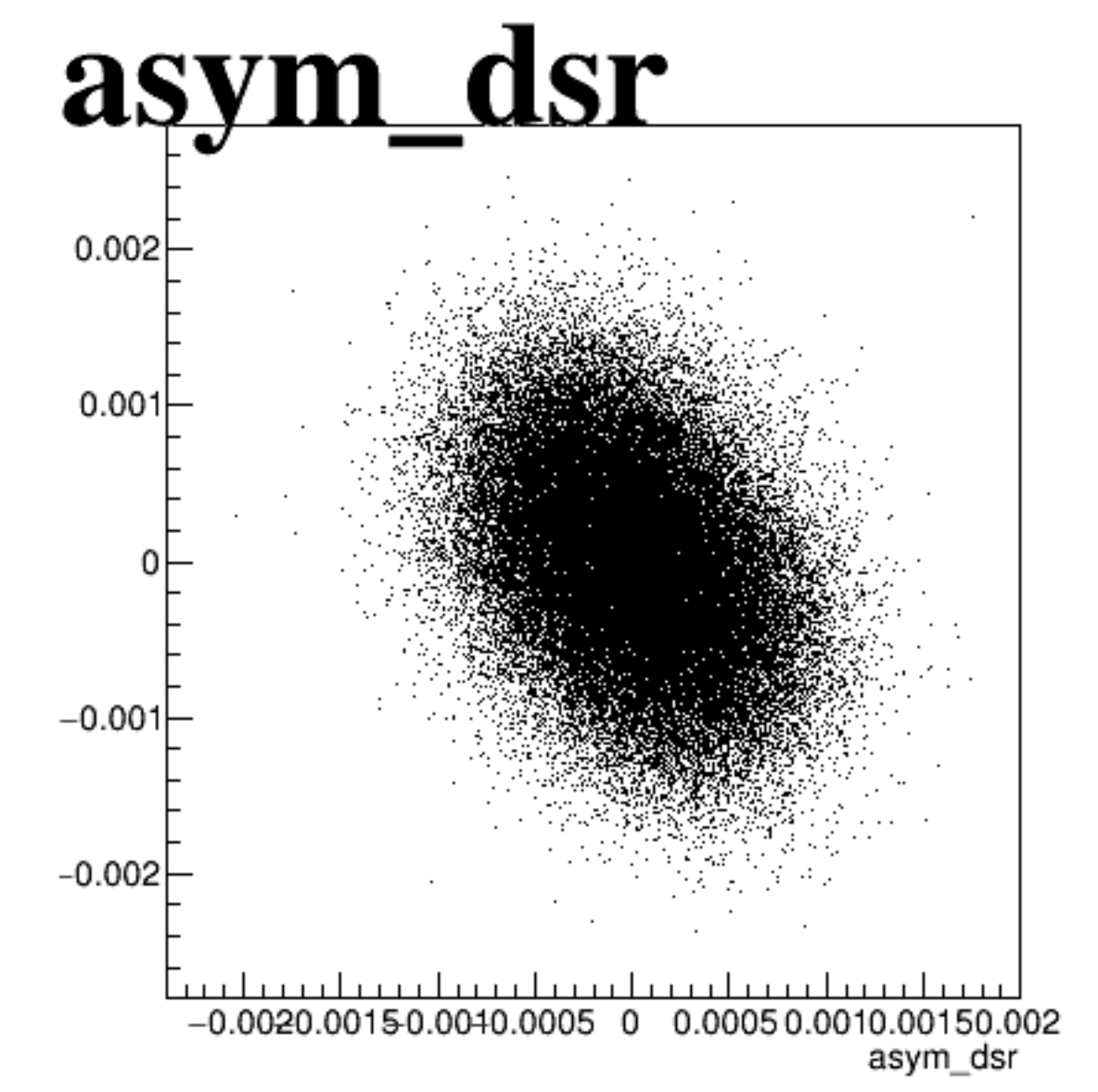
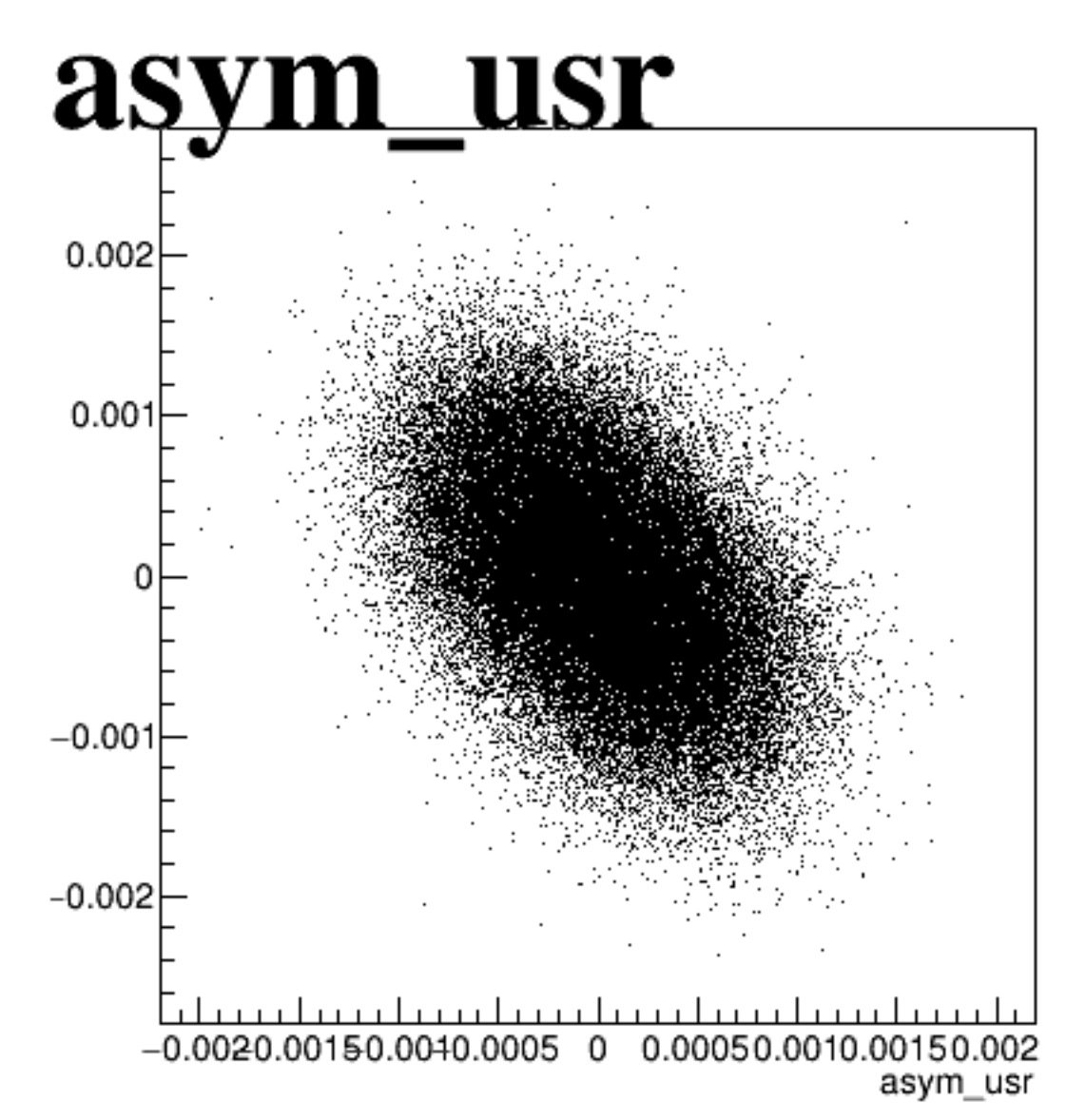
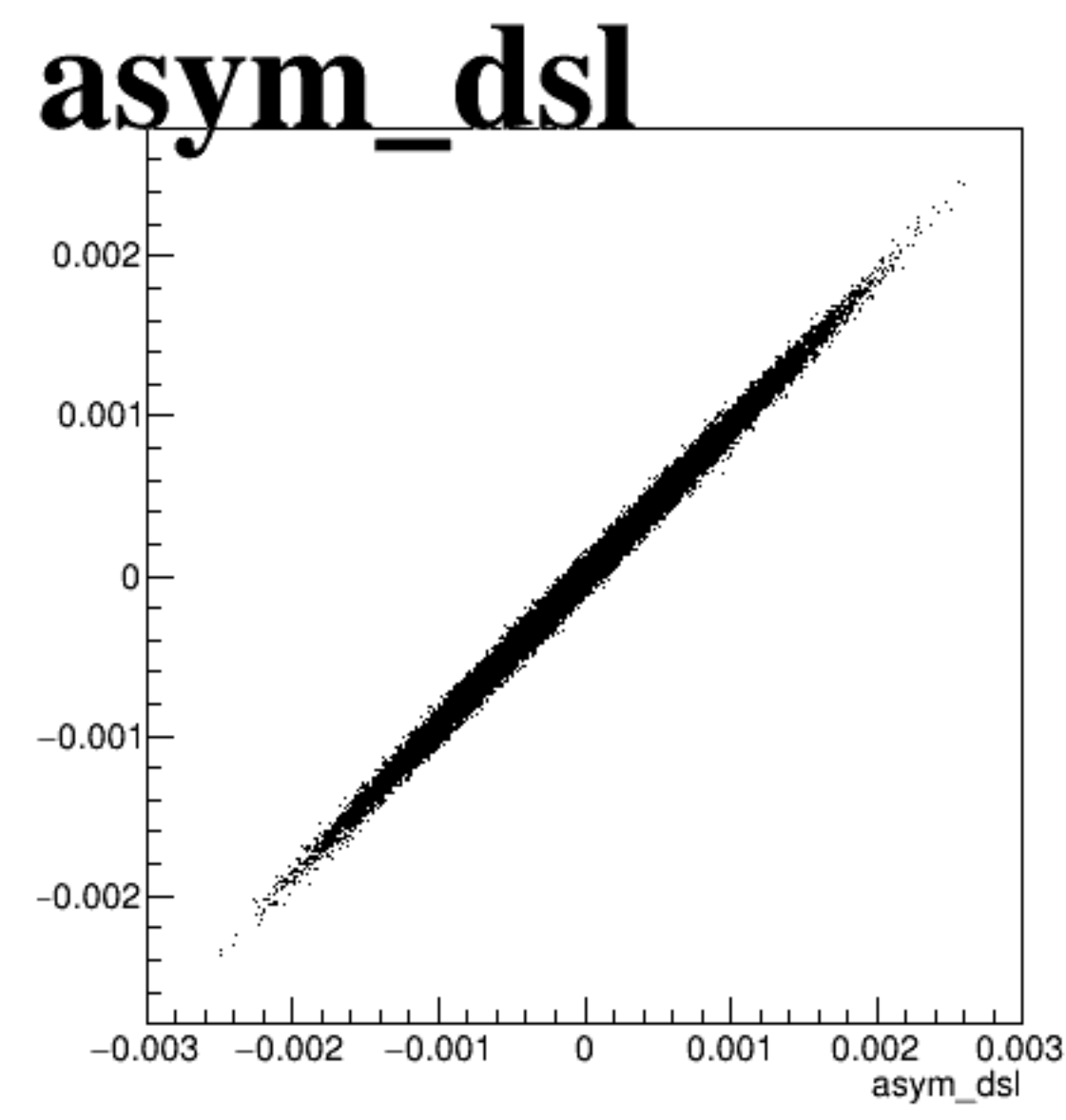
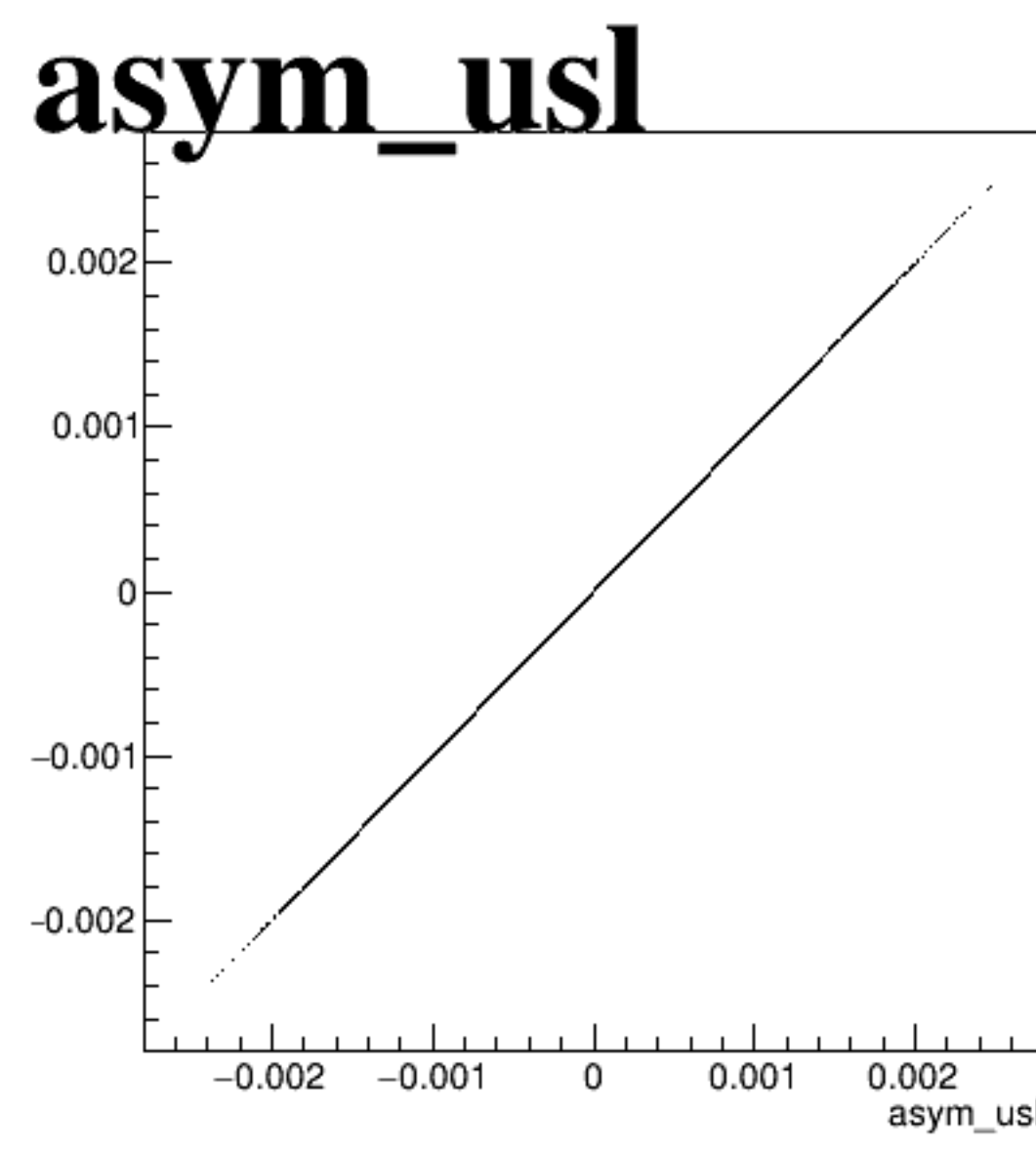
**asym\_usr**



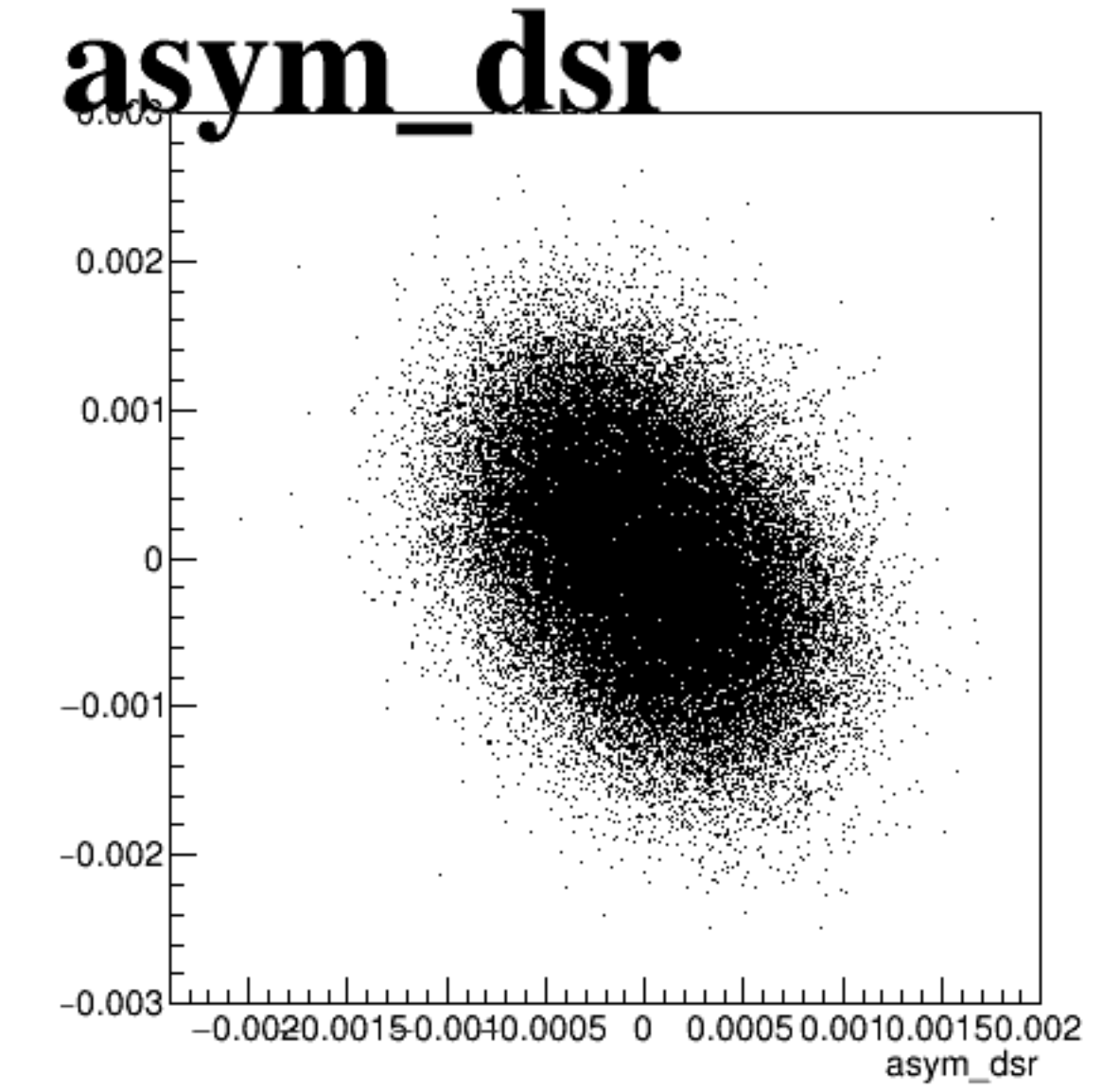
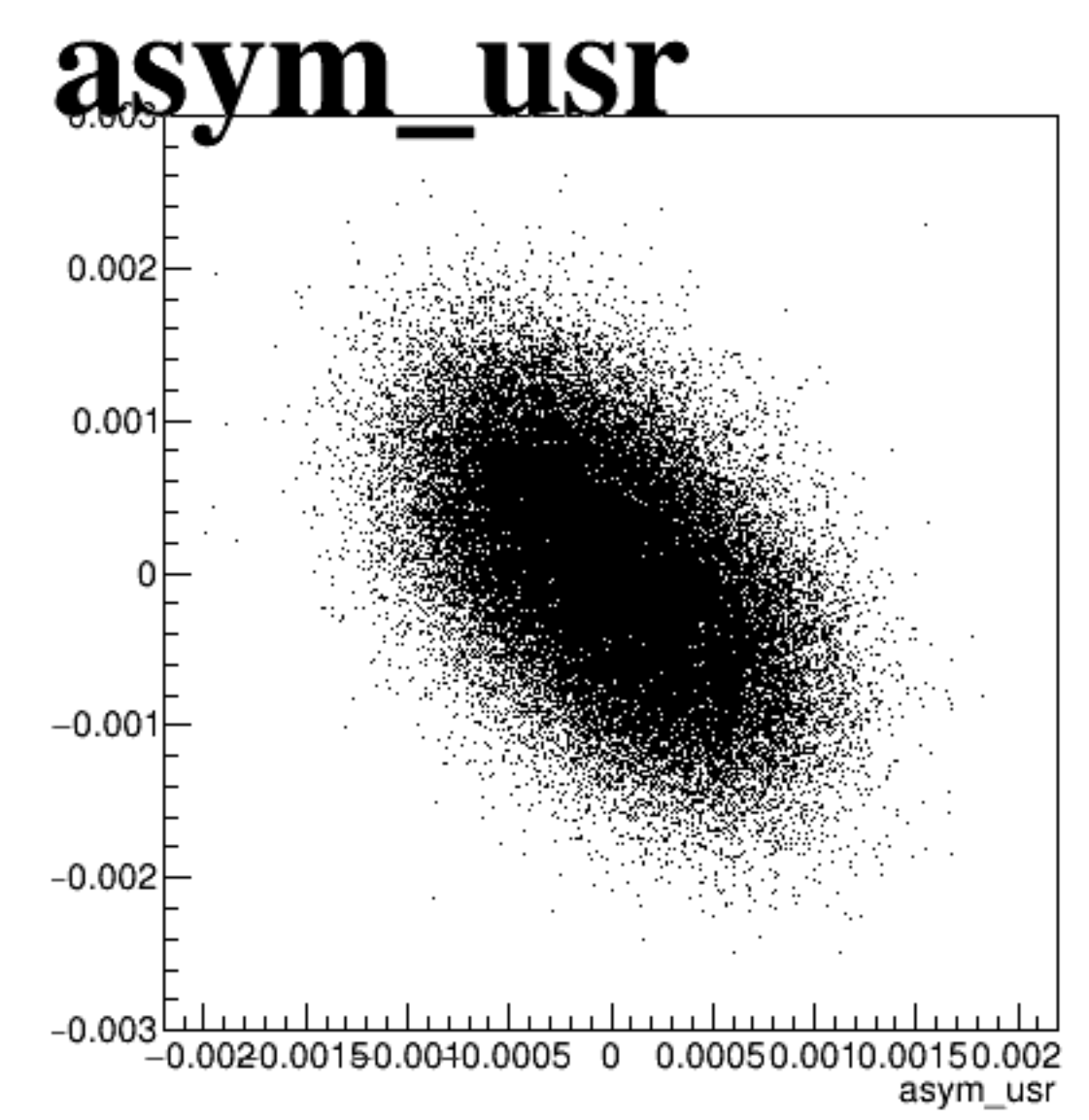
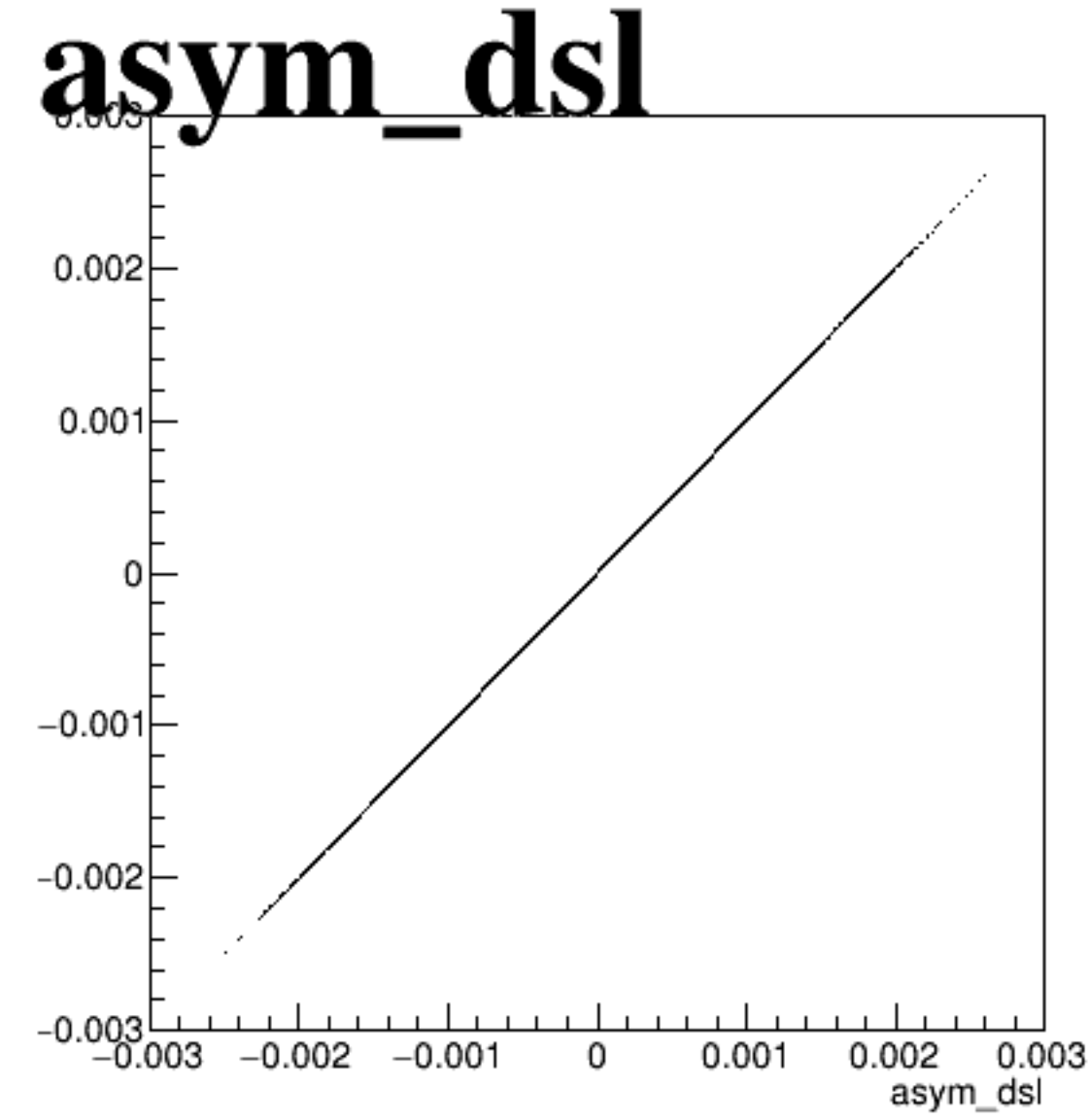
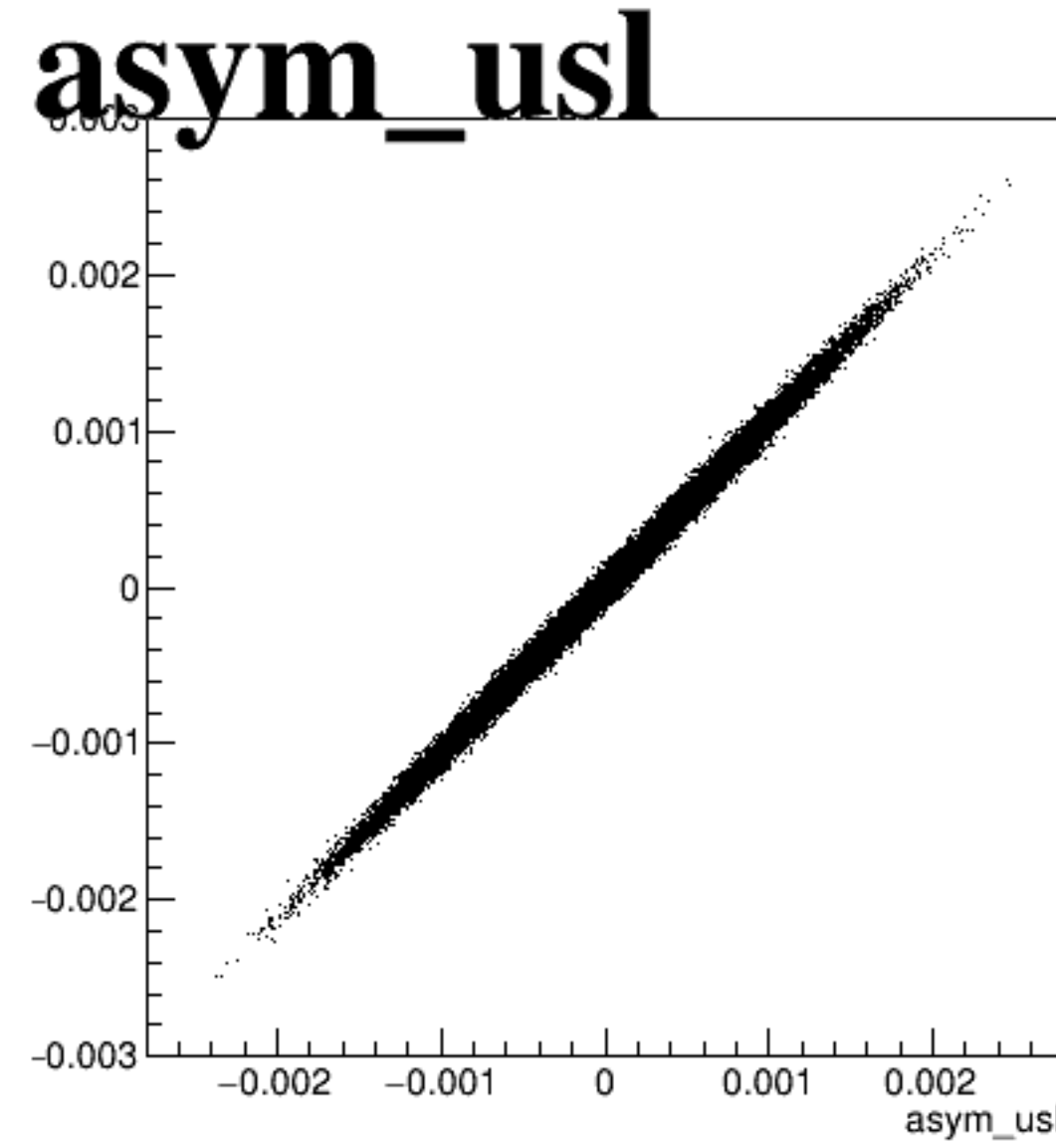
**asym\_dsr**



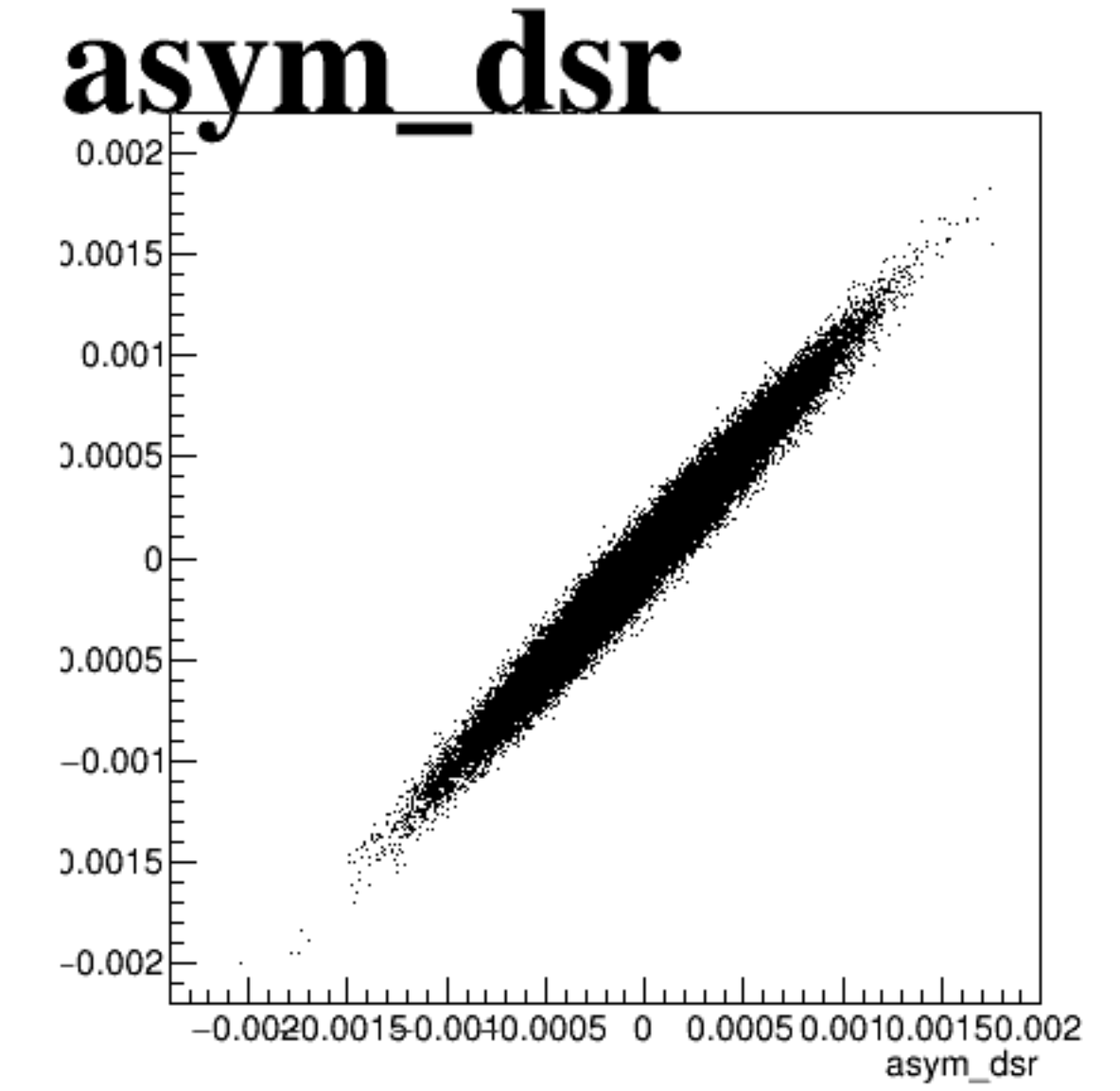
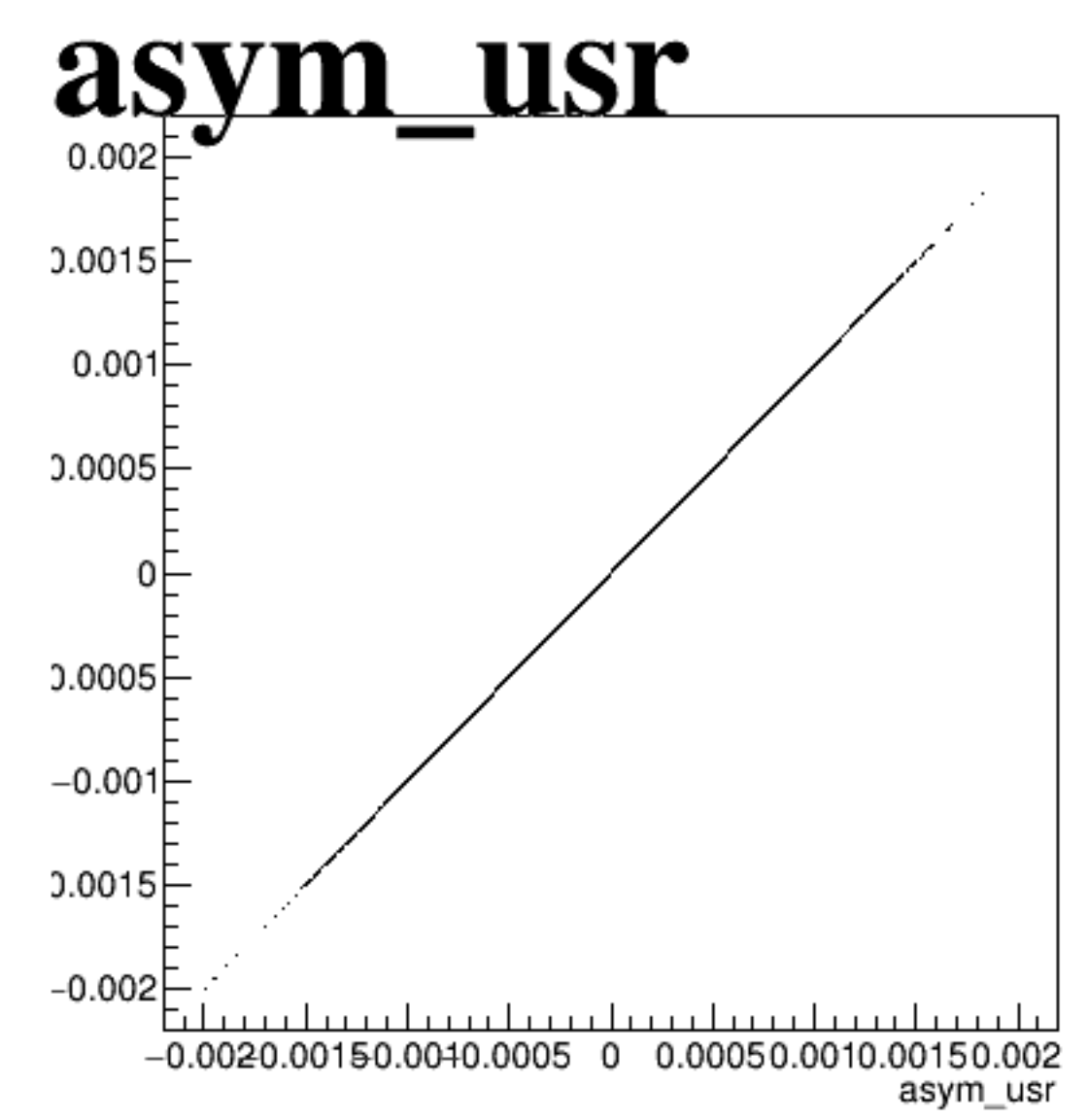
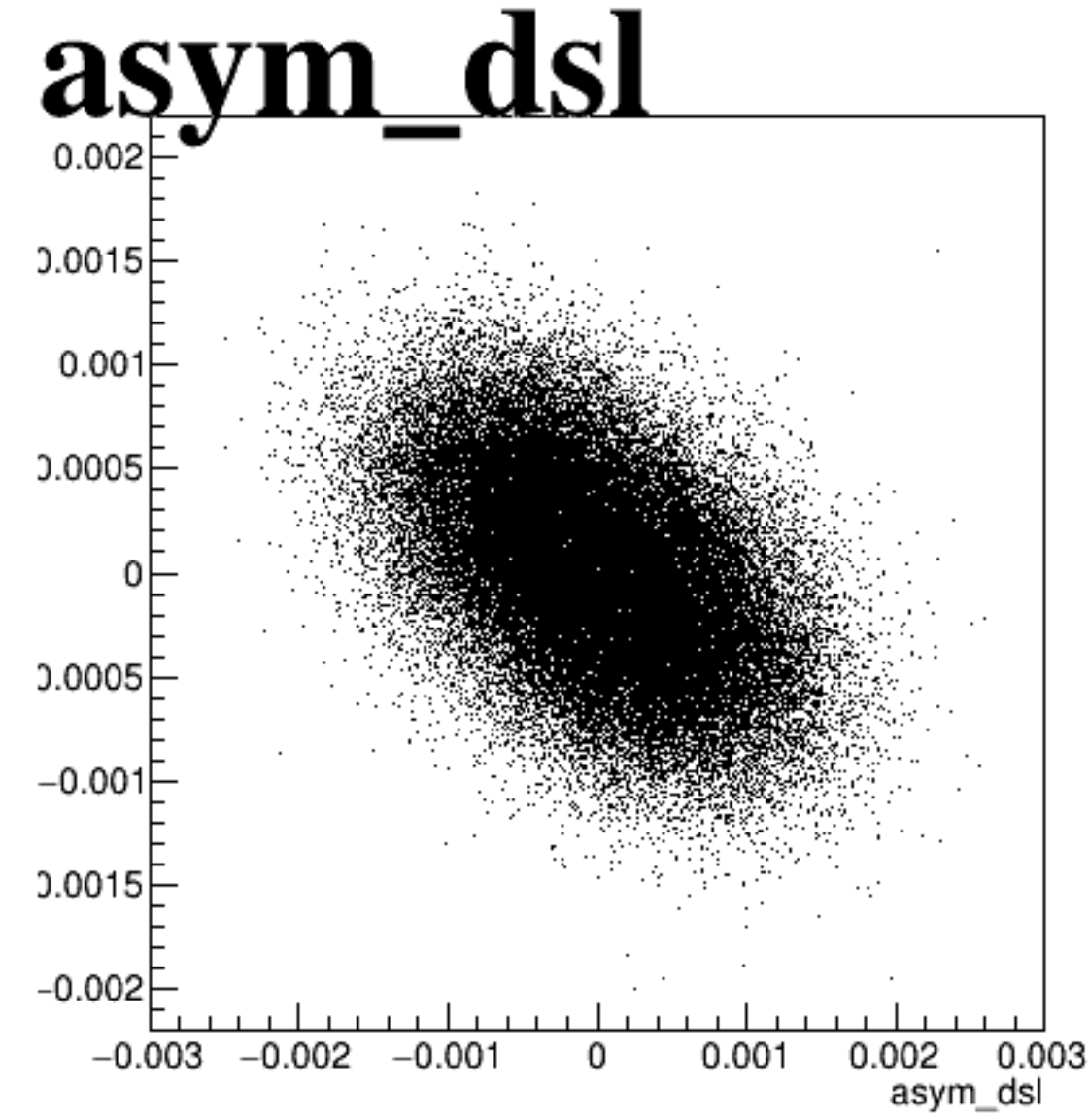
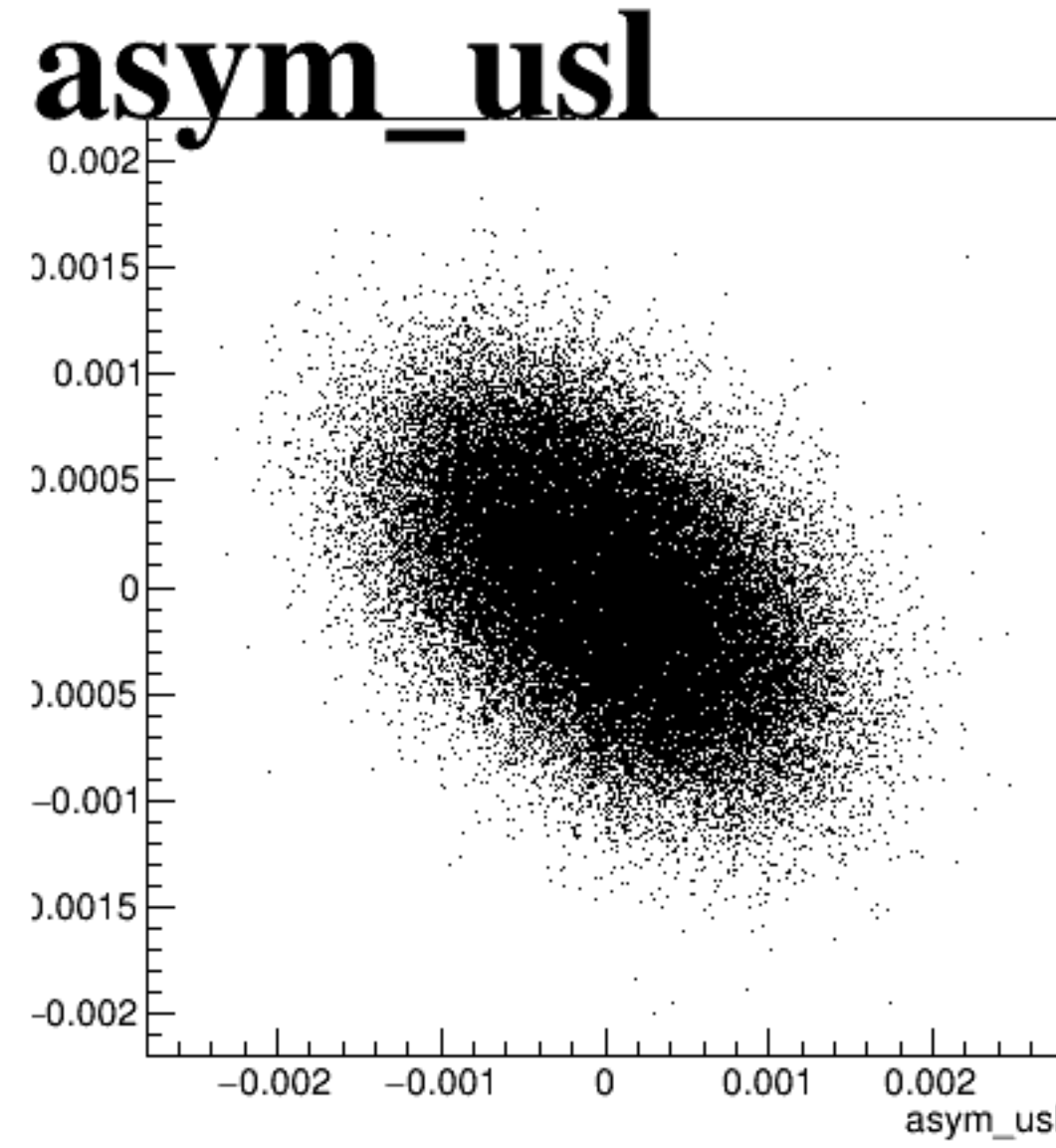
**asym\_usl**



**asym\_dsl**



**asym\_usr**



**asym\_dsr**

