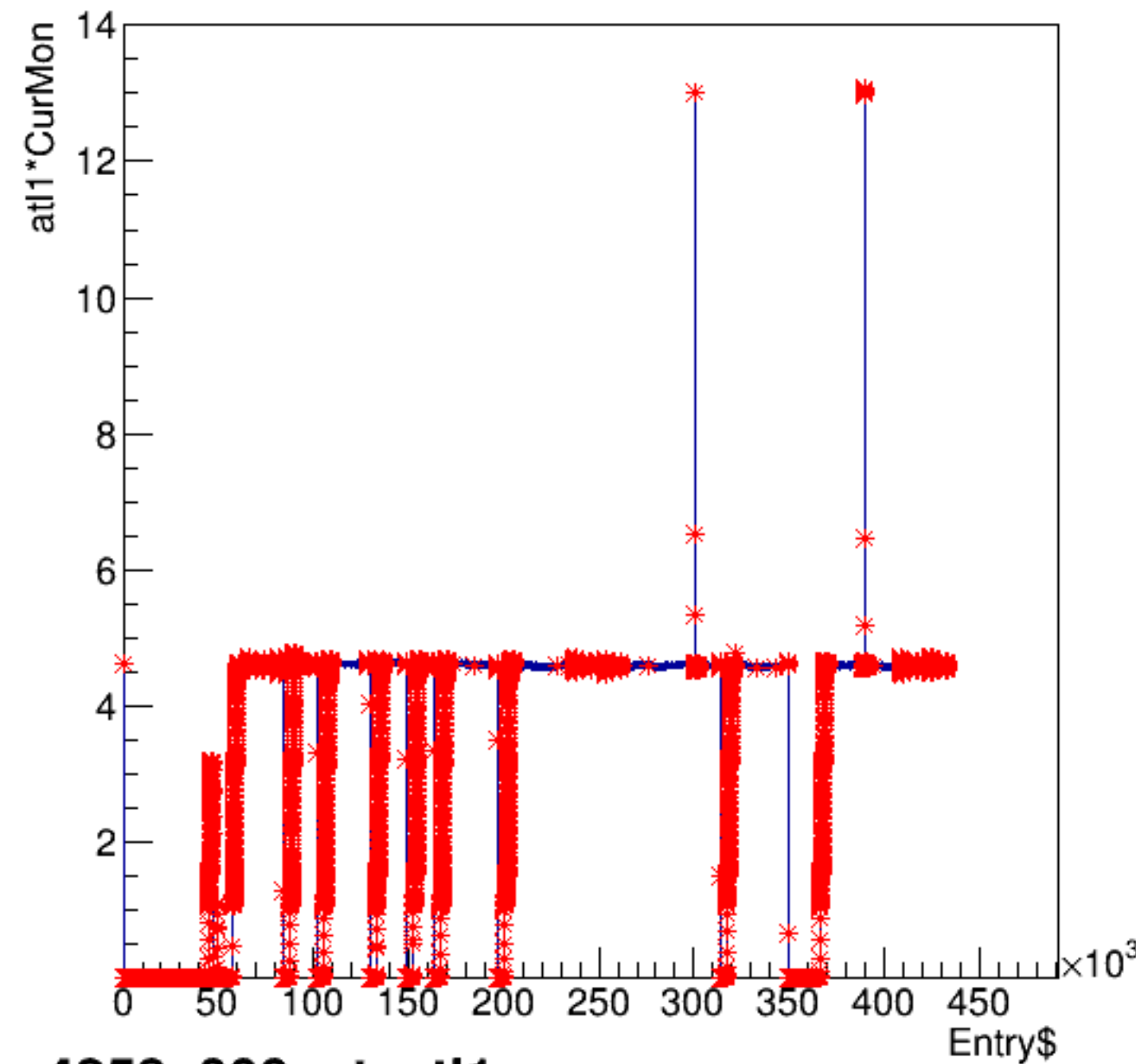
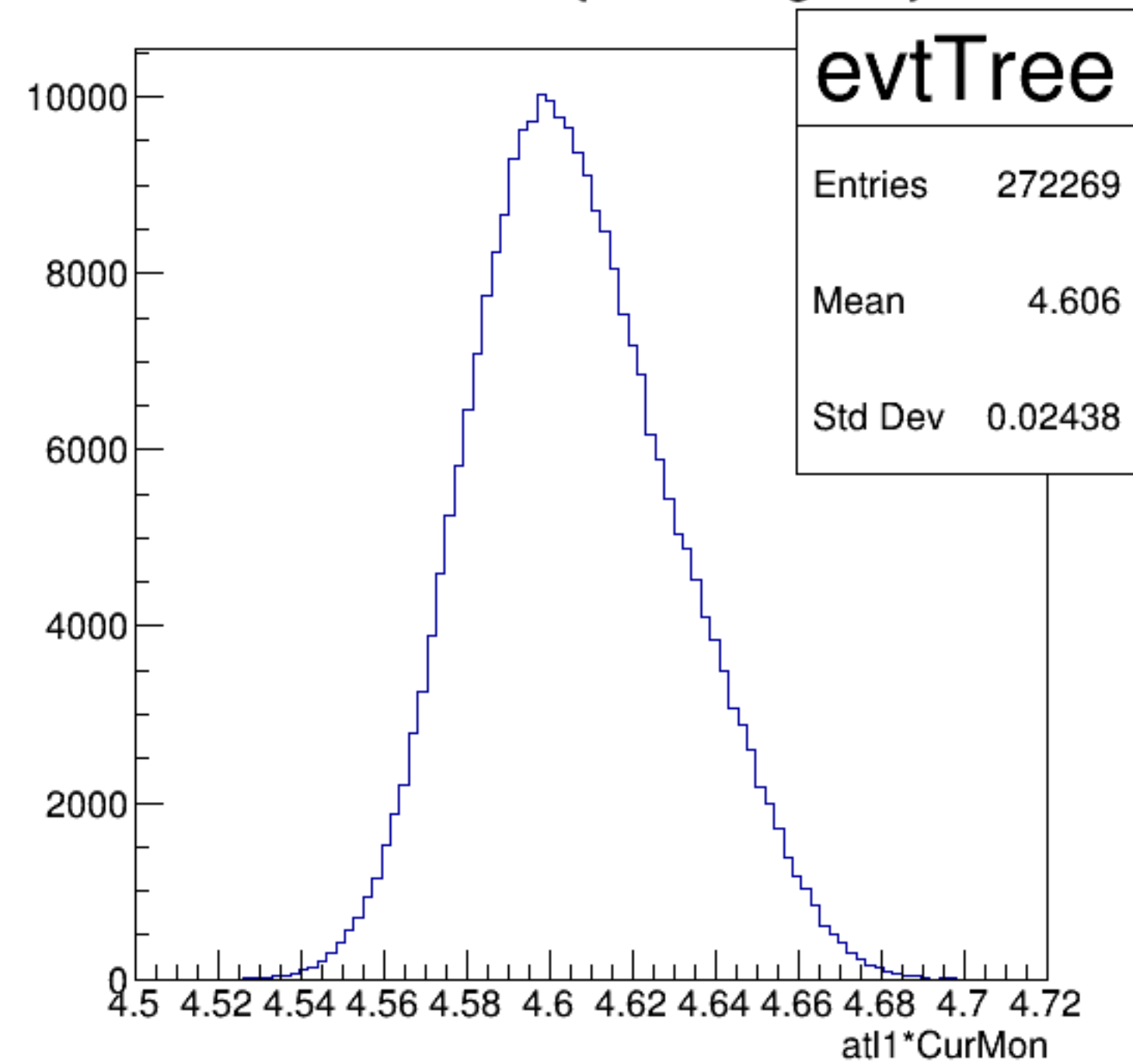


atl1*CurMon:Entry\$

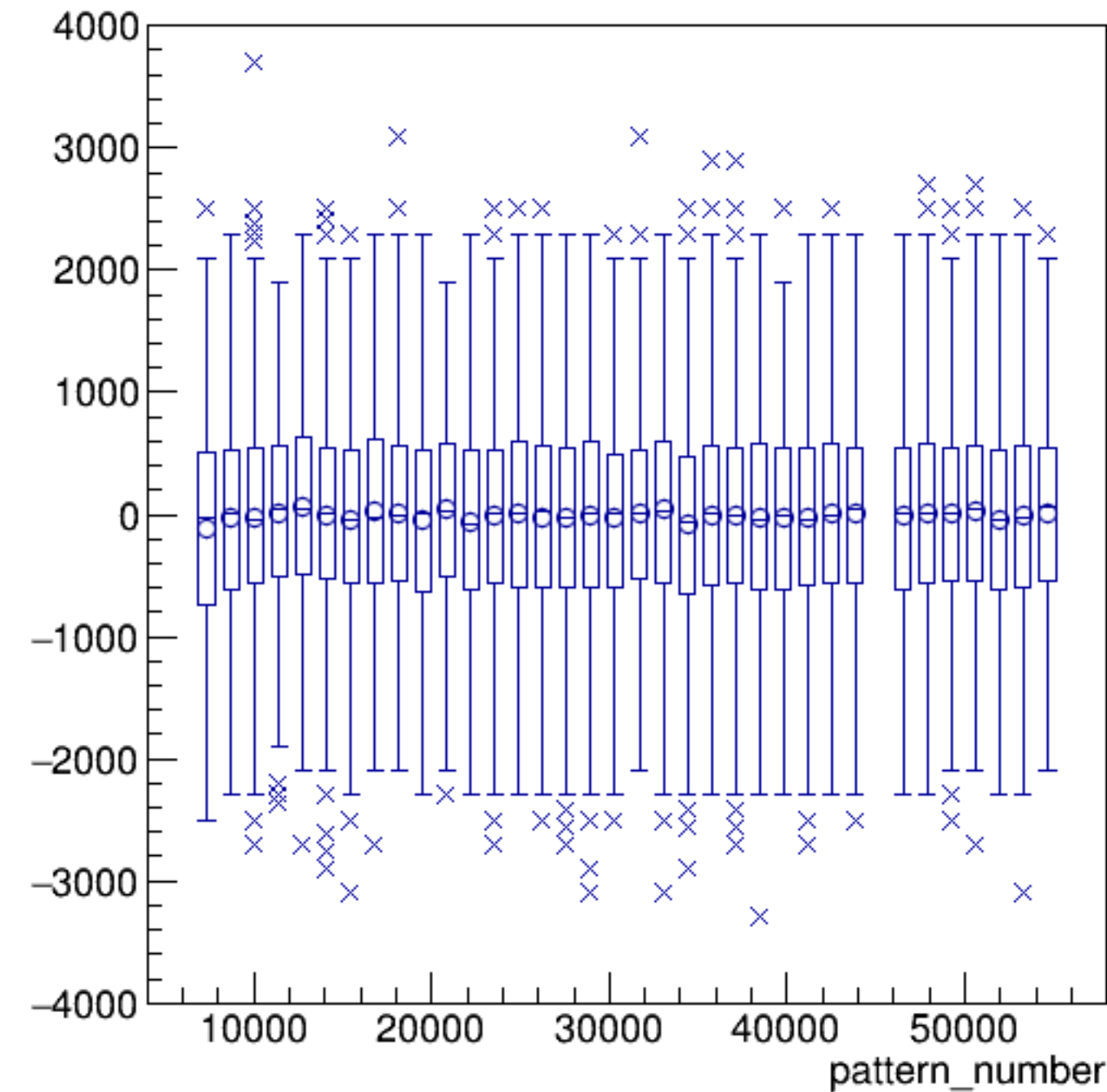


run4258_000_at_atl1.png

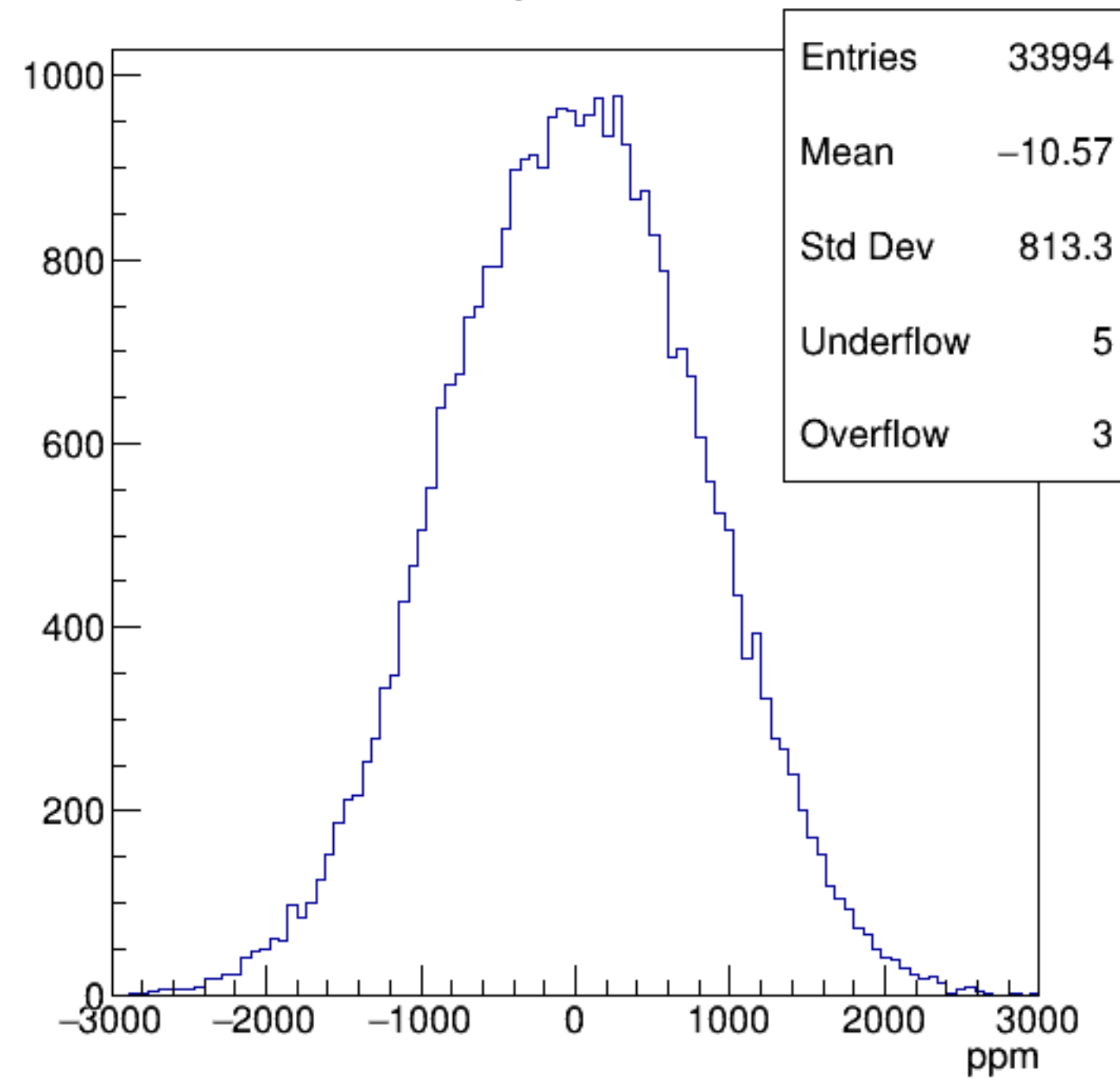
atl1*CurMon {ErrorFlag==0}



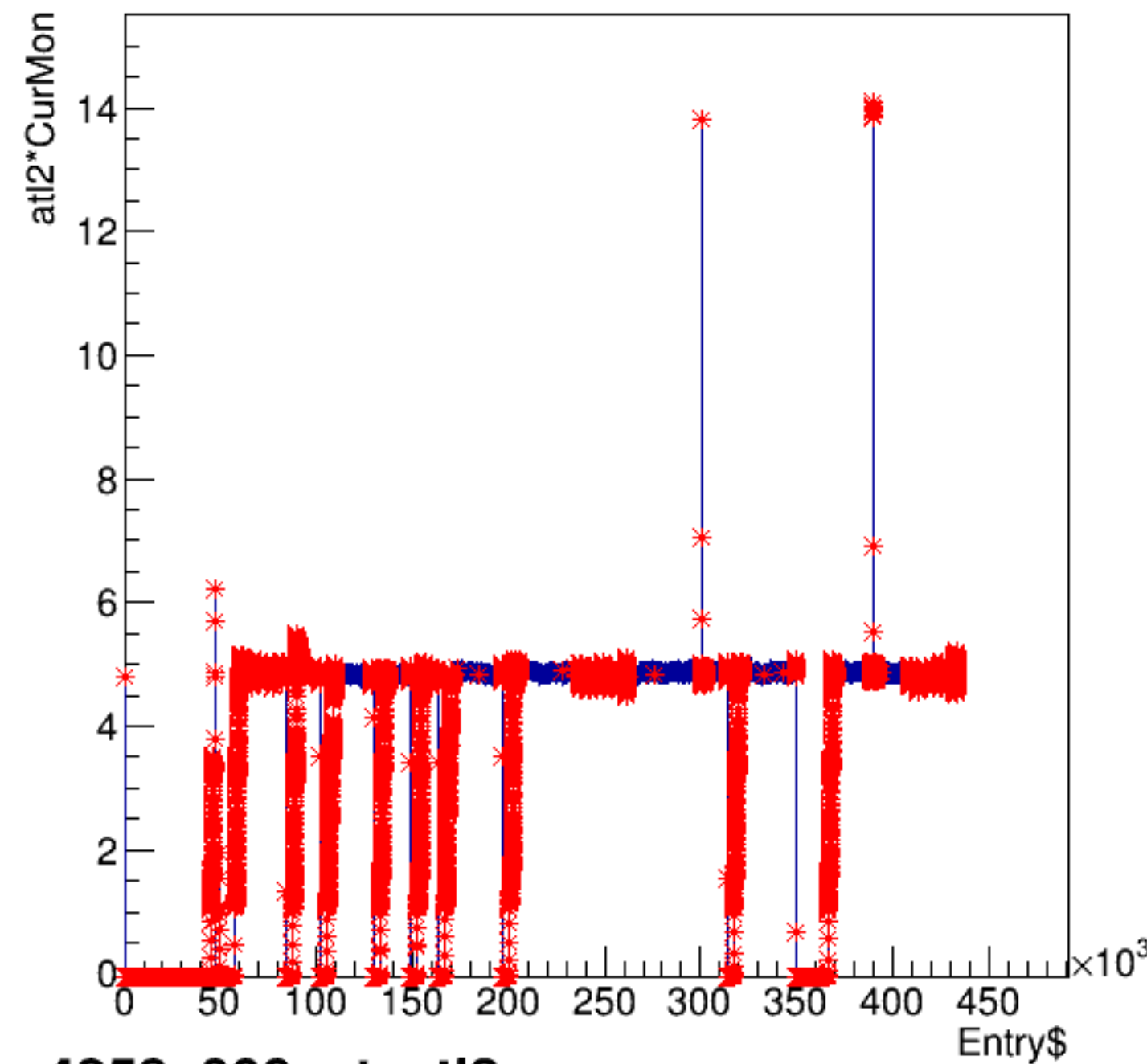
asym_atl1/ppm:pattern_number {ErrorFlag==0}



asym_atl1

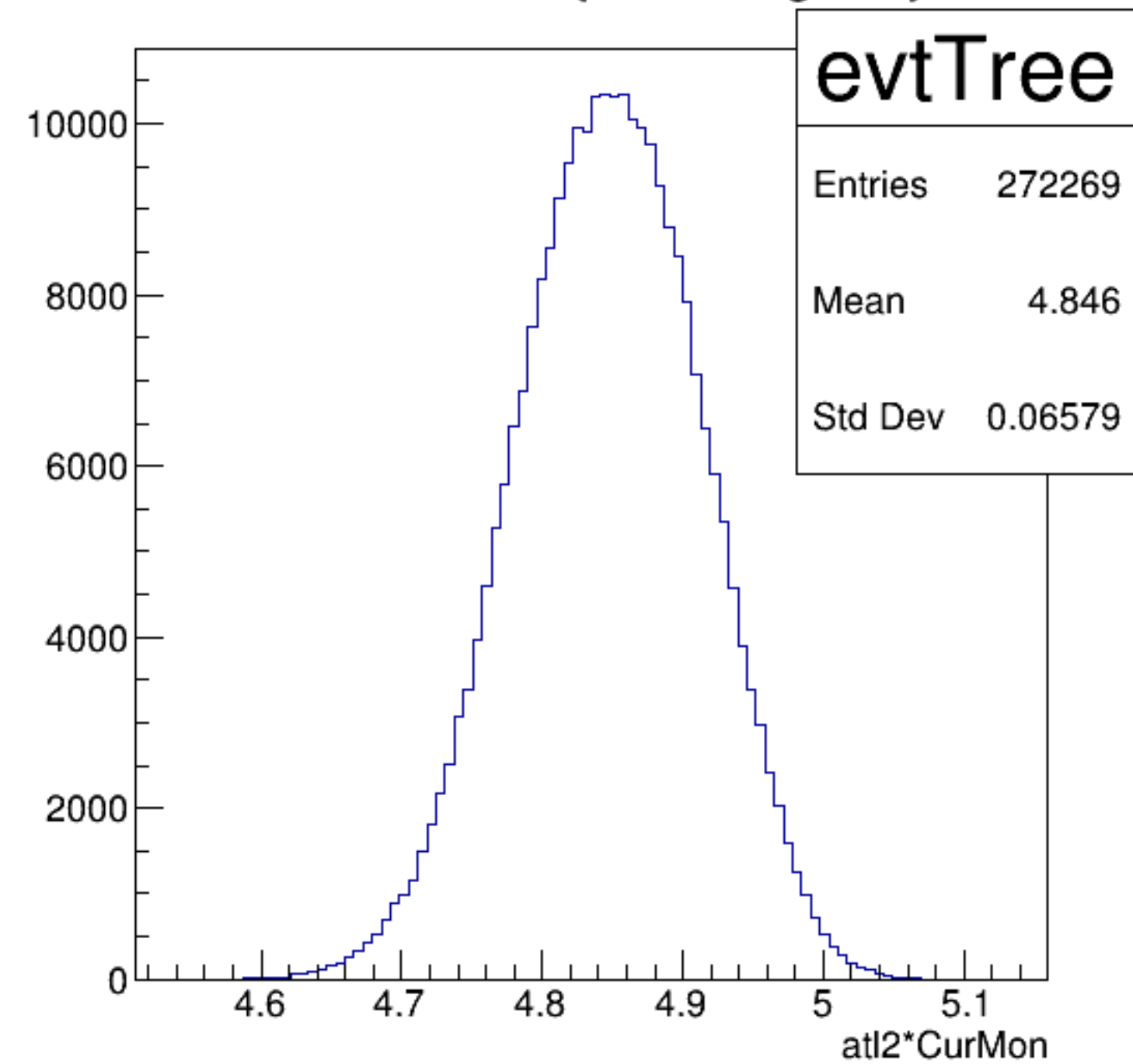


atl2*CurMon:Entry\$



run4258_000_at_atl2.png

atl2*CurMon {ErrorFlag==0}



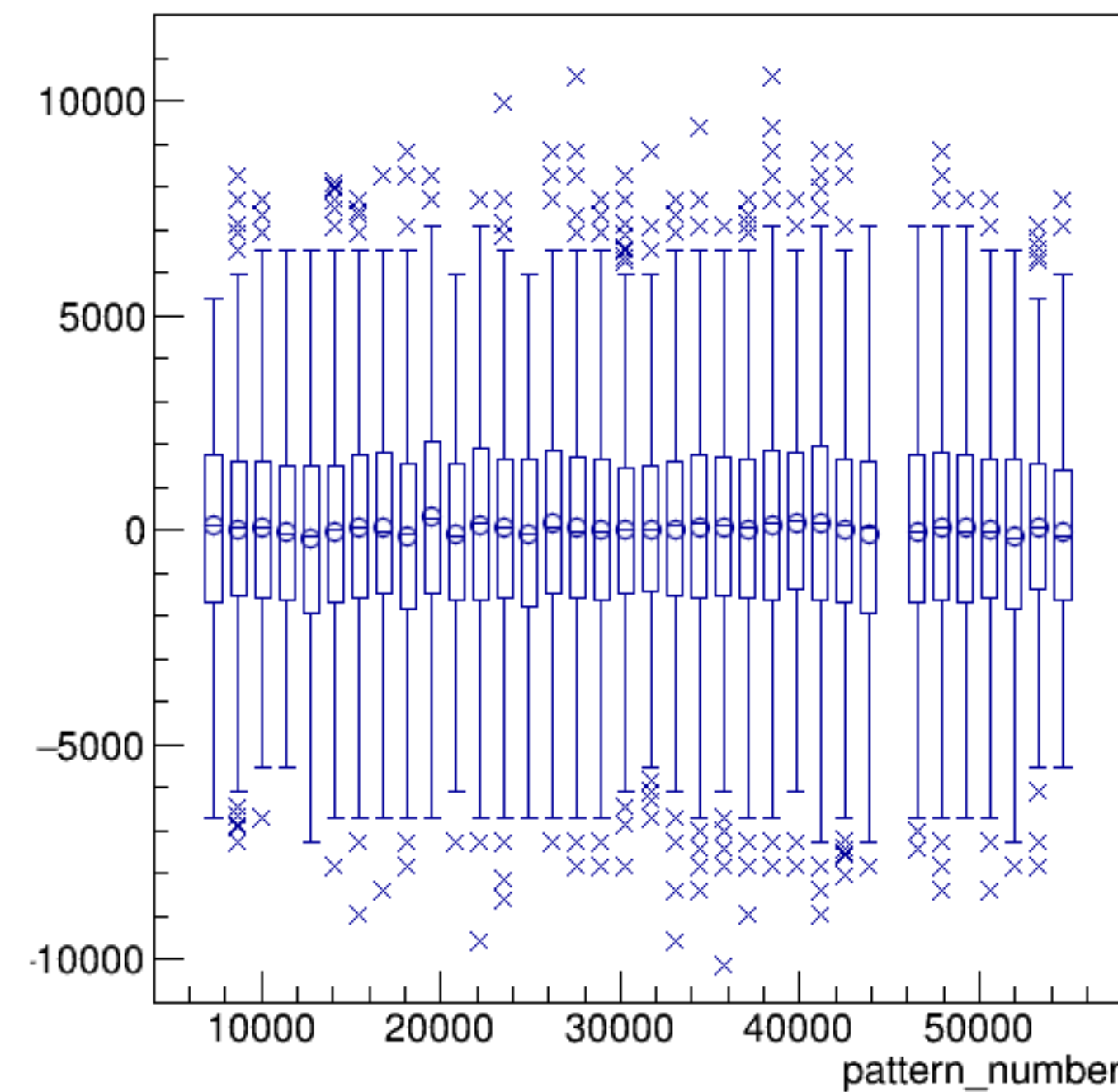
evtTree

Entries 272269

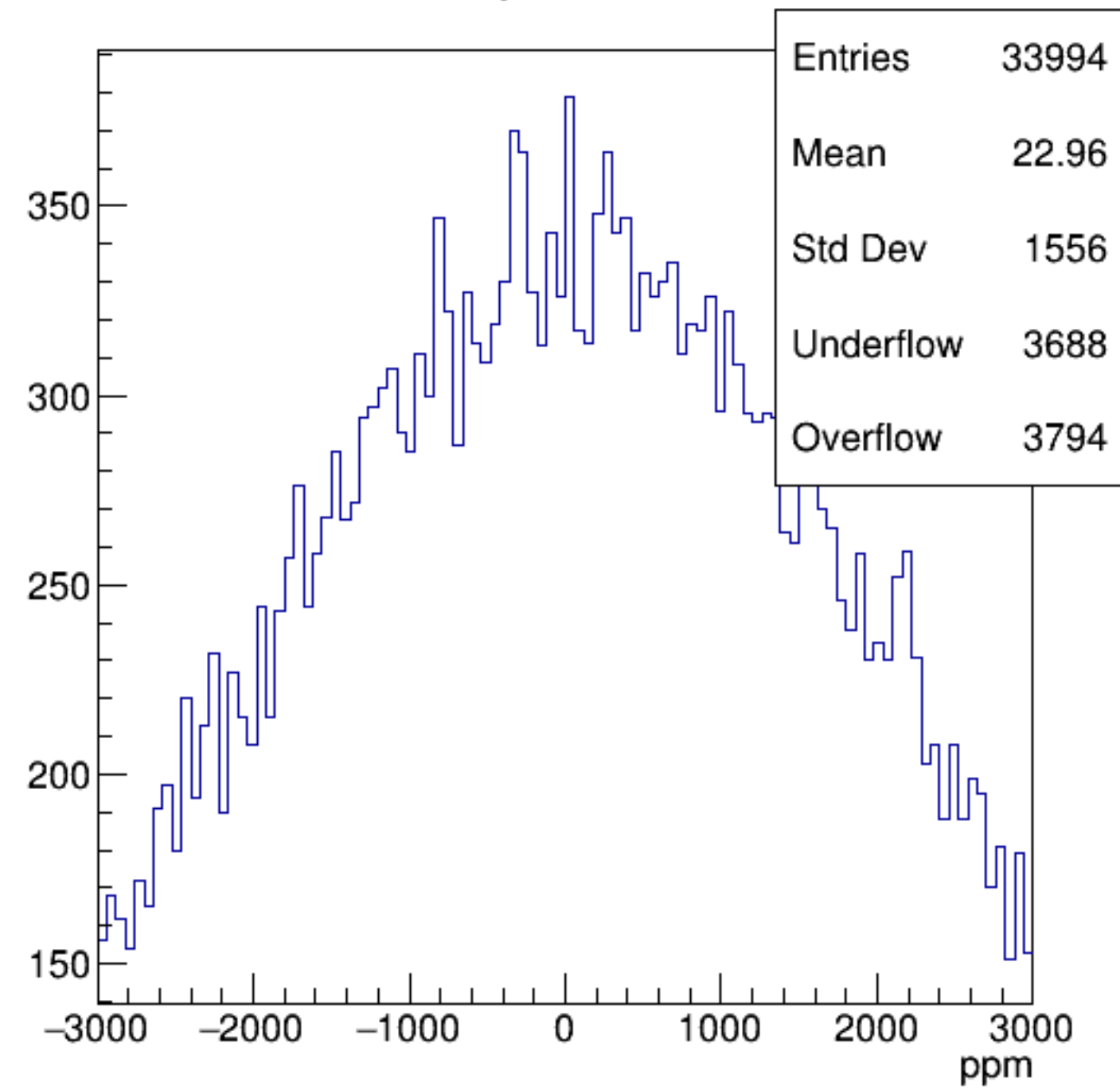
Mean 4.846

Std Dev 0.06579

asym_atl2/ppm:pattern_number {ErrorFlag==0}

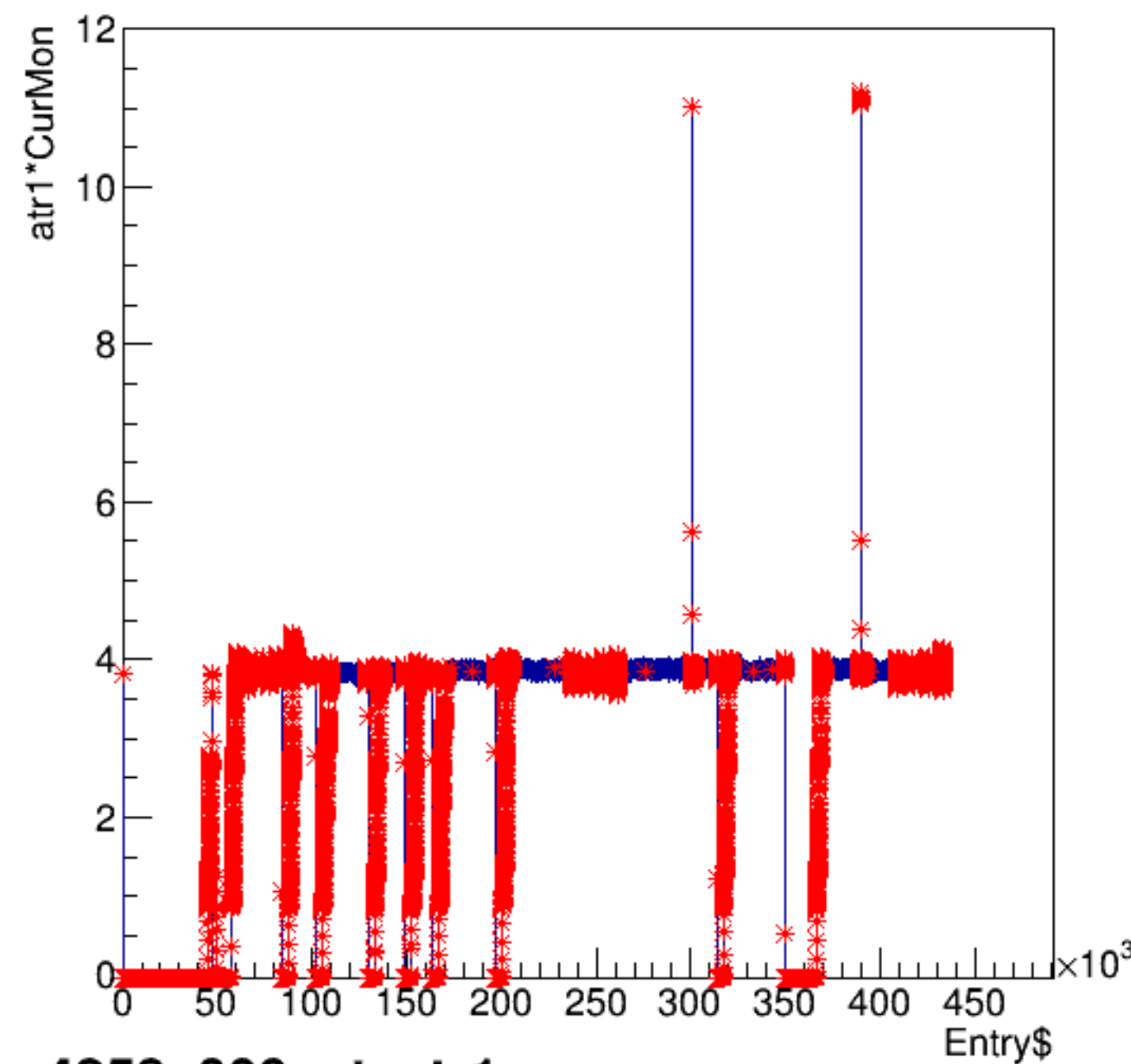


asym_atl2



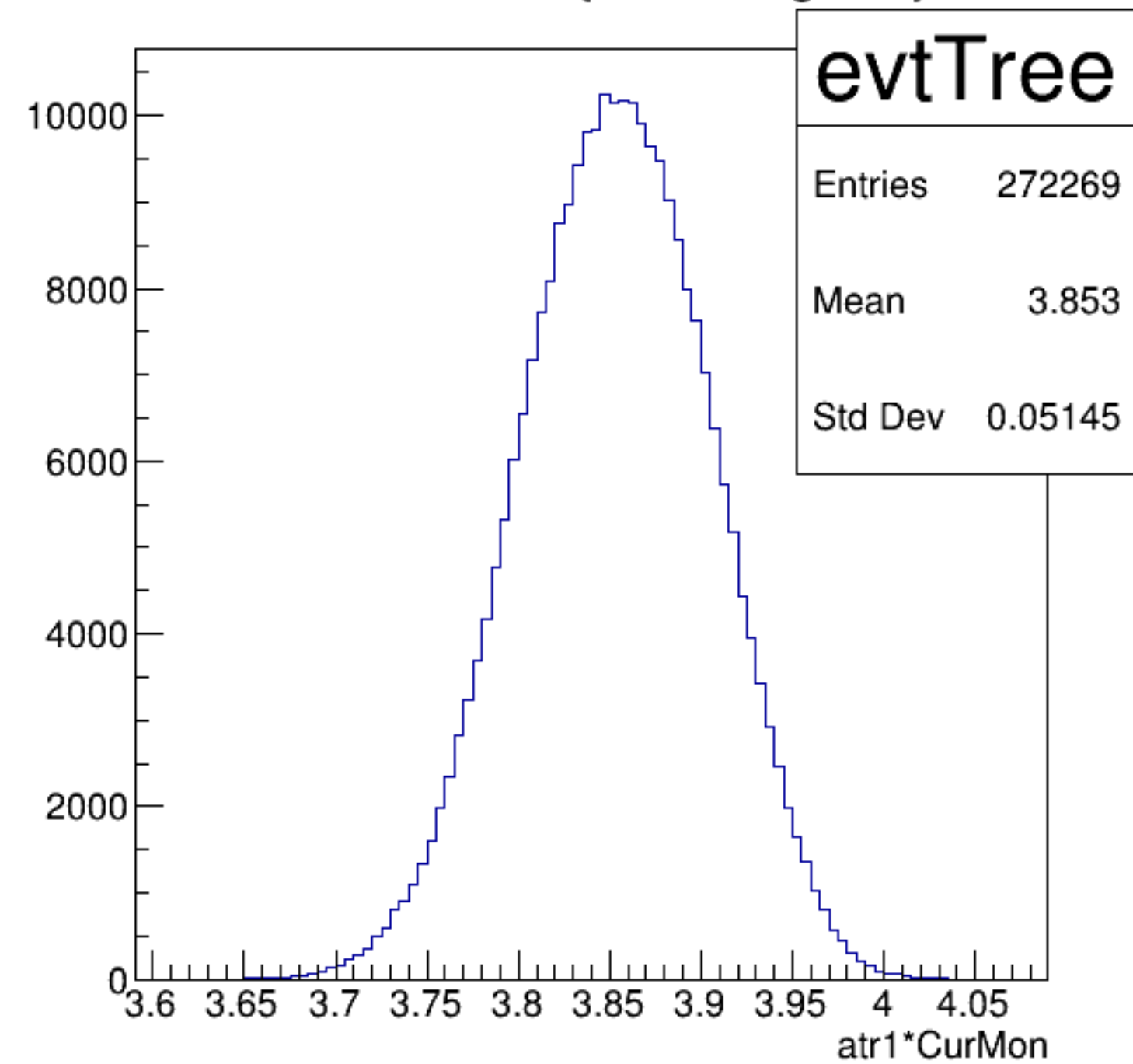
Entries	33994
Mean	22.96
Std Dev	1556
Underflow	3688
Overflow	3794

atr1*CurMon:Entry\$

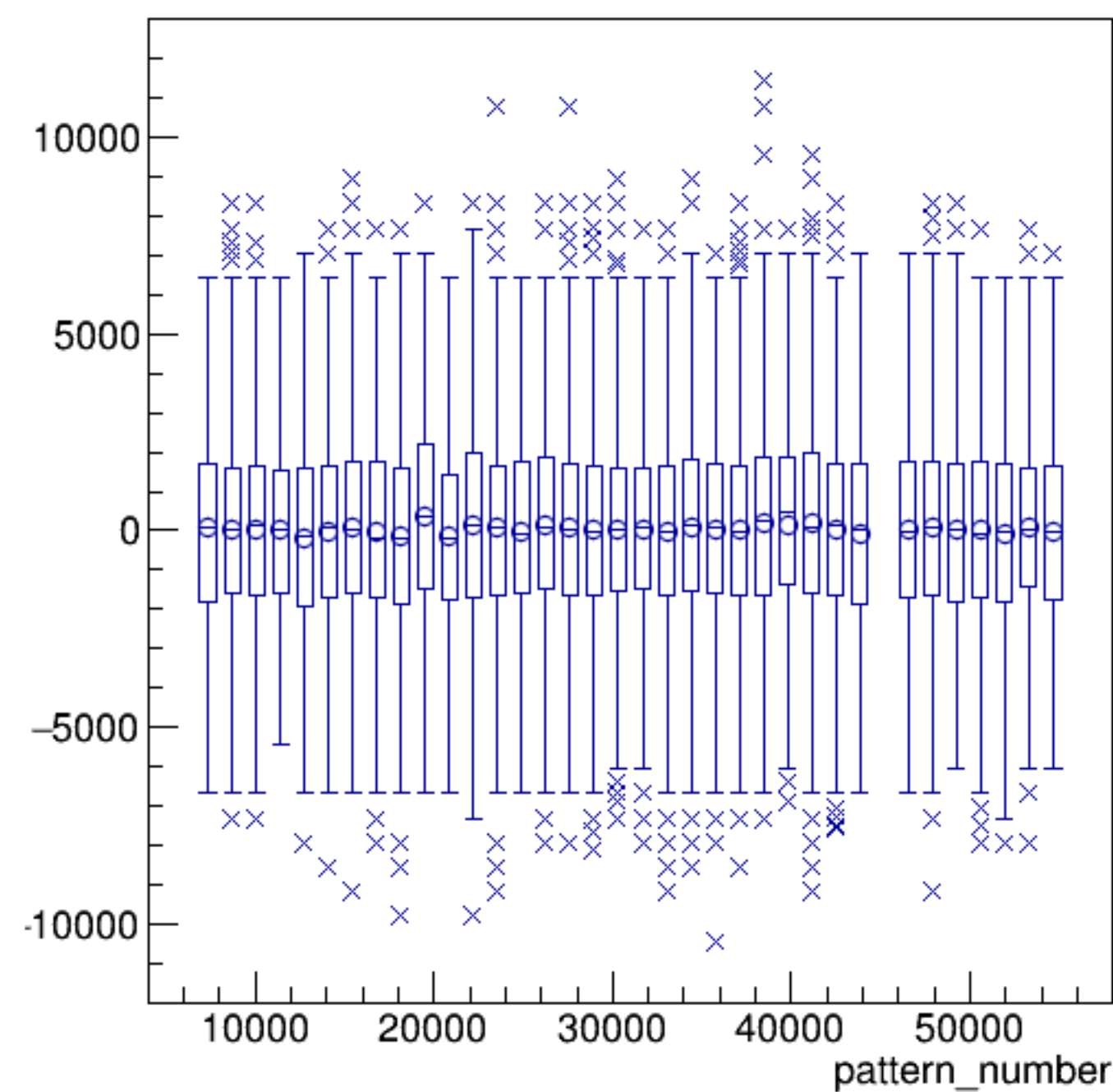


run4258_000_at_atr1.png

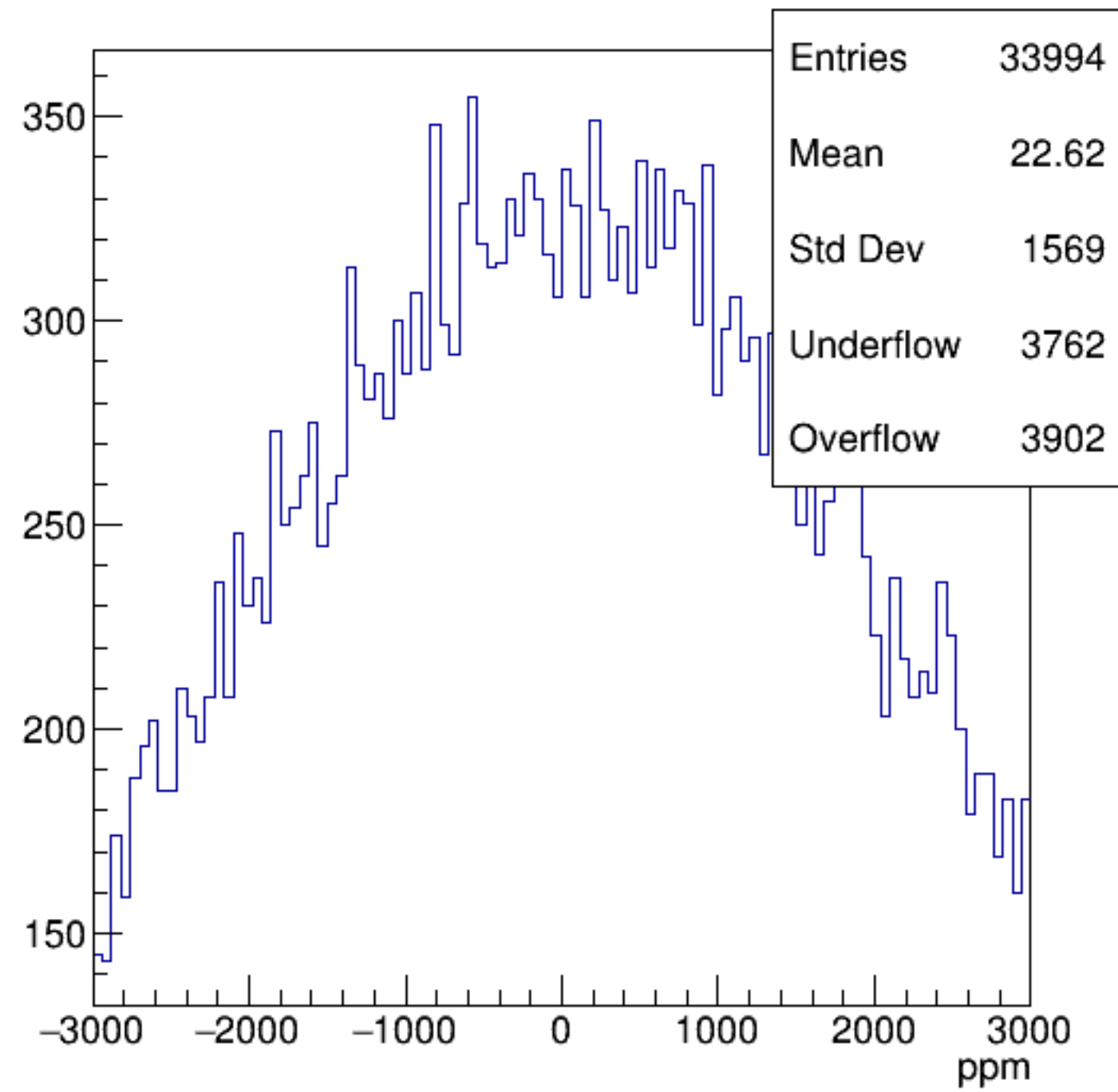
atr1*CurMon {ErrorFlag==0}



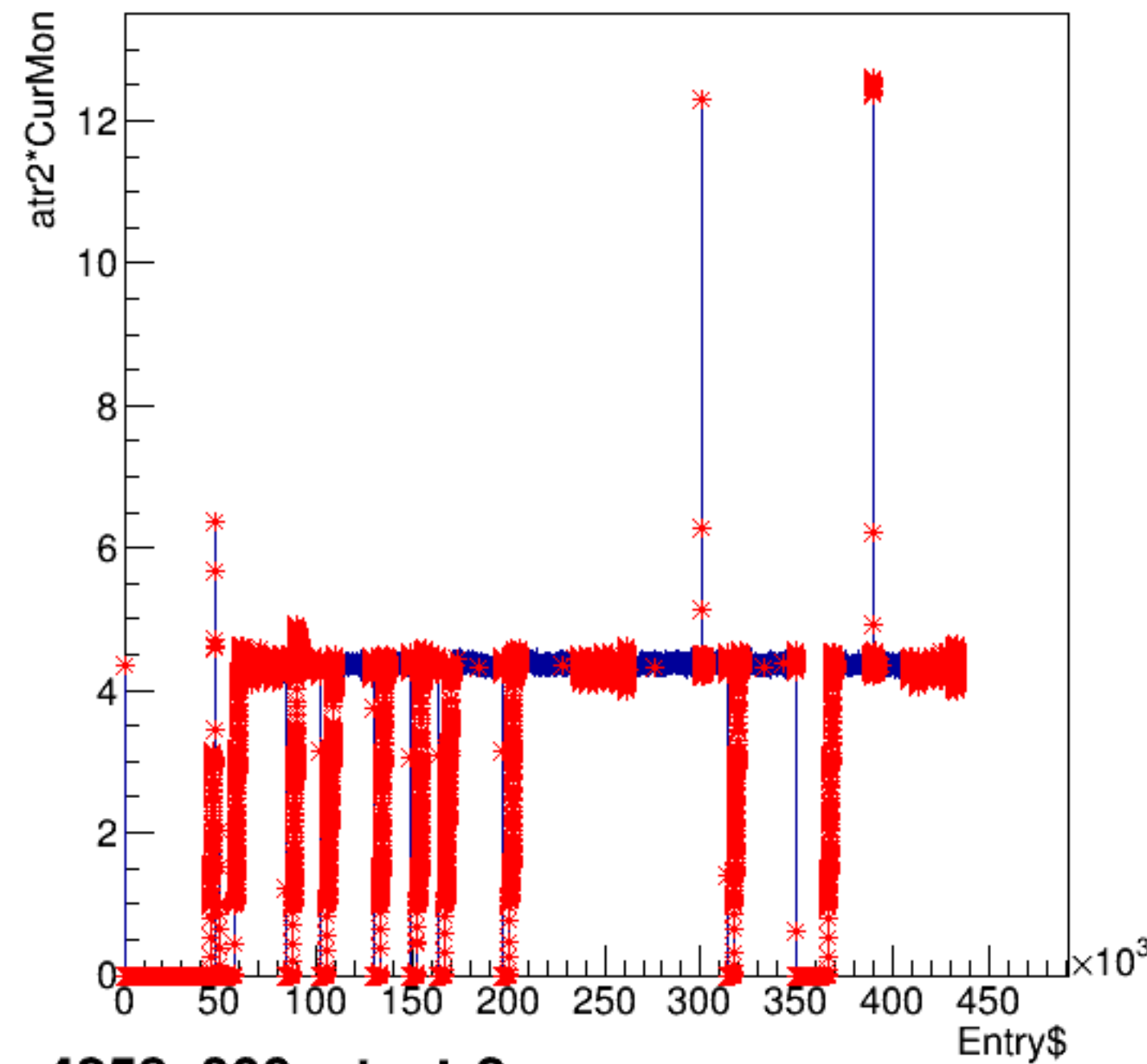
asym_atr1/ppm:pattern_number {ErrorFlag==0}



asym_atr1

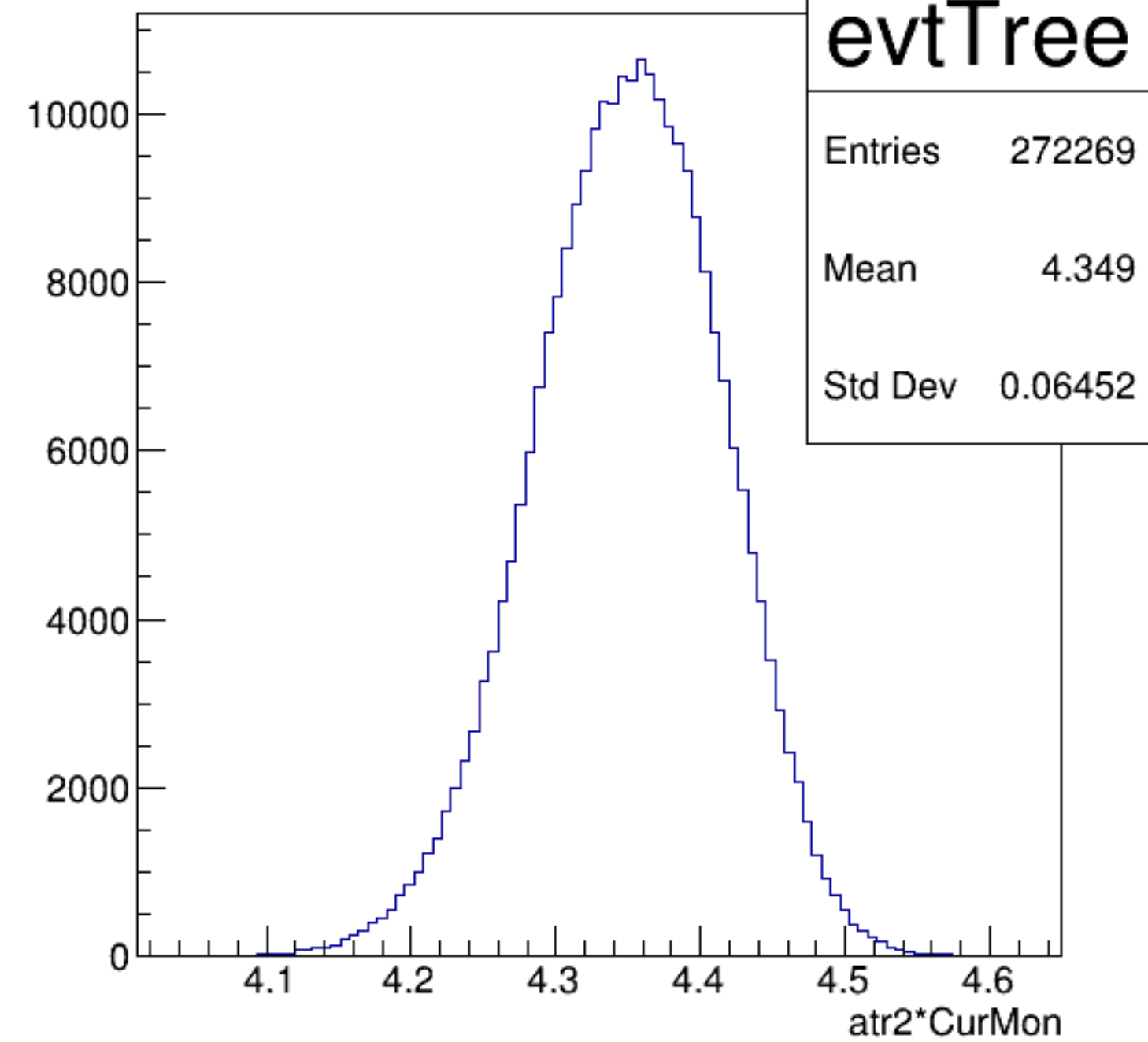


atr2*CurMon:Entry\$

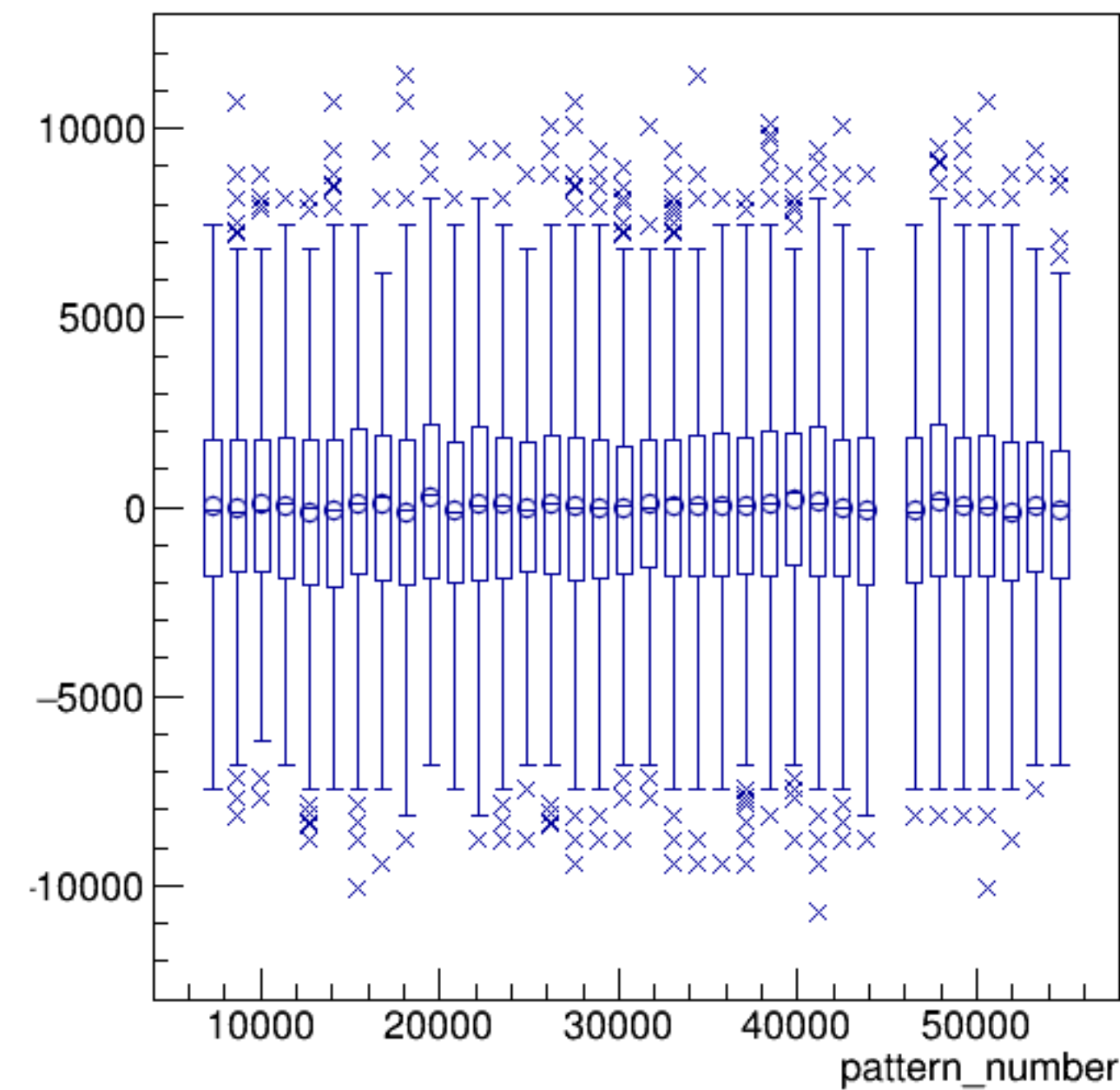


run4258_000_at_atr2.png

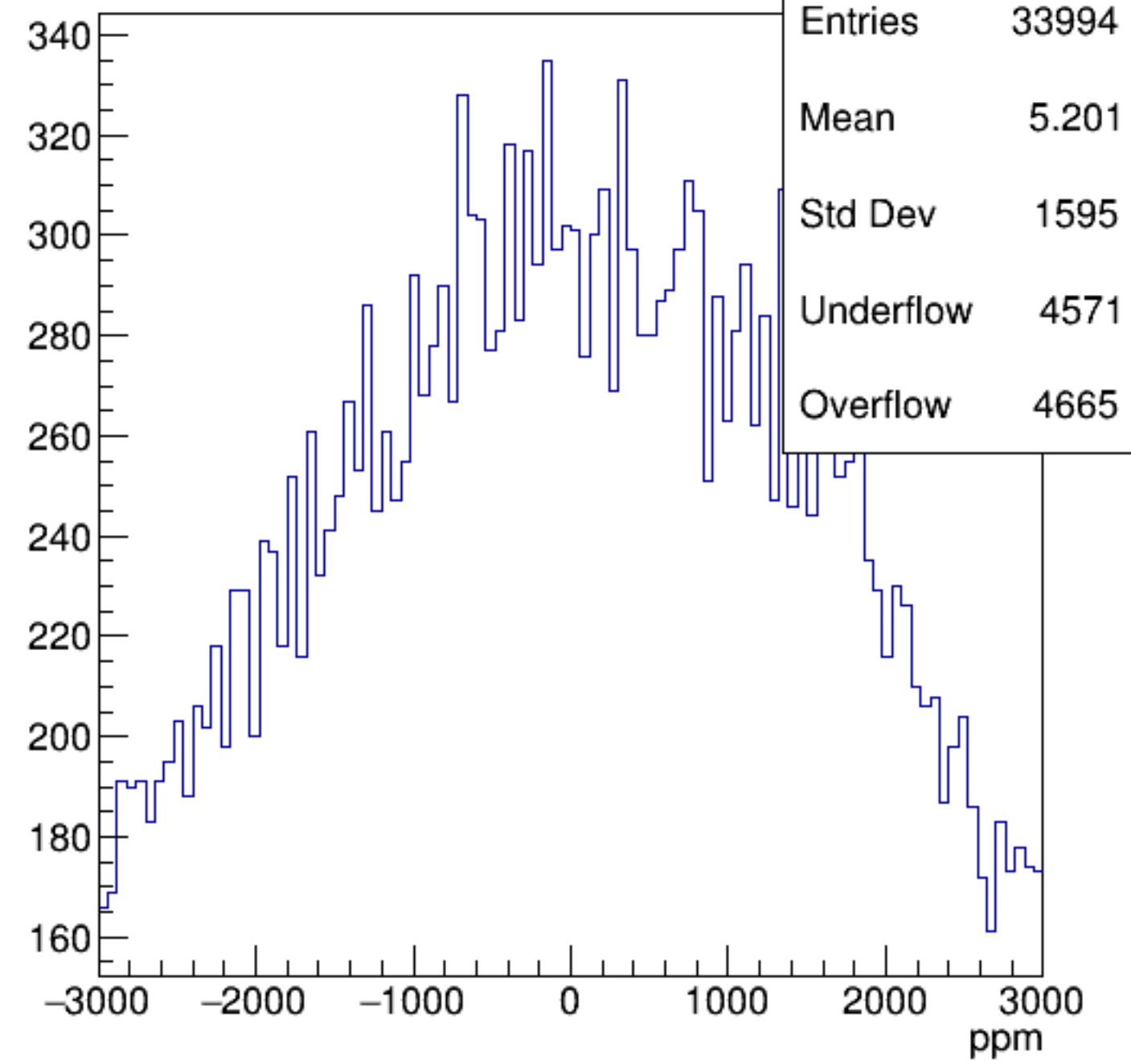
atr2*CurMon {ErrorFlag==0}



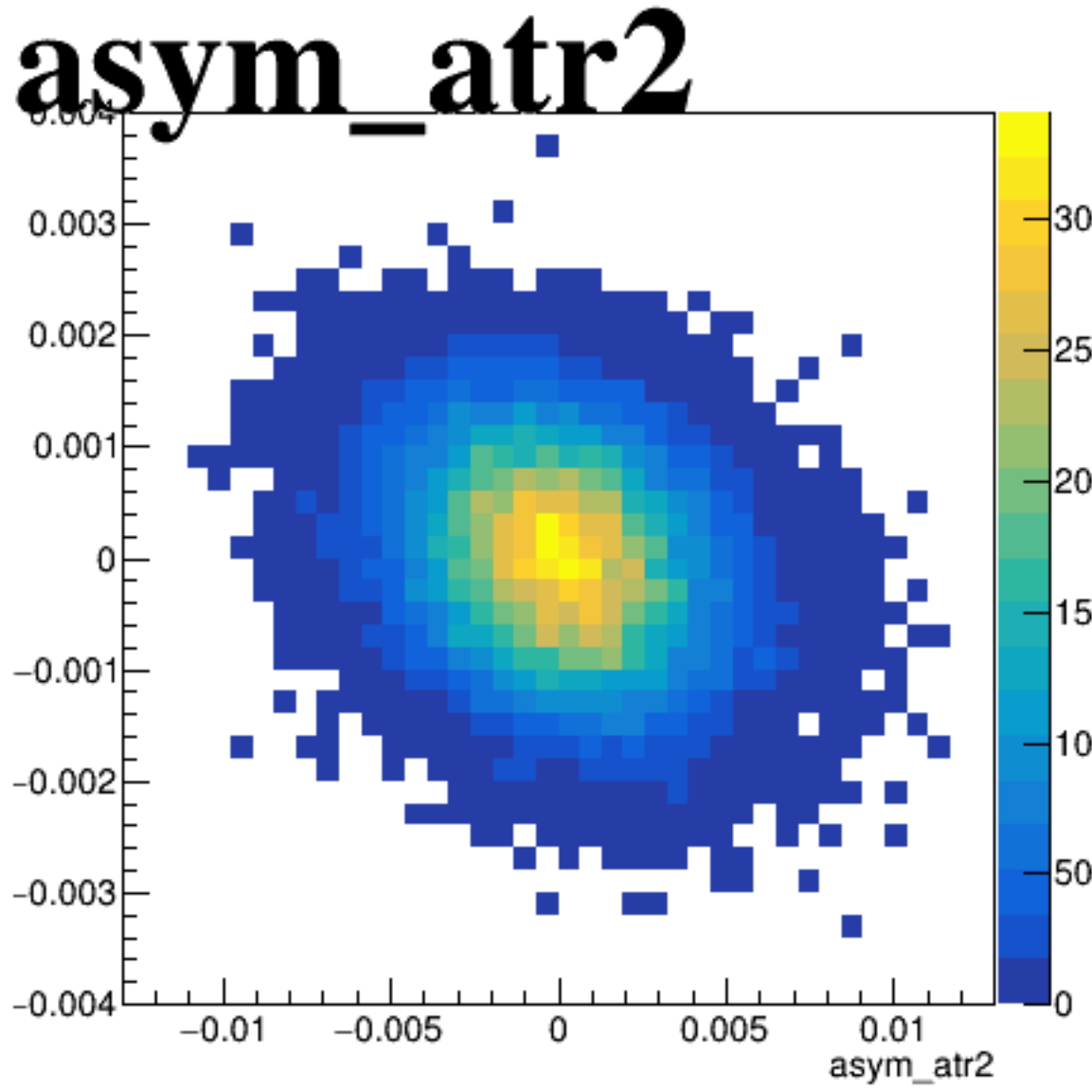
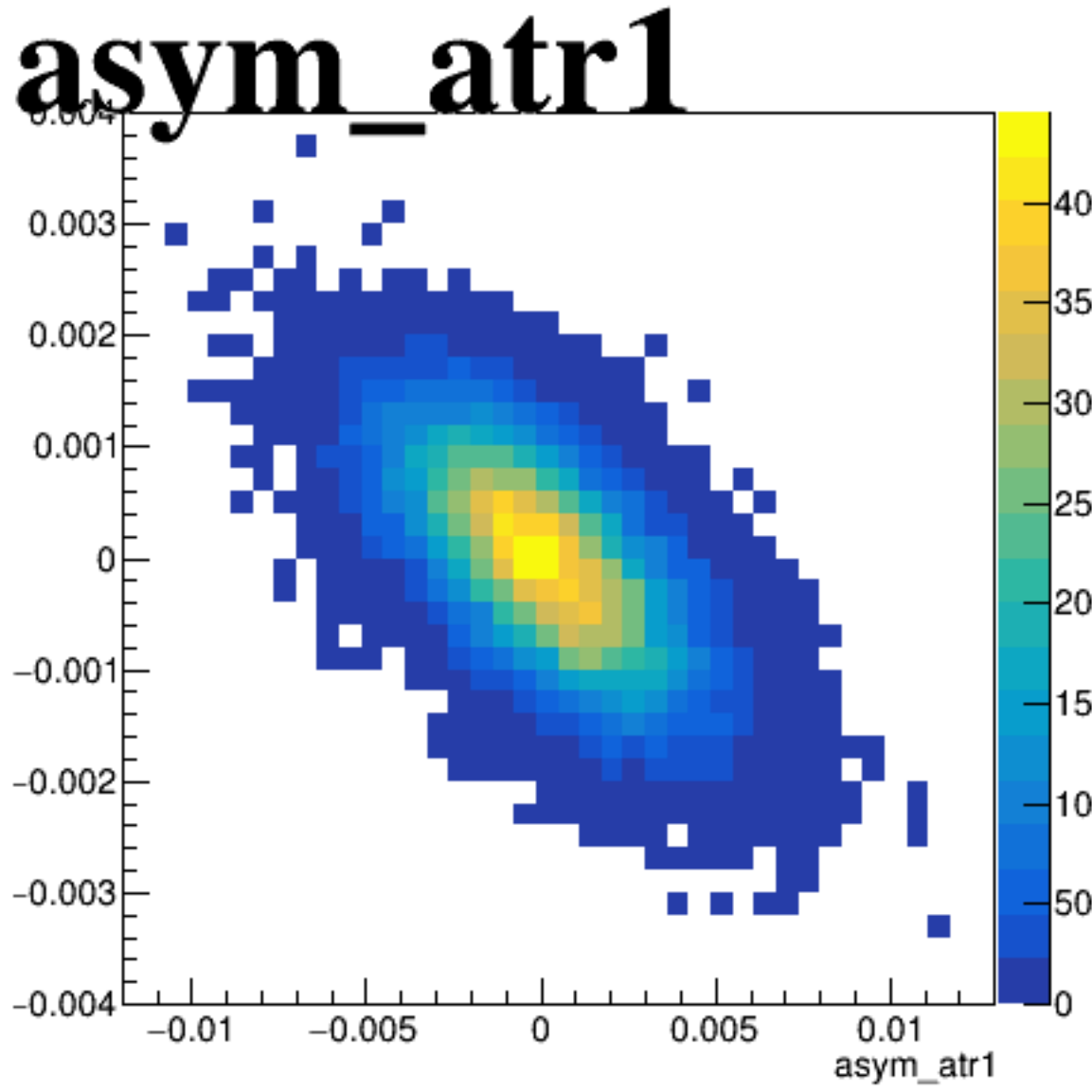
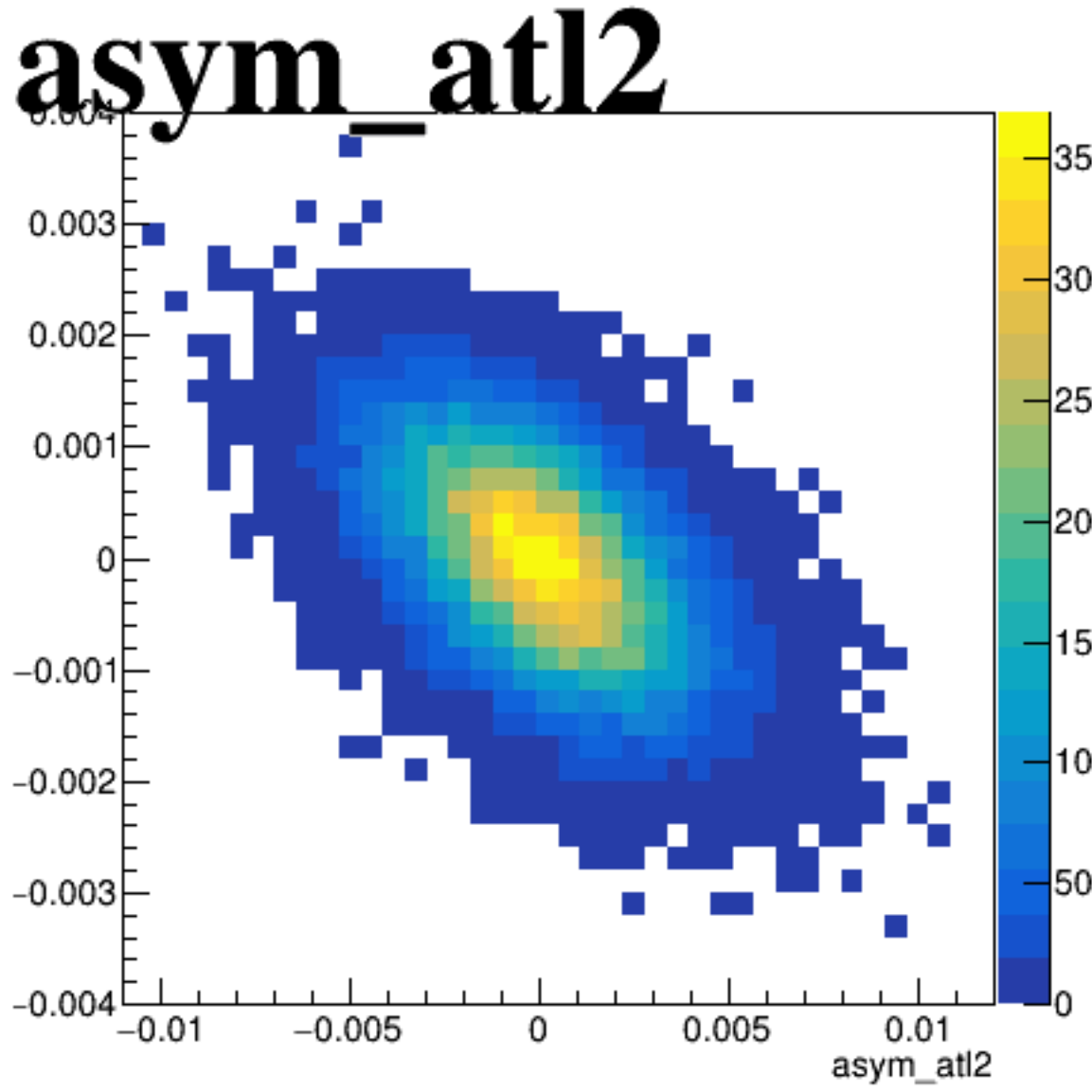
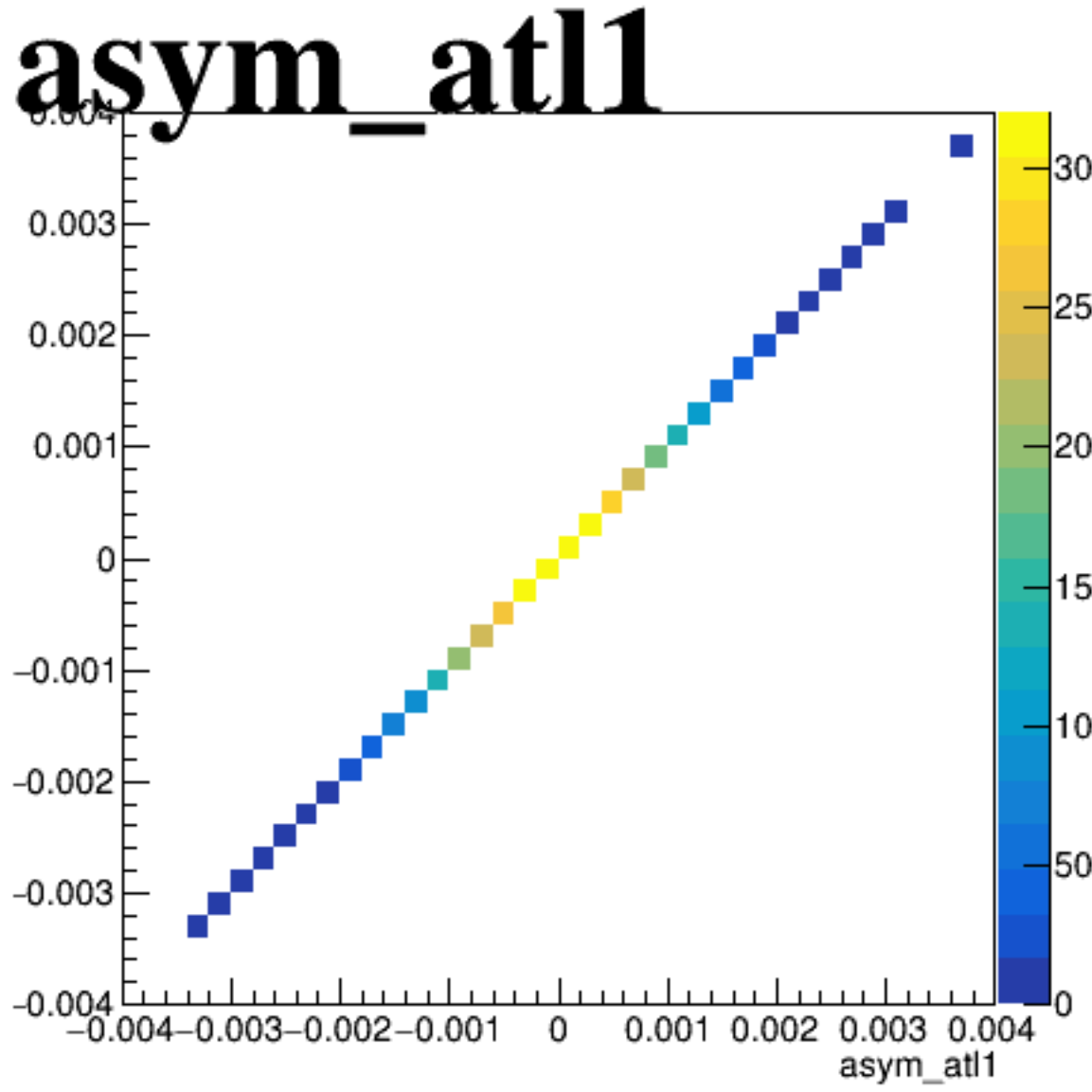
asym_atr2/ppm:pattern_number {ErrorFlag==0}



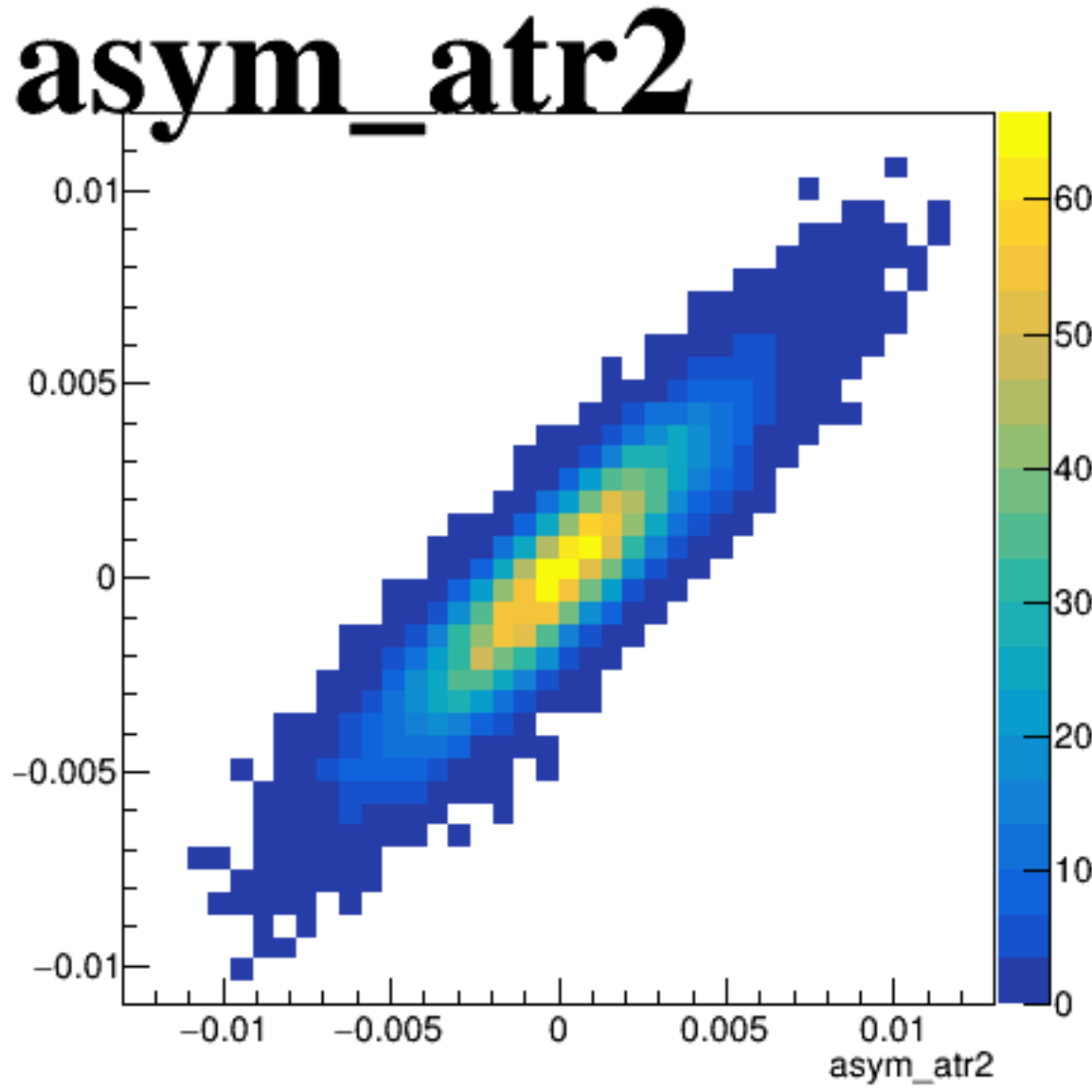
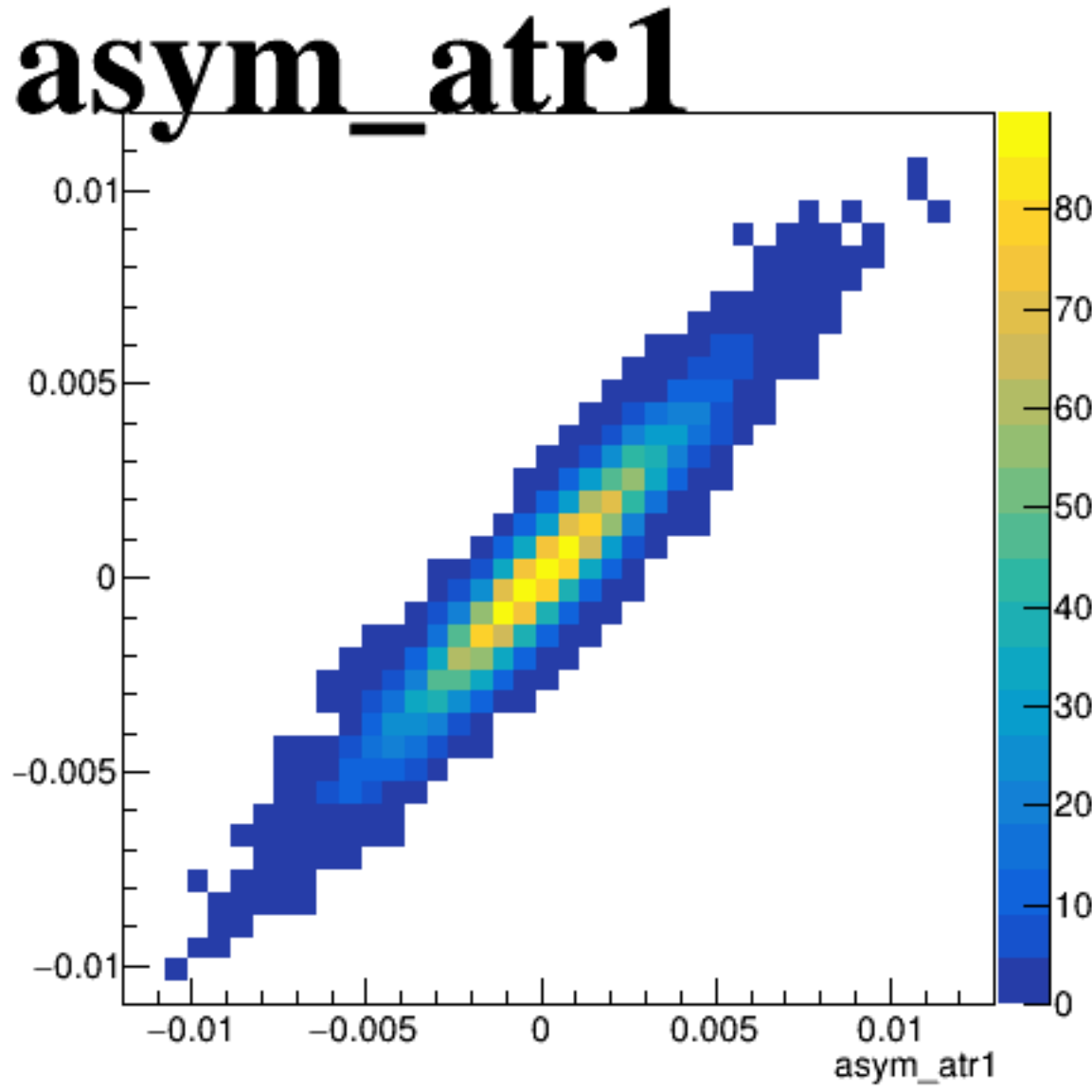
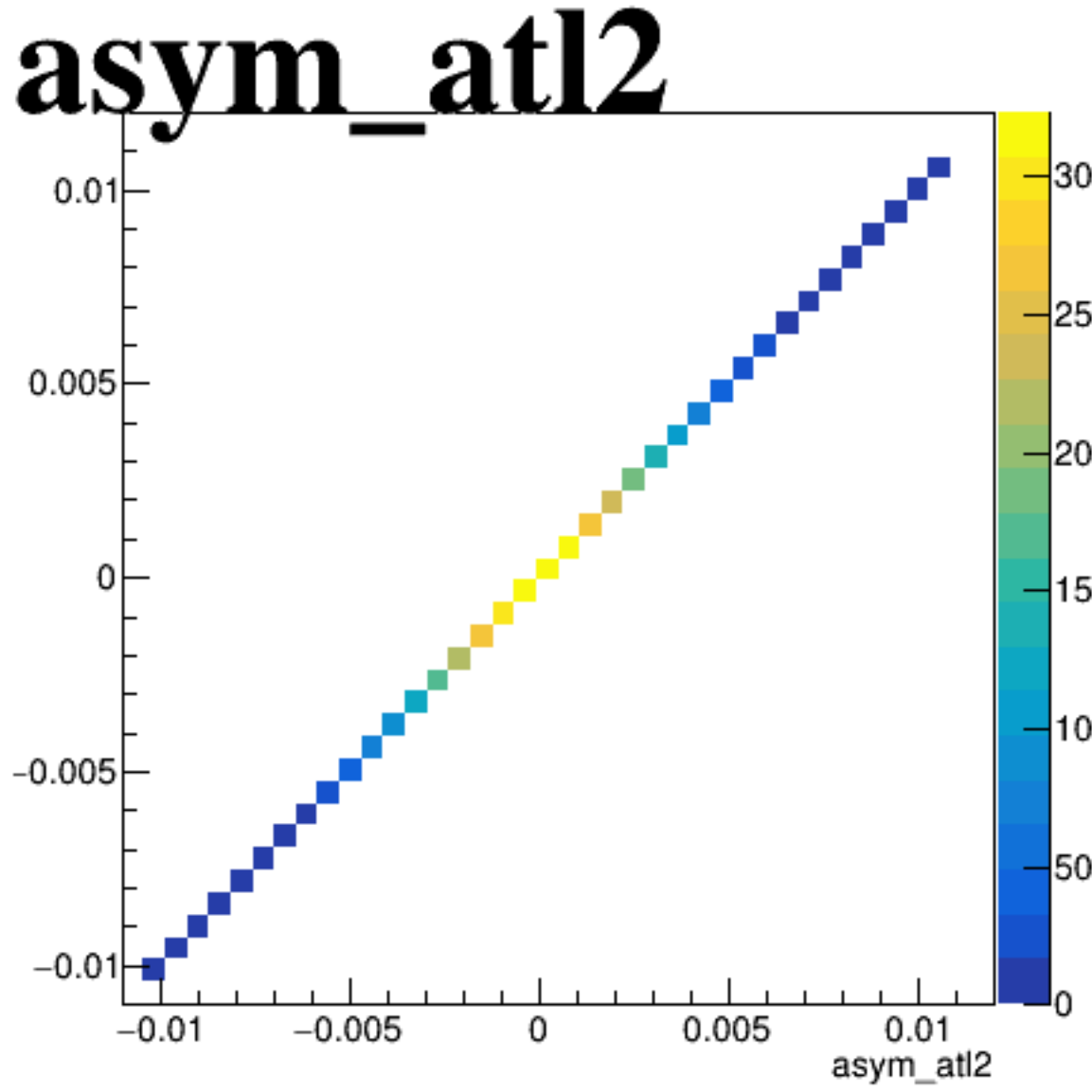
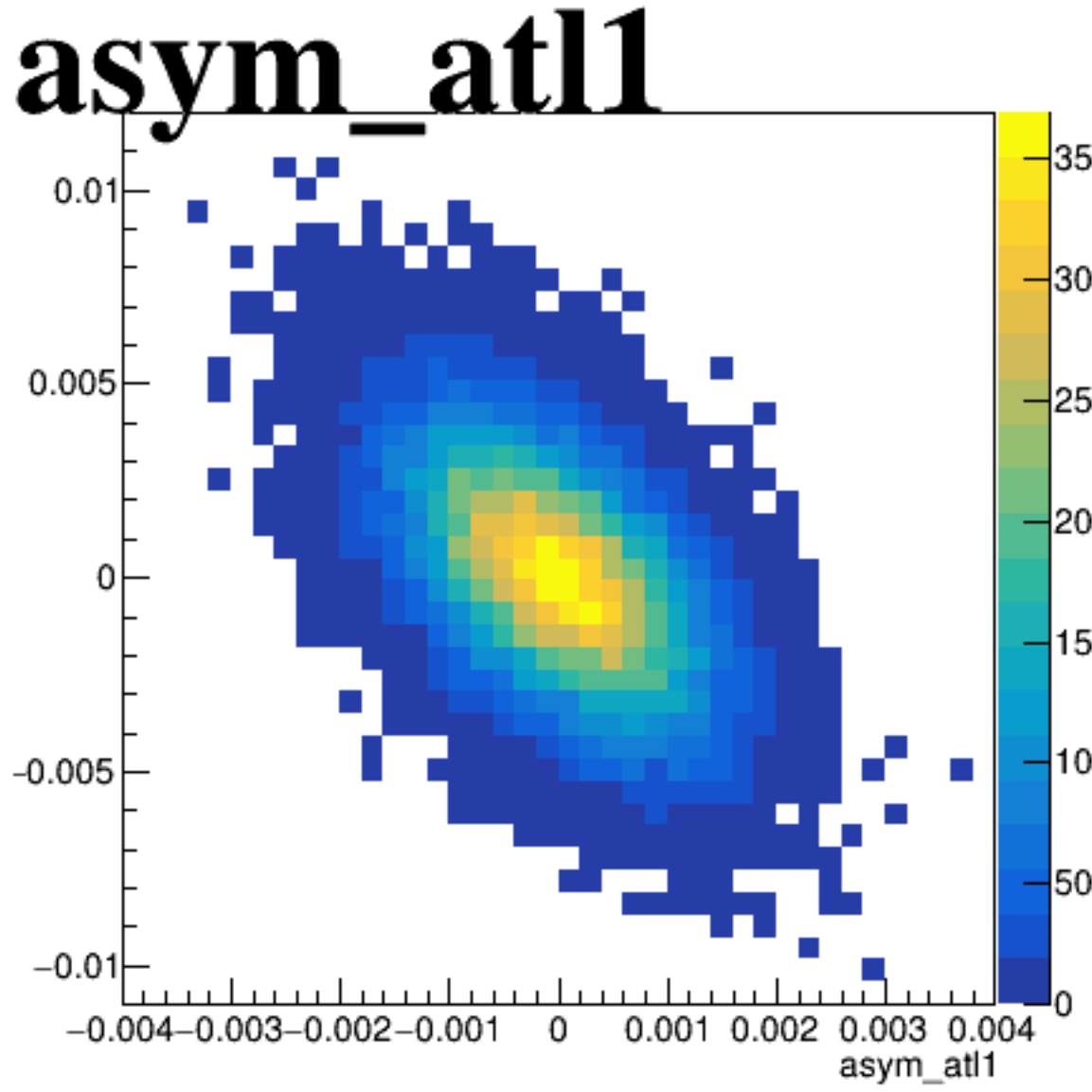
asym_atr2



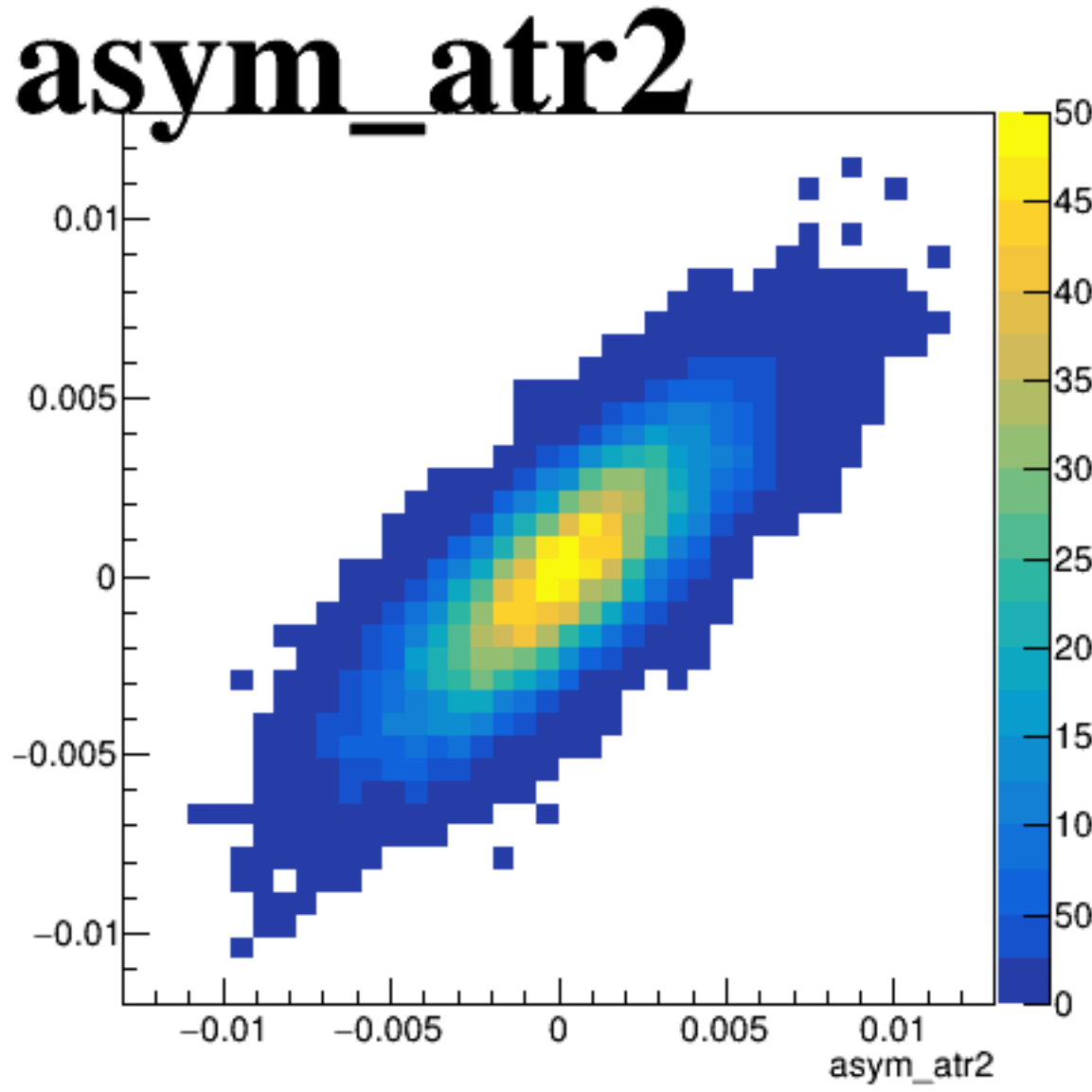
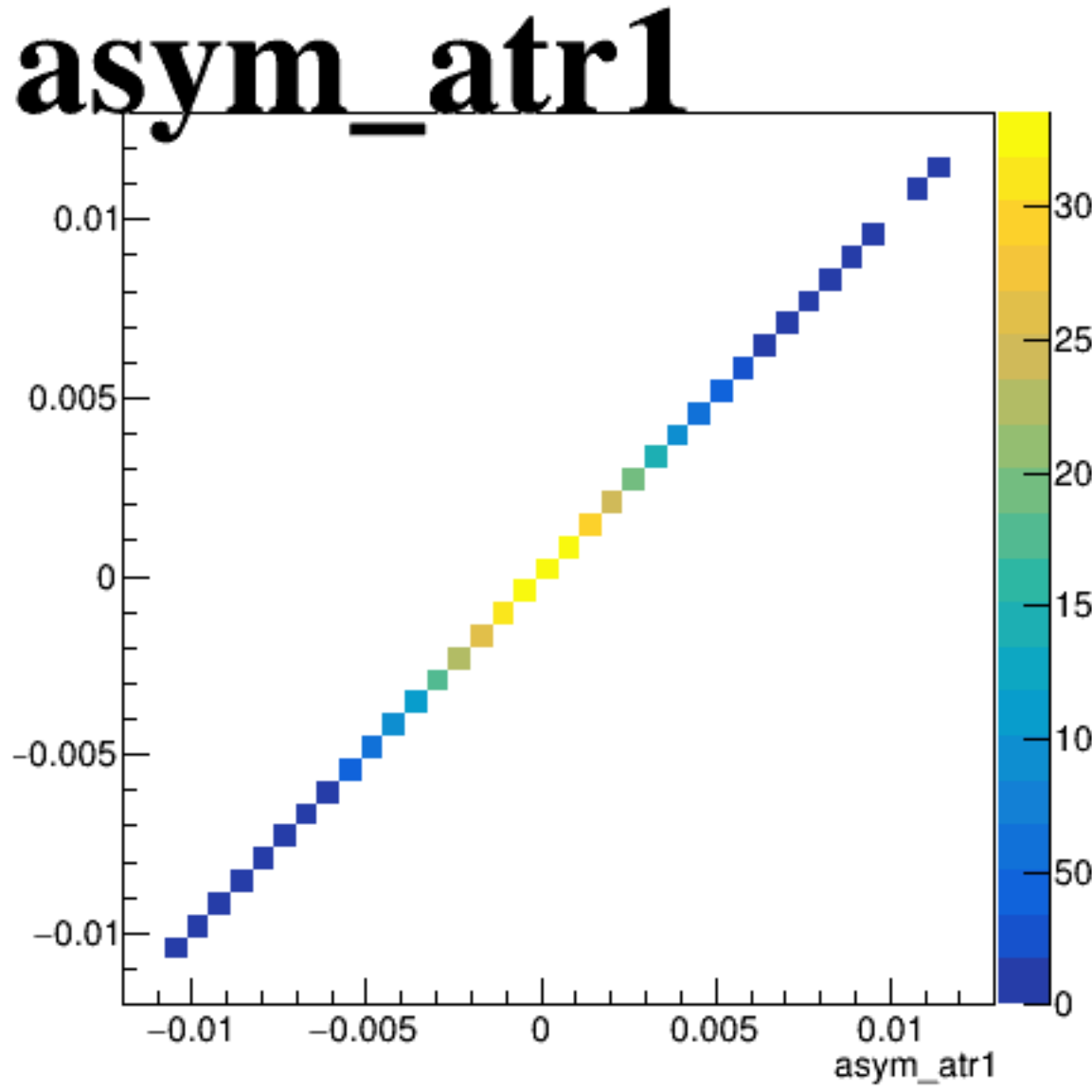
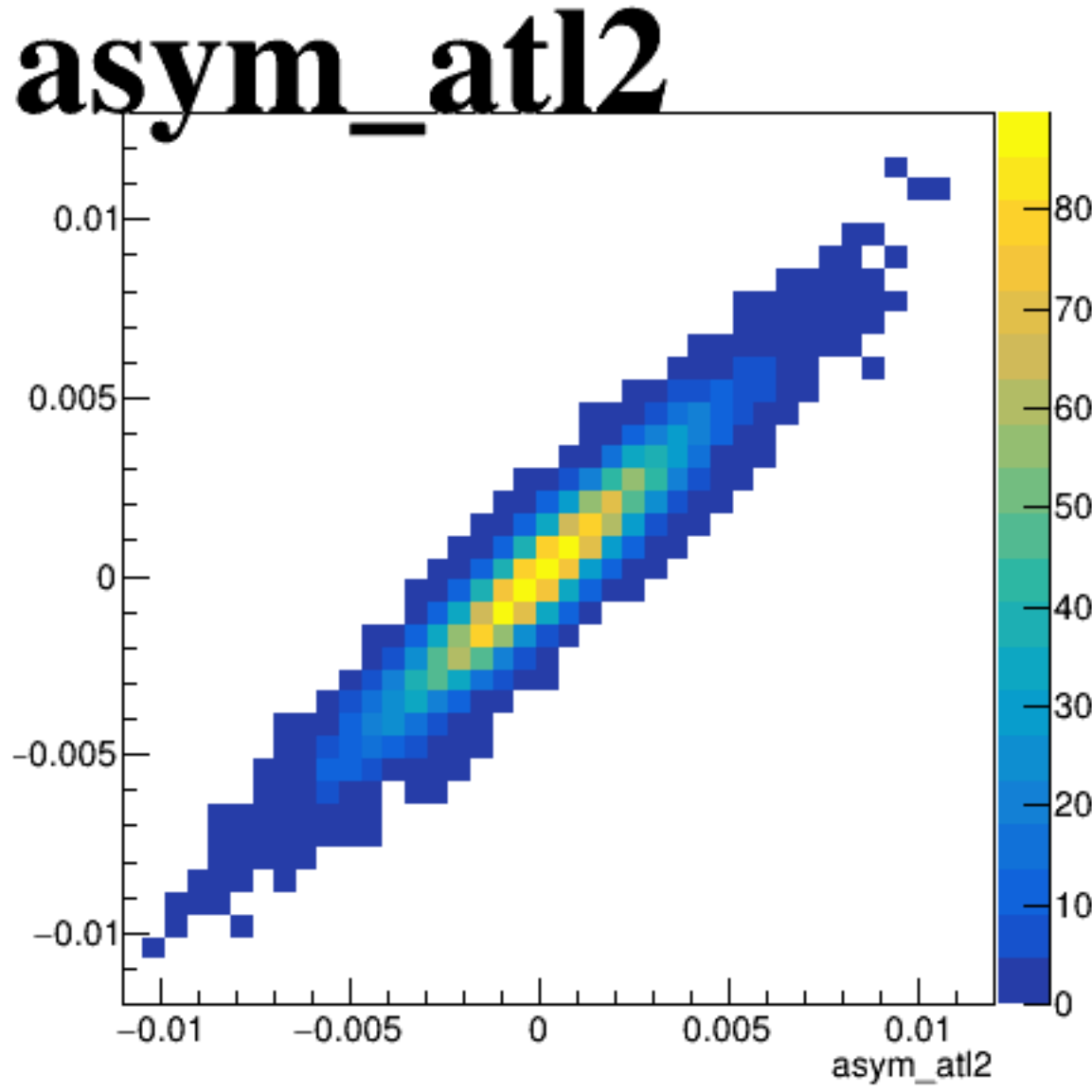
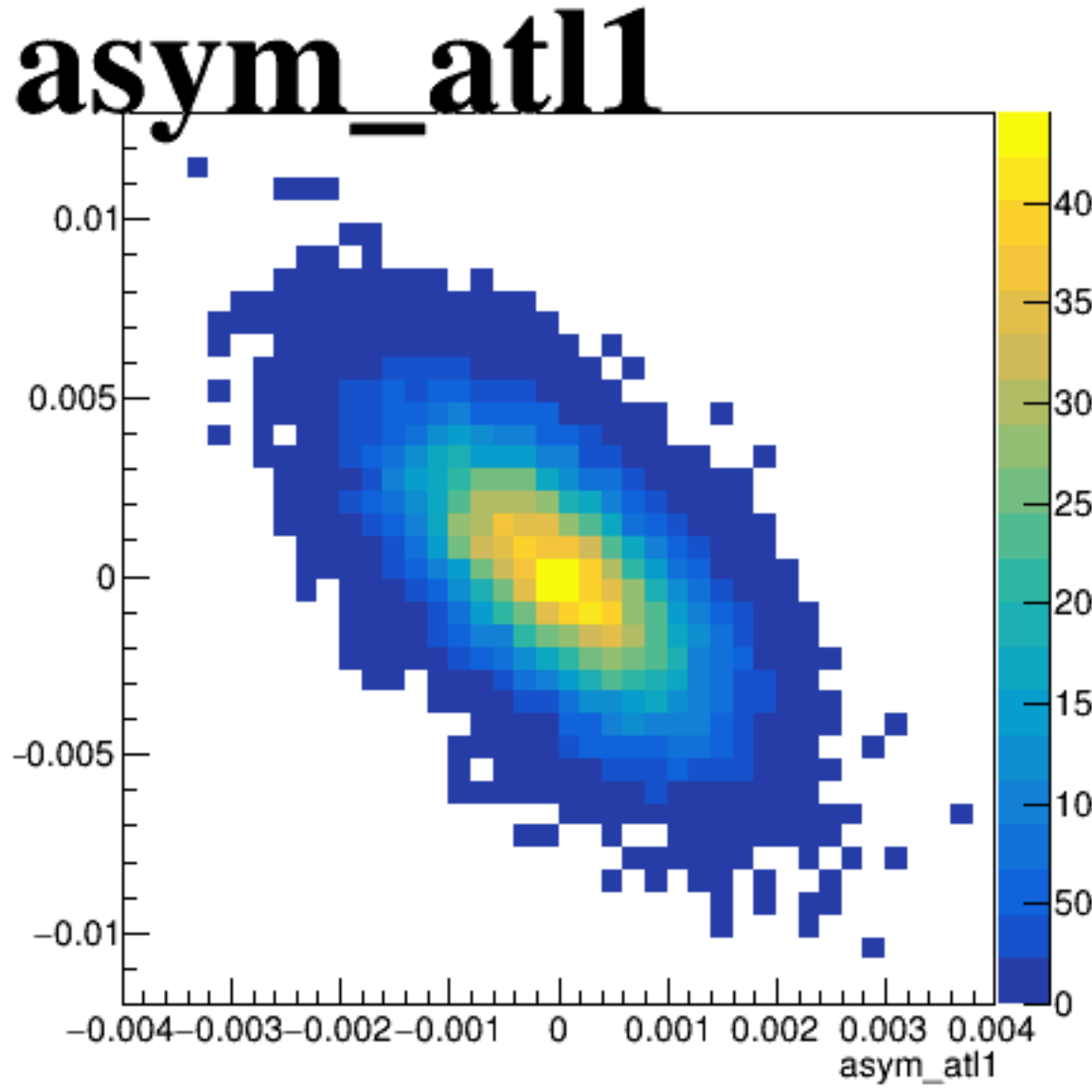
asym_atl1



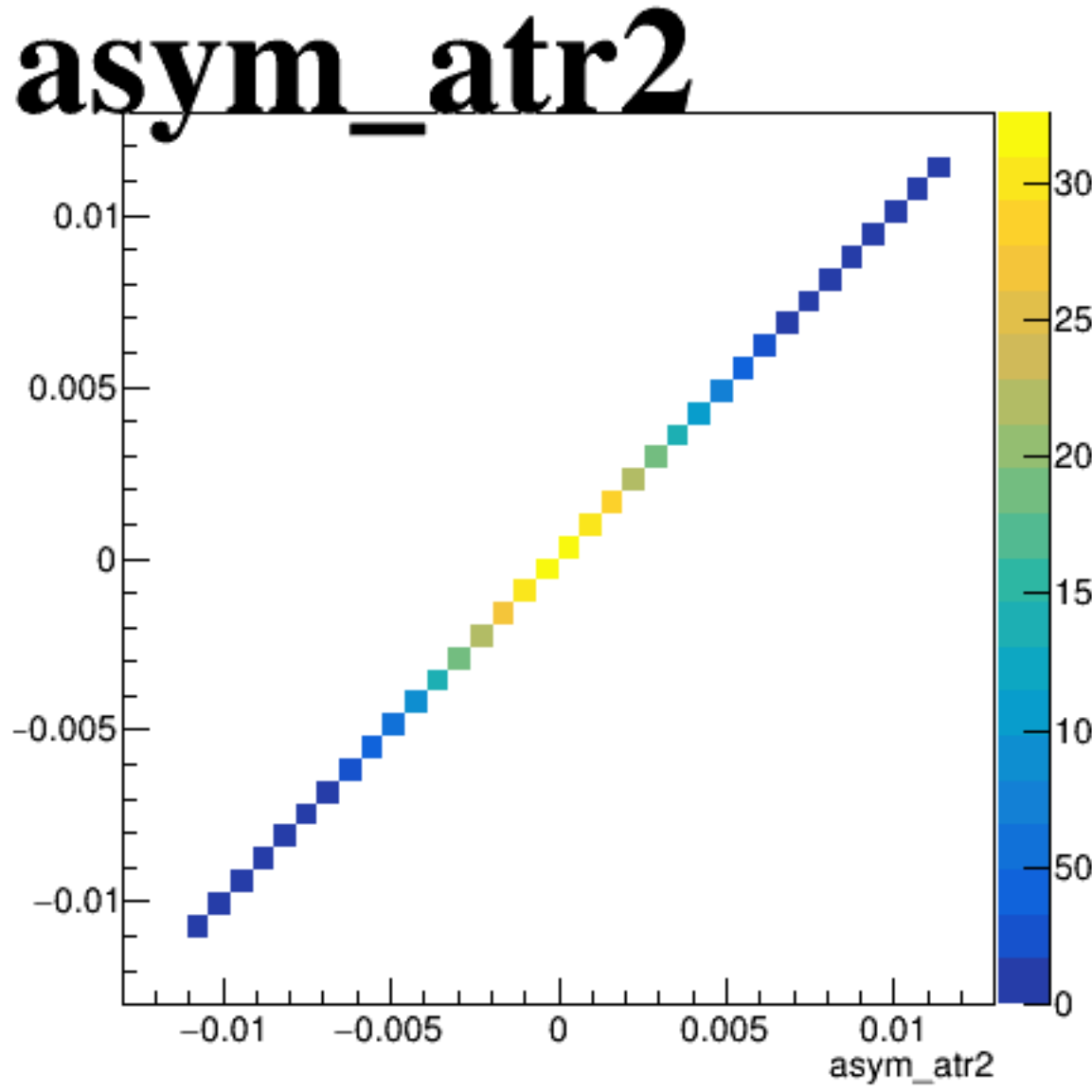
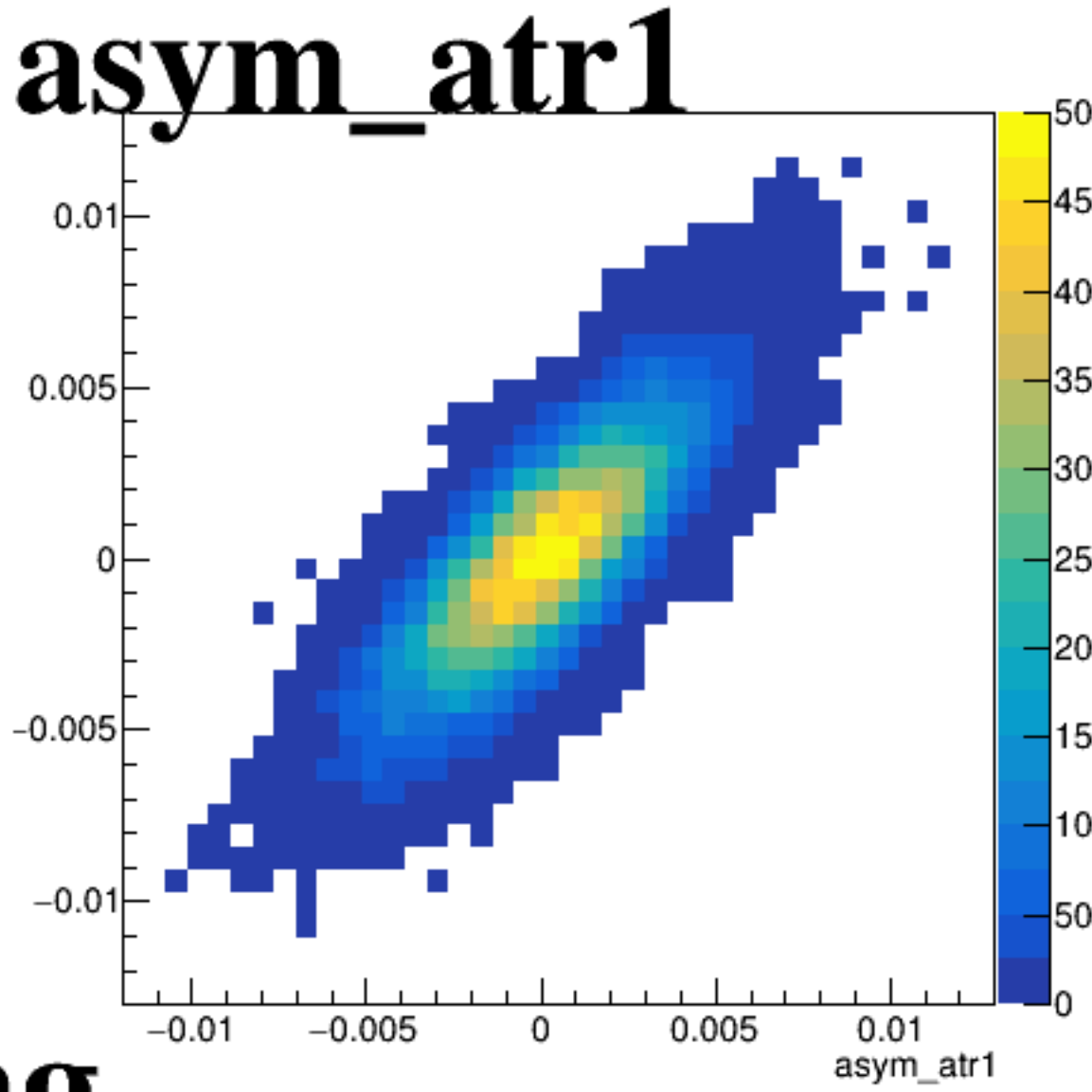
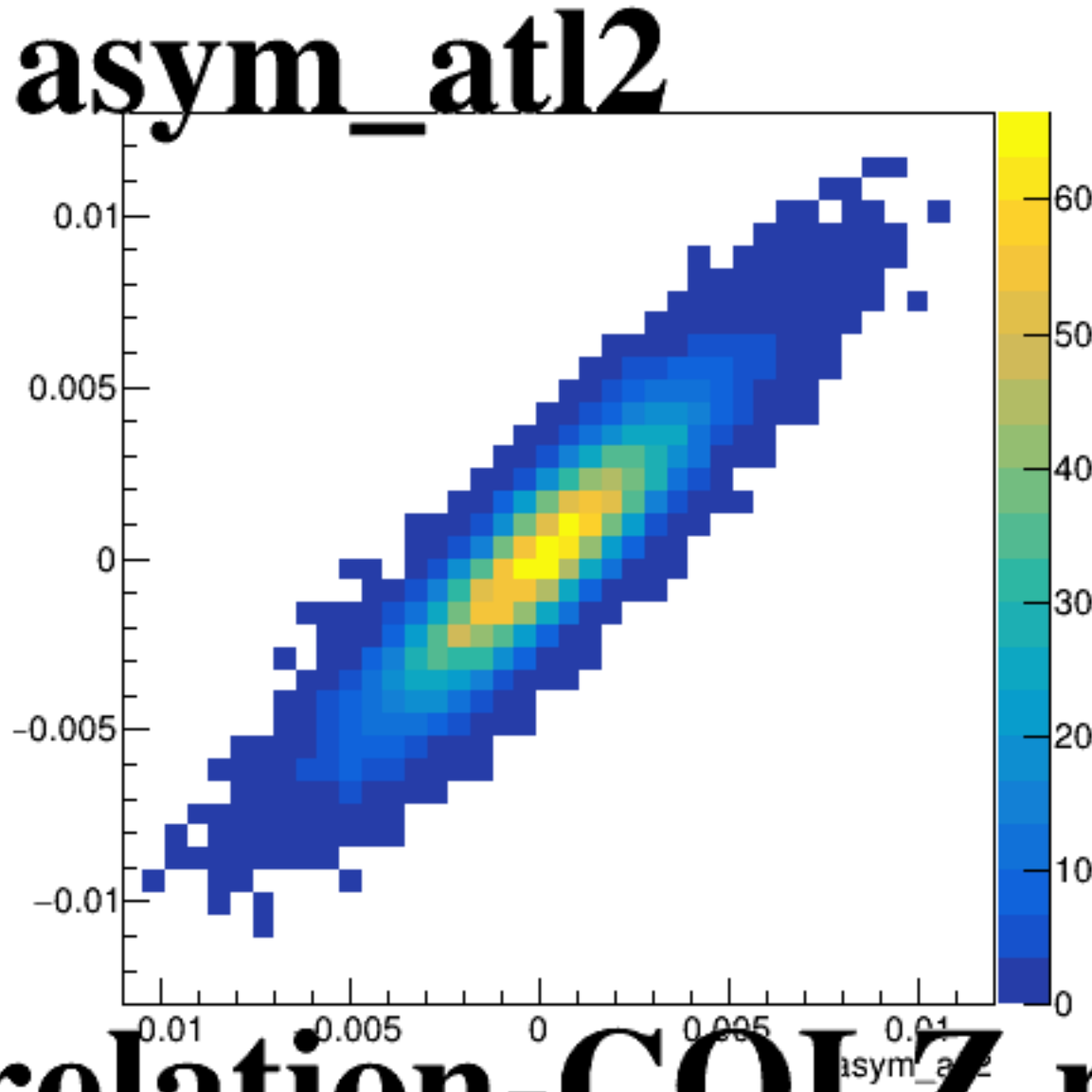
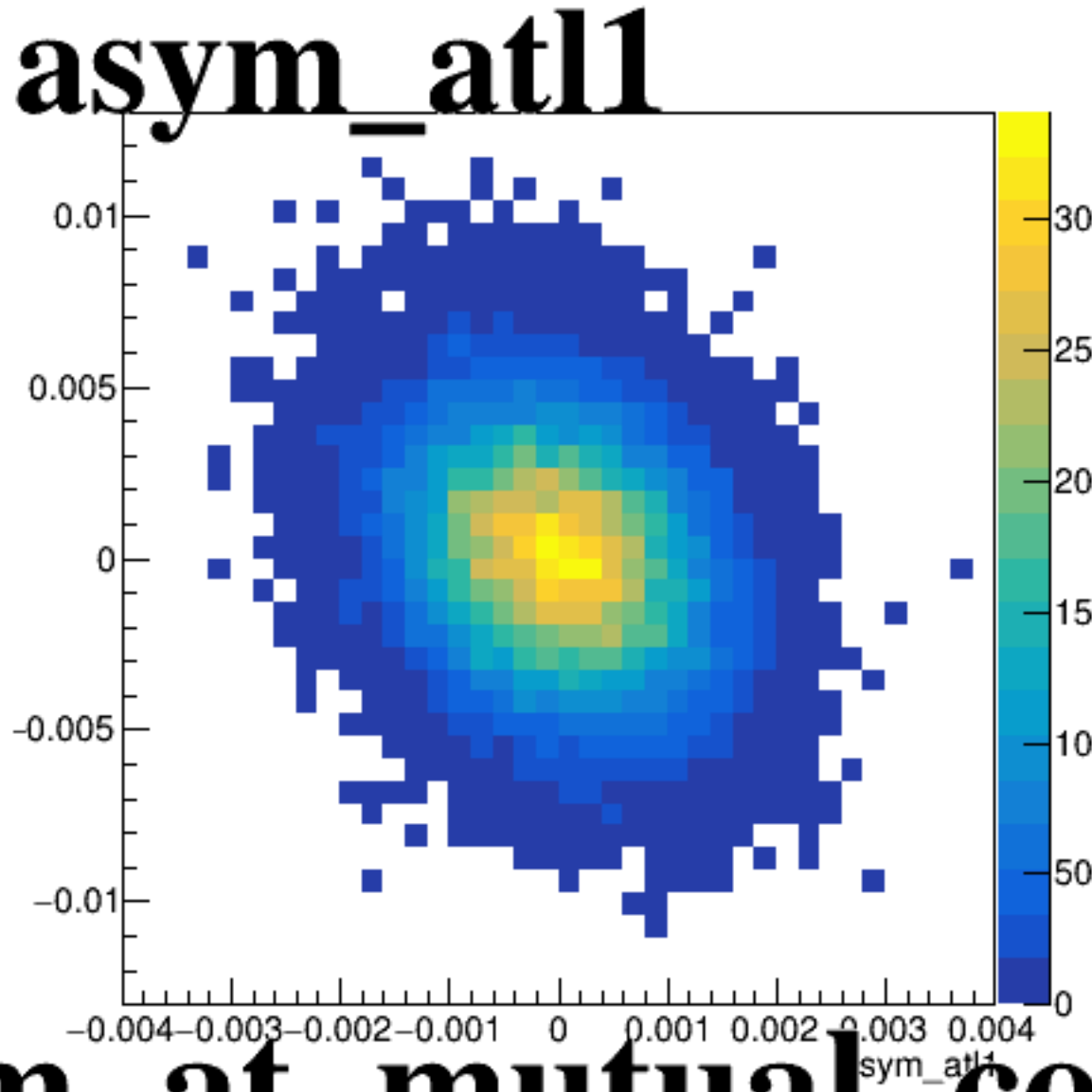
asym_atl2



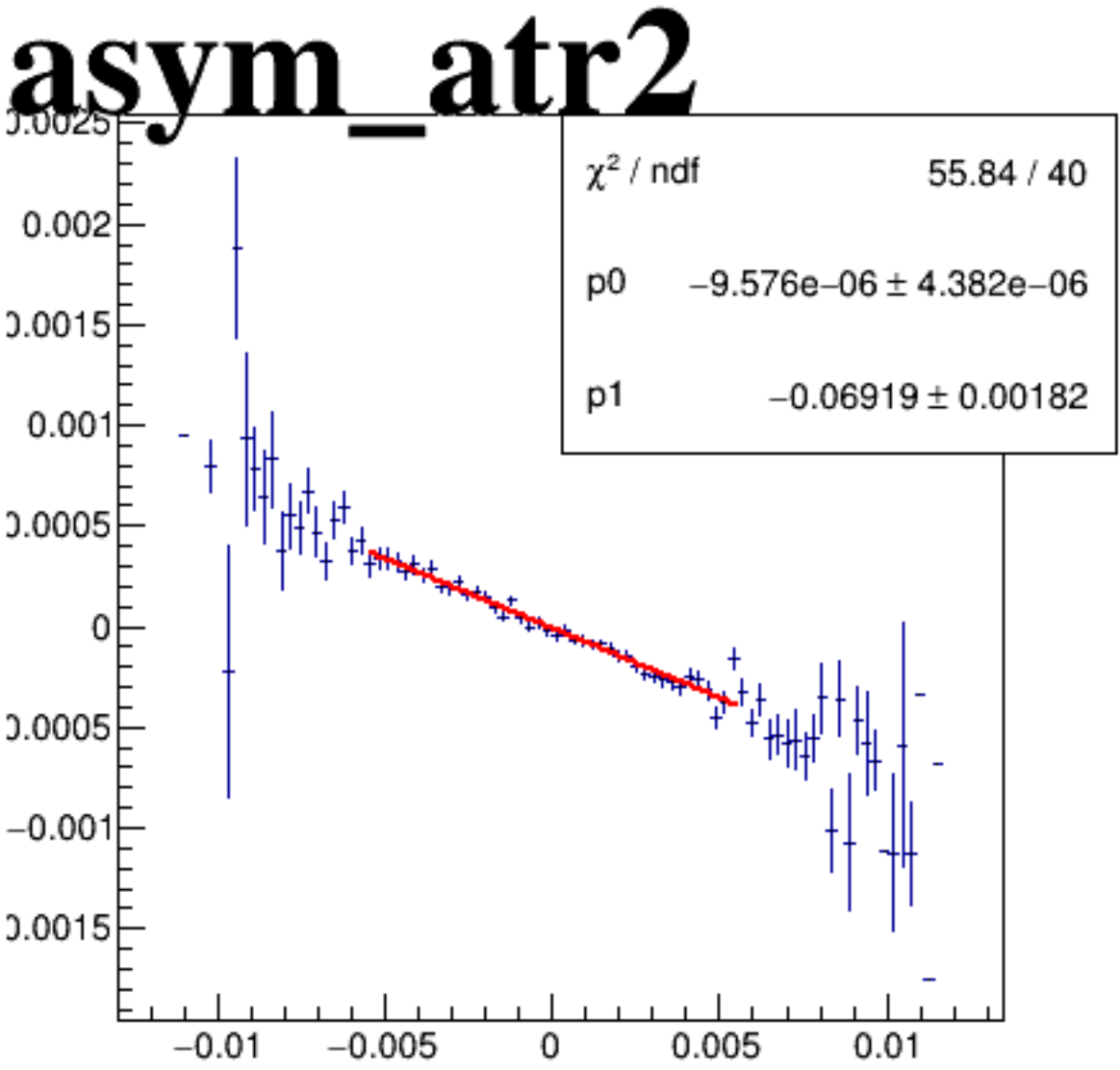
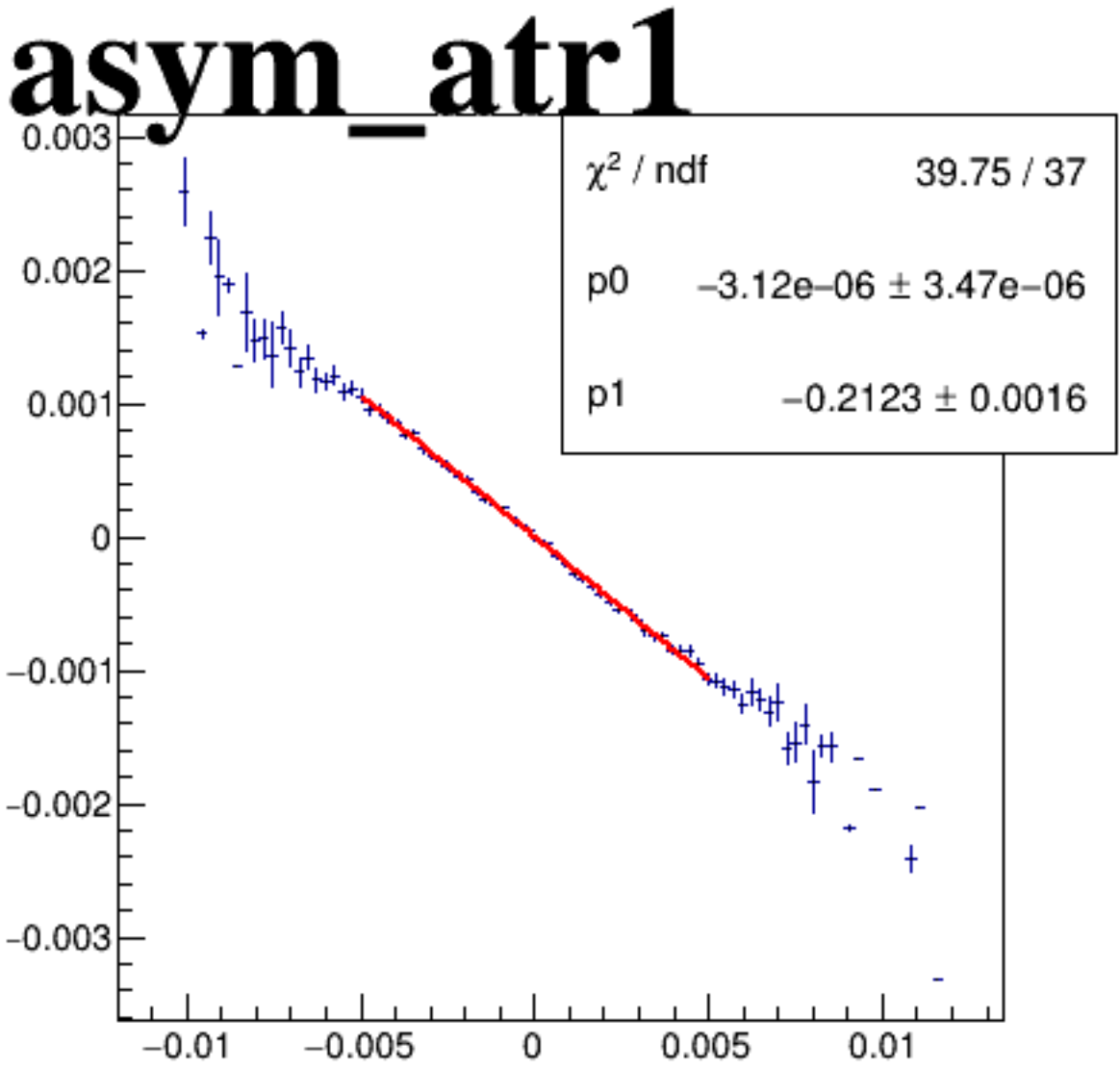
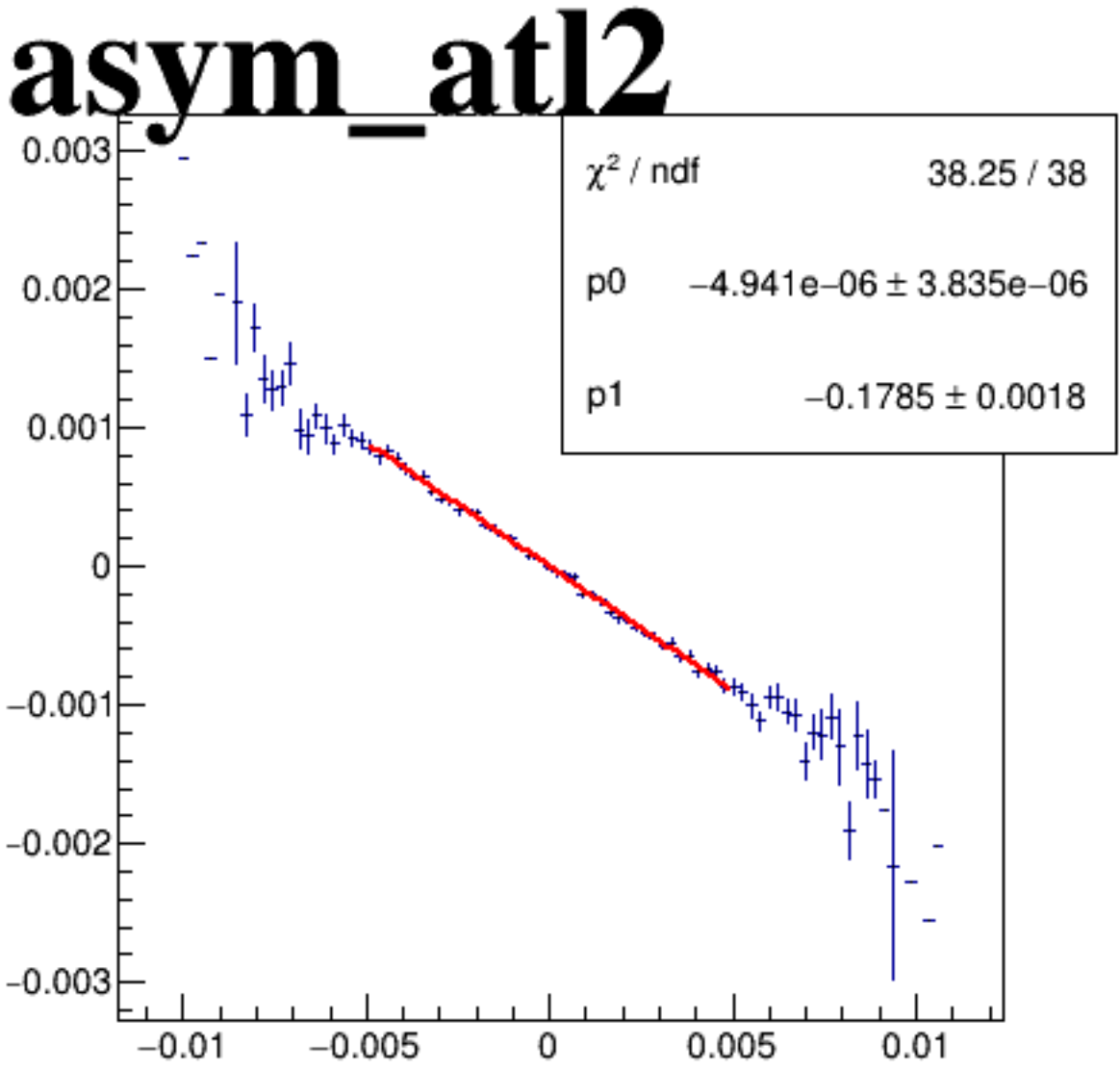
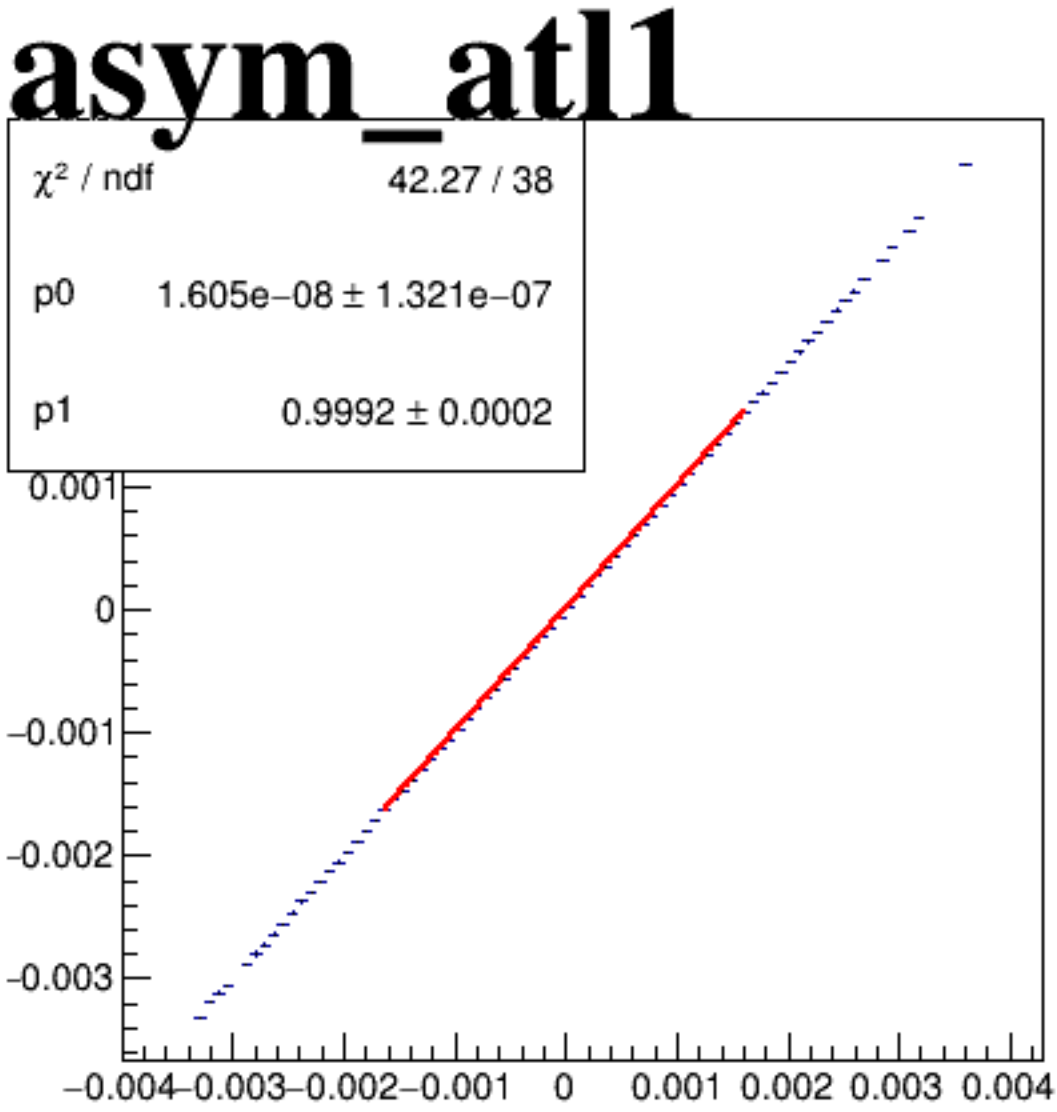
asym_atr1



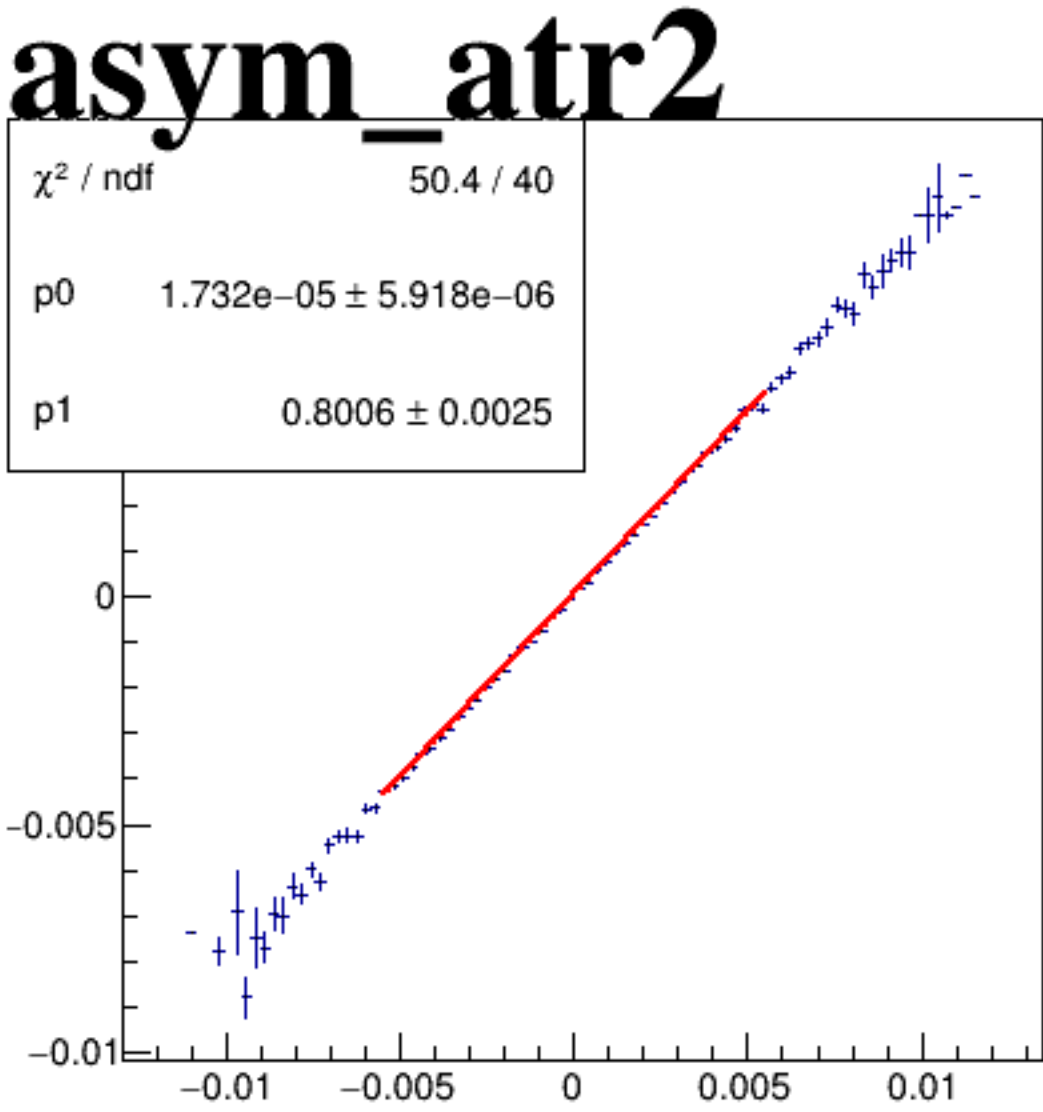
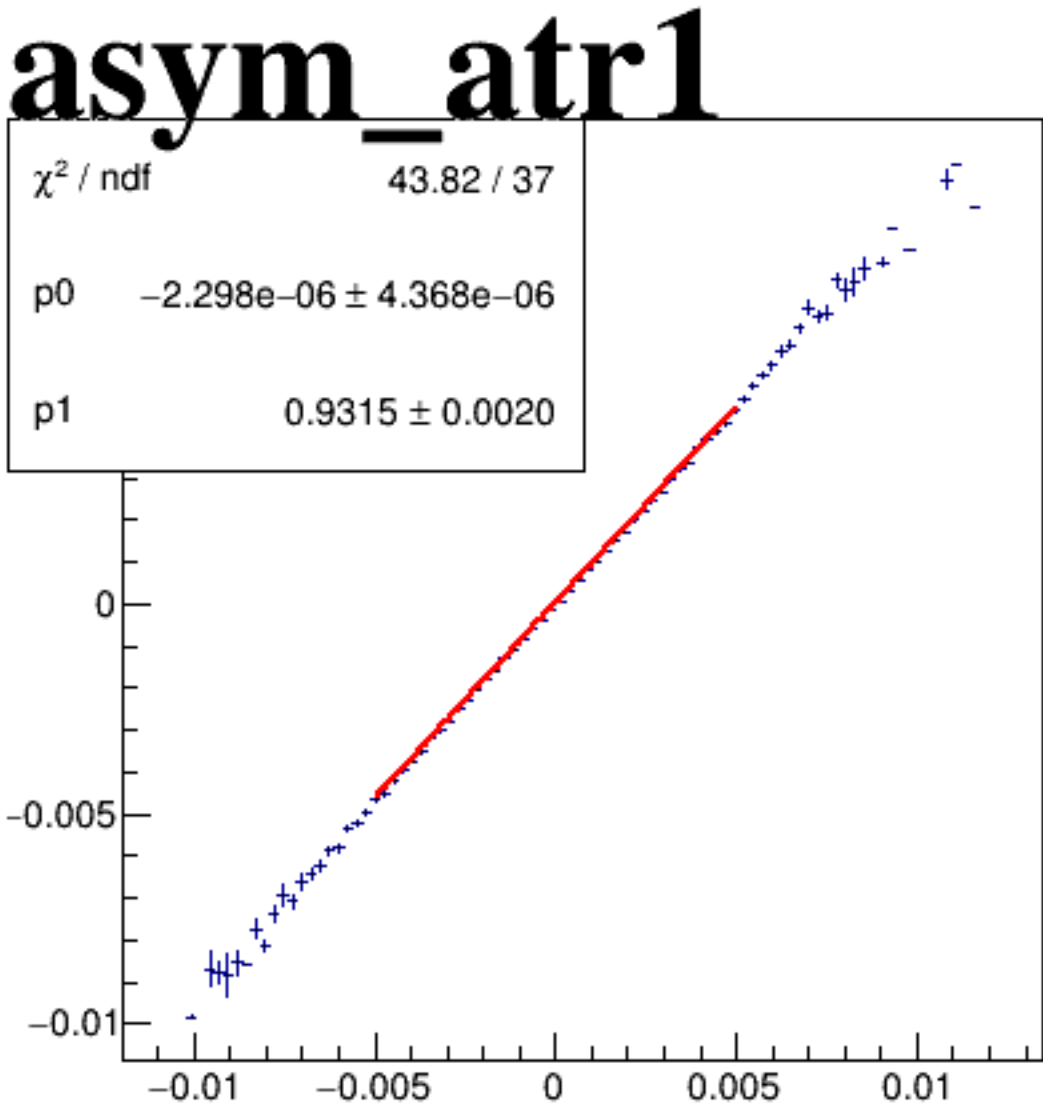
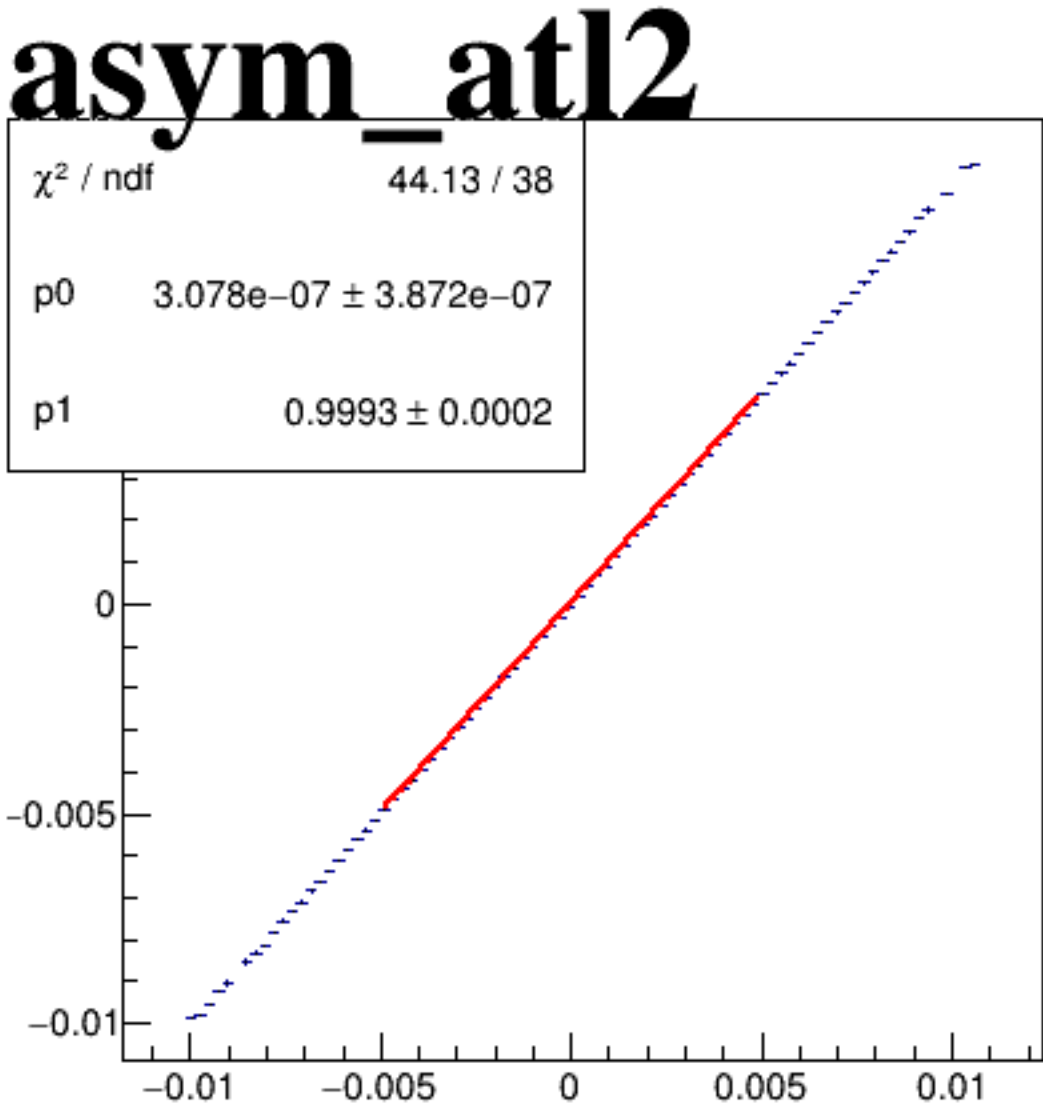
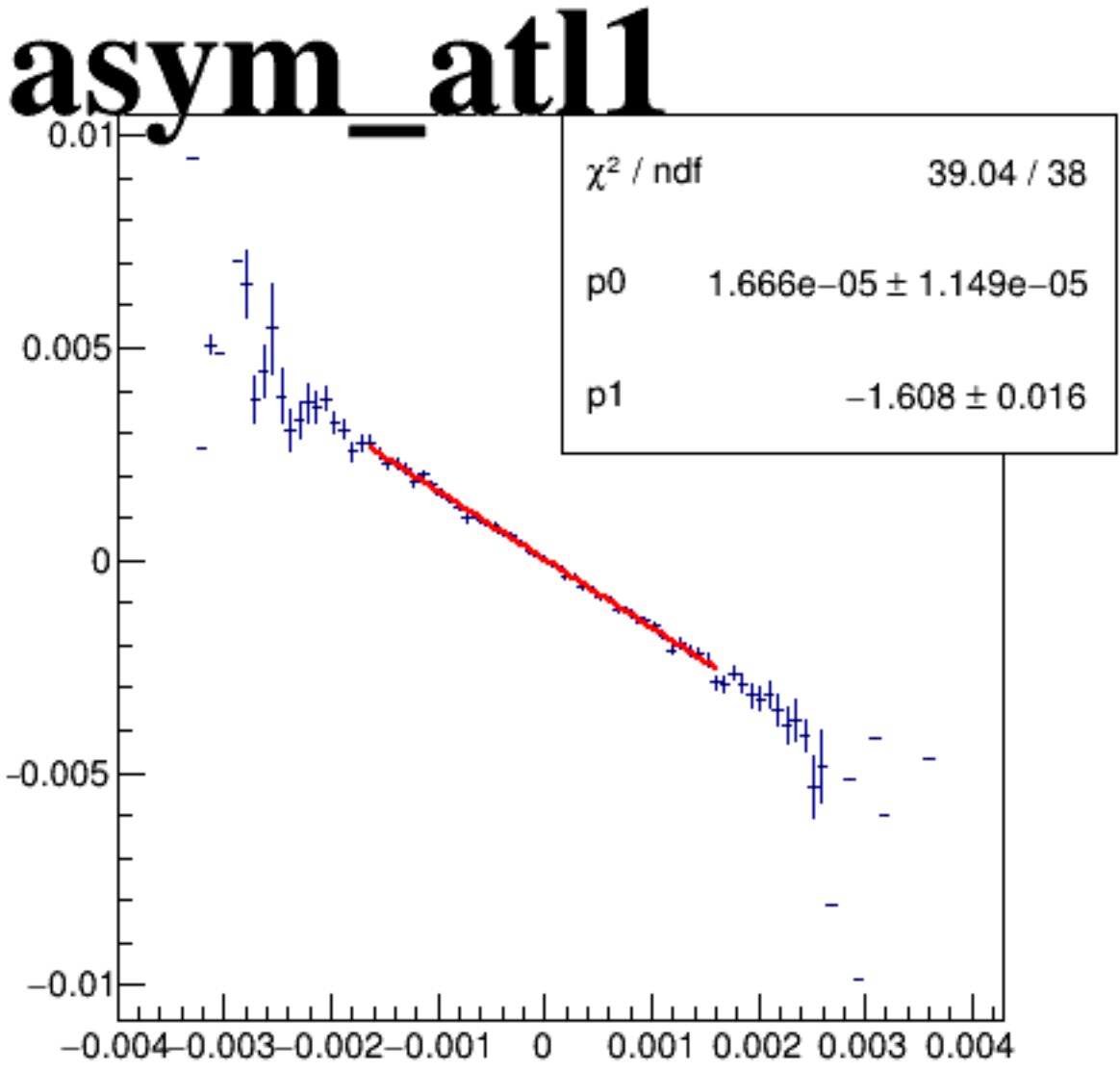
asym_atr2



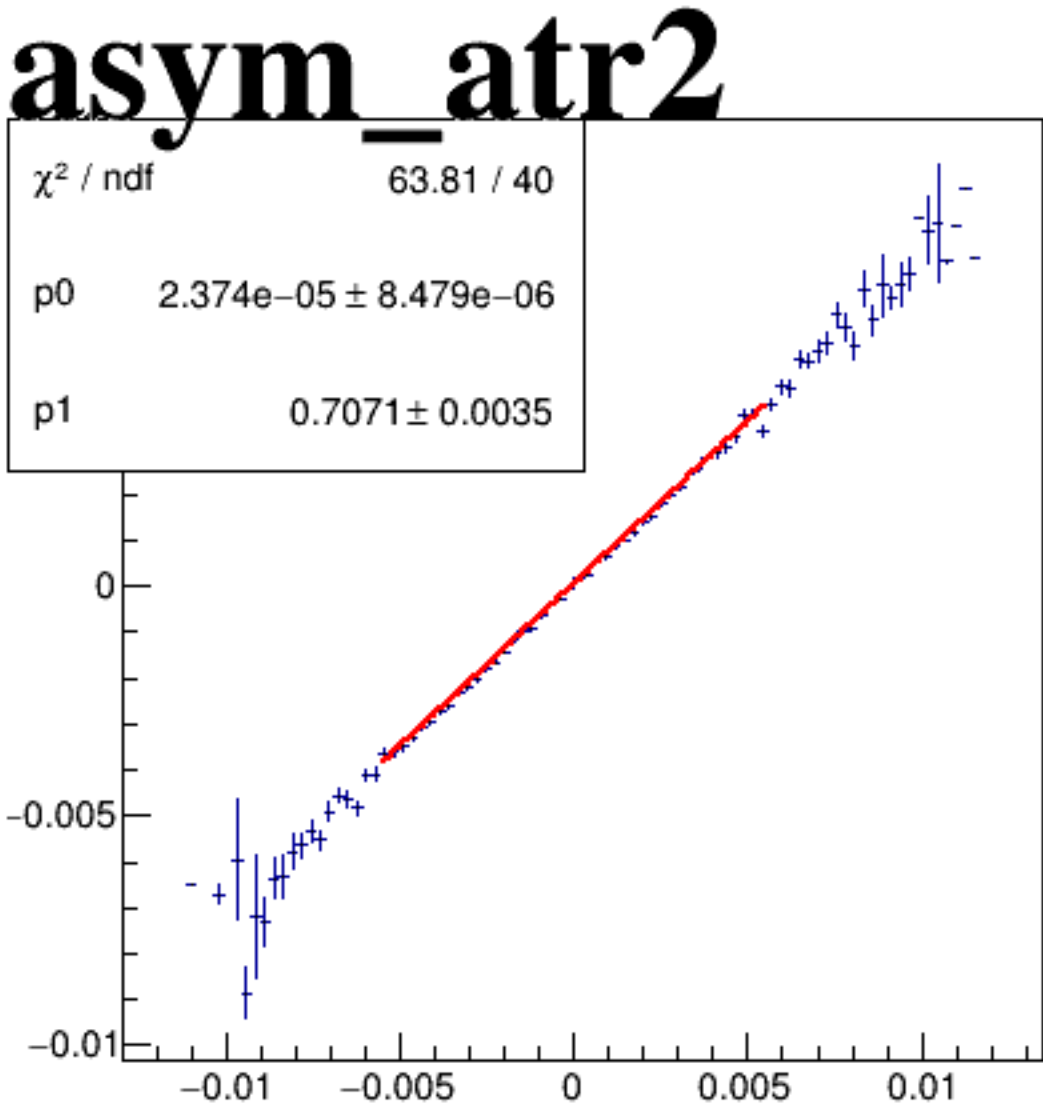
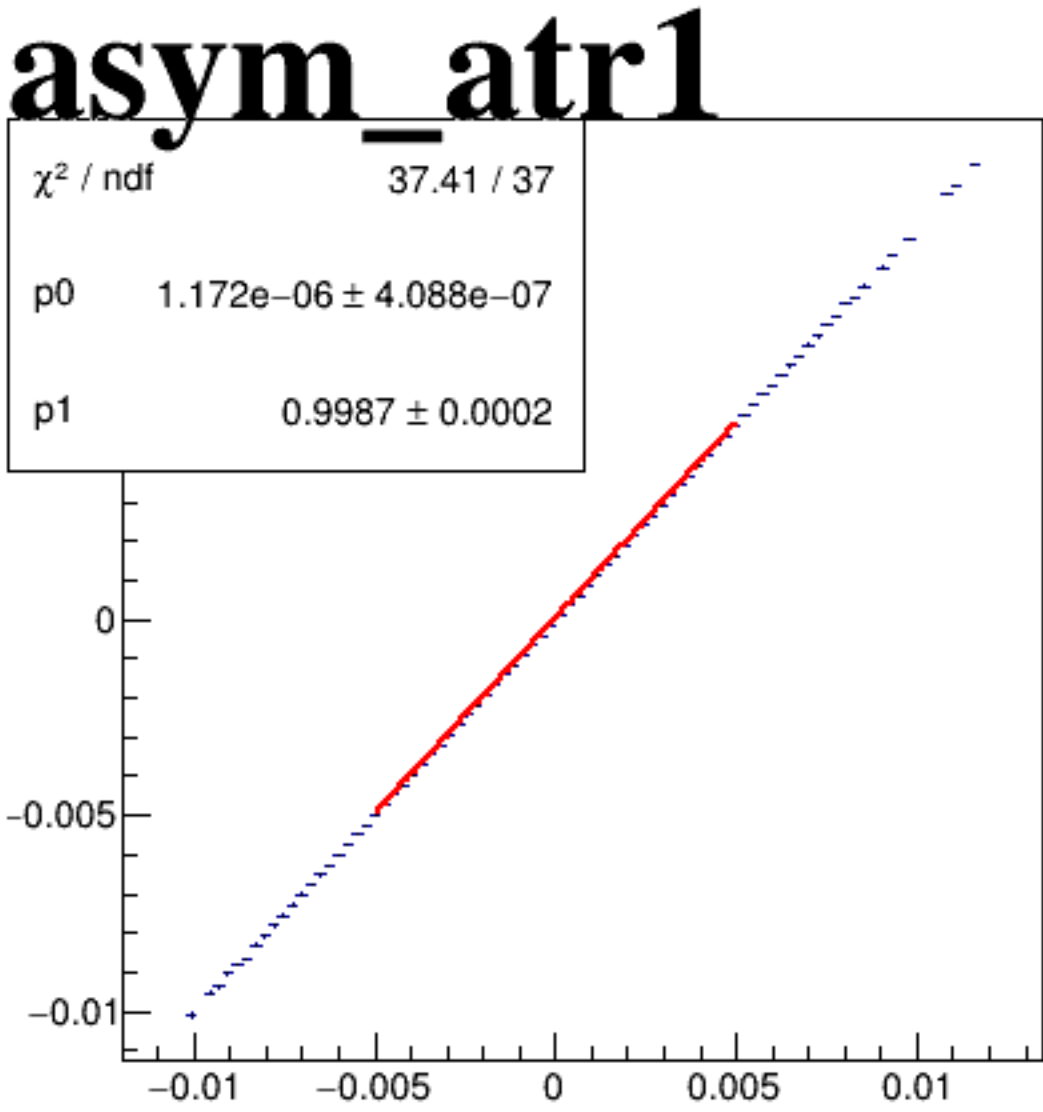
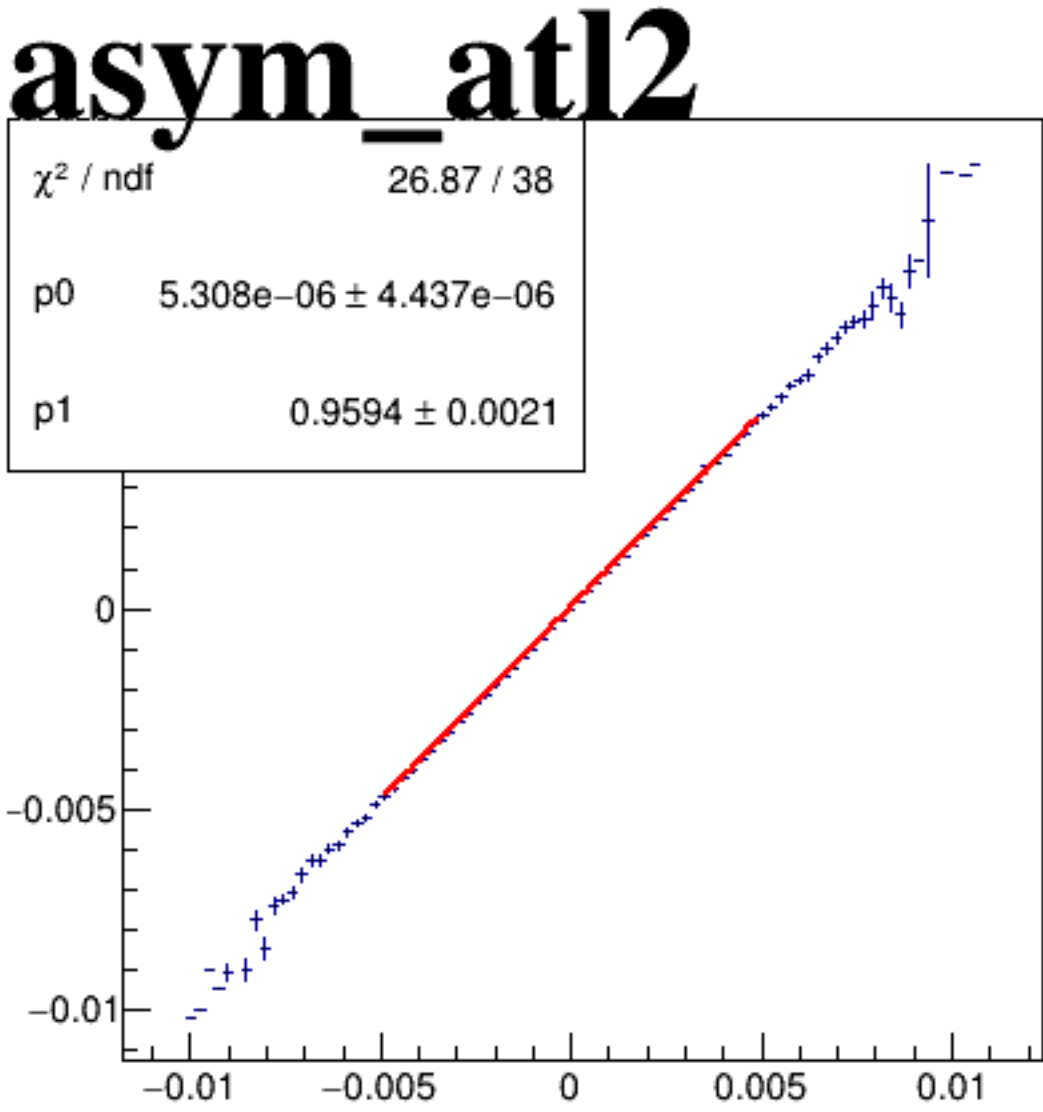
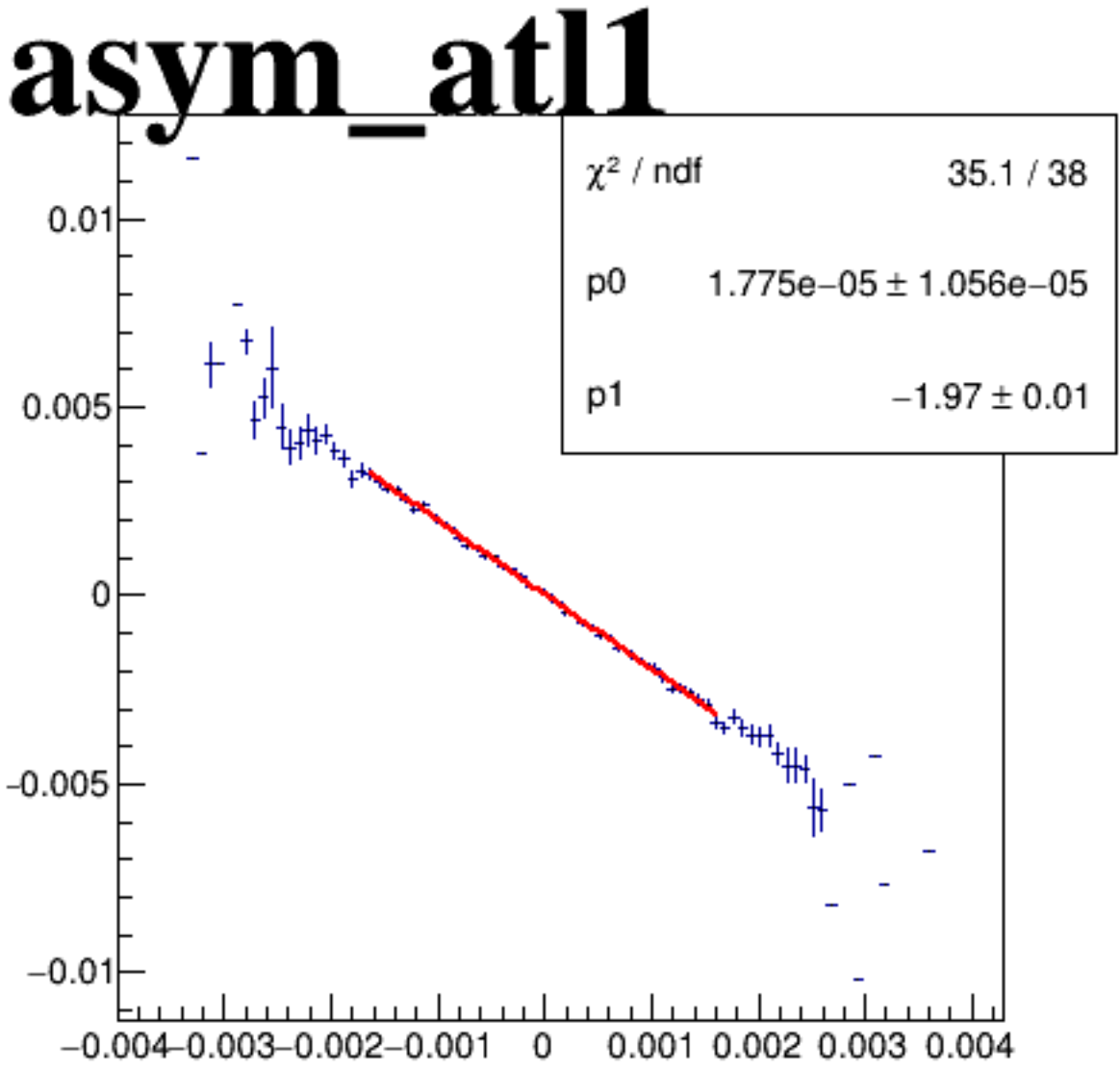
asym_atl1



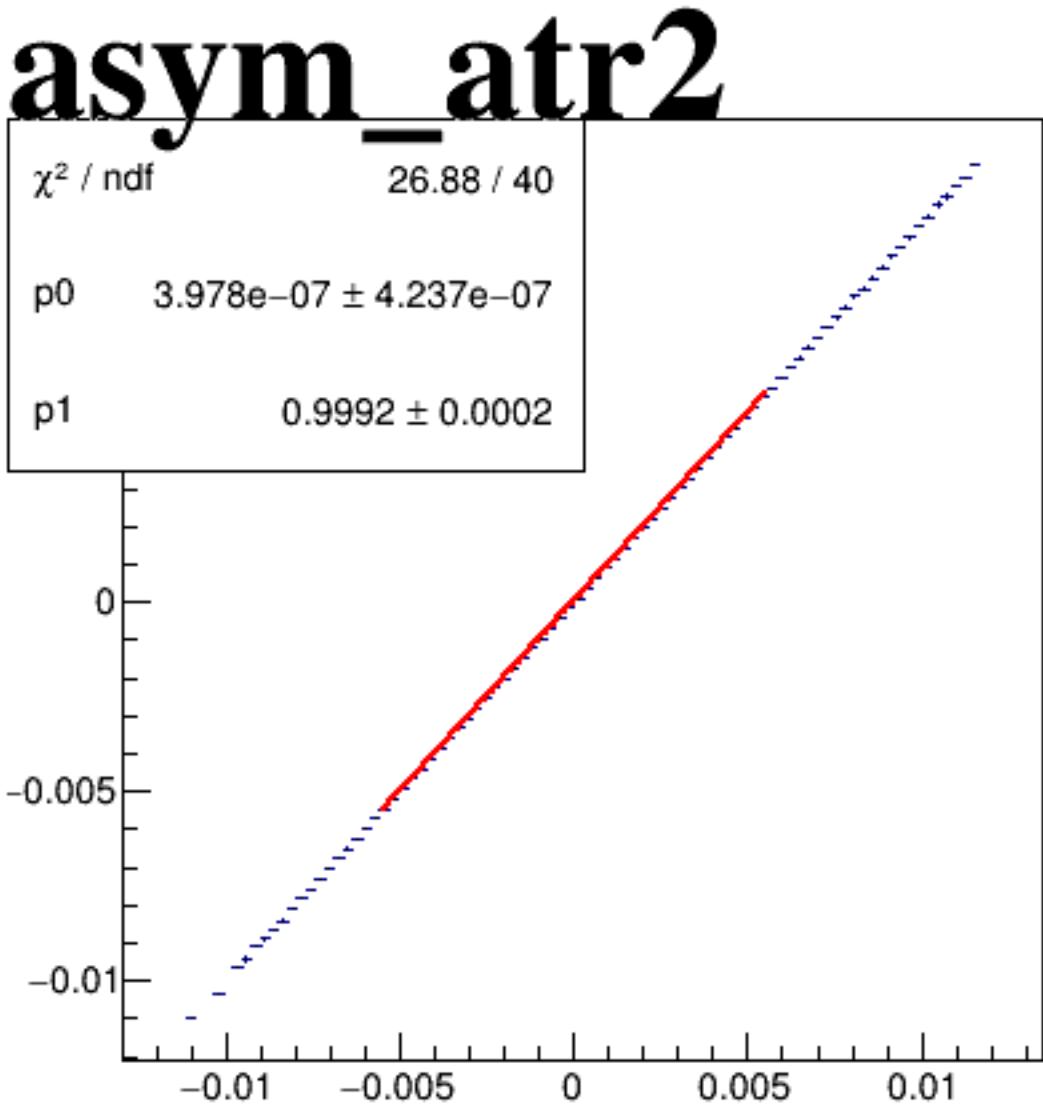
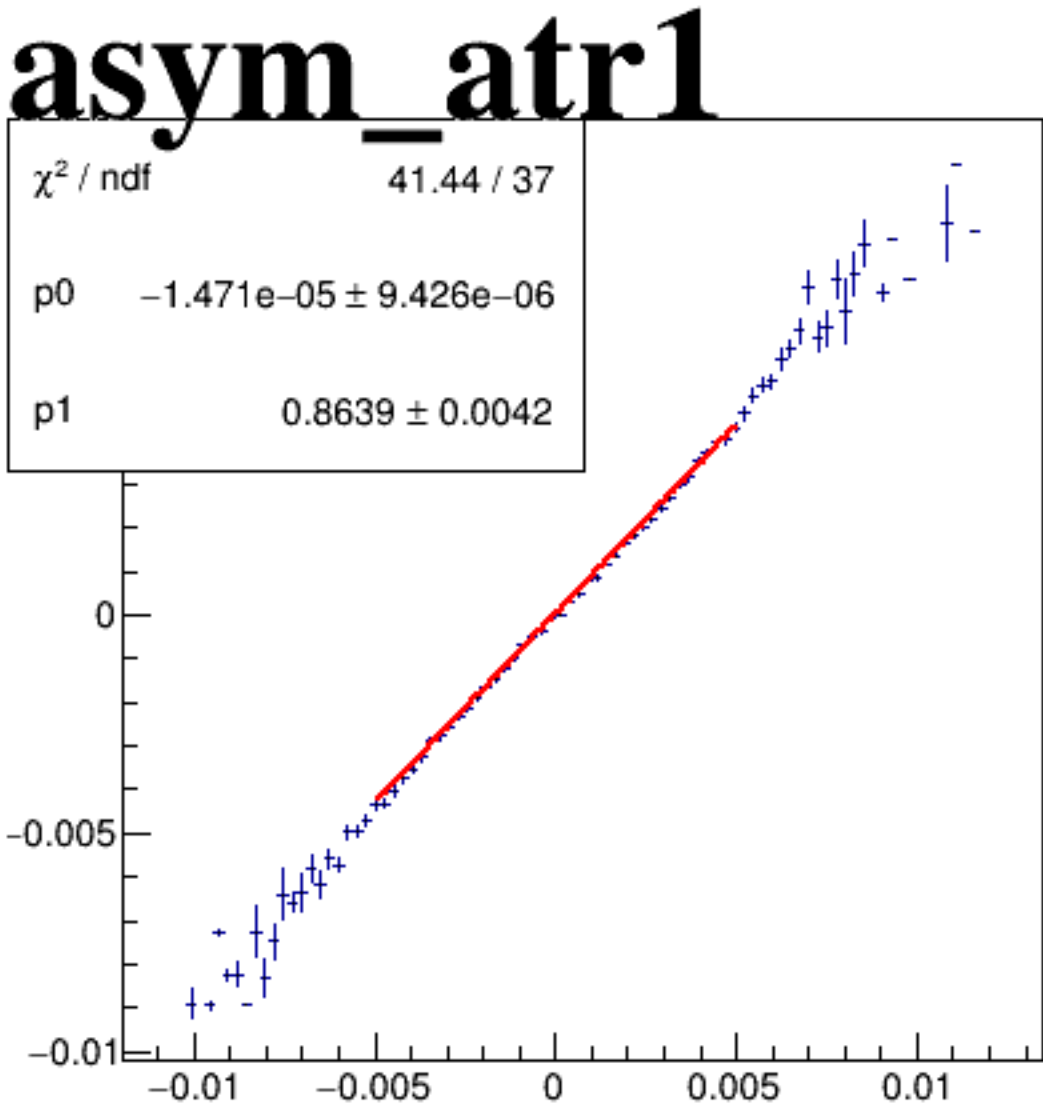
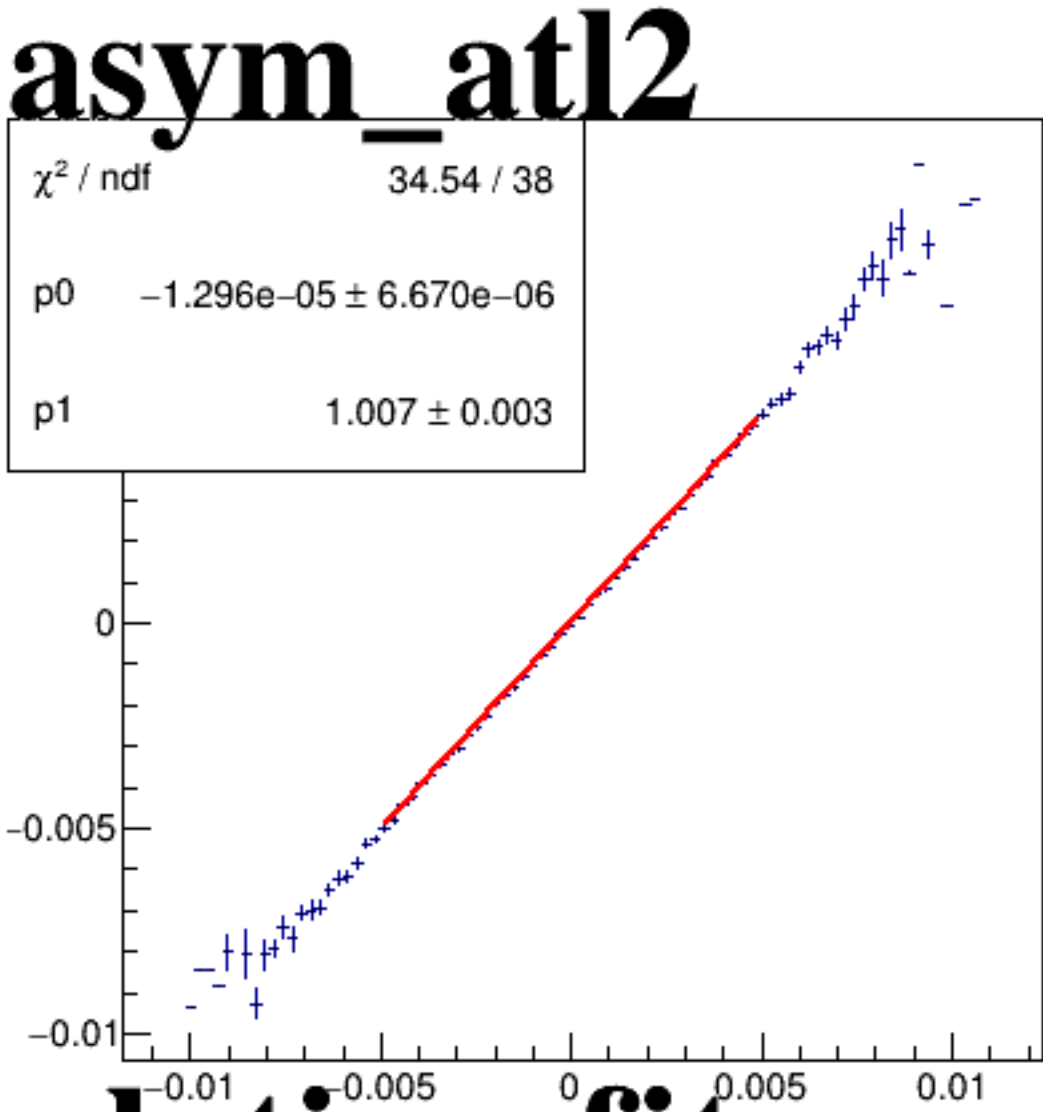
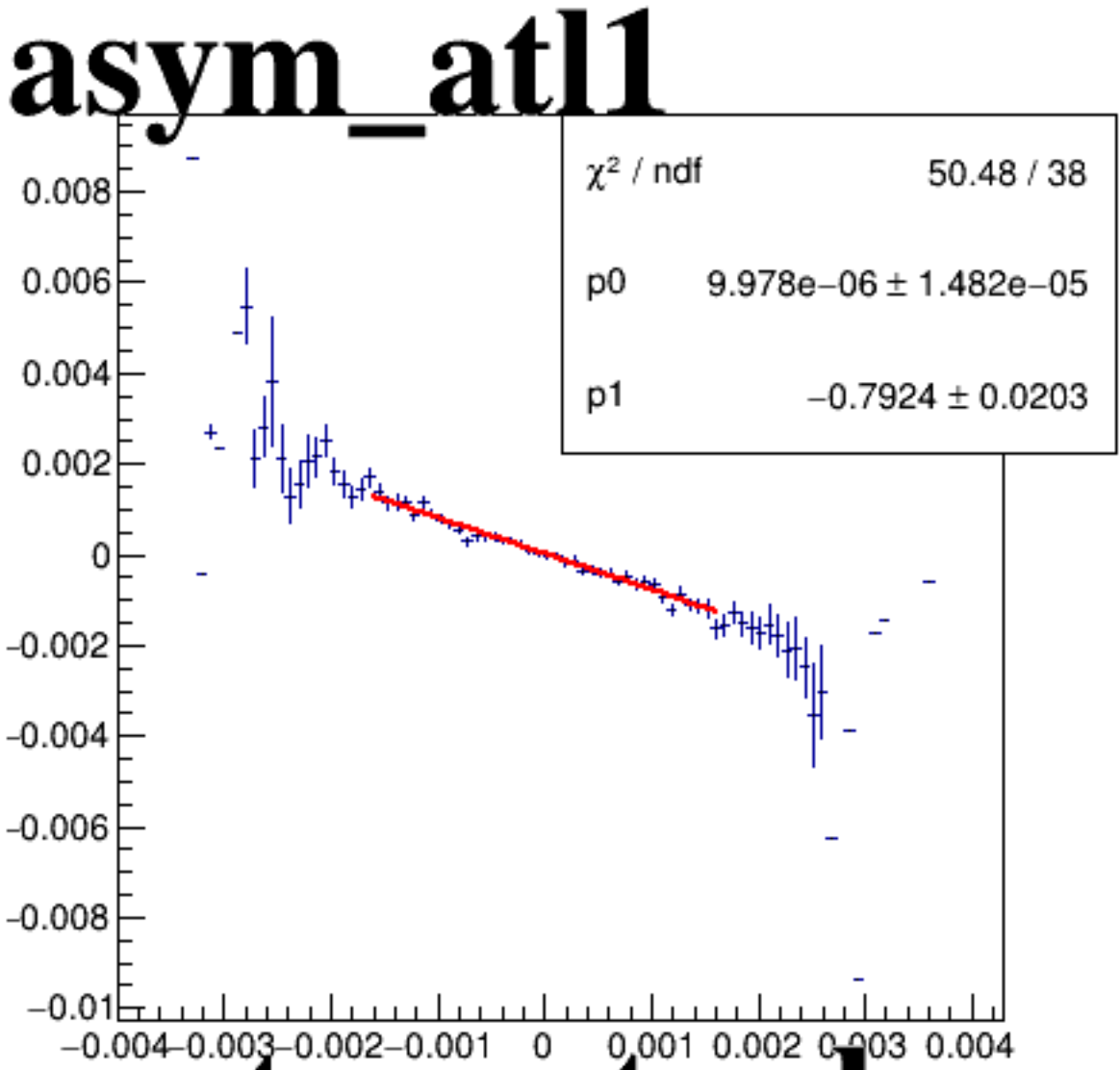
asym_atl2



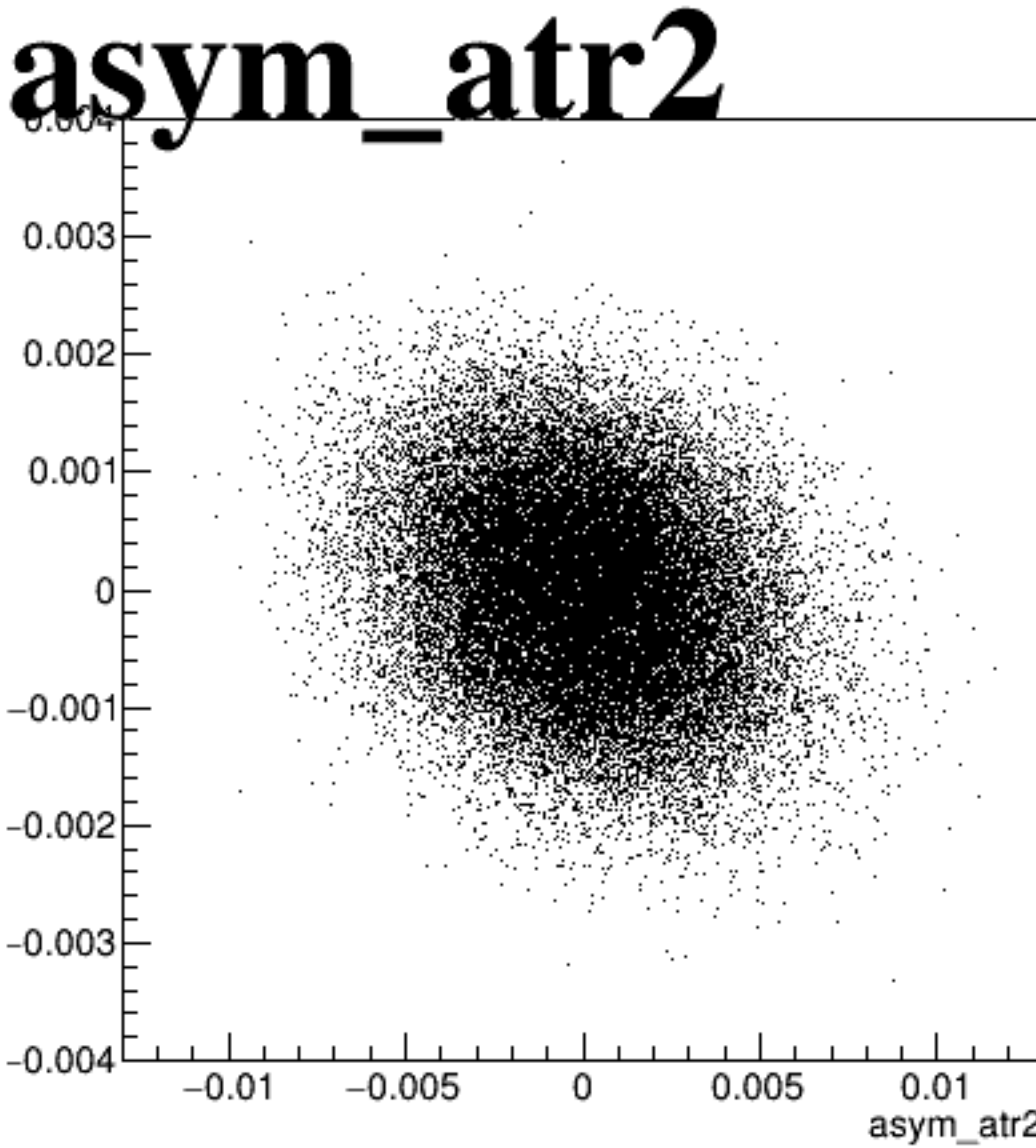
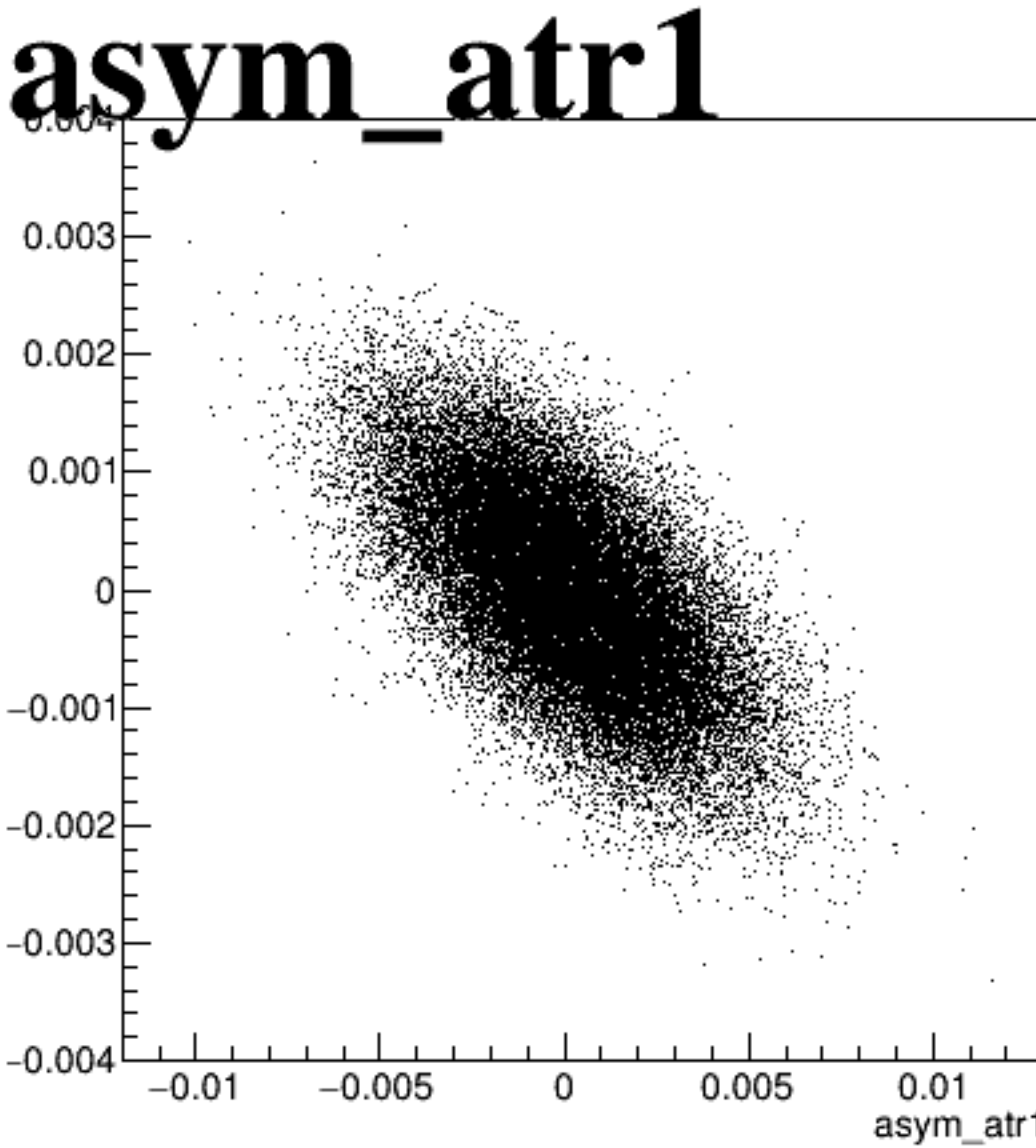
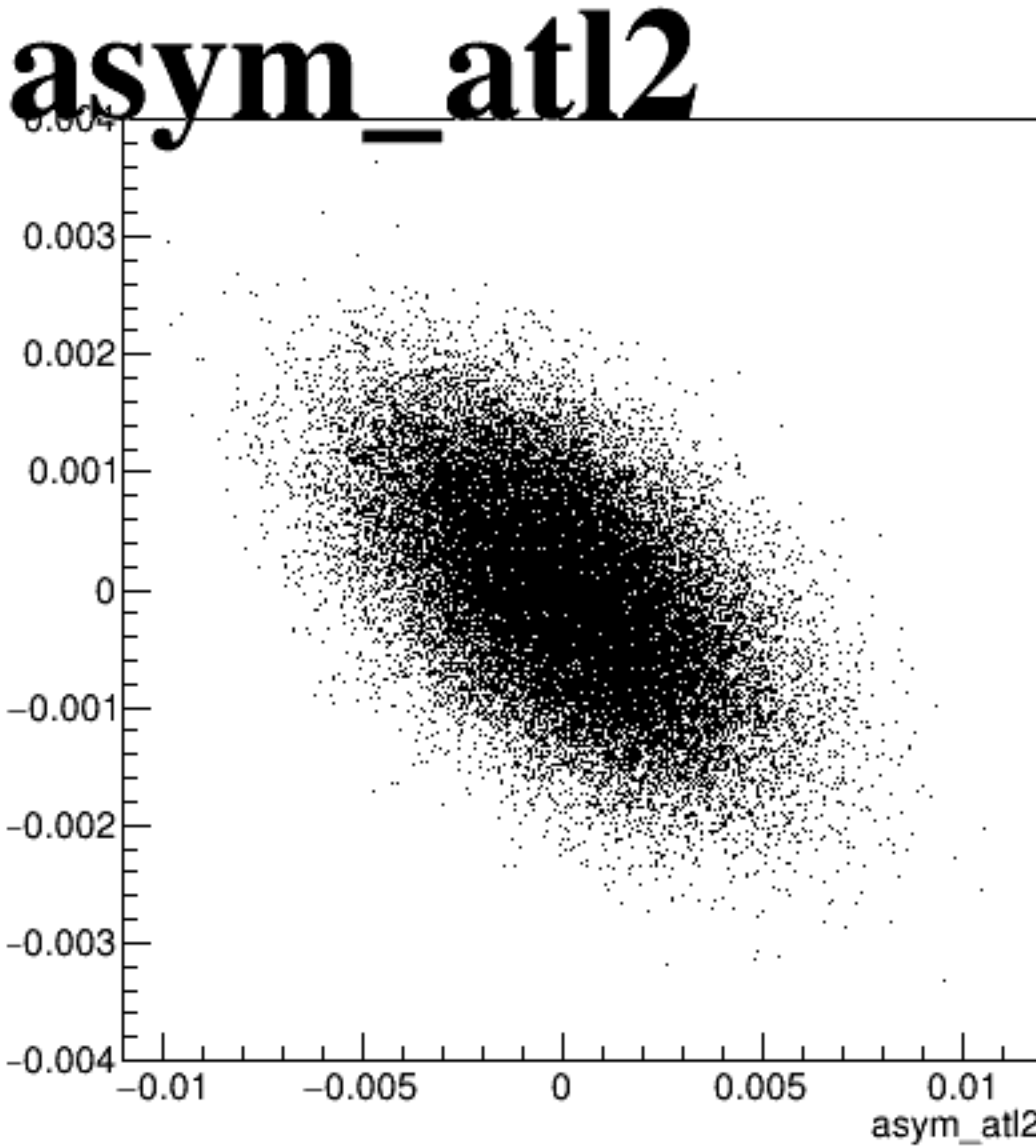
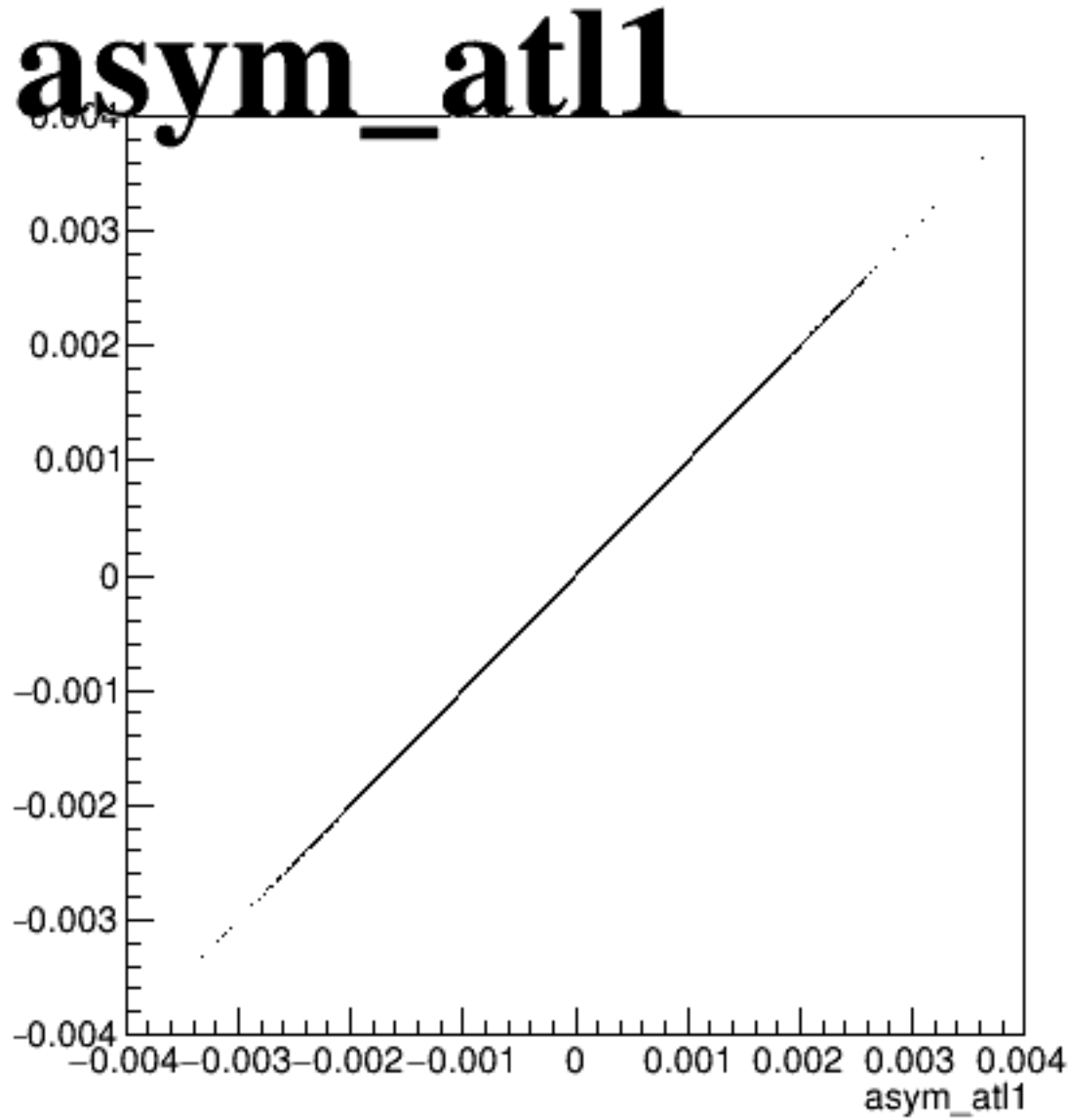
asym_atr1



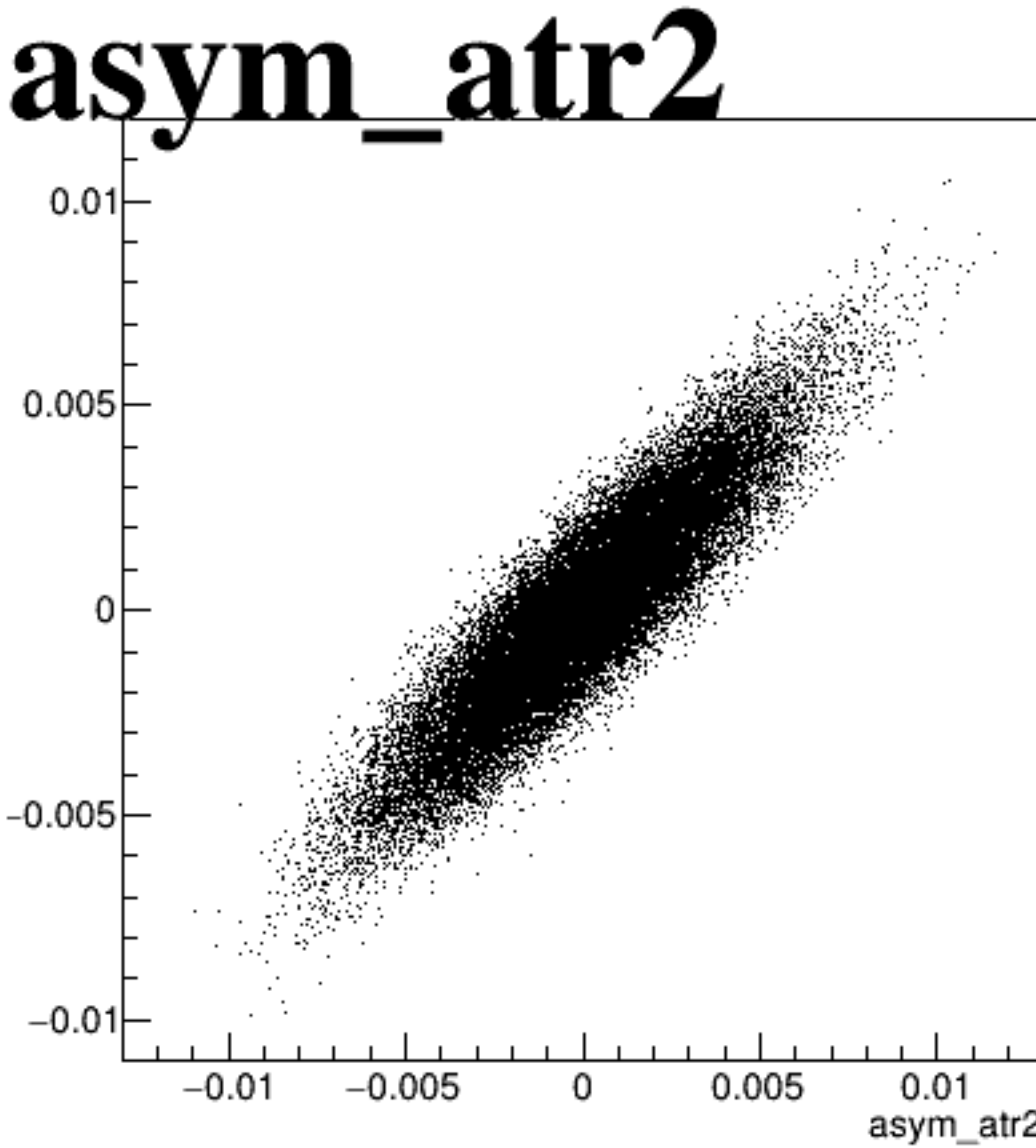
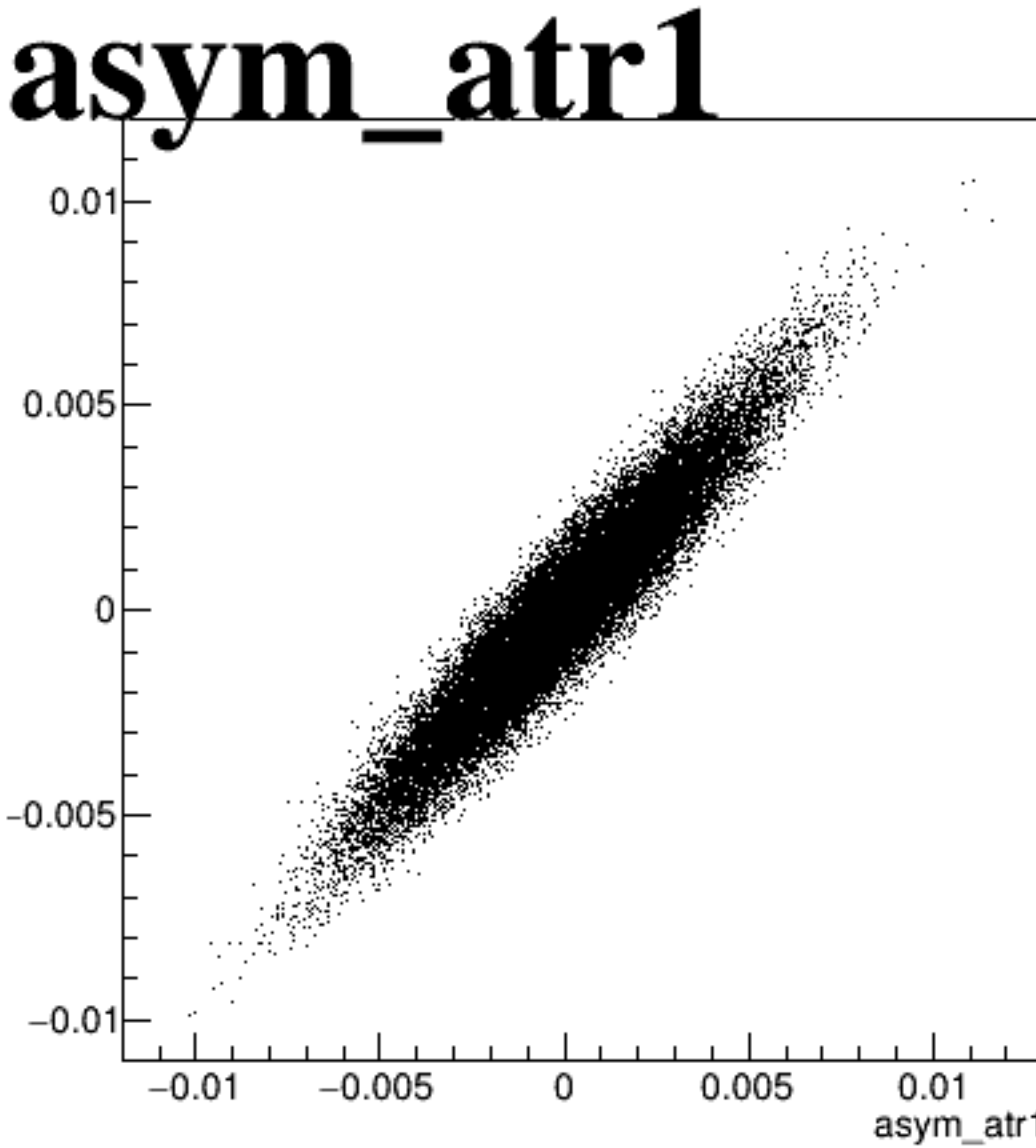
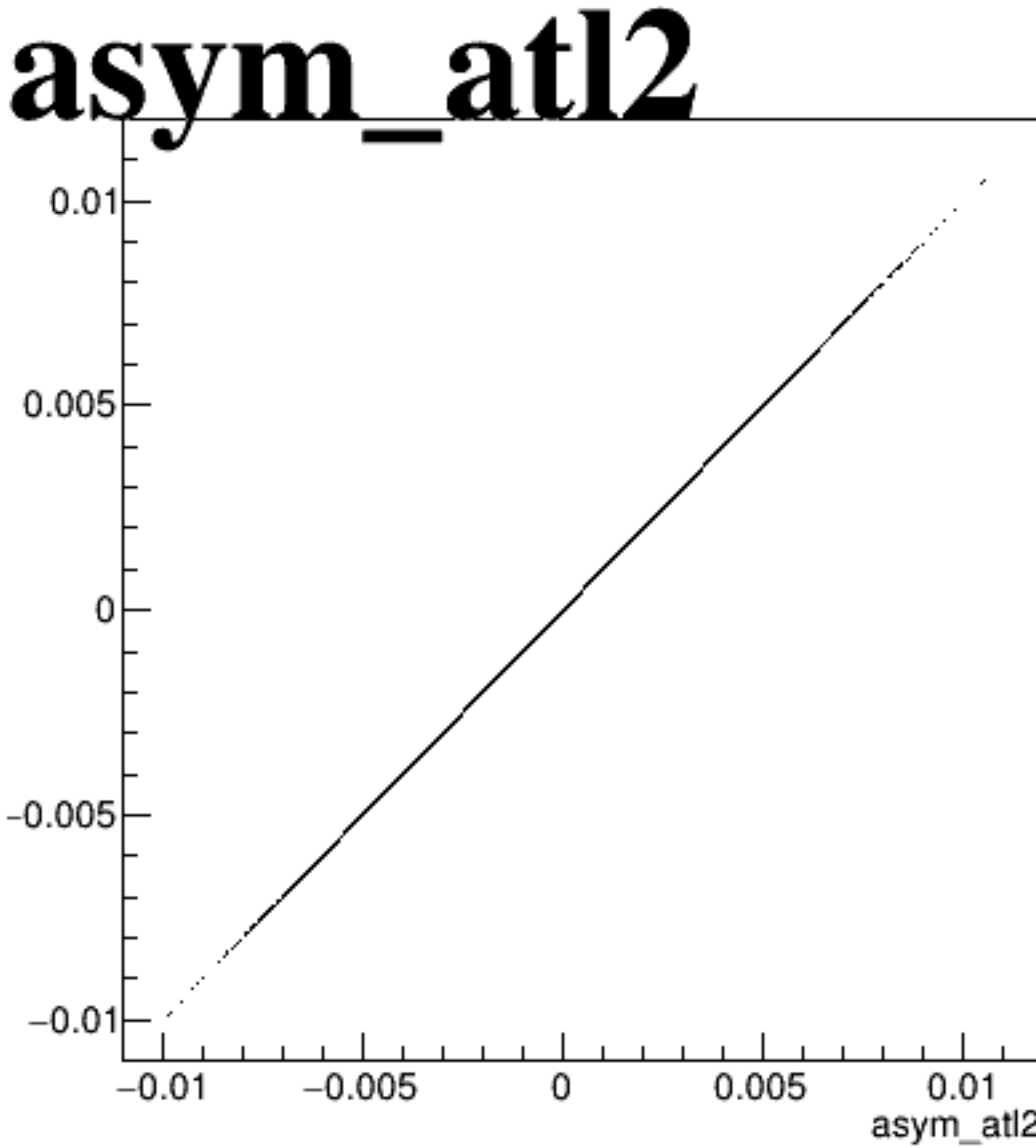
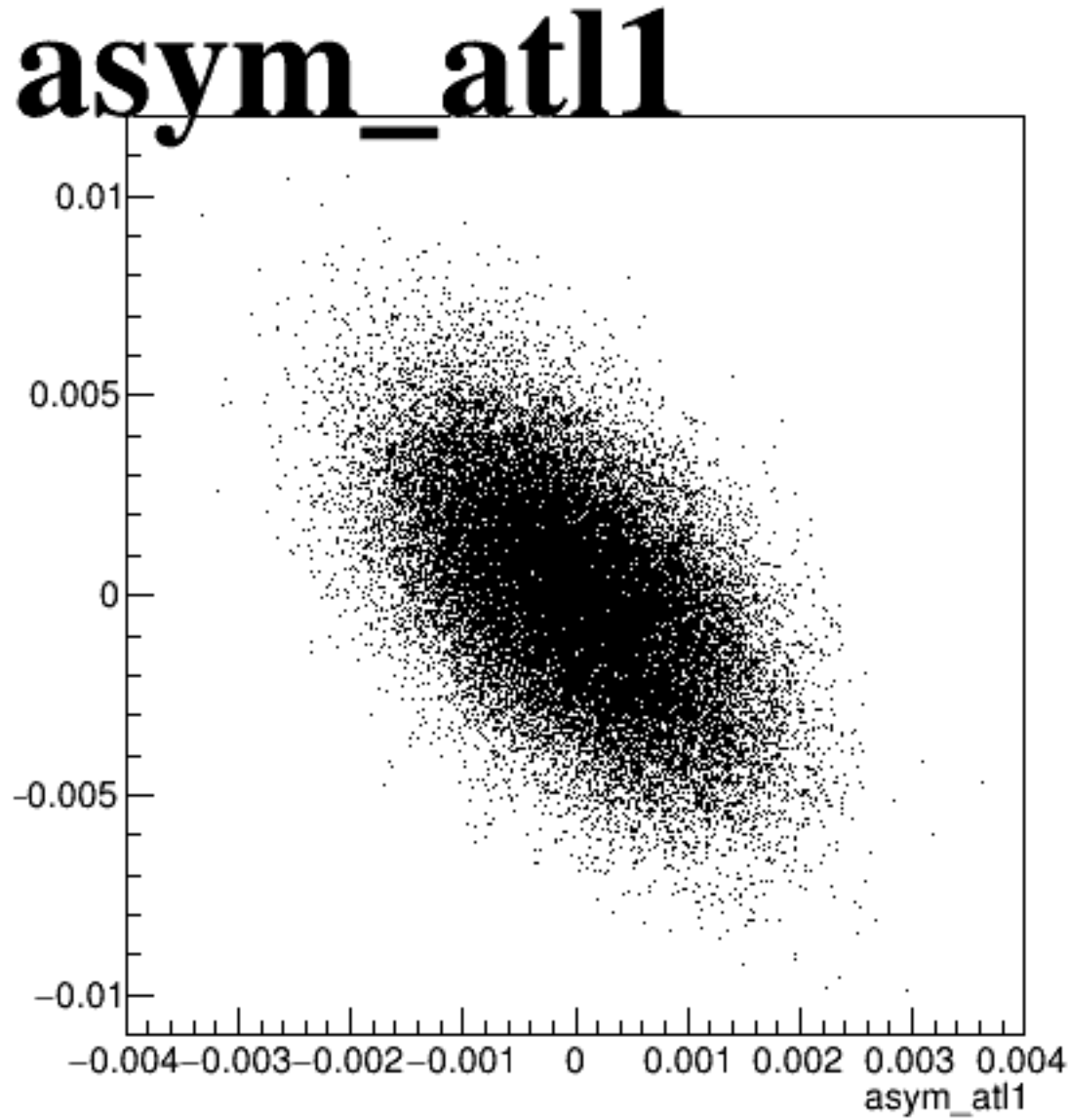
asym_atr2



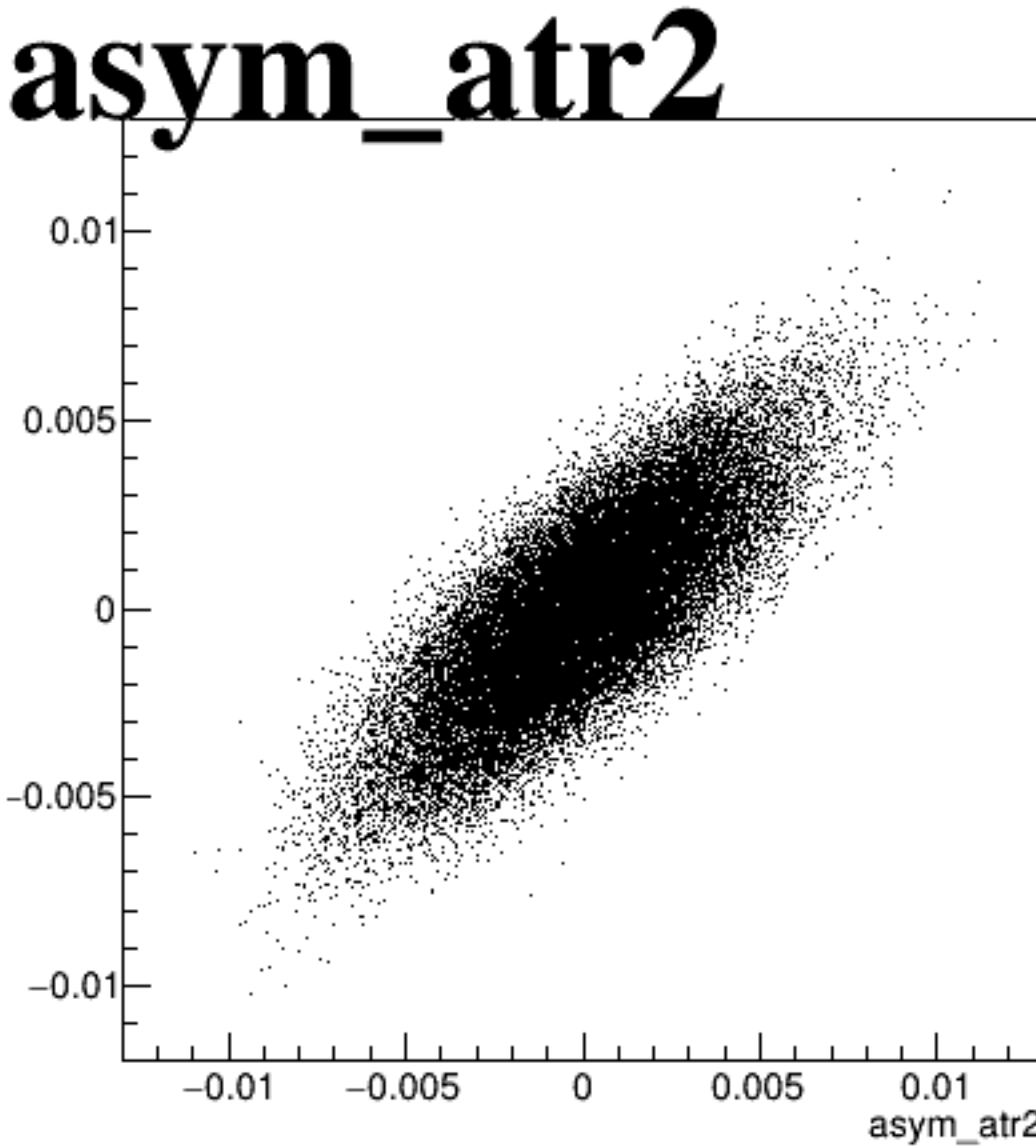
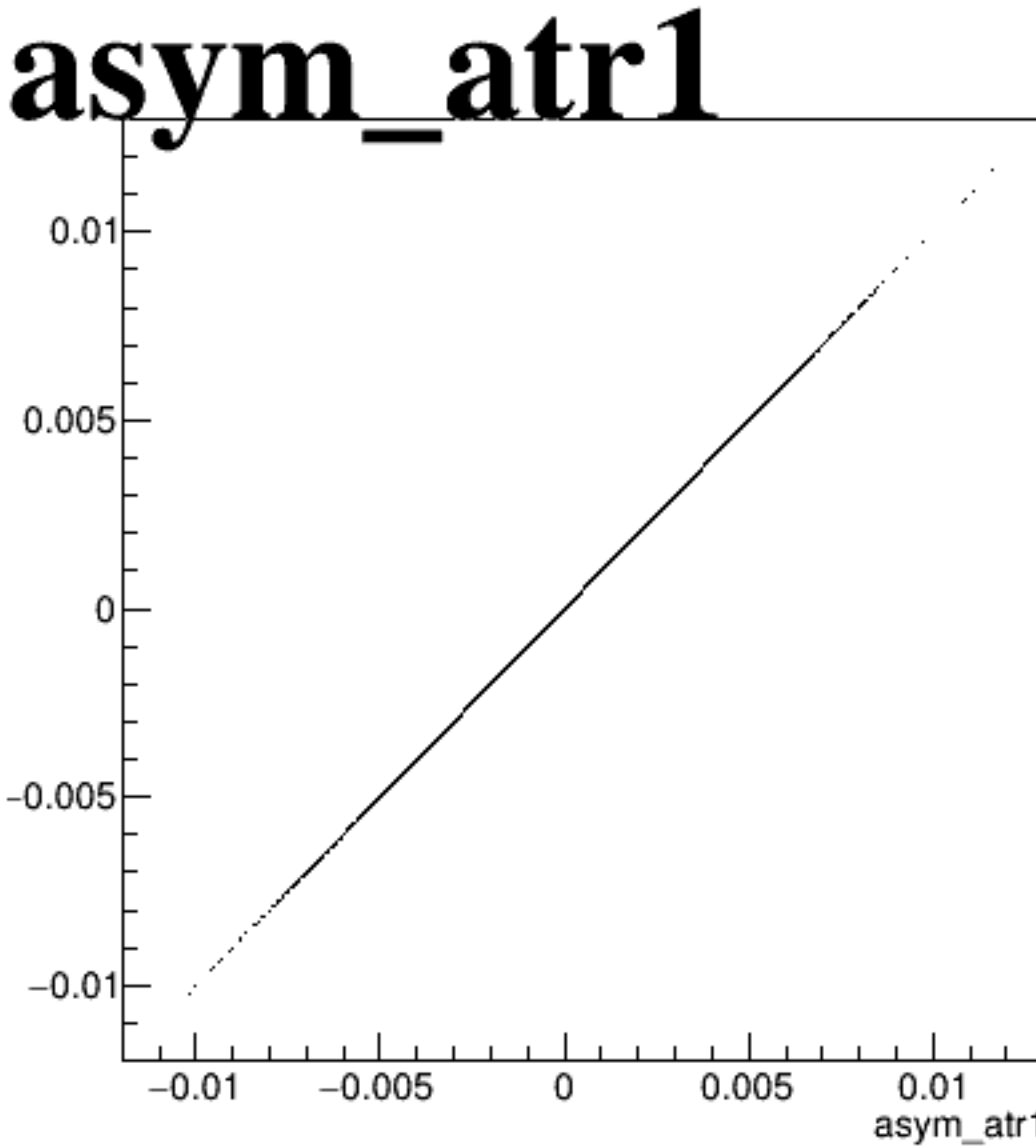
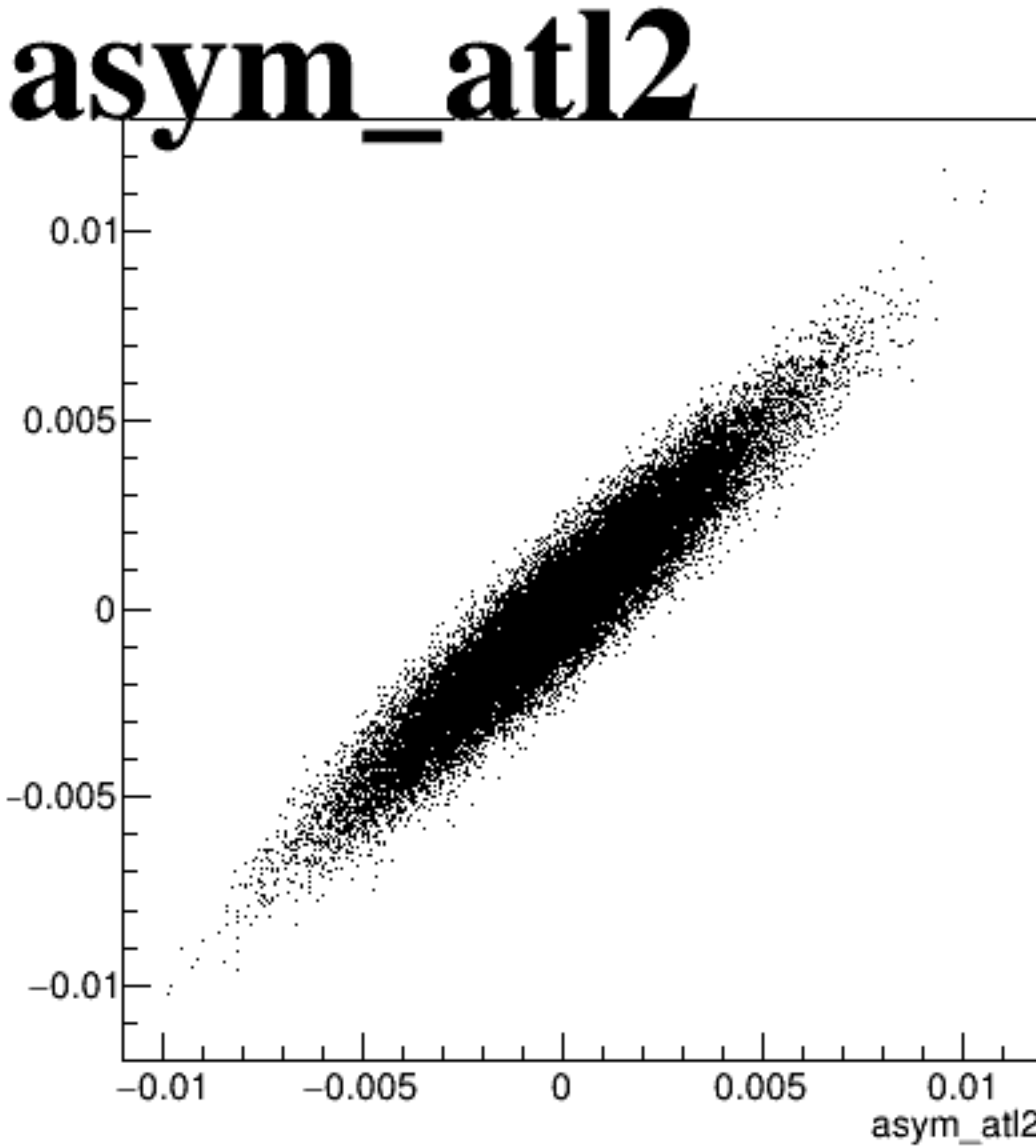
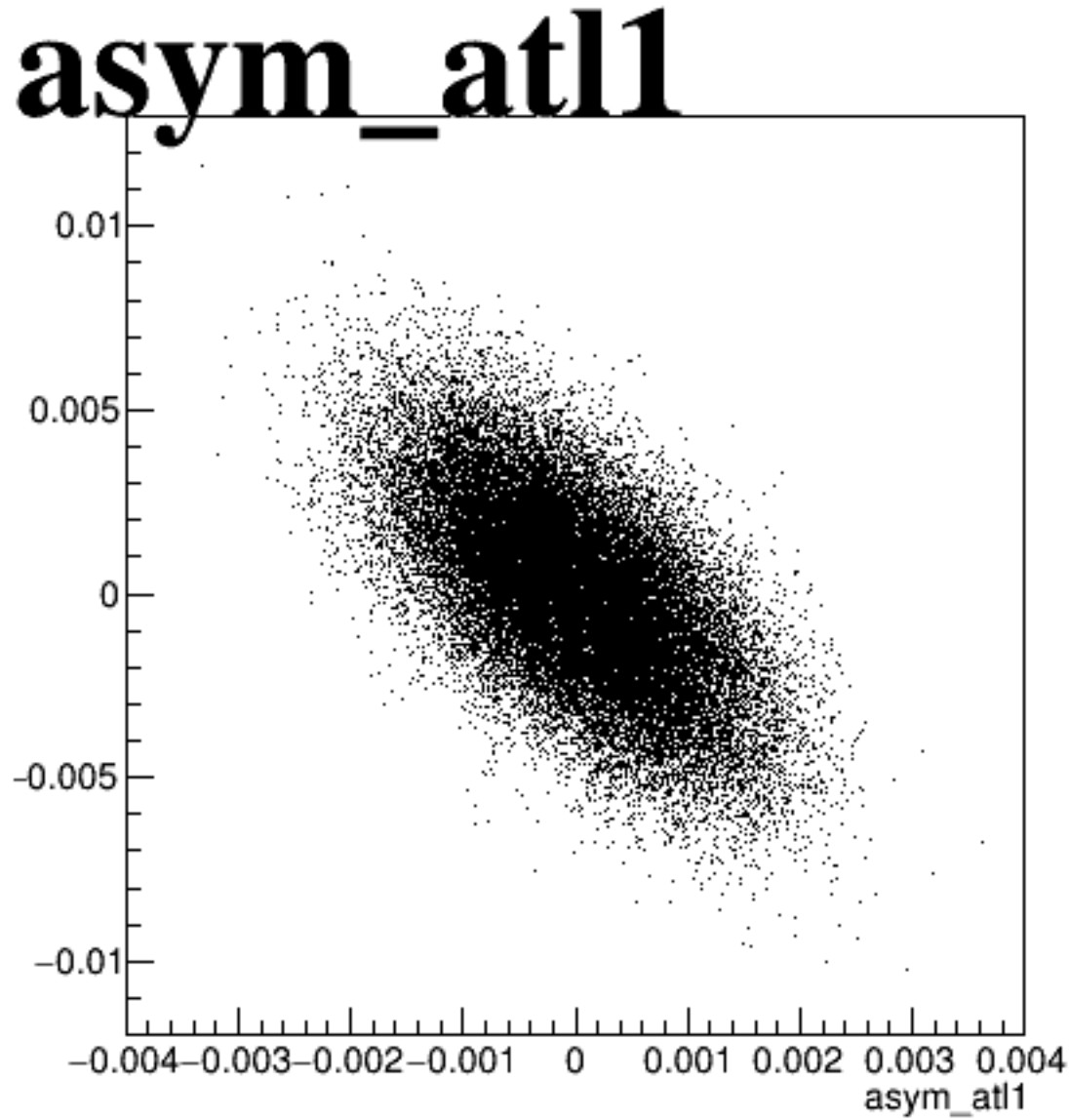
asym_atl1



asym_atl2



asym_atr1



asym_atr2

