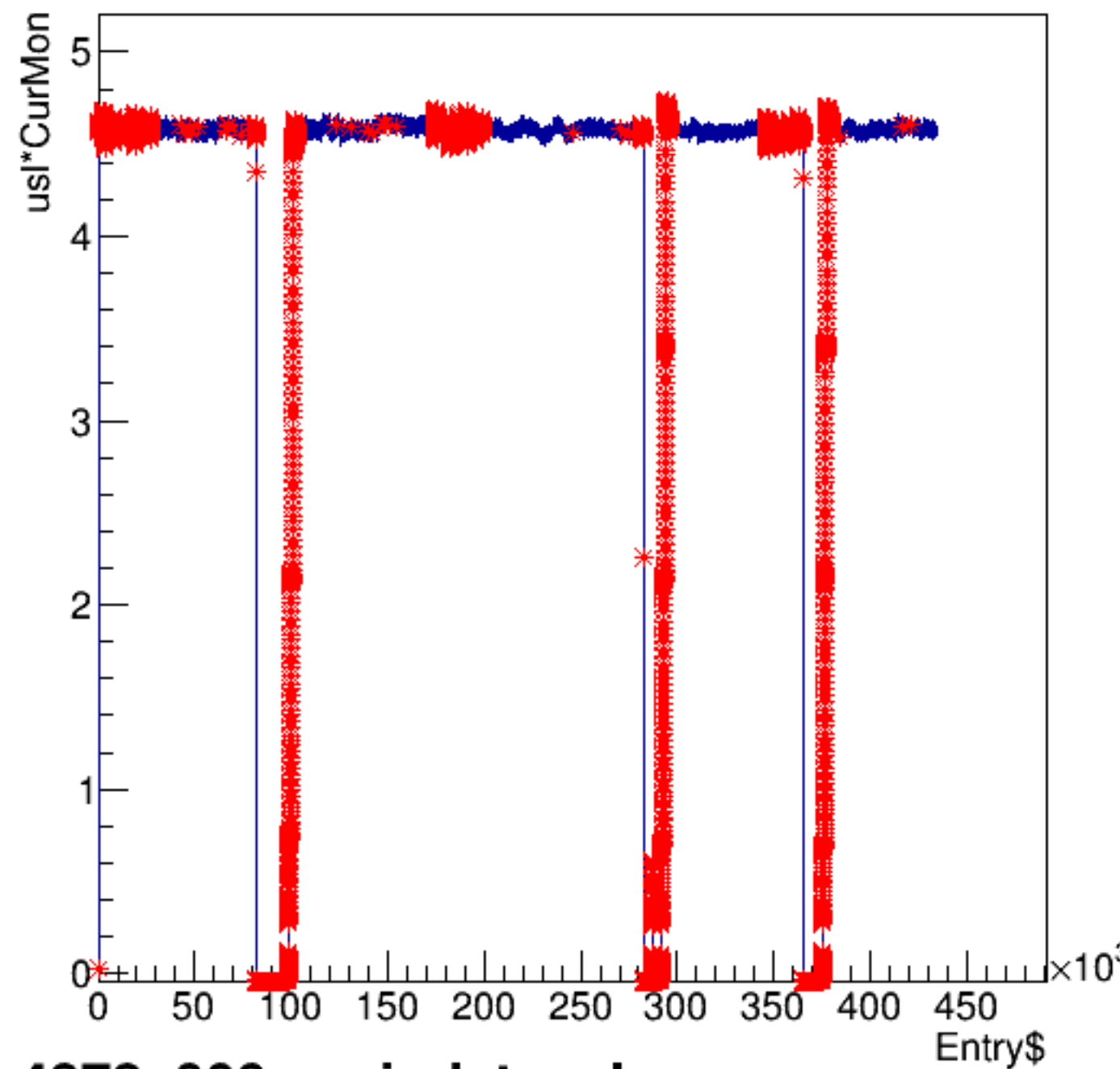
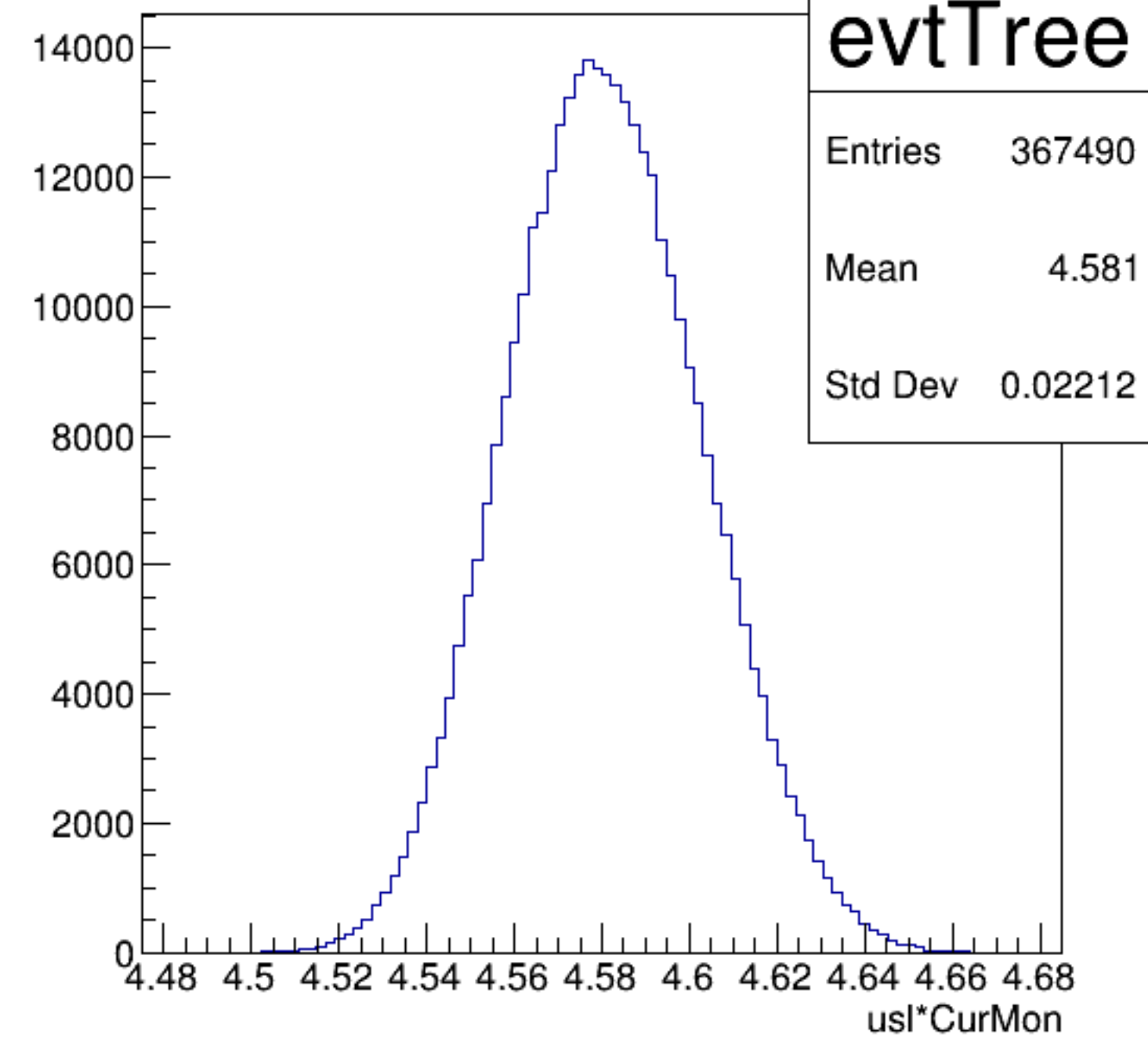


usl*CurMon:Entry\$



run4372_000_maindet_usl.png

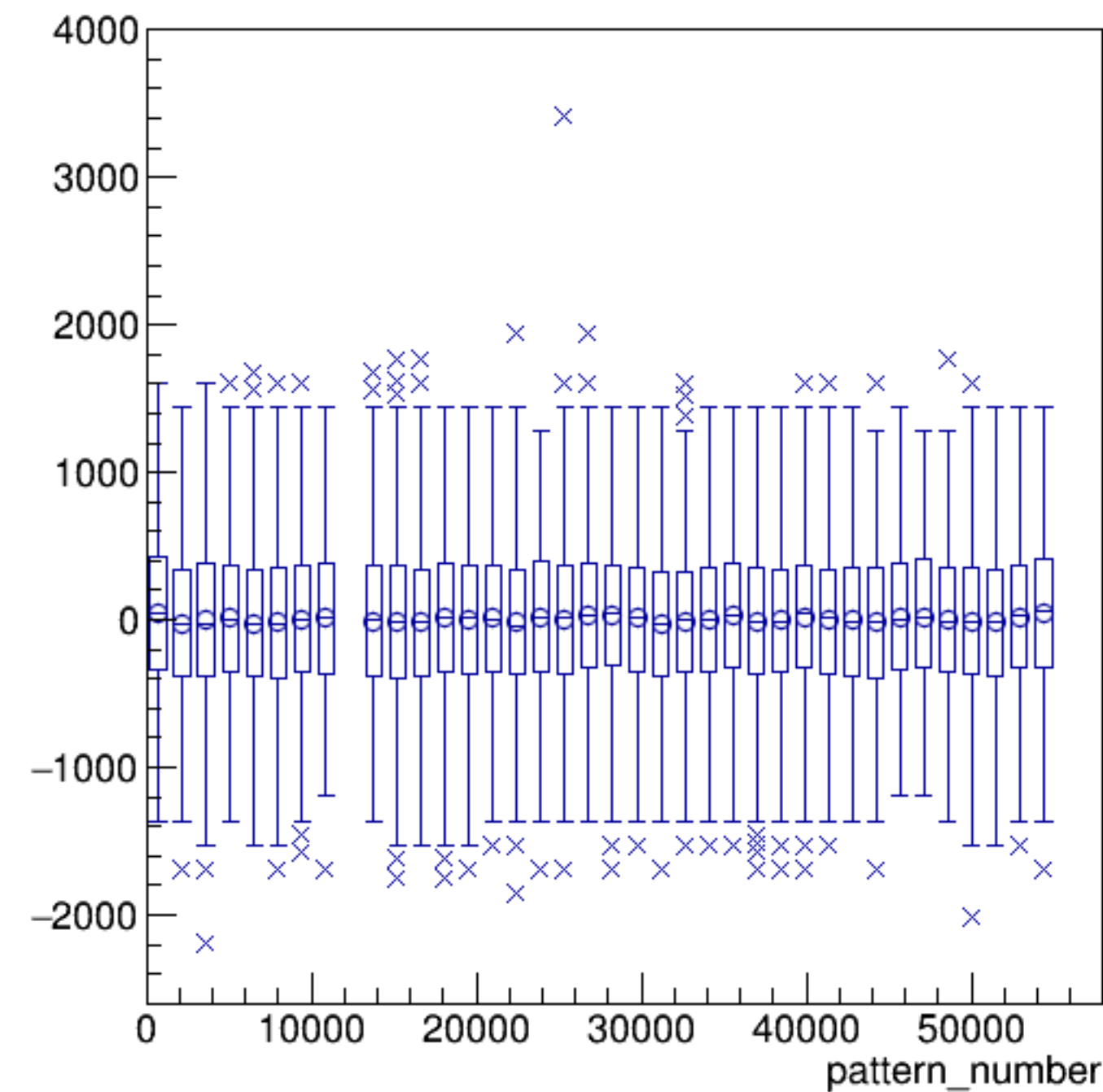
usl*CurMon {ErrorFlag==0}



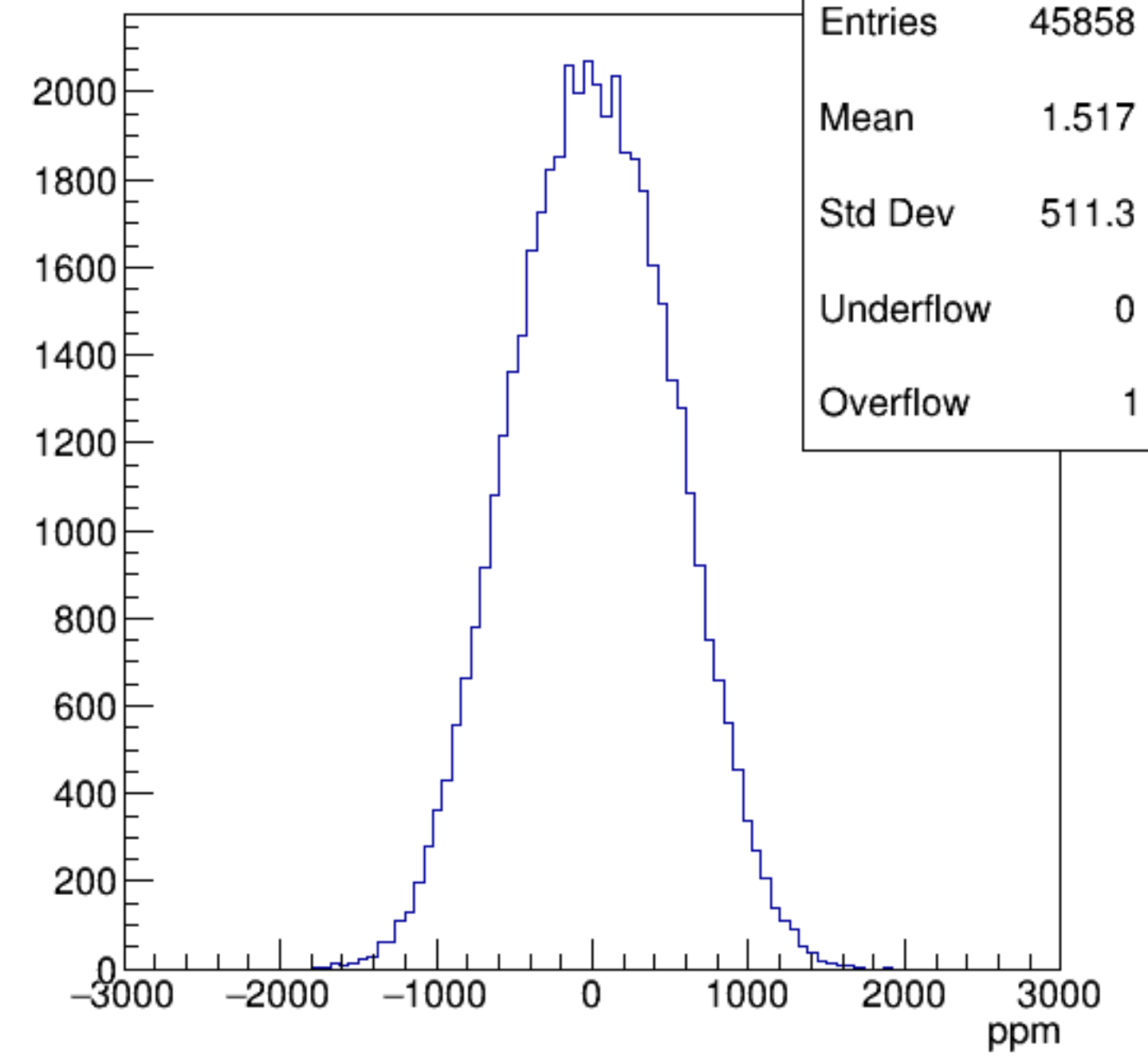
evtTree

Entries	367490
Mean	4.581
Std Dev	0.02212

asym_usl/ppm:pattern_number {ErrorFlag==0}

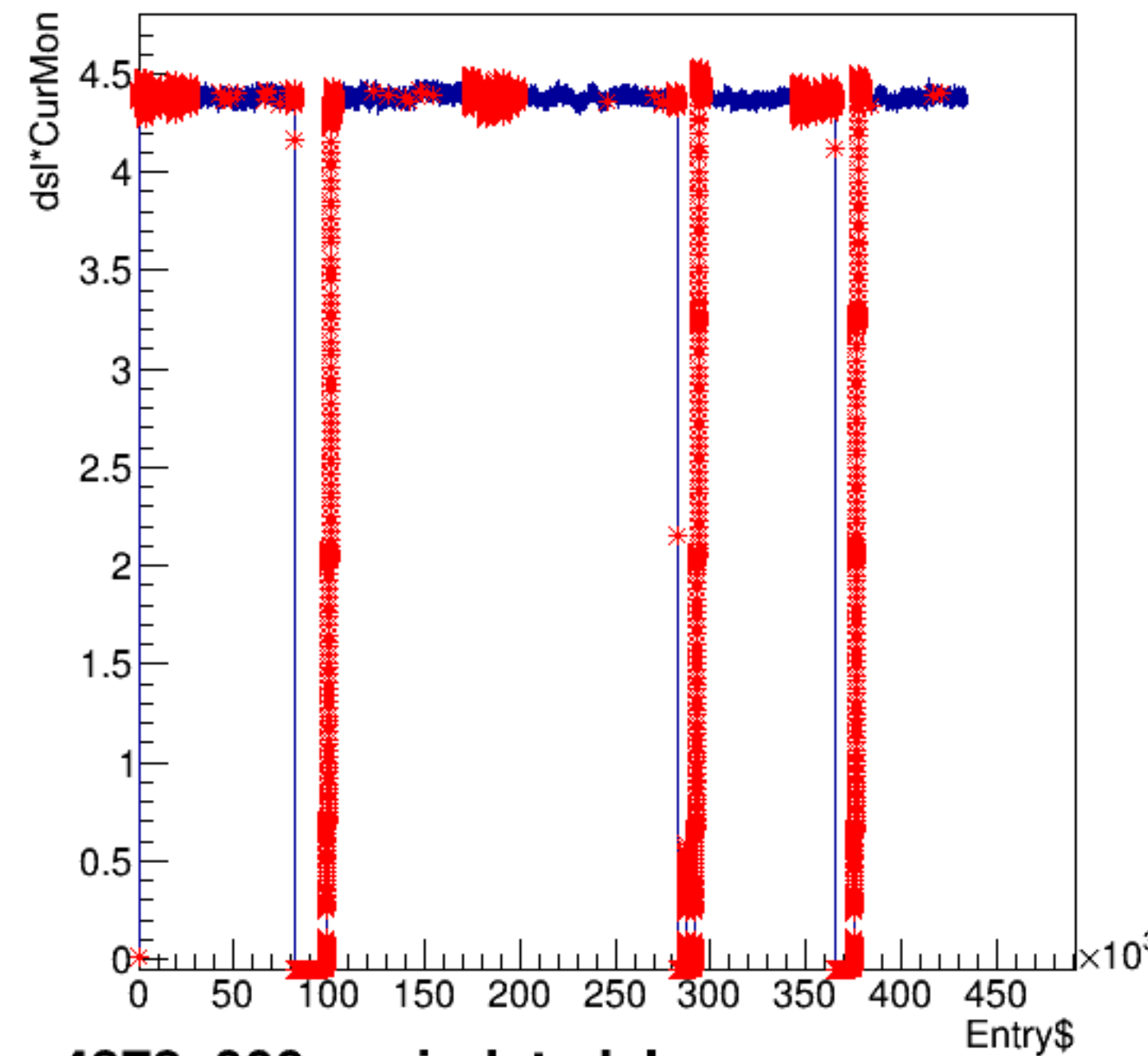


asym_usl



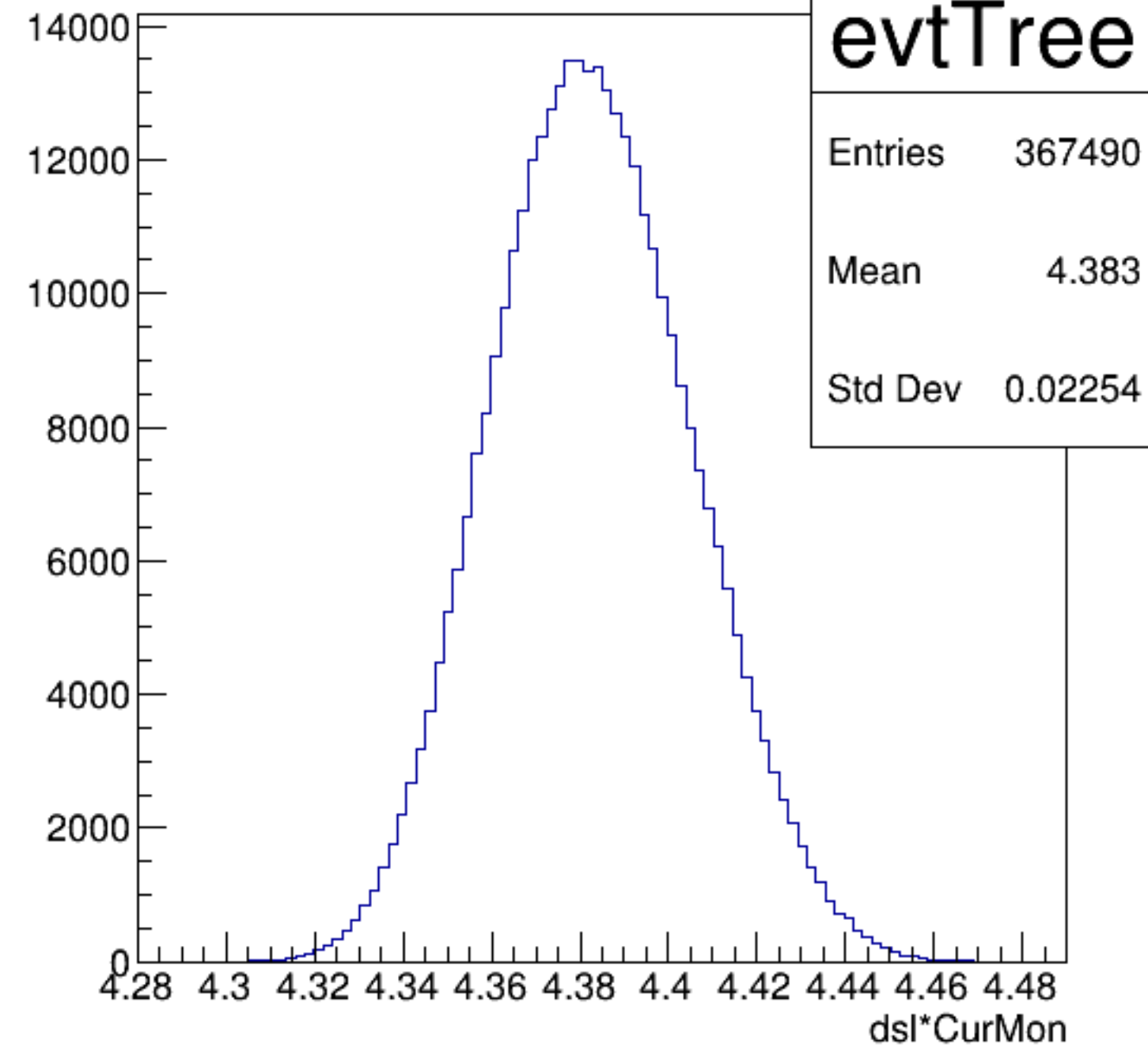
Entries	45858
Mean	1.517
Std Dev	511.3
Underflow	0
Overflow	1

dsl*CurMon:Entry\$

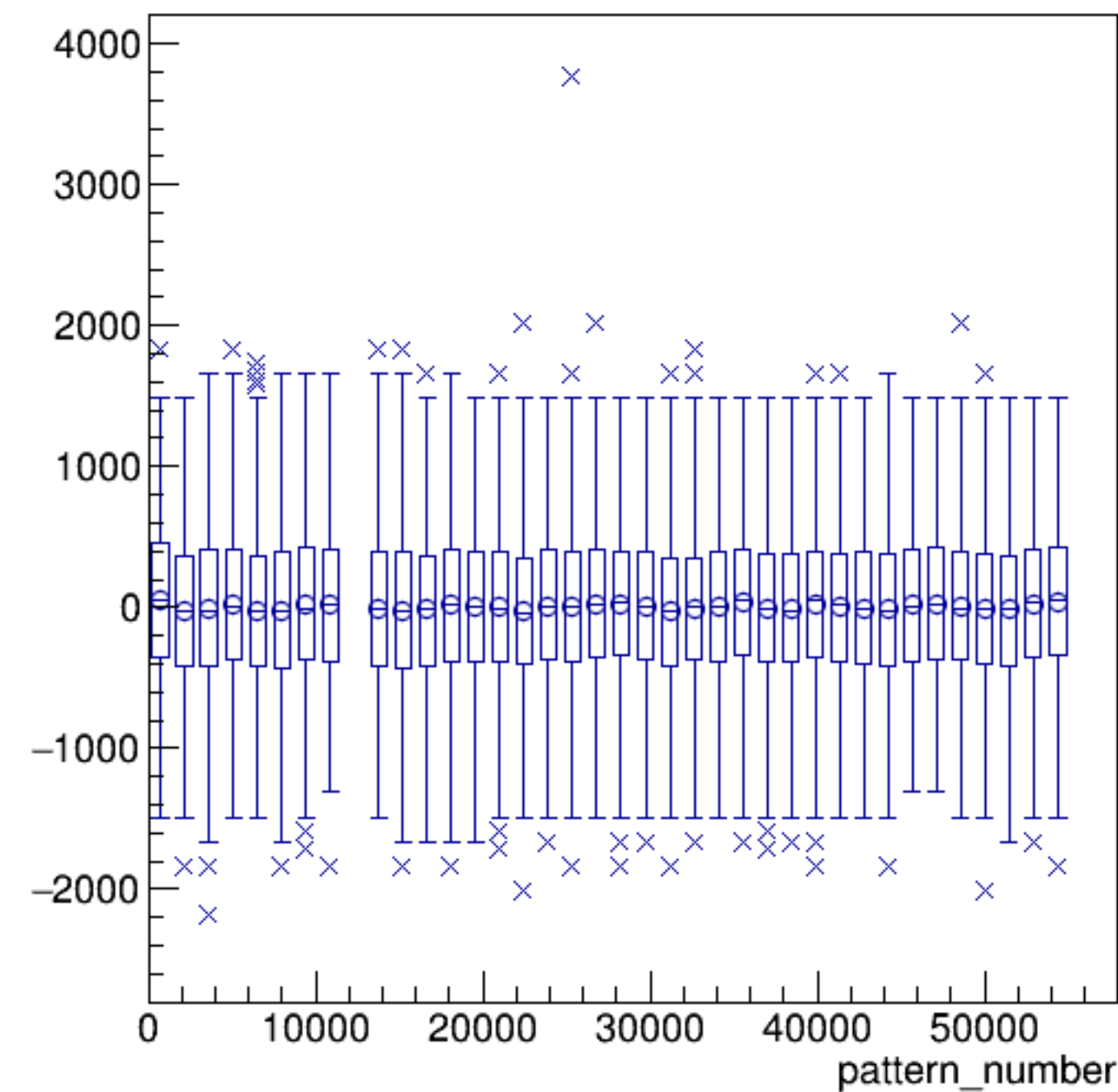


run4372_000_maindet_dsl.png

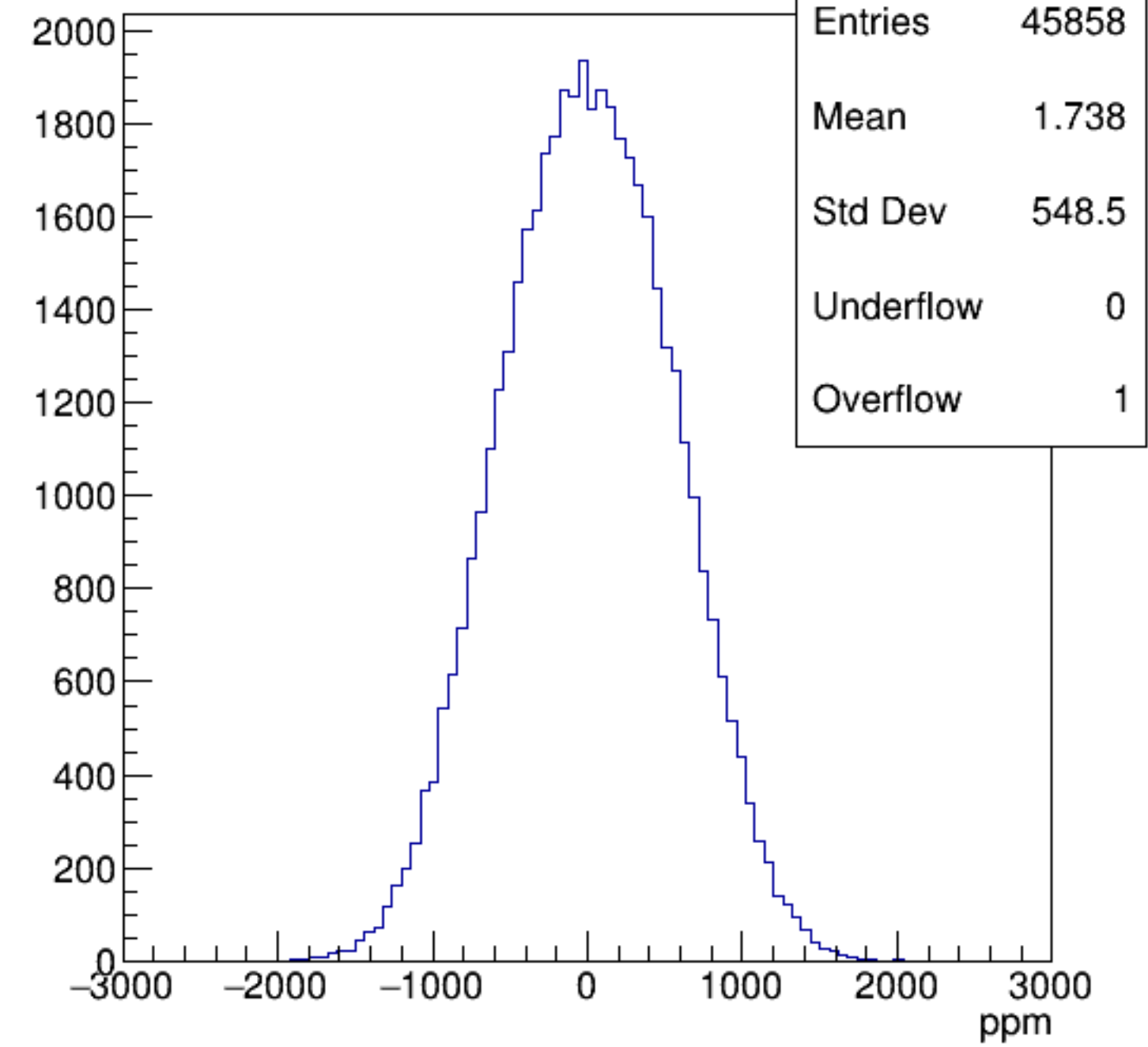
dsl*CurMon {ErrorFlag==0}



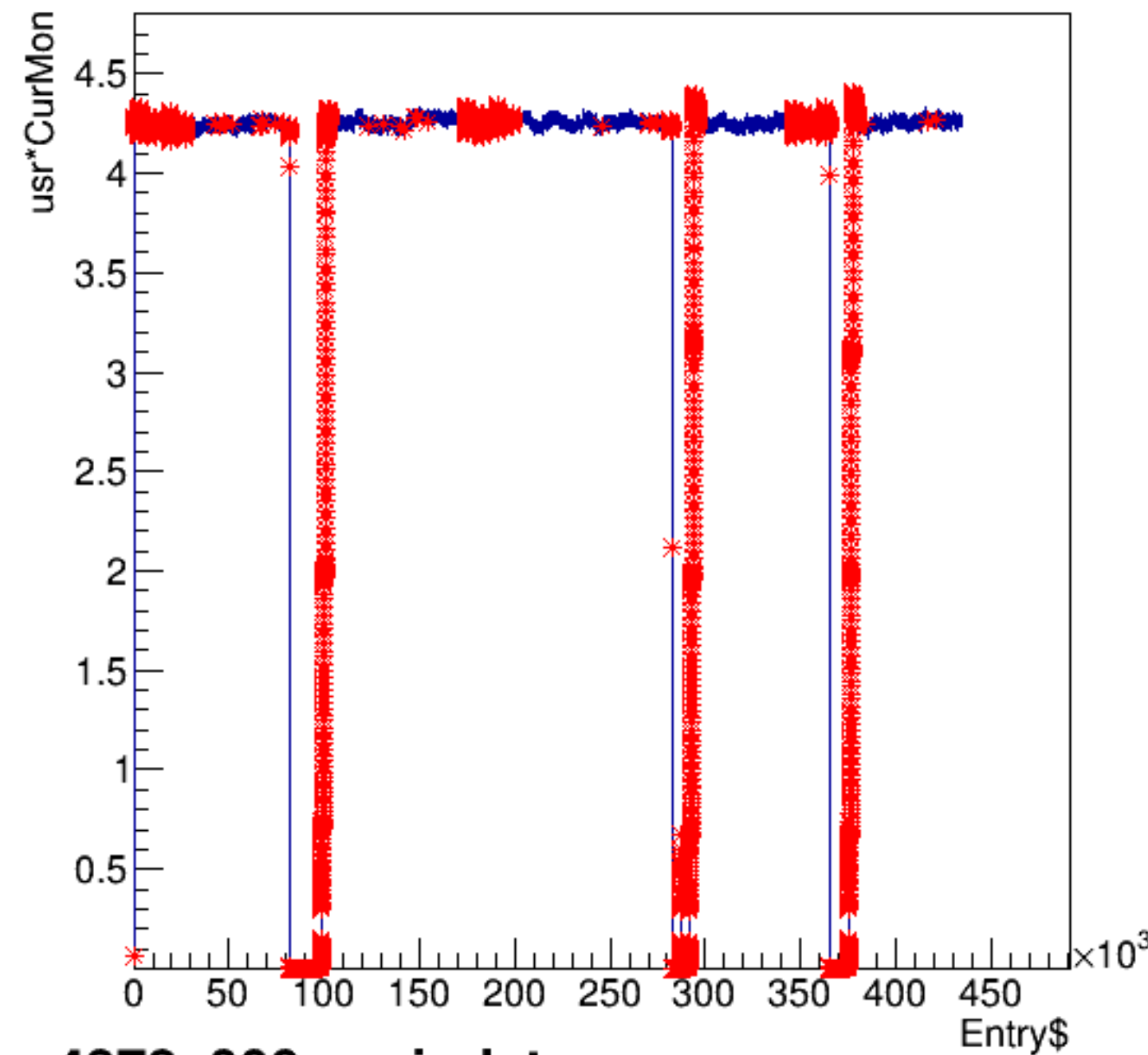
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl

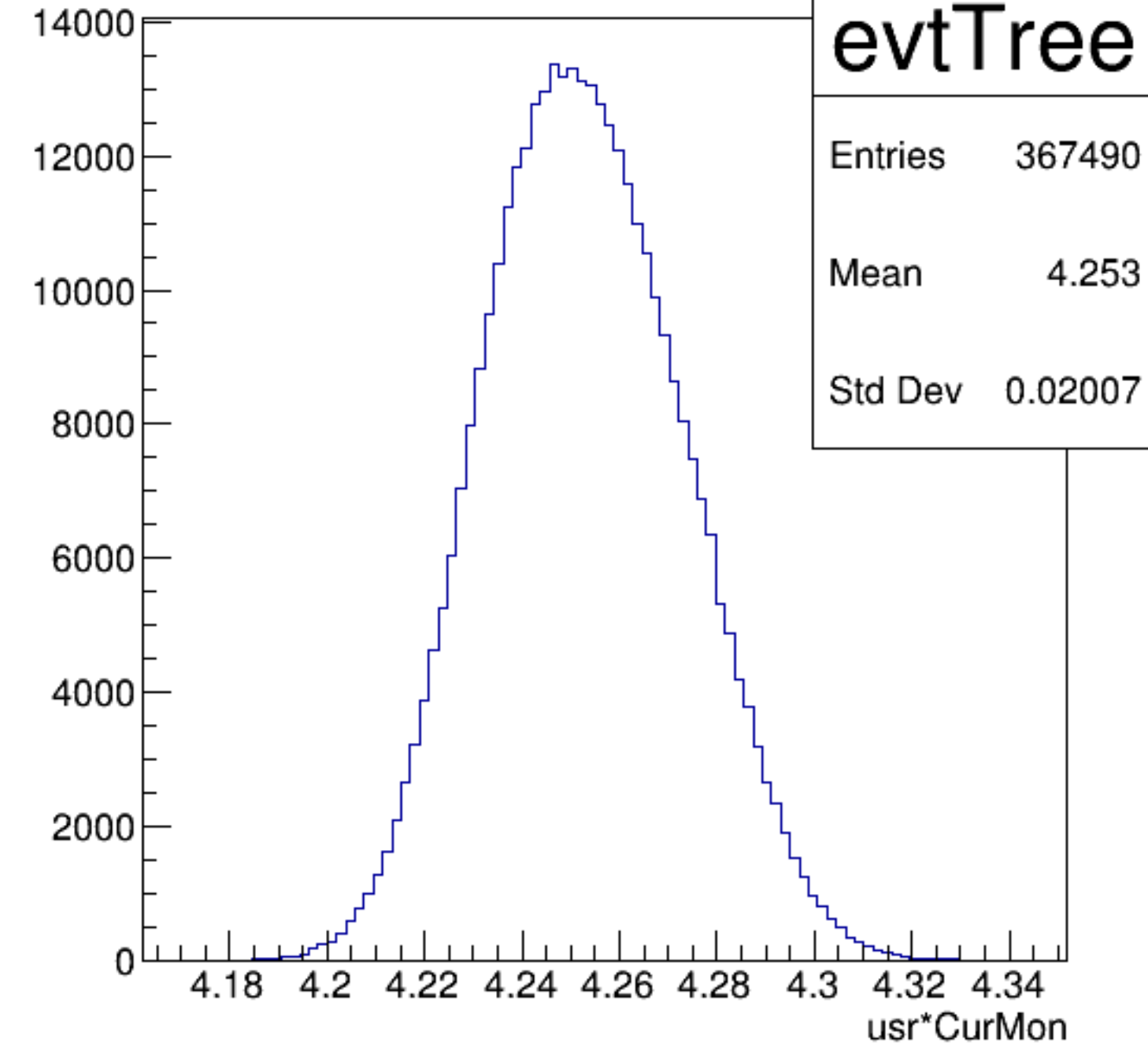


usr*CurMon:Entry\$

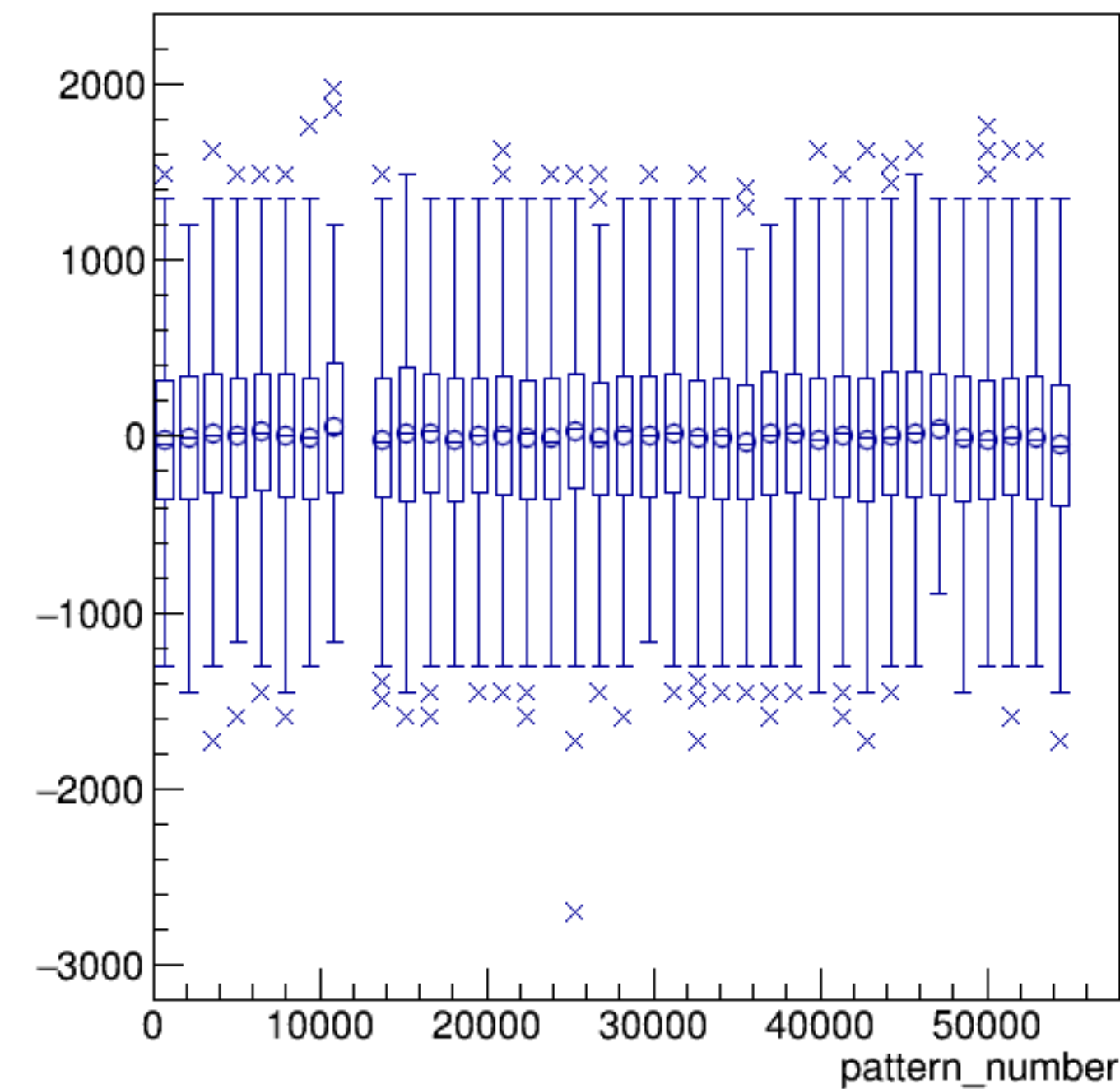


run4372_000_maindet_usr.png

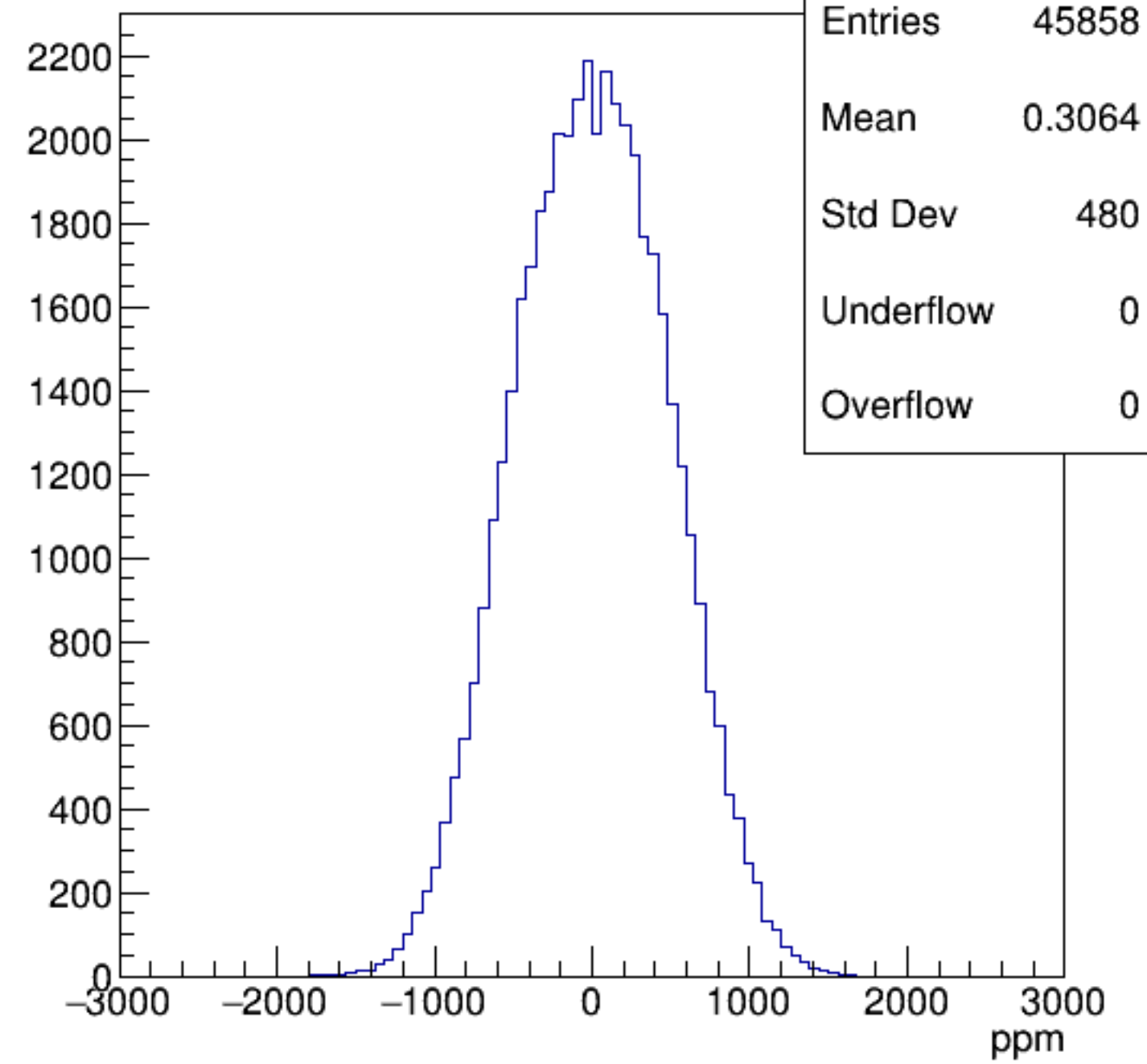
usr*CurMon {ErrorFlag==0}



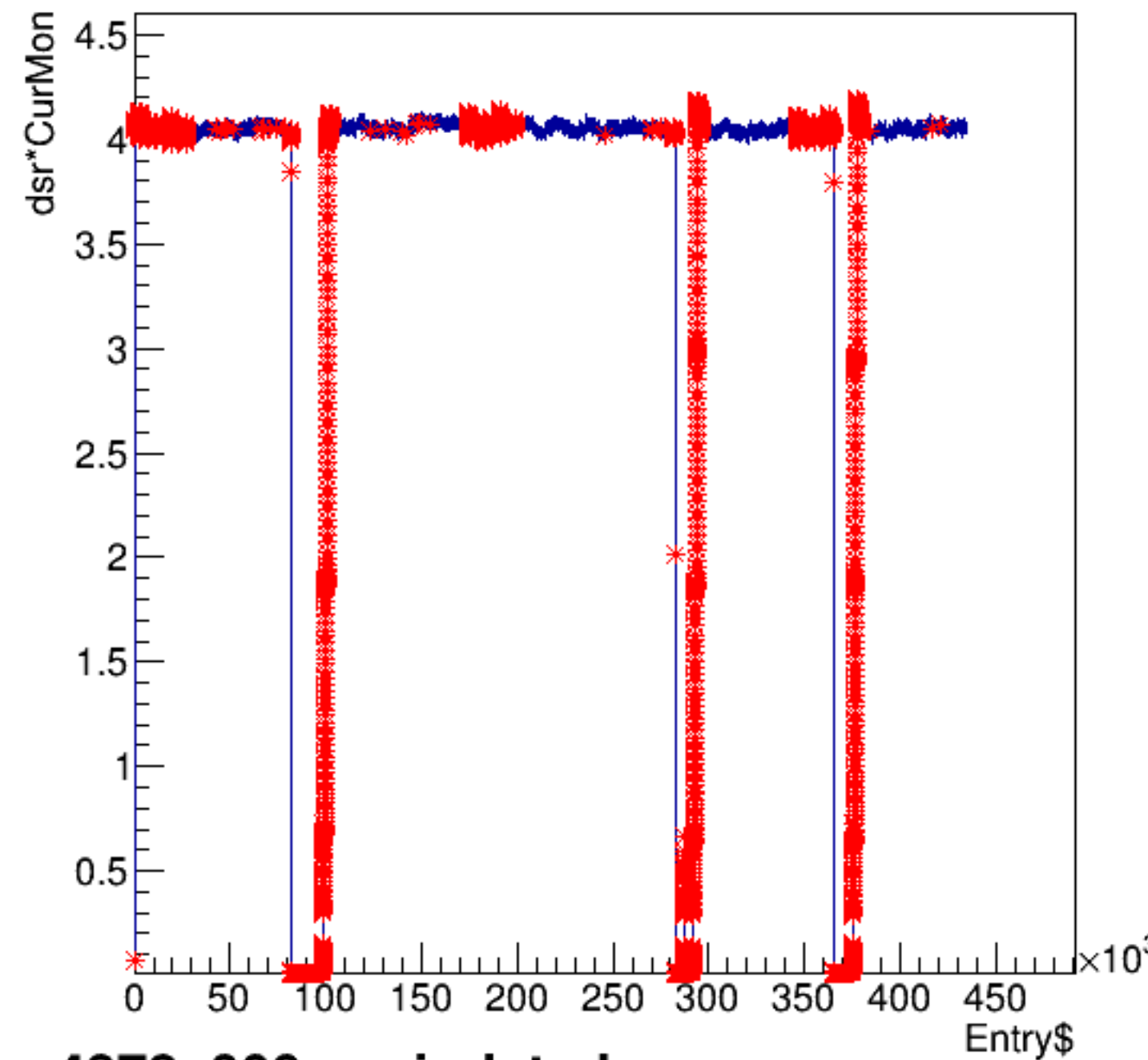
asym_usr/ppm:pattern_number {ErrorFlag==0}



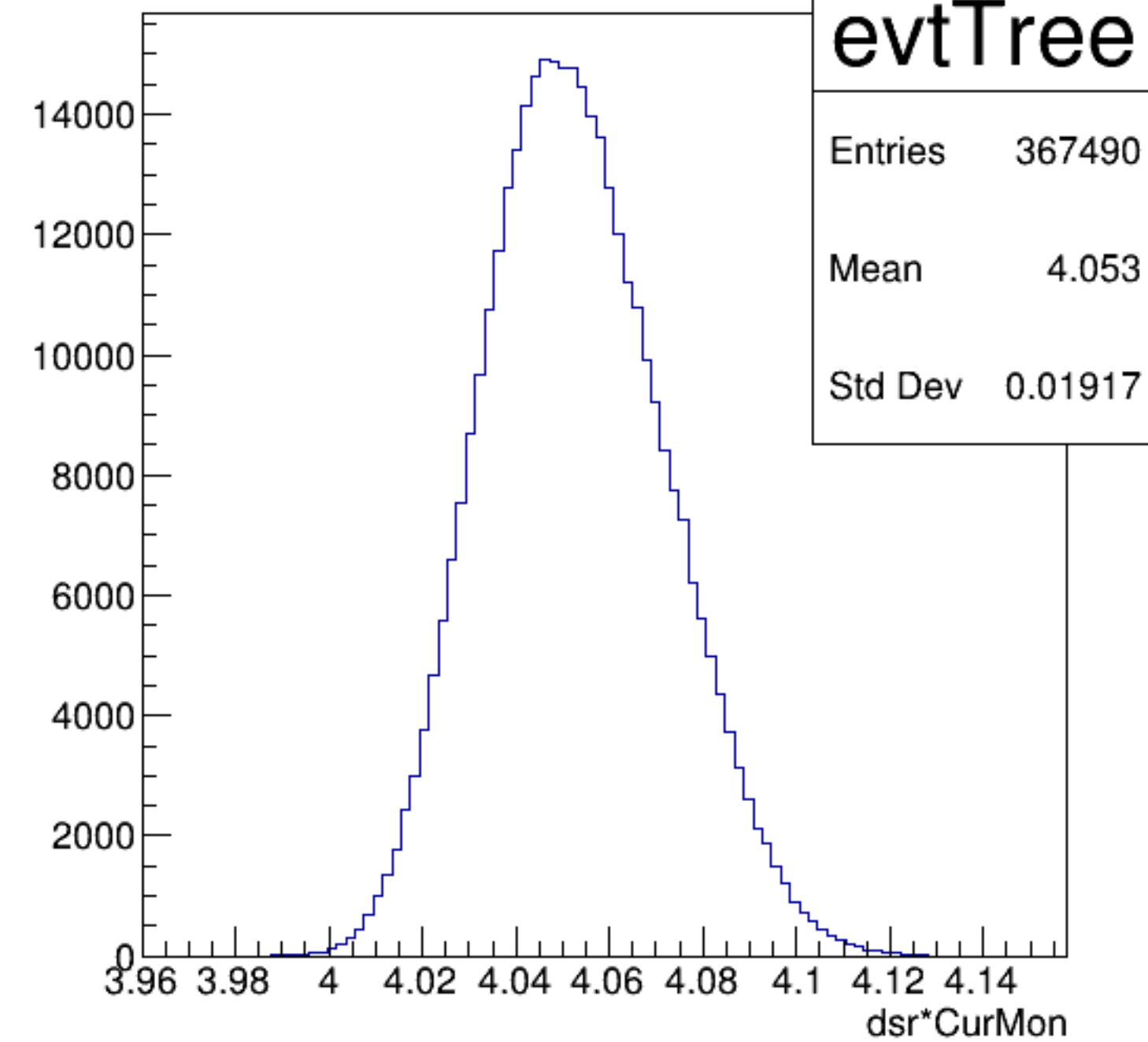
asym_usr



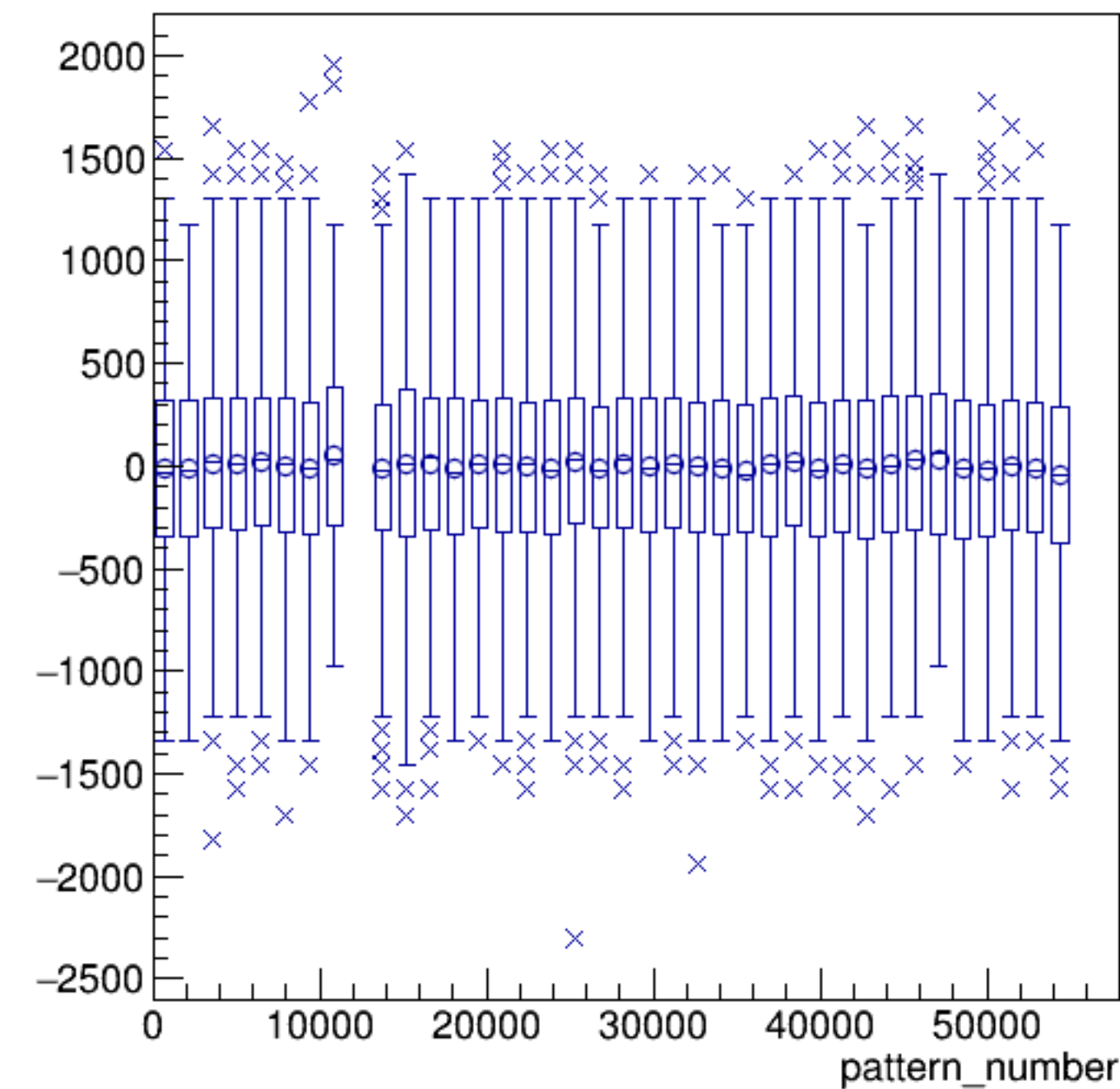
dsr*CurMon:Entry\$



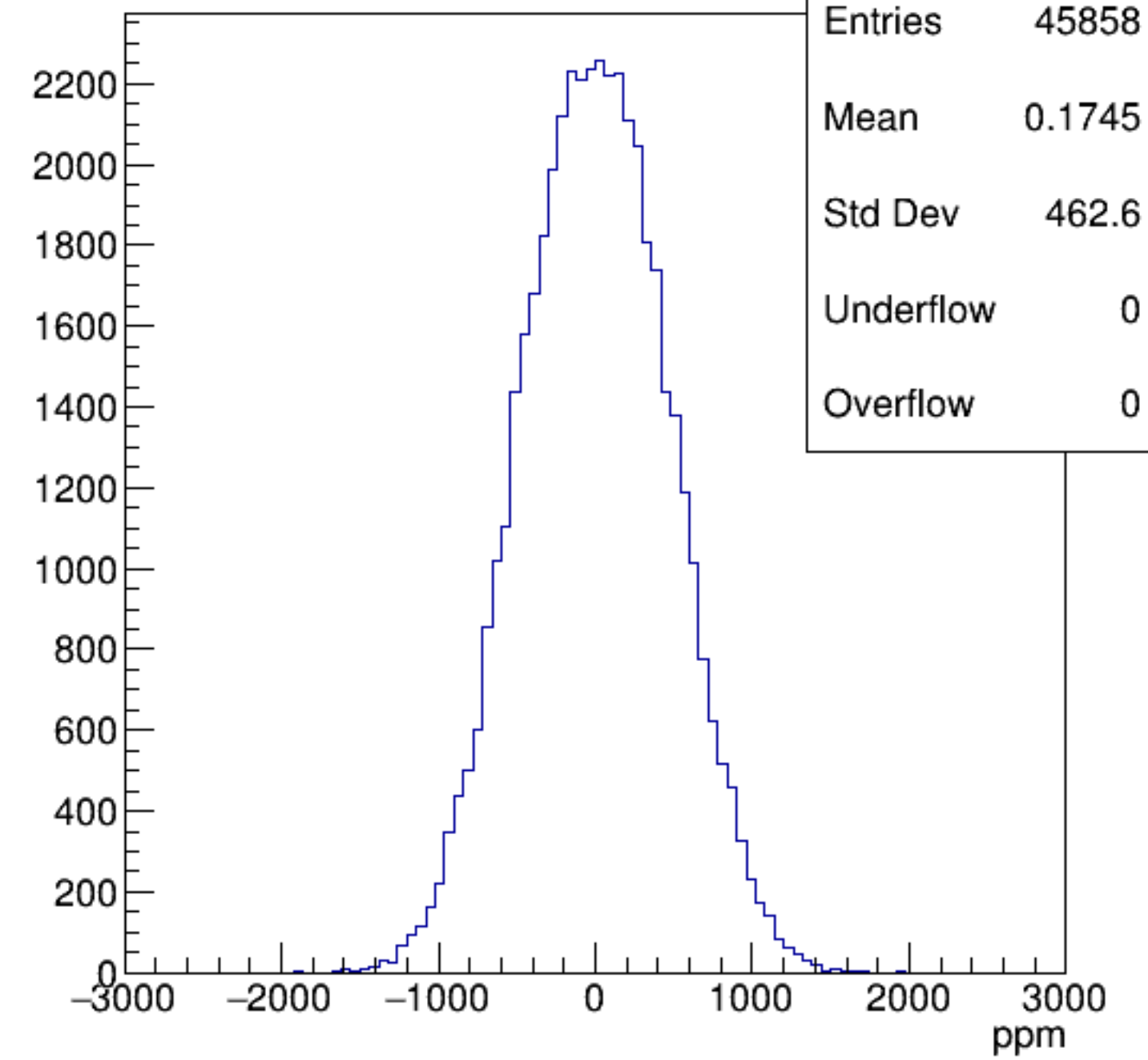
dsr*CurMon {ErrorFlag==0}



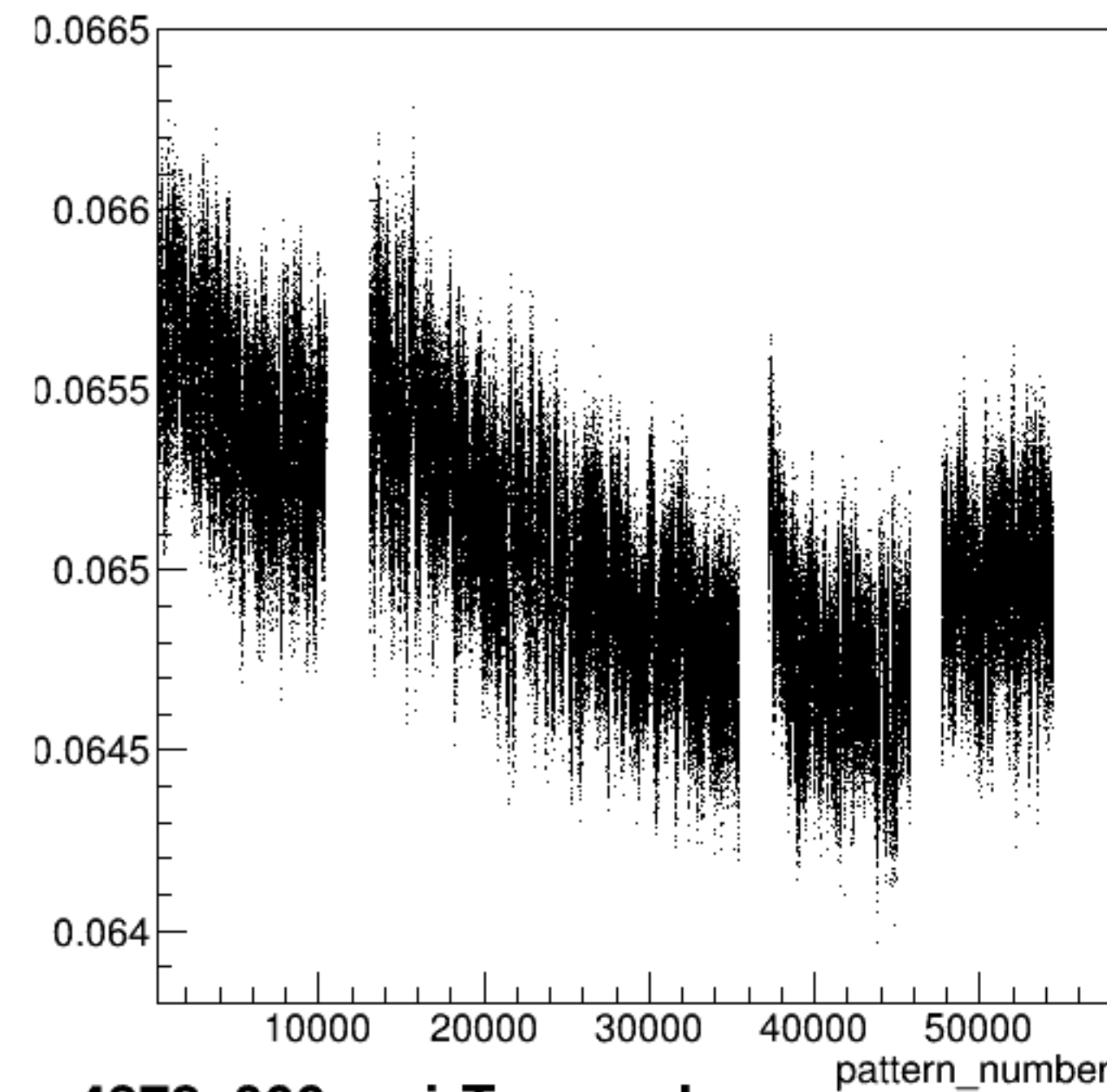
asym_dsr/ppm:pattern_number {ErrorFlag==0}



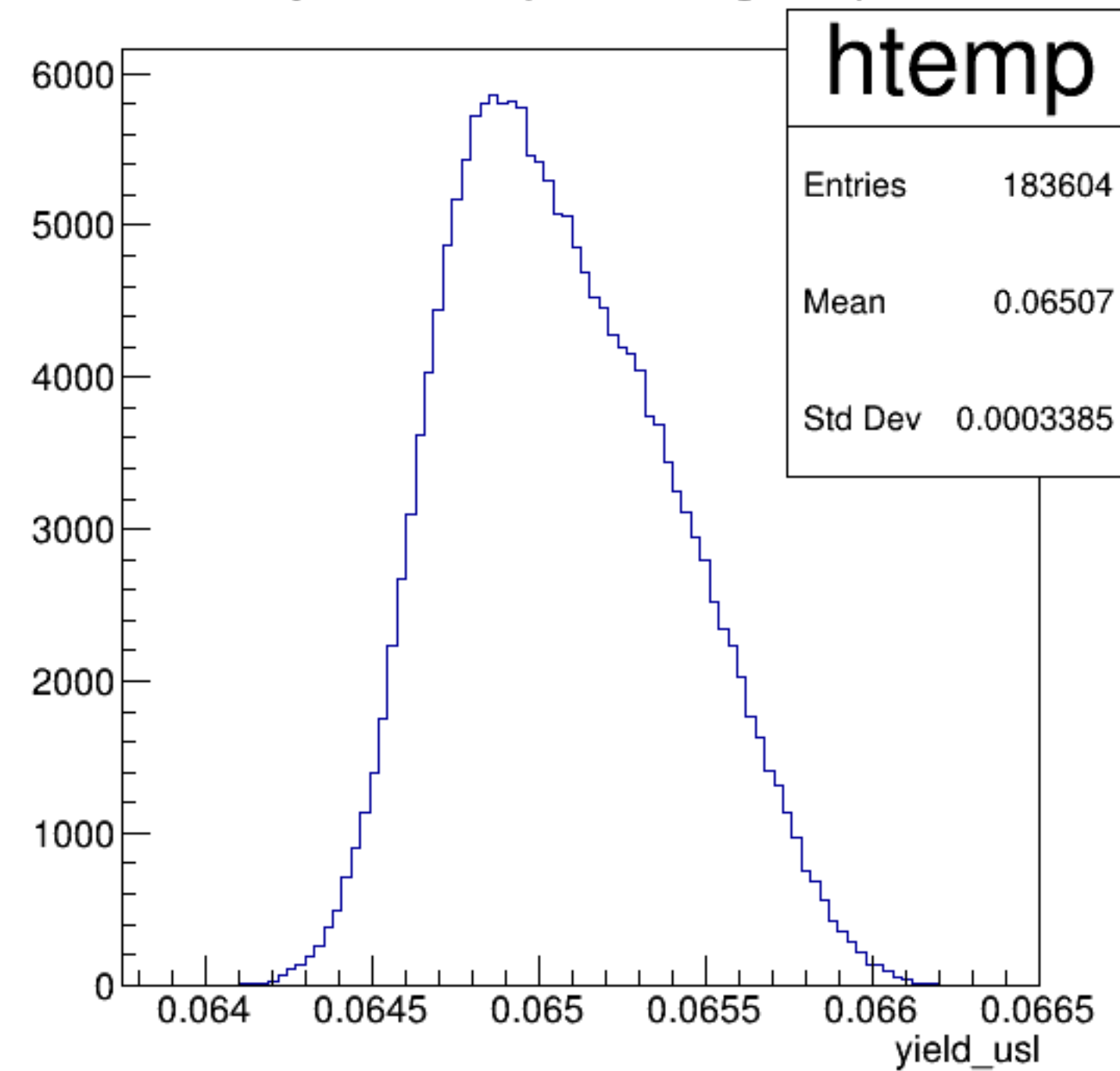
asym_dsr



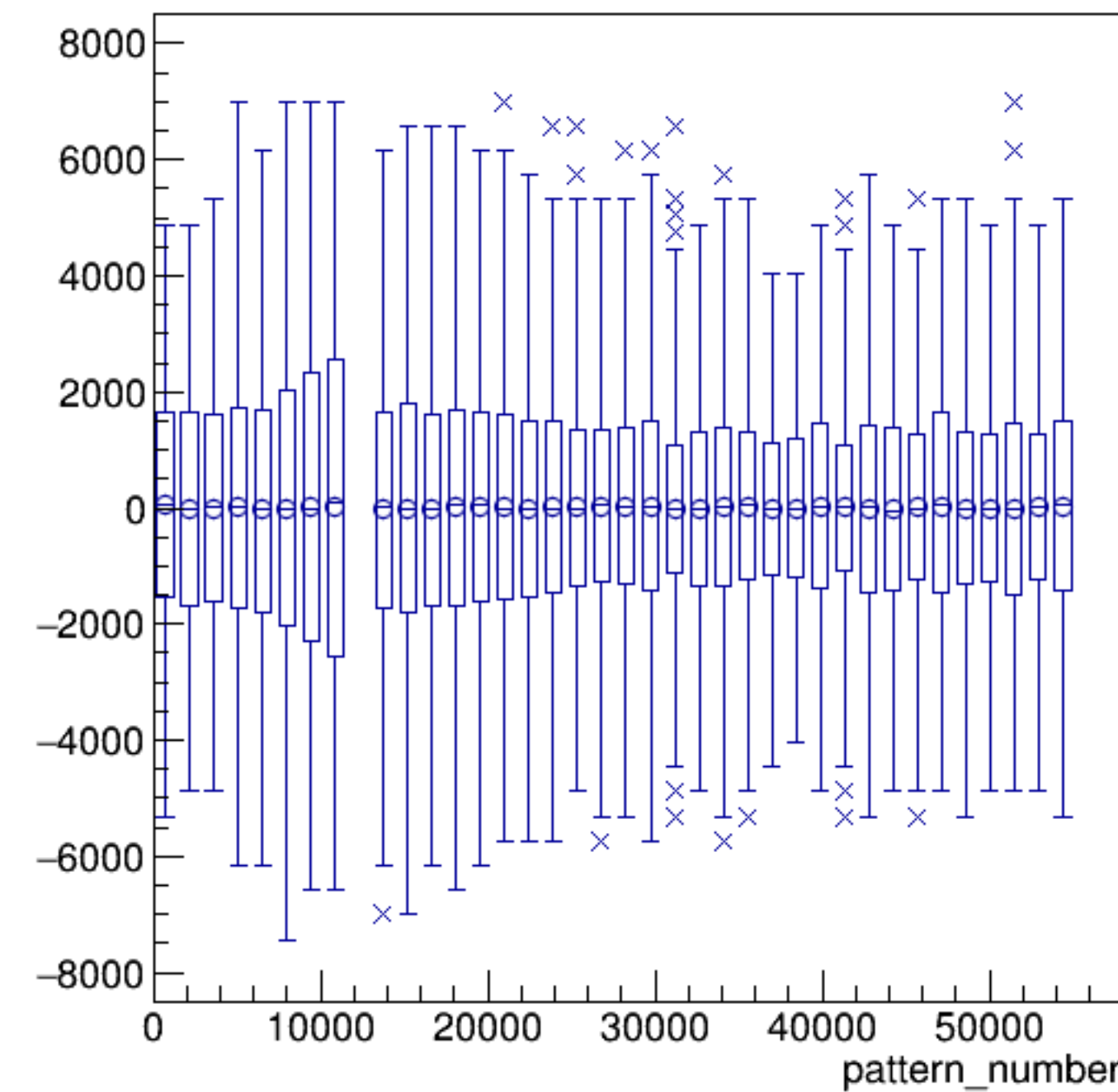
yield_usl:pattern_number {ErrorFlag==0}



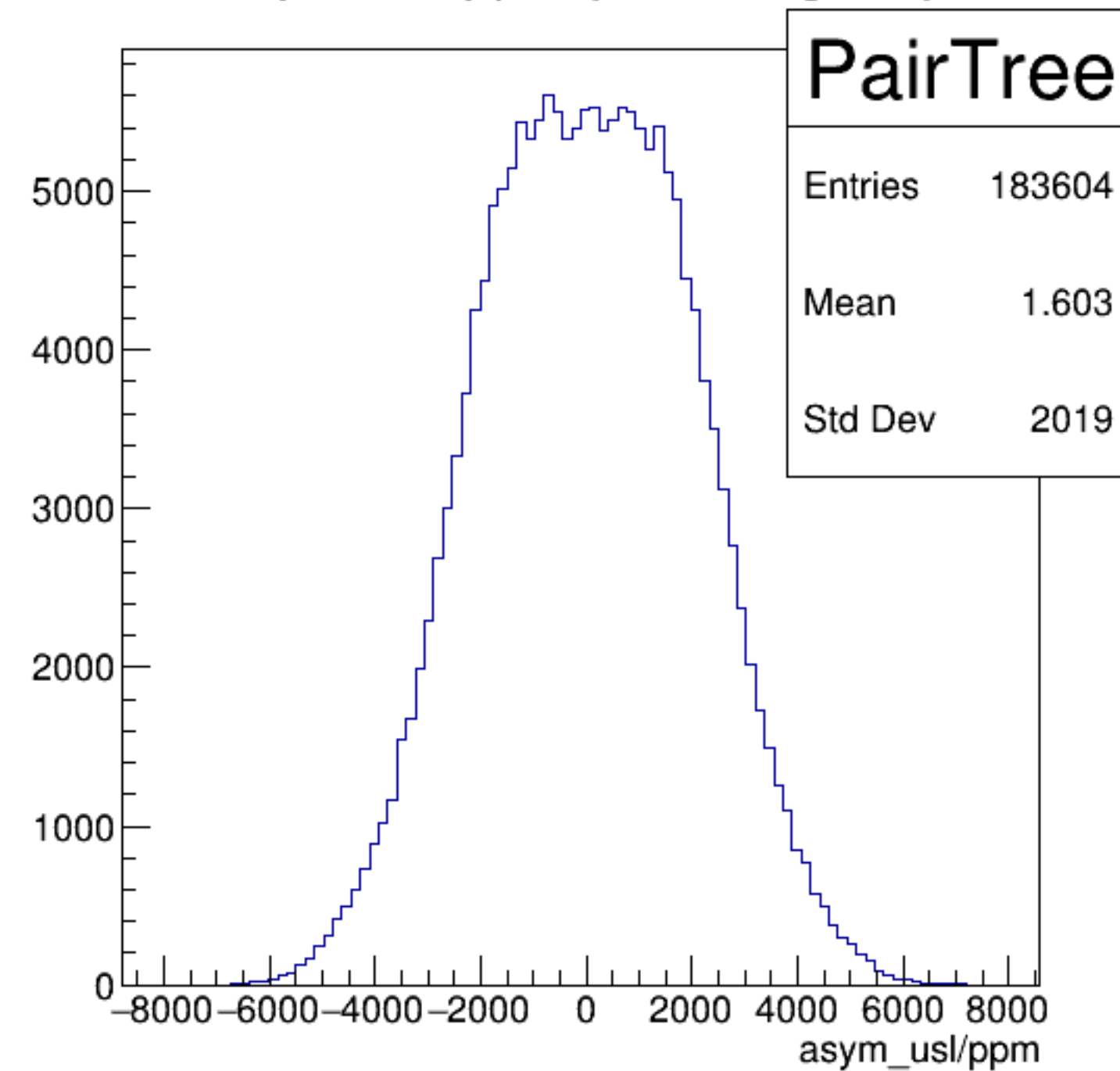
yield_usl {ErrorFlag==0}



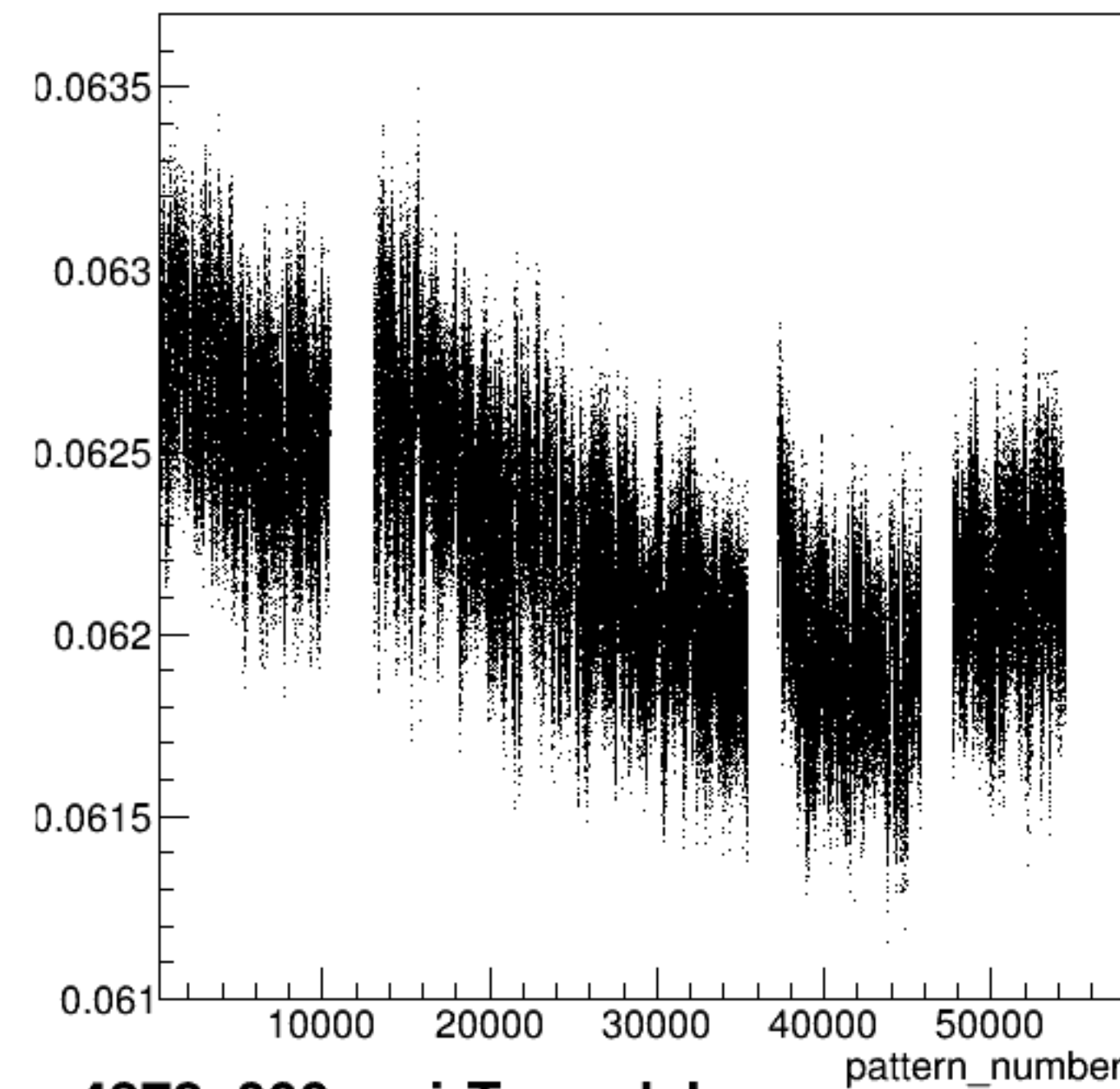
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl/ppm {ErrorFlag==0}

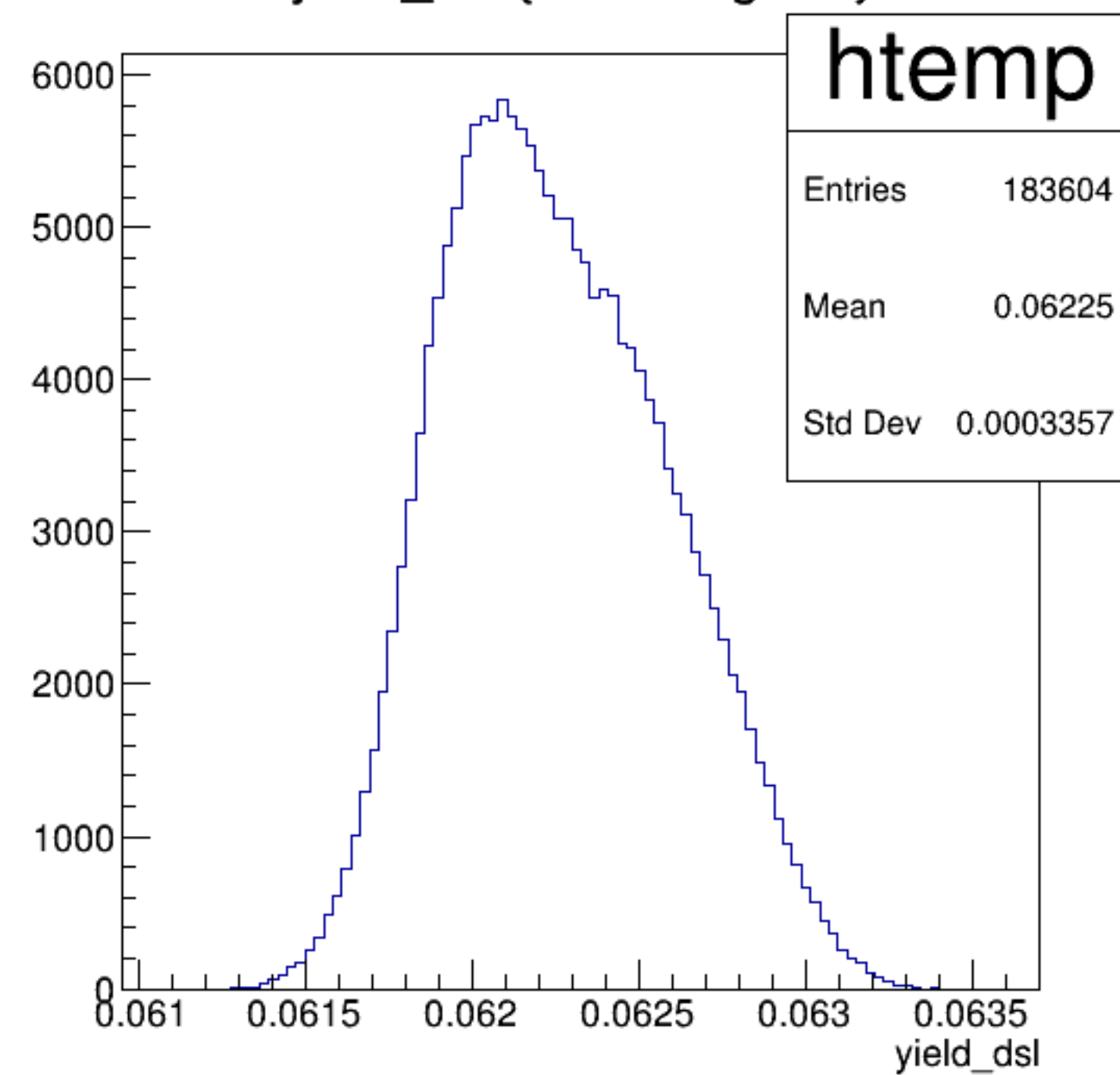


yield_dsl:pattern_number {ErrorFlag==0}

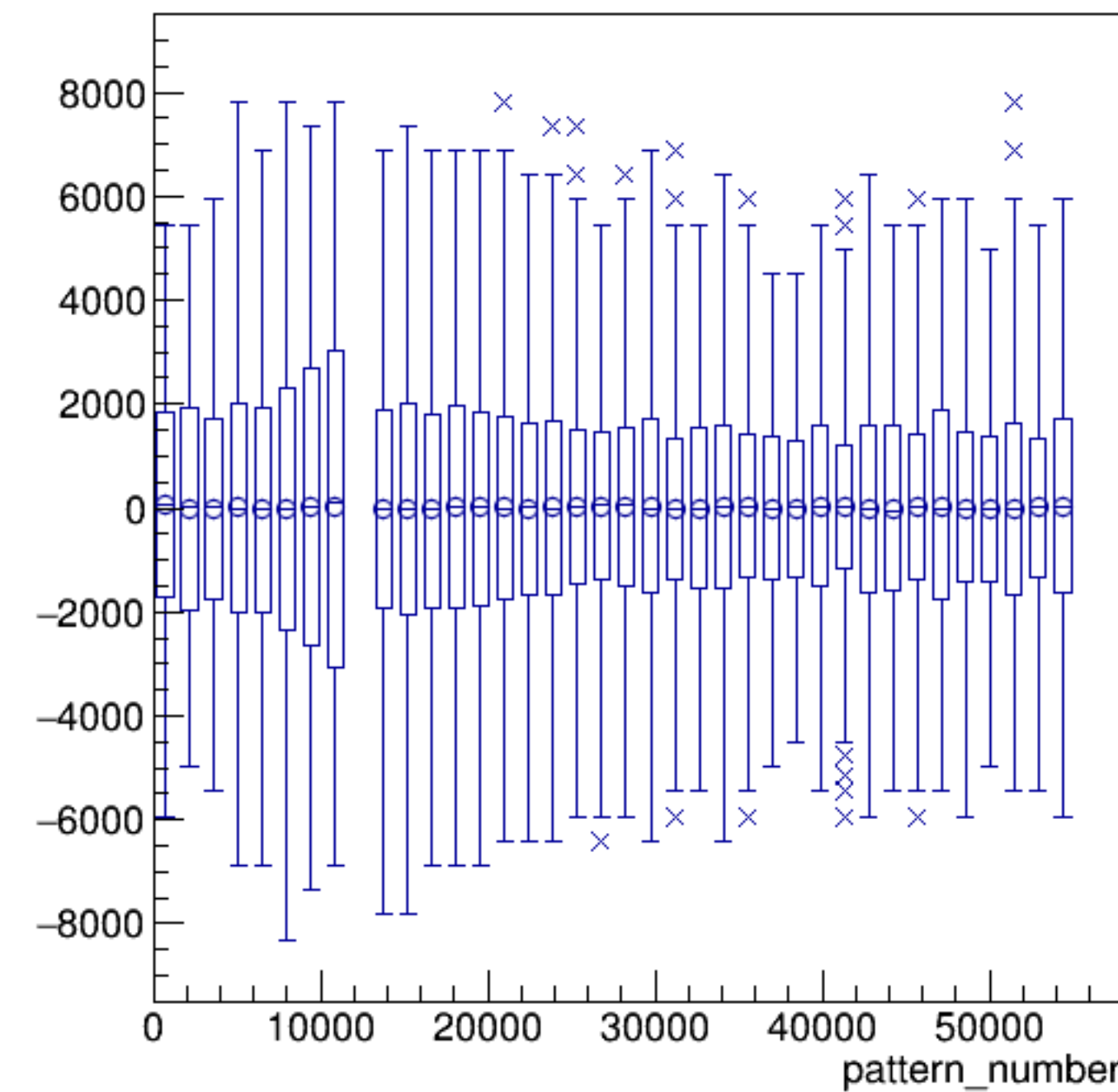


run4372_000_pairTree_dsl.png

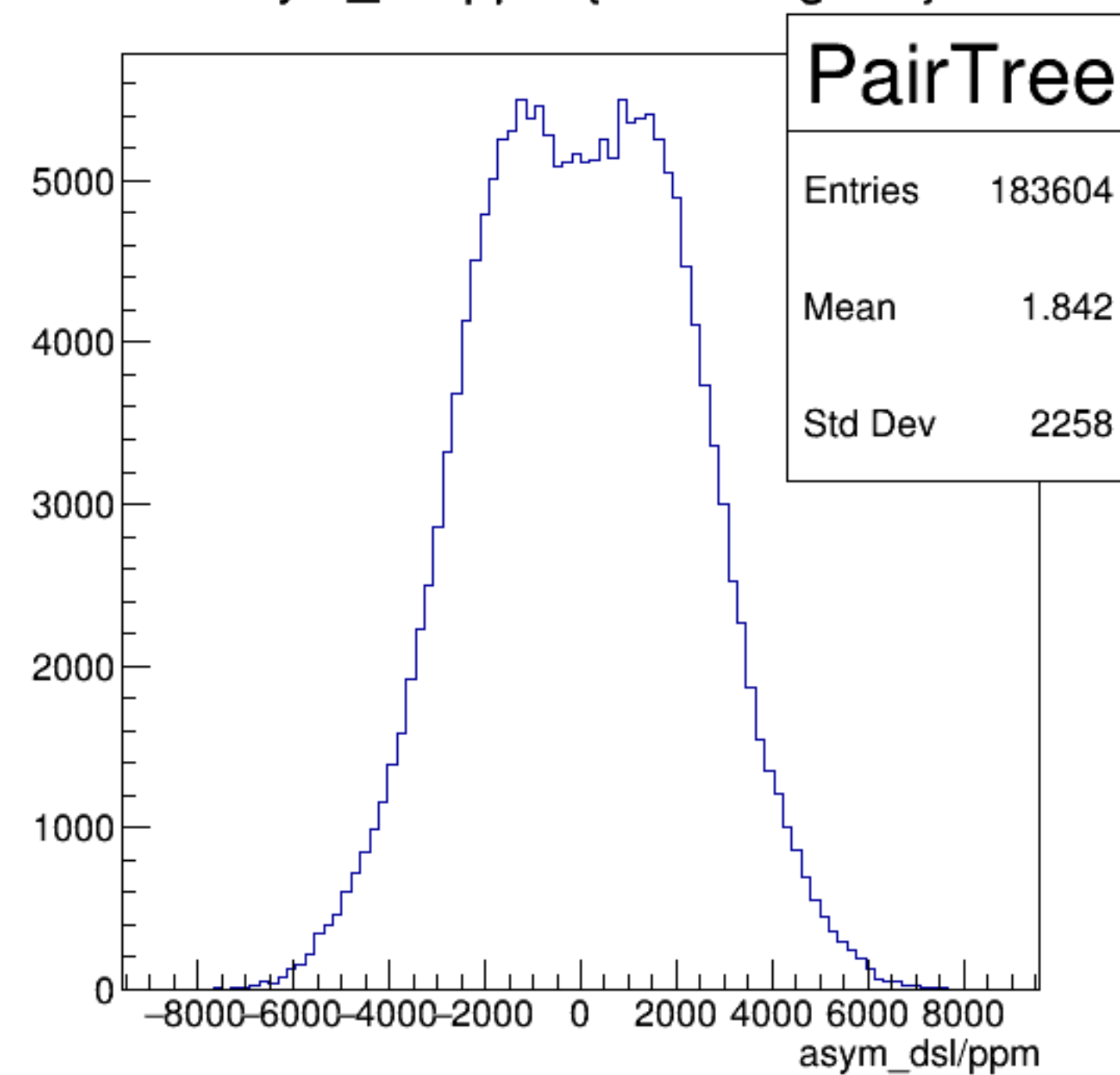
yield_dsl {ErrorFlag==0}

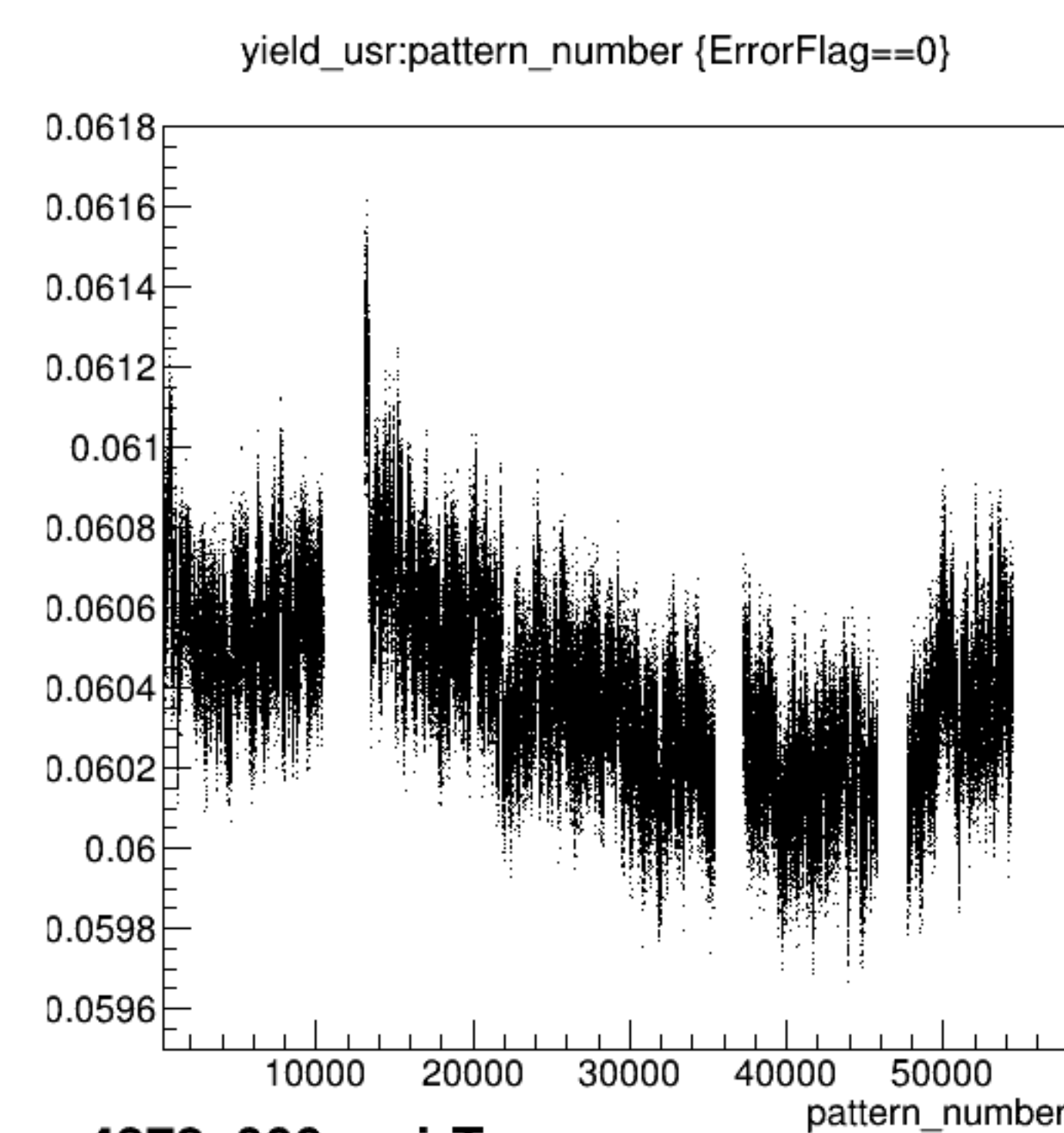


asym_dsl/ppm:pattern_number {ErrorFlag==0}

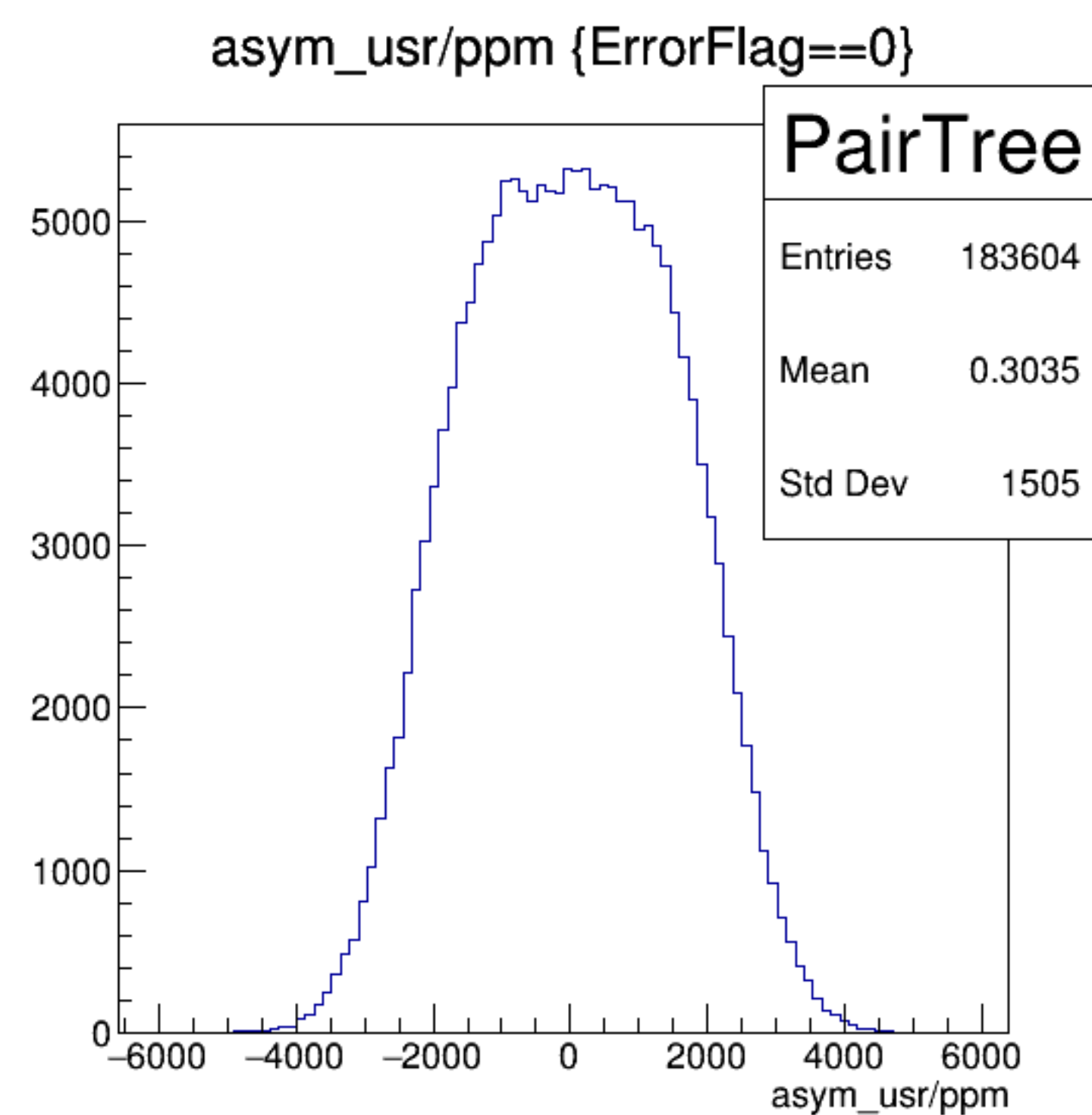
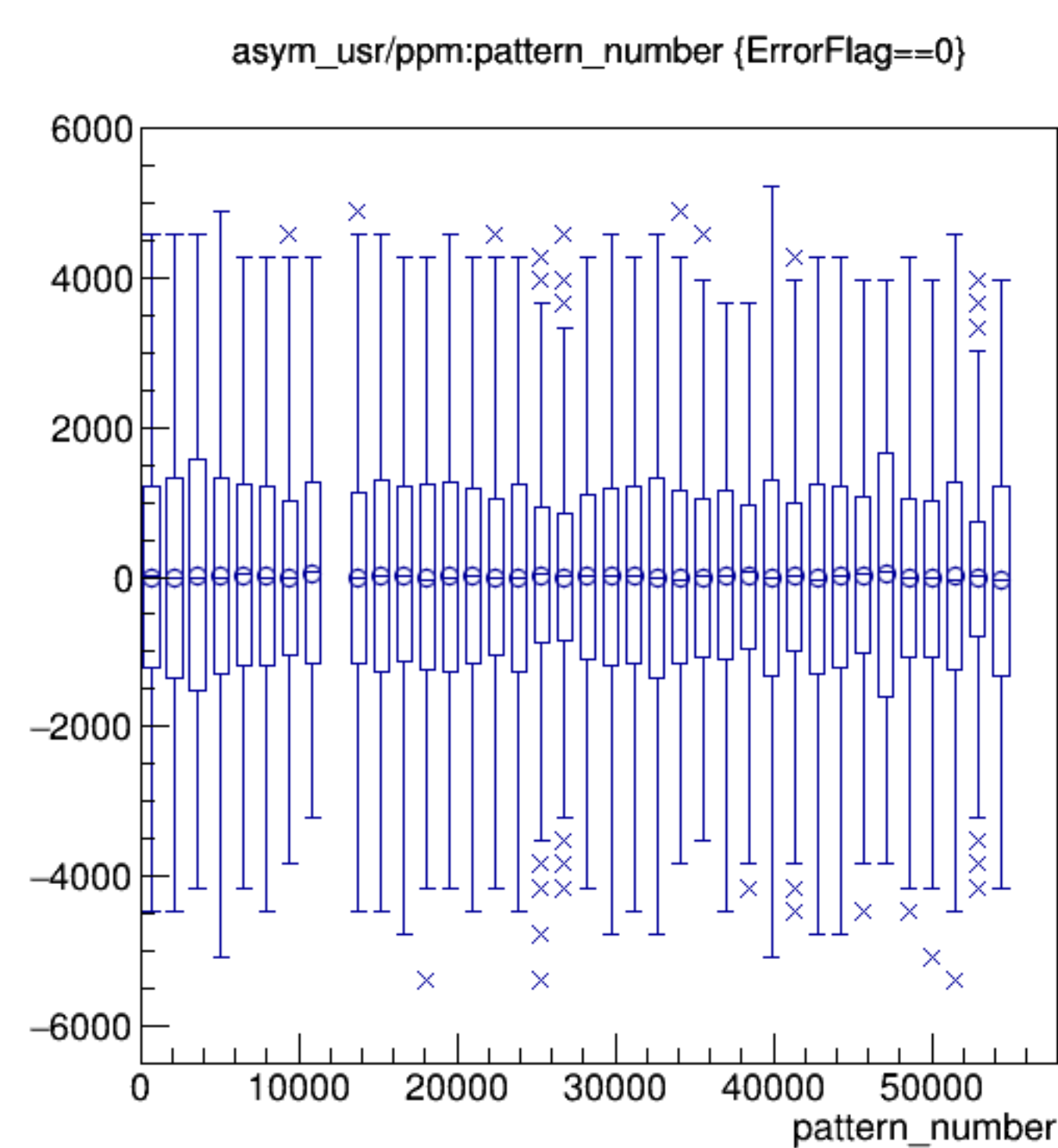
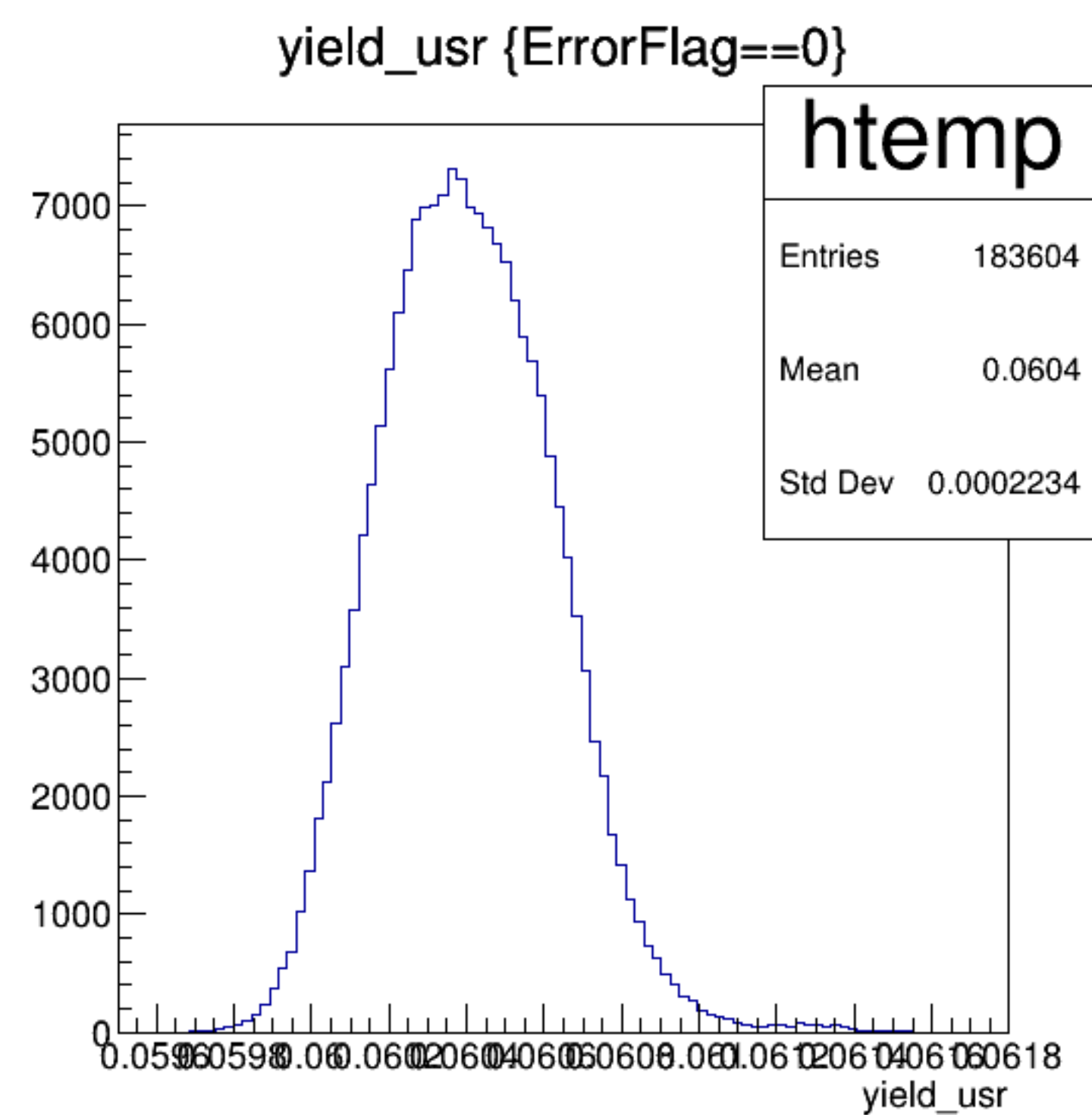


asym_dsl/ppm {ErrorFlag==0}

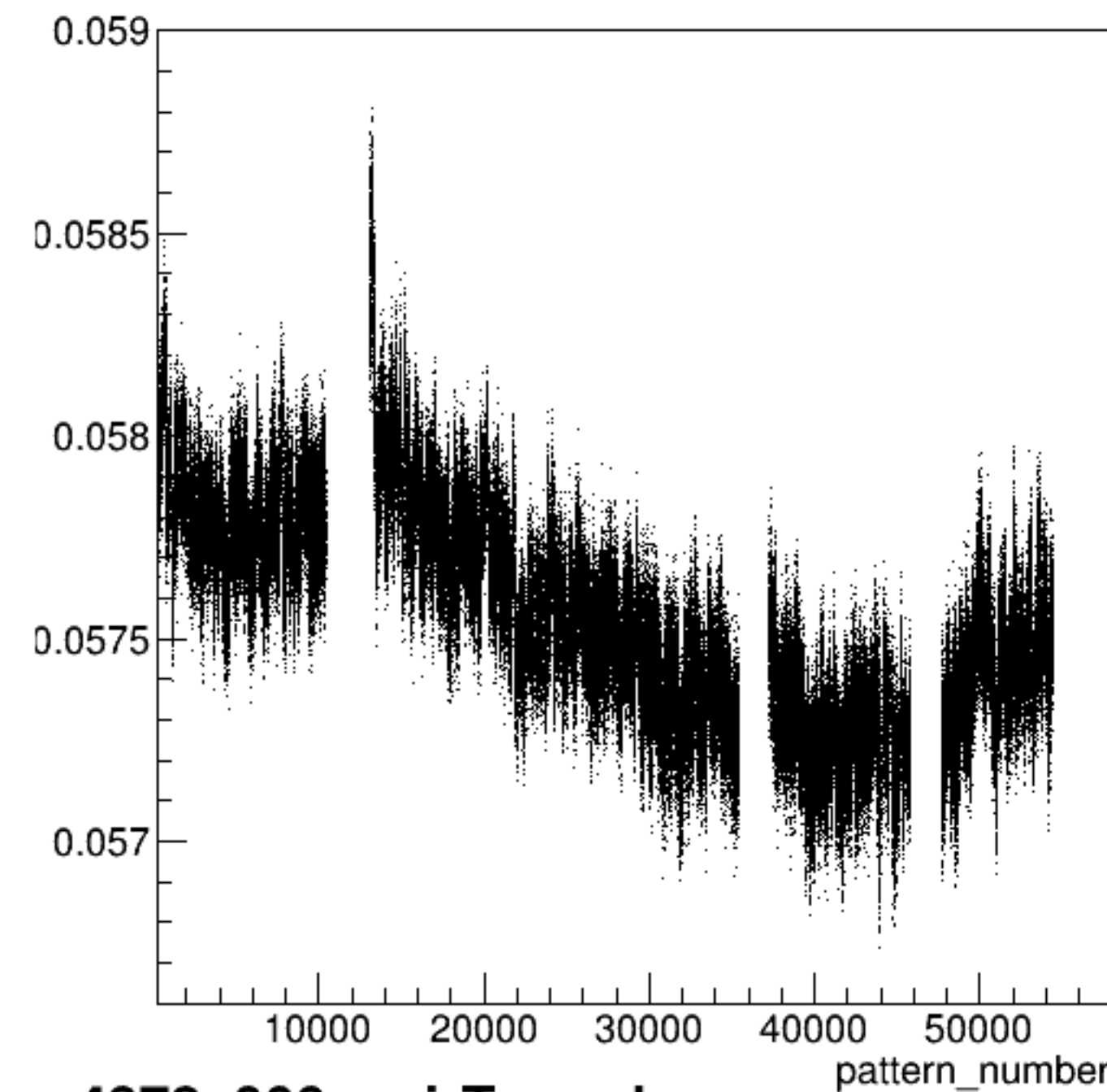




run4372_000_pairTree_usr.png

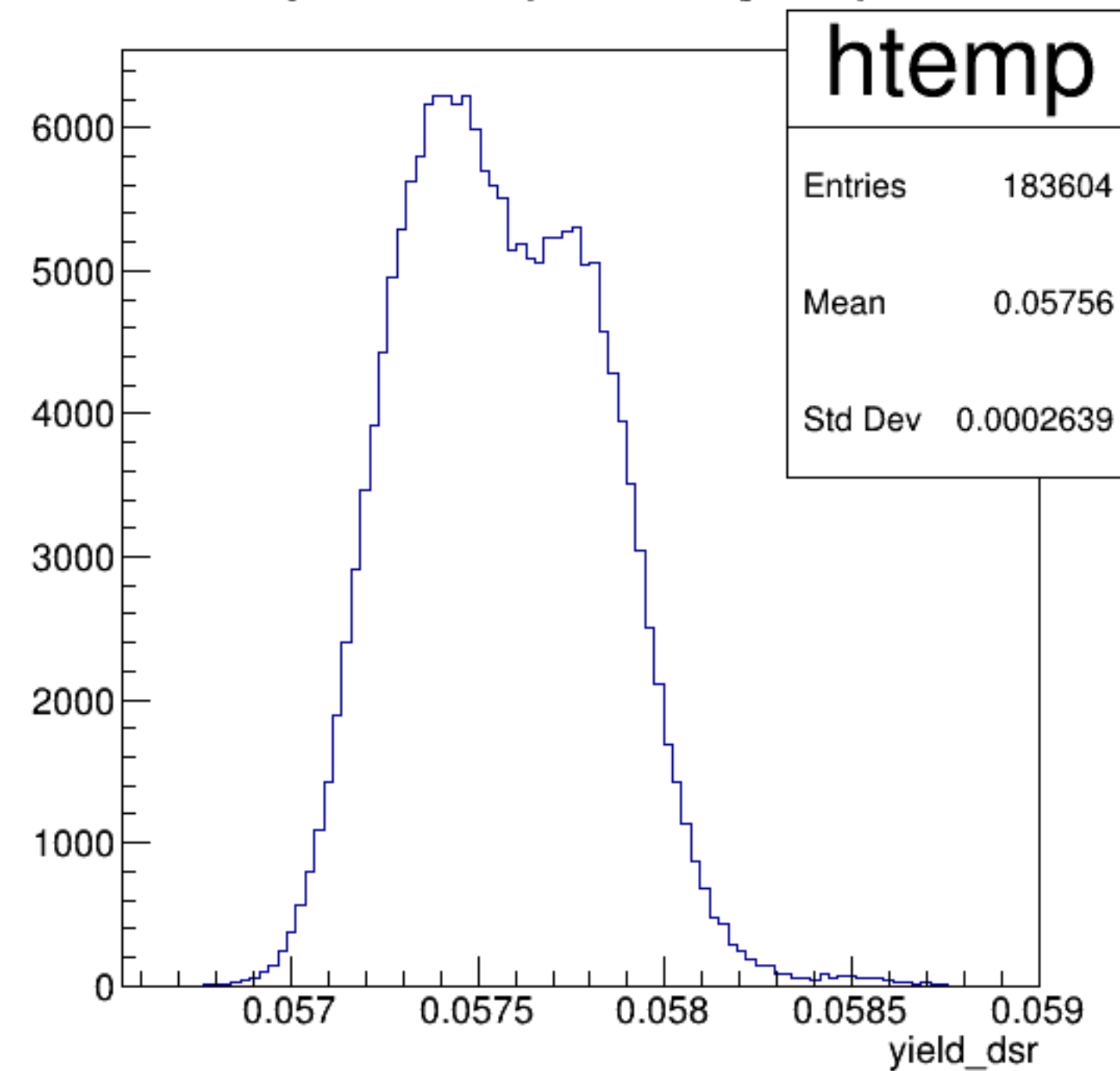


yield_dsr:pattern_number {ErrorFlag==0}

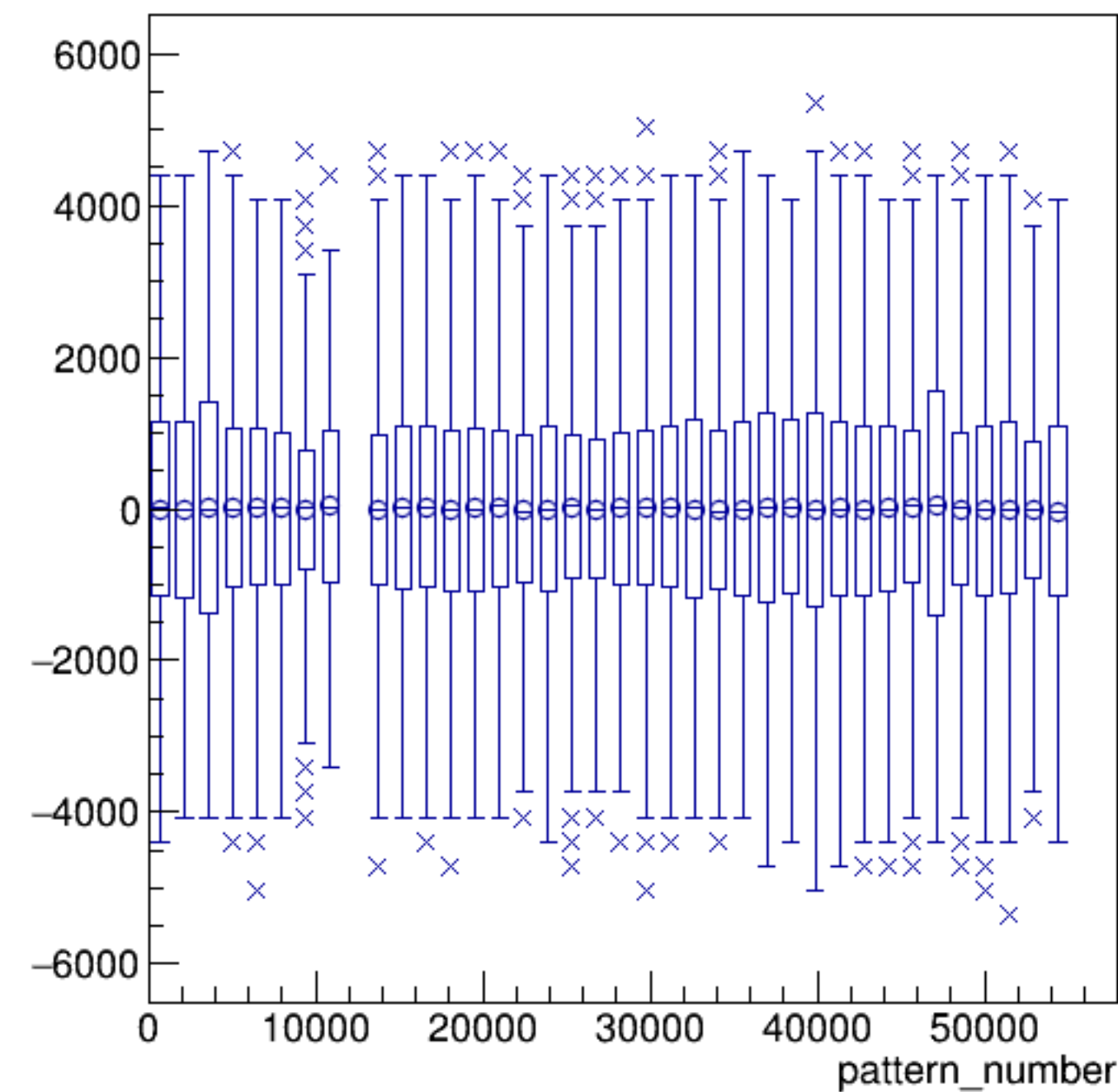


run4372_000_pairTree_dsr.png

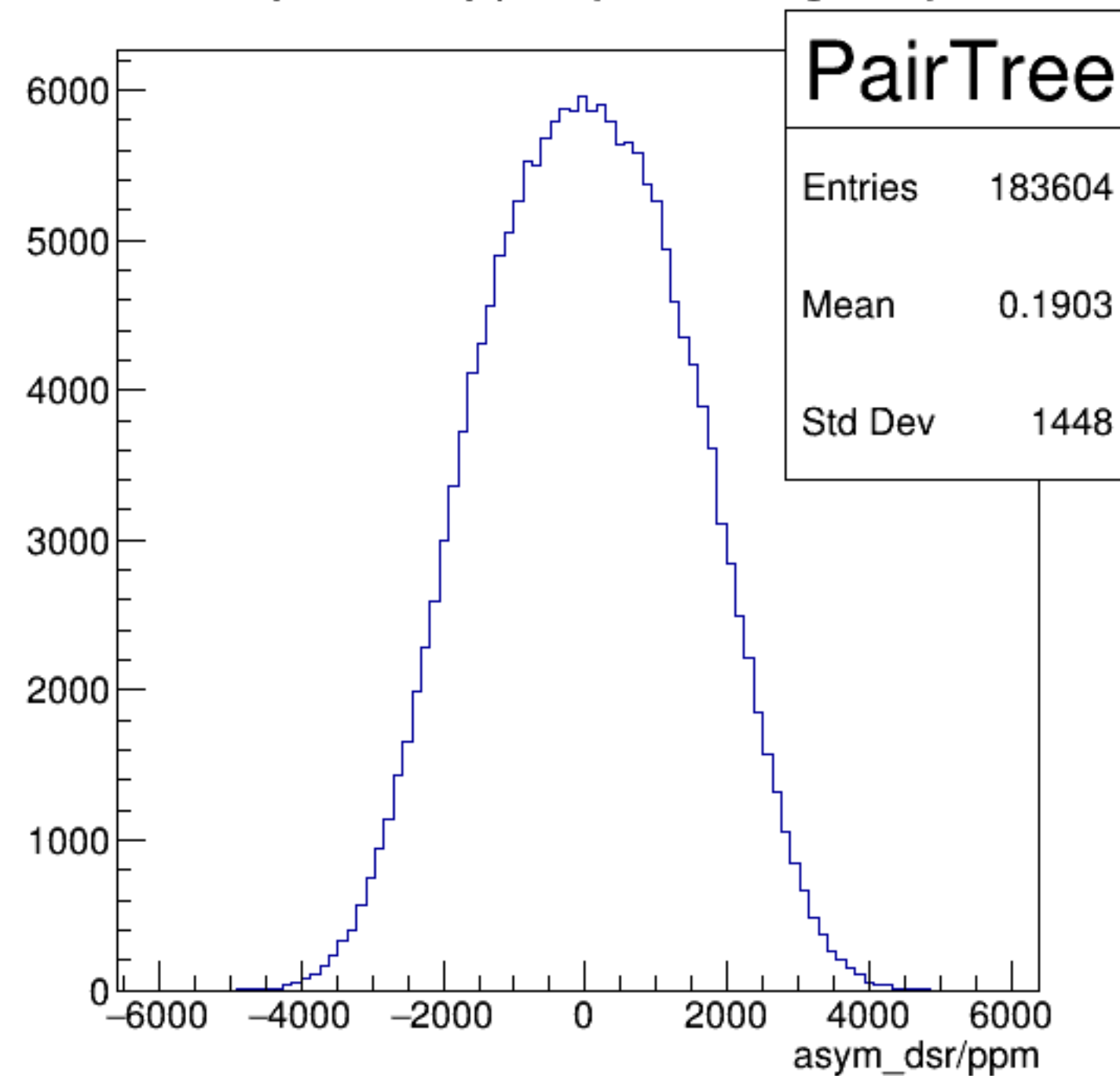
yield_dsr {ErrorFlag==0}



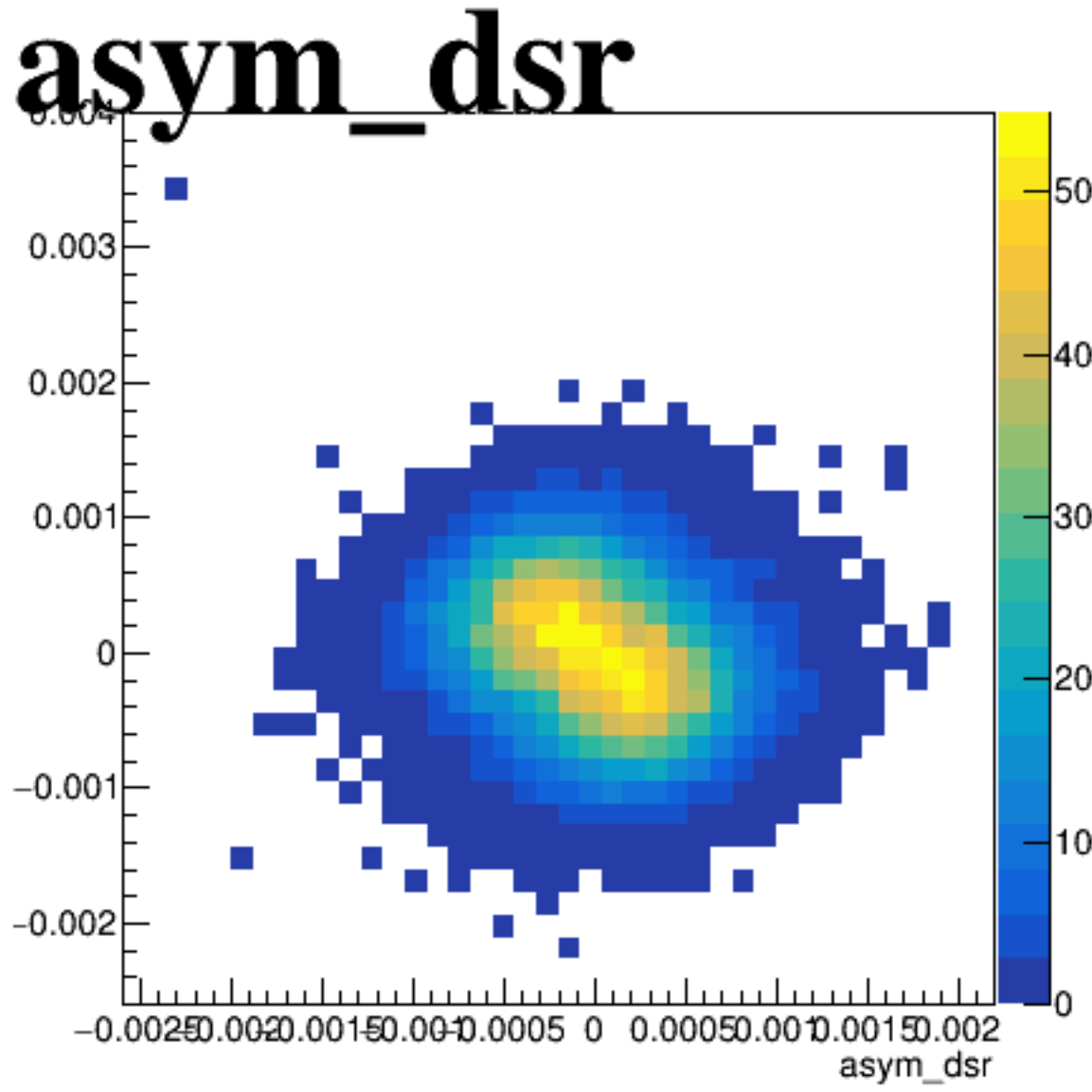
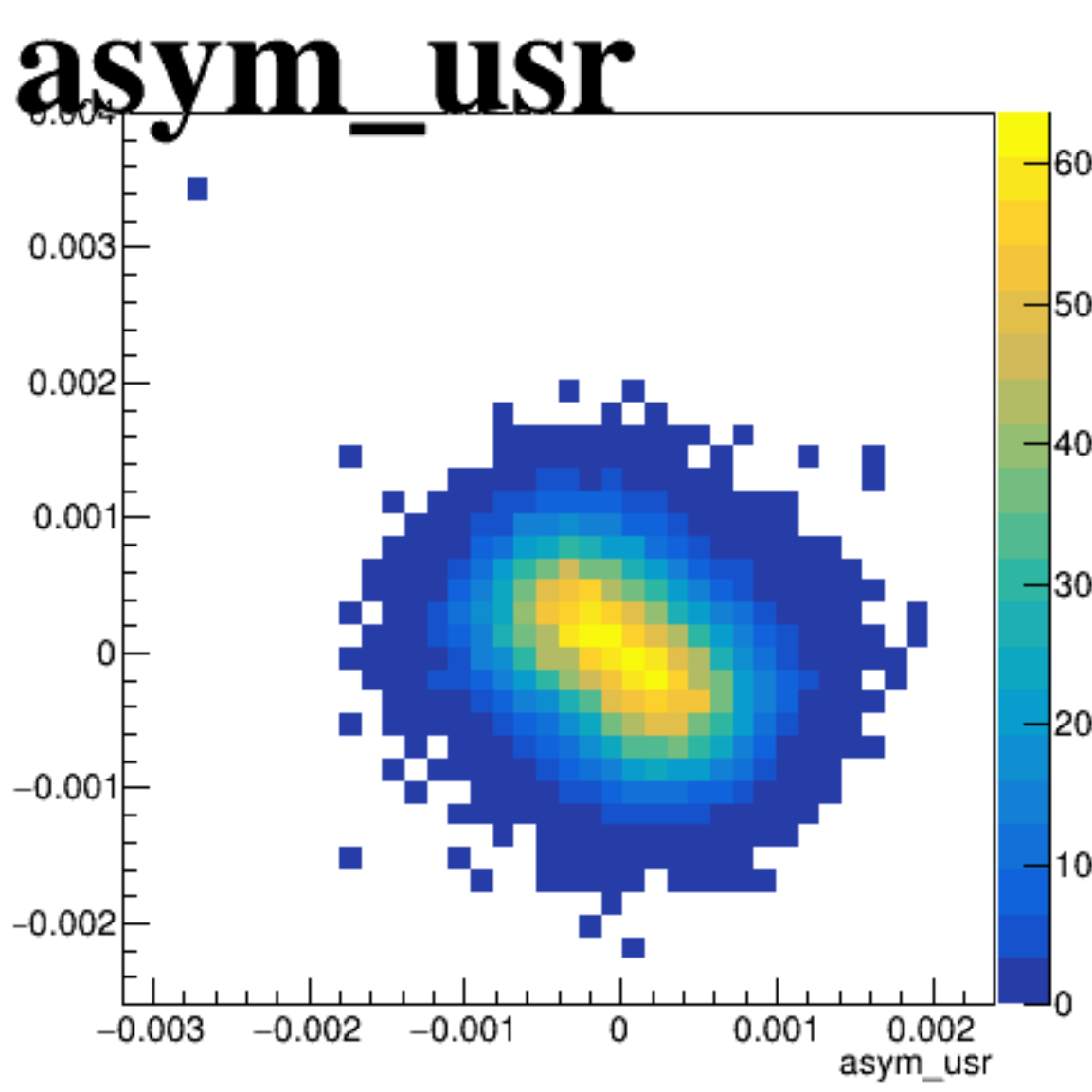
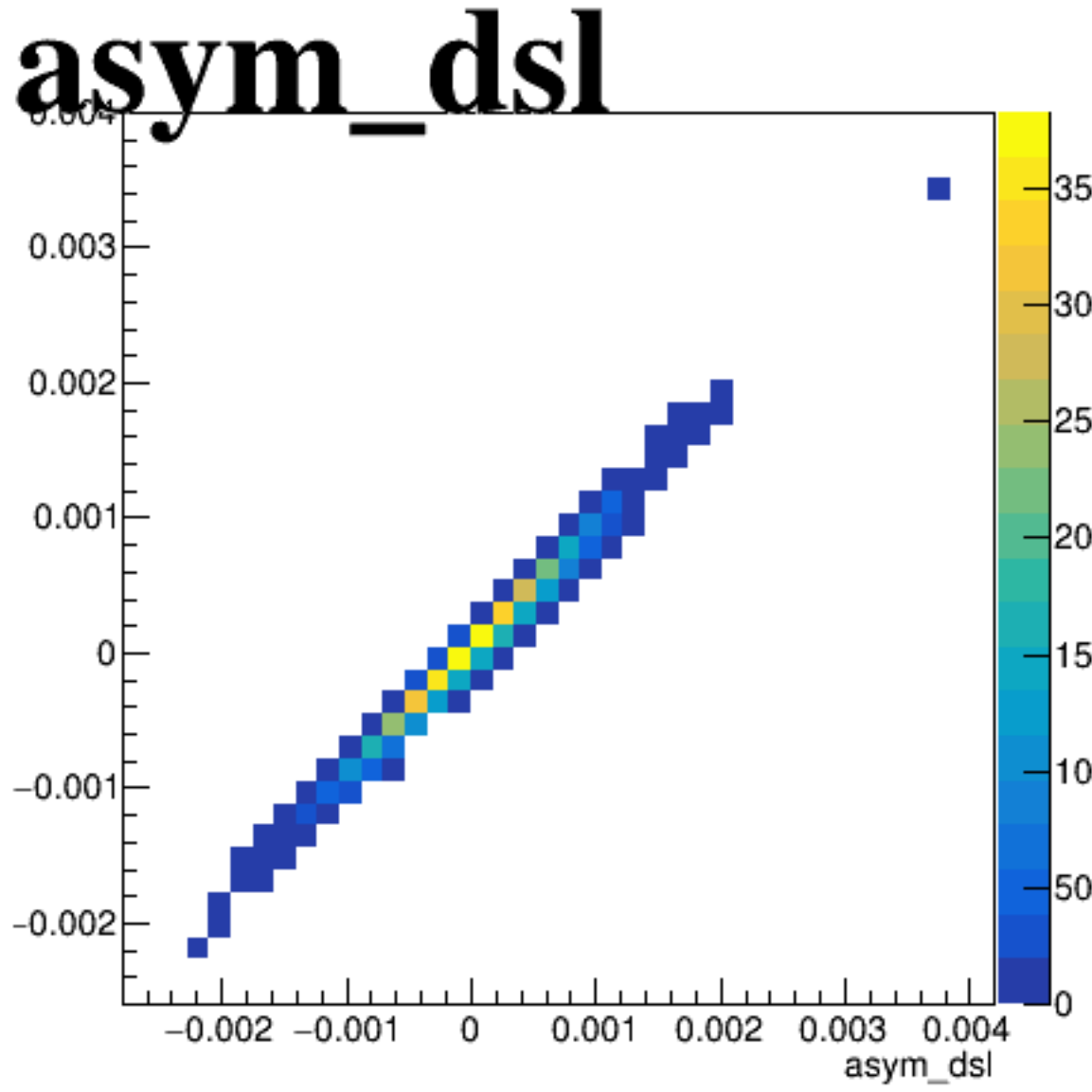
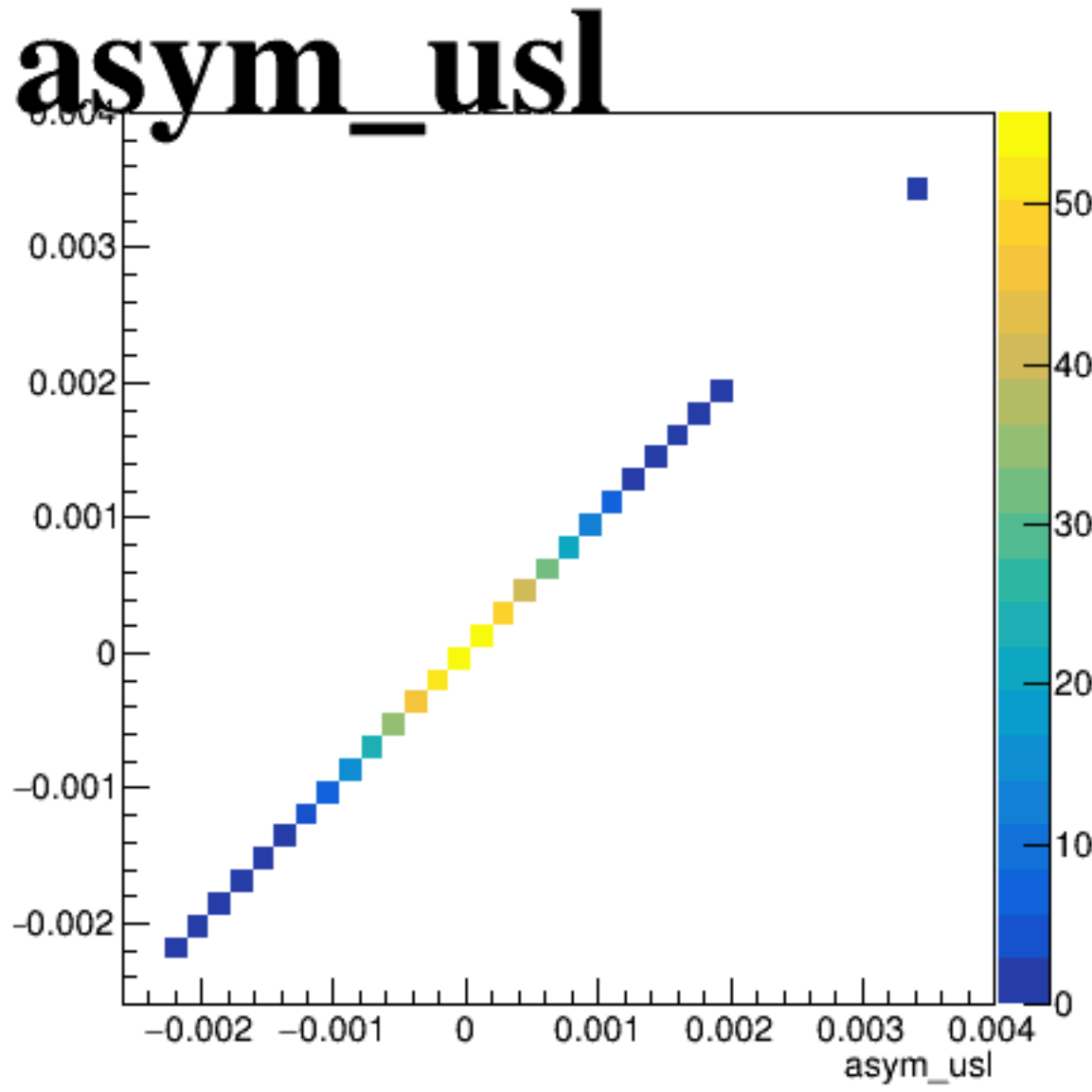
asym_dsr/ppm:pattern_number {ErrorFlag==0}



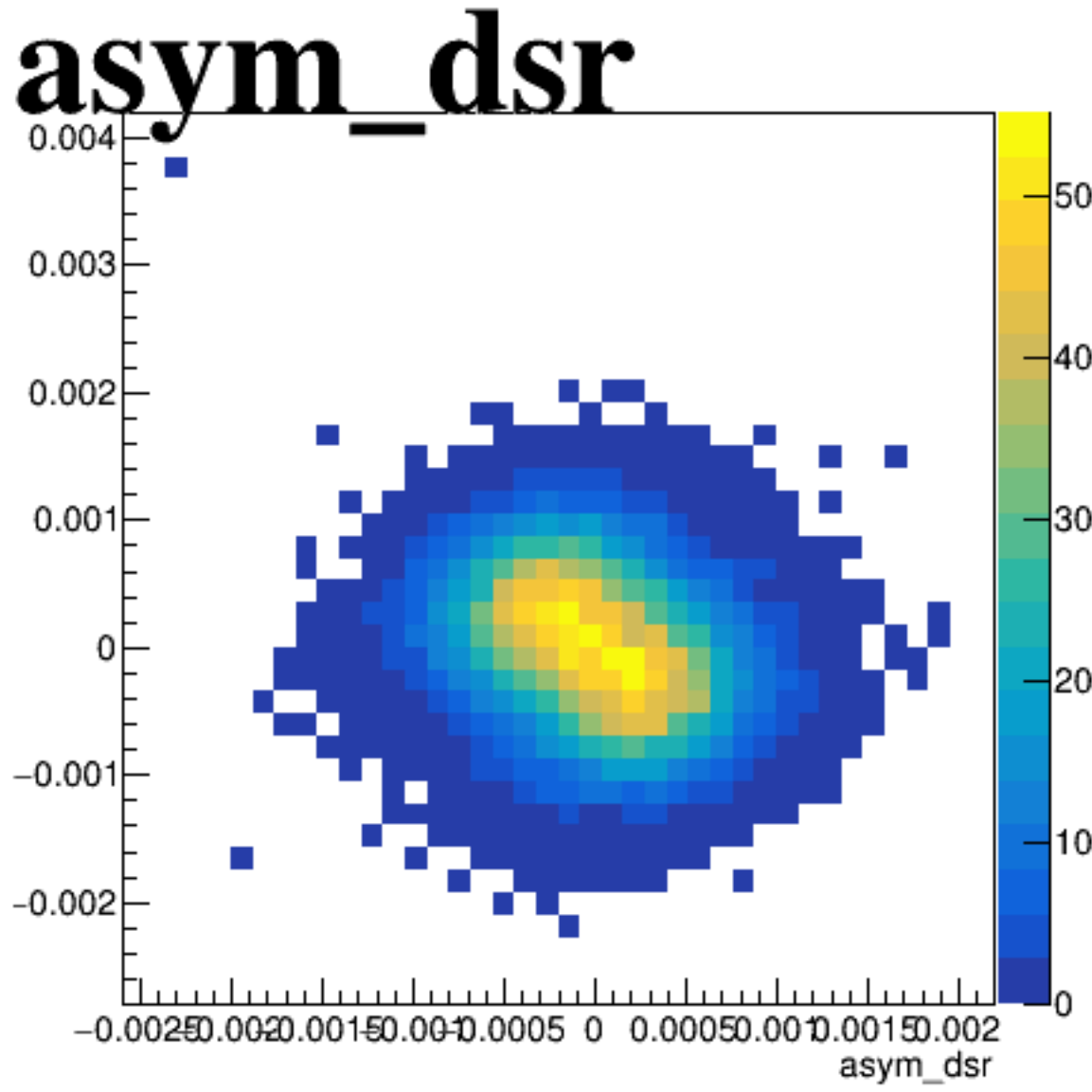
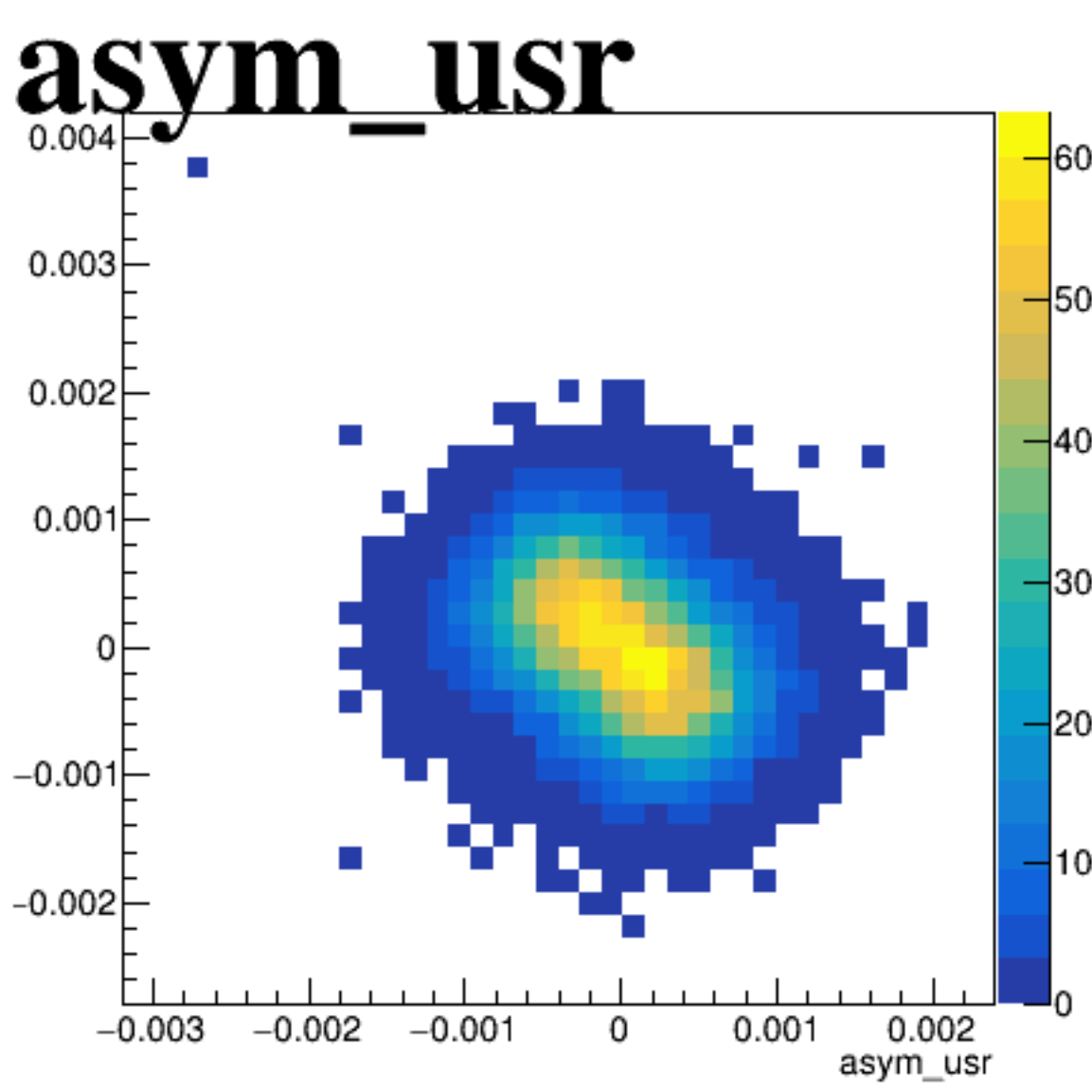
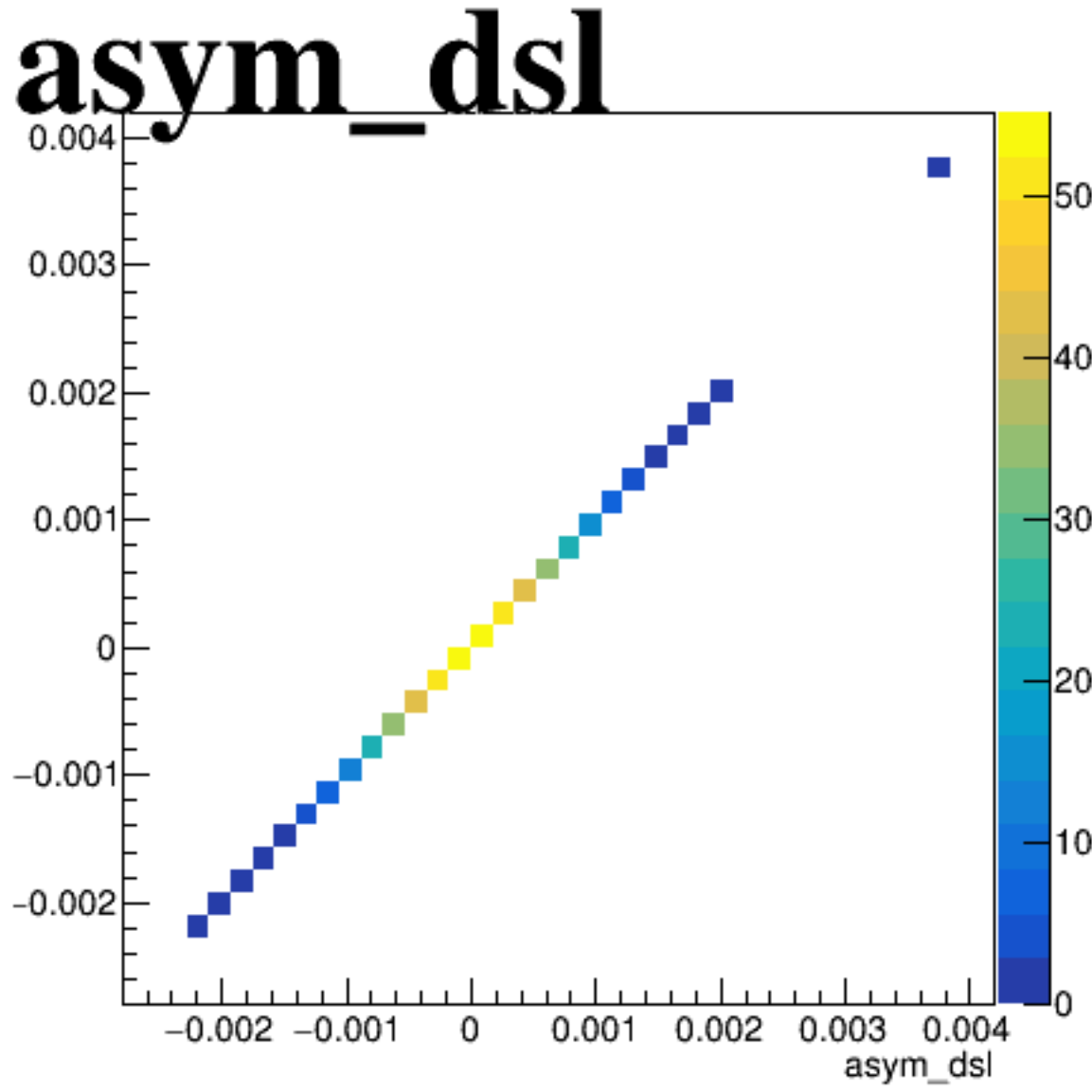
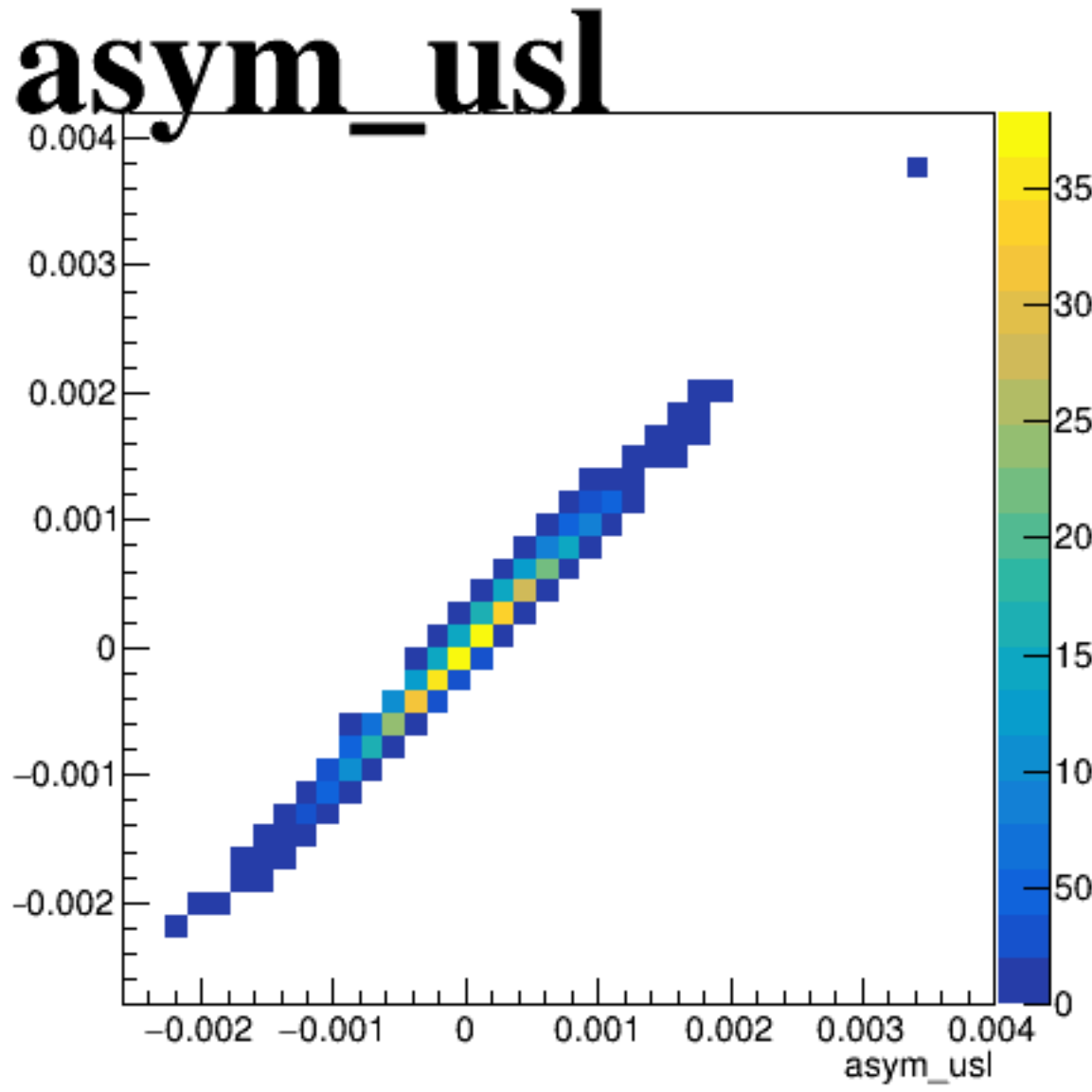
asym_dsr/ppm {ErrorFlag==0}



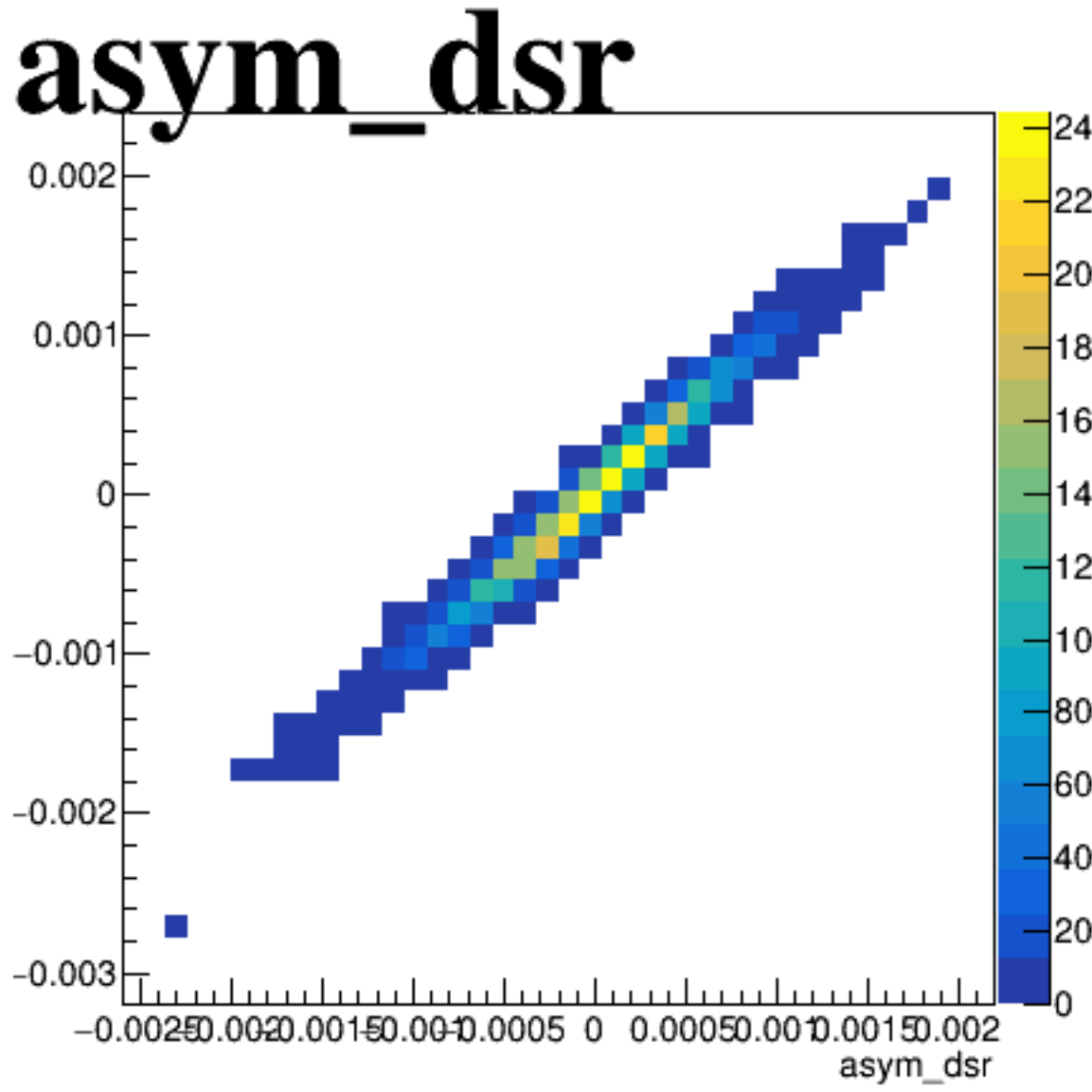
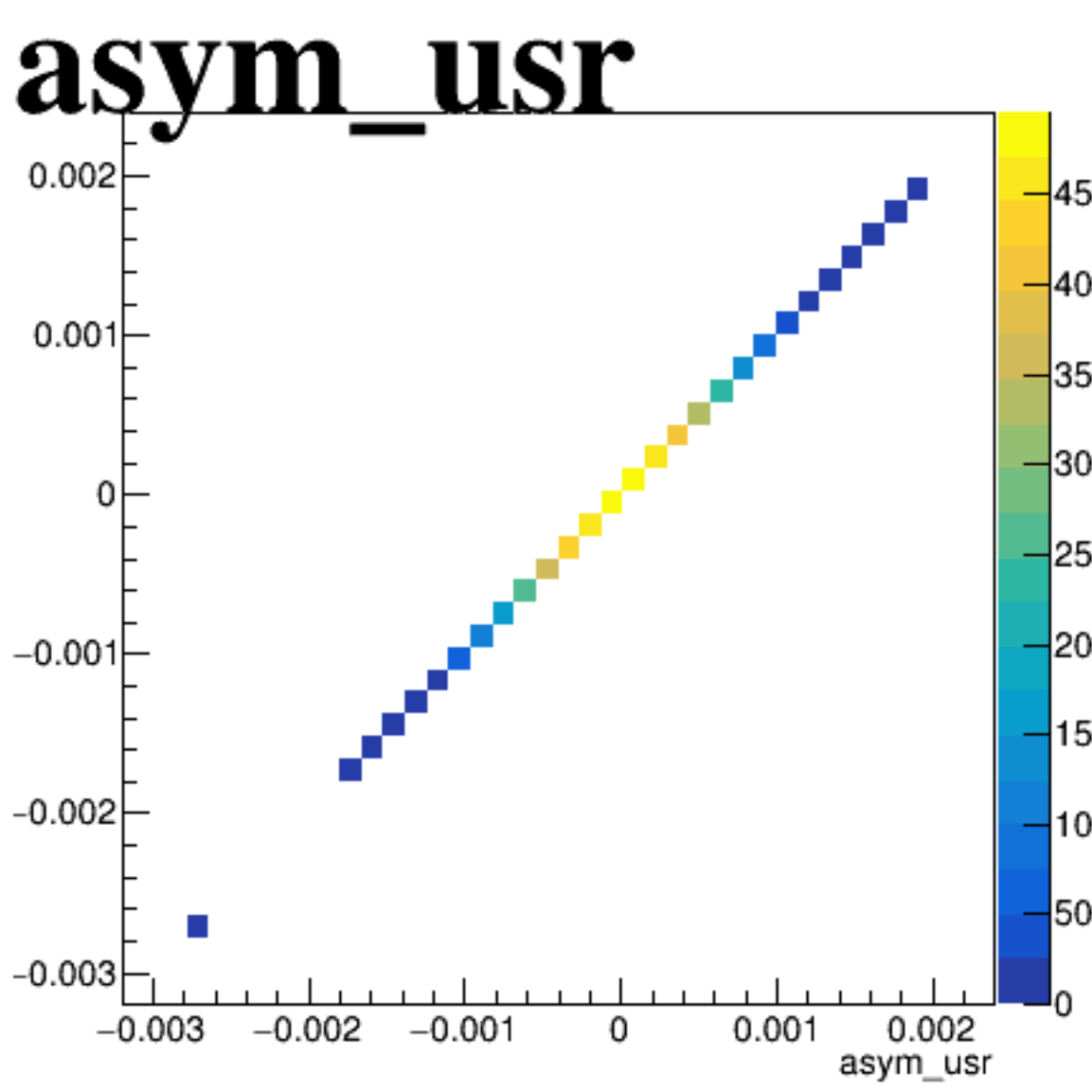
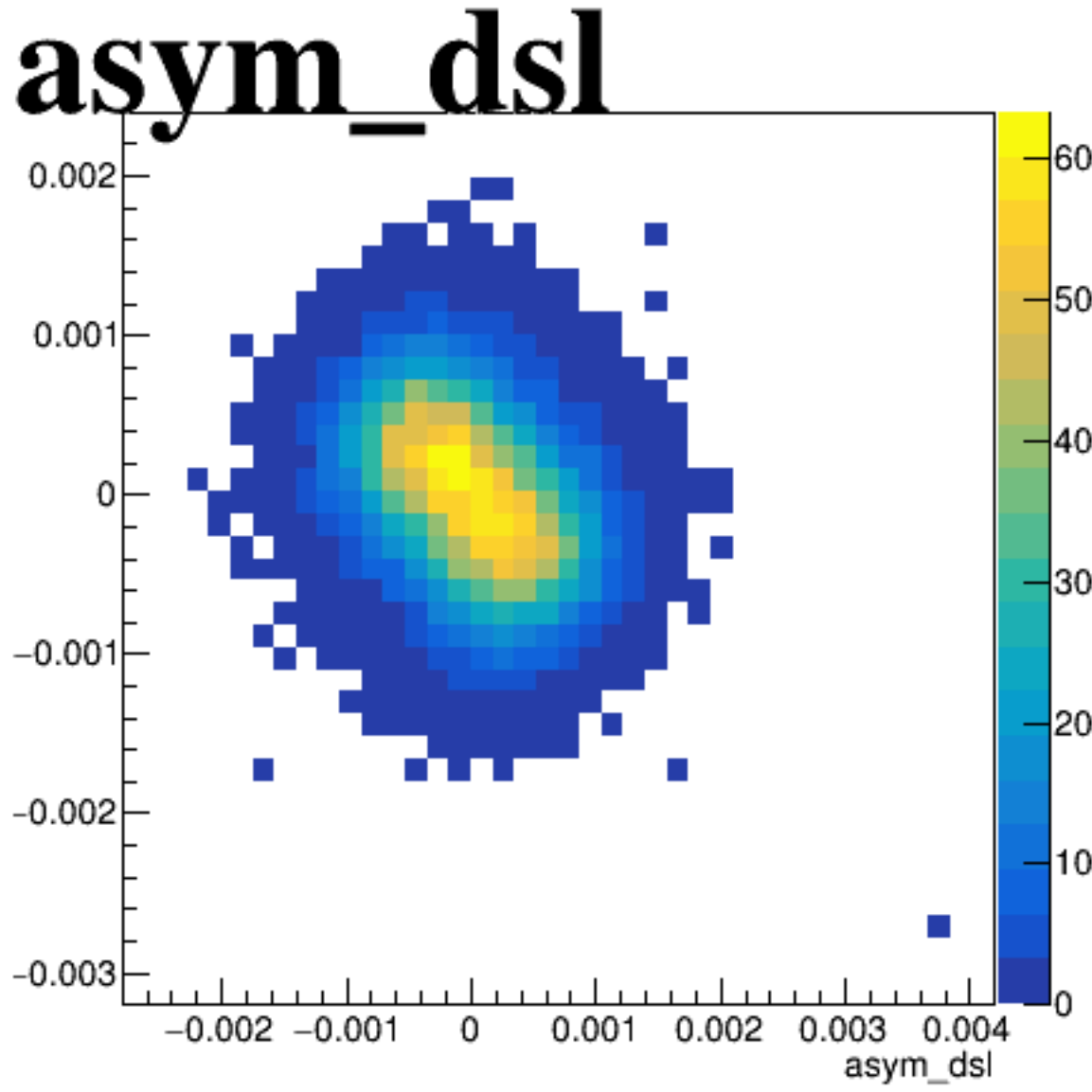
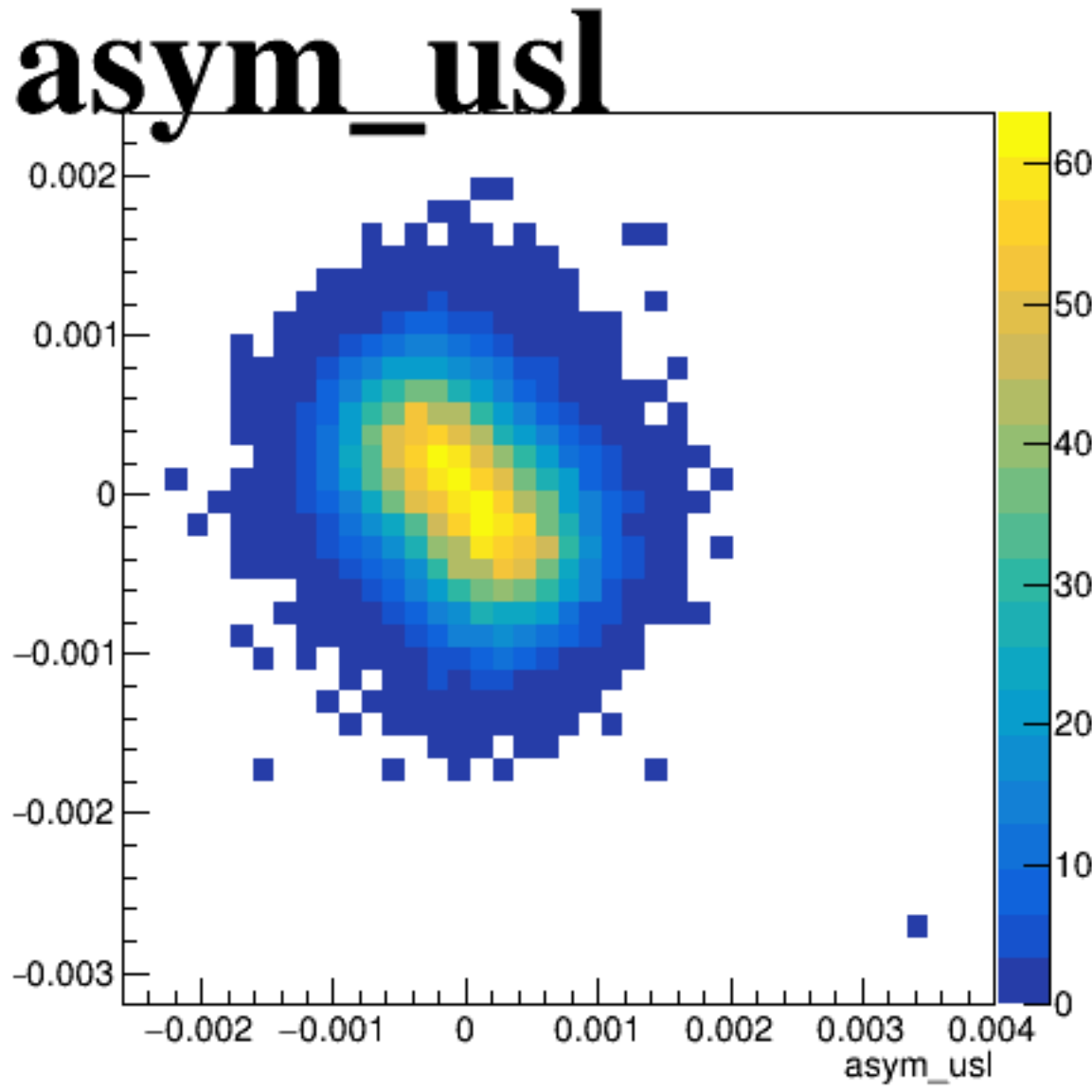
asym_usl



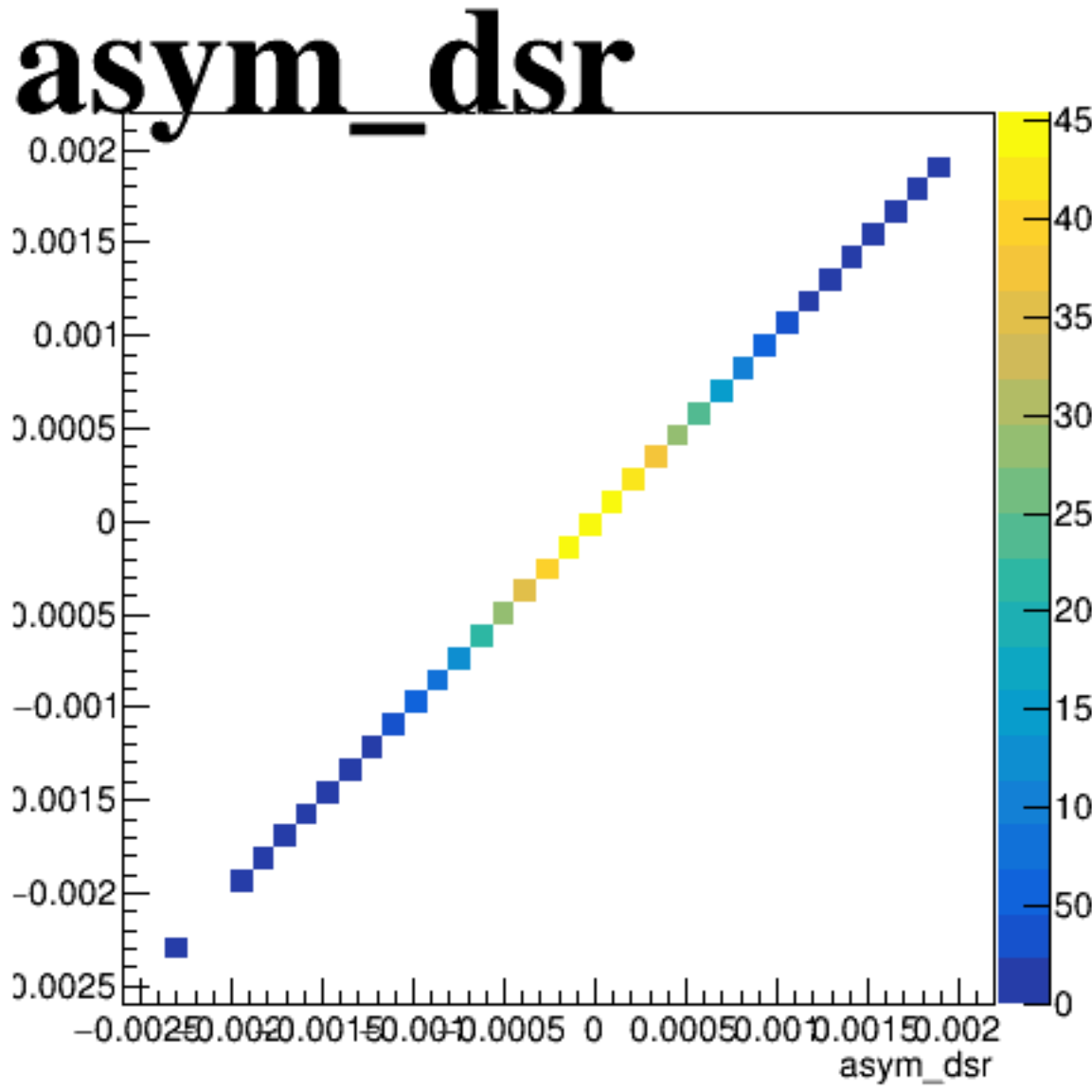
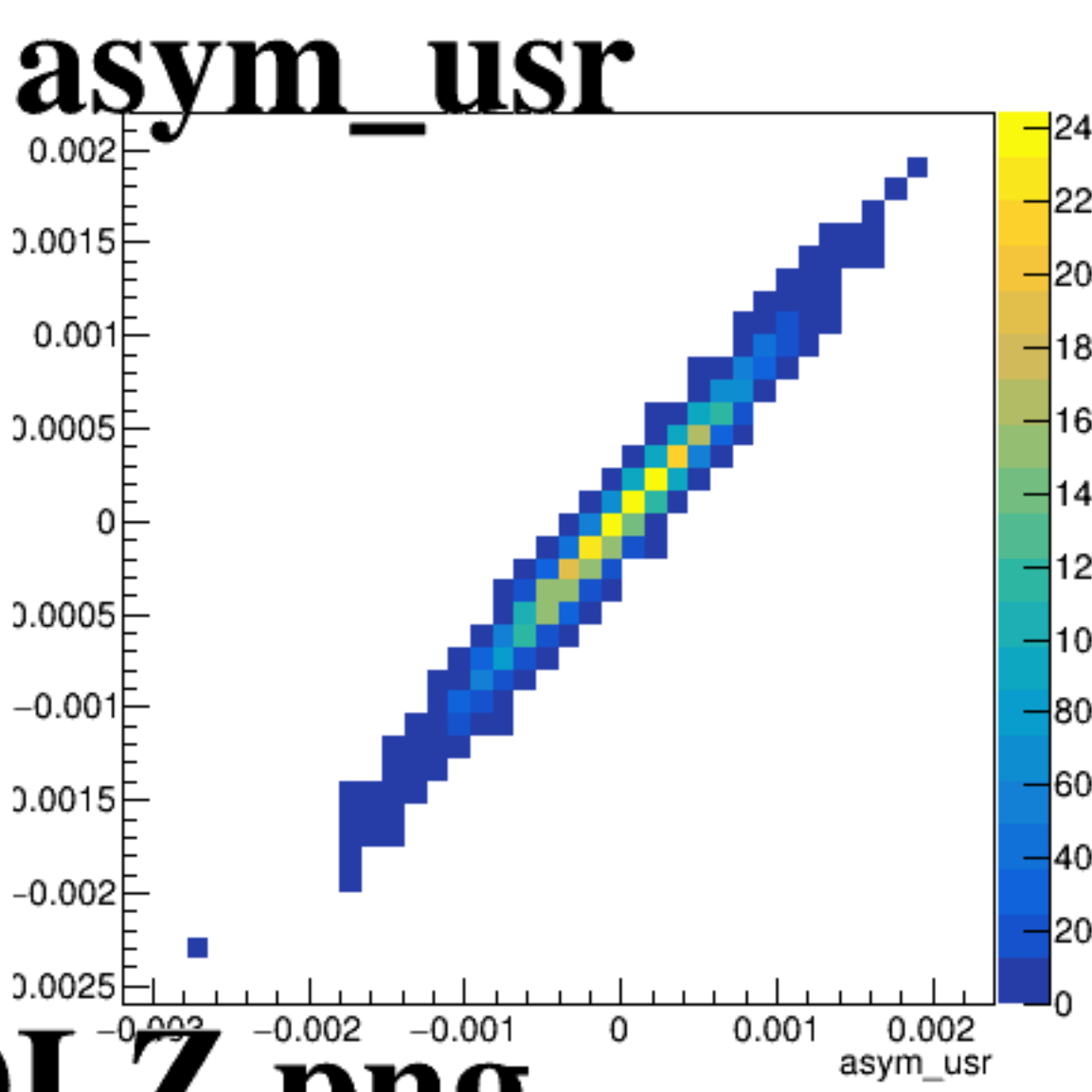
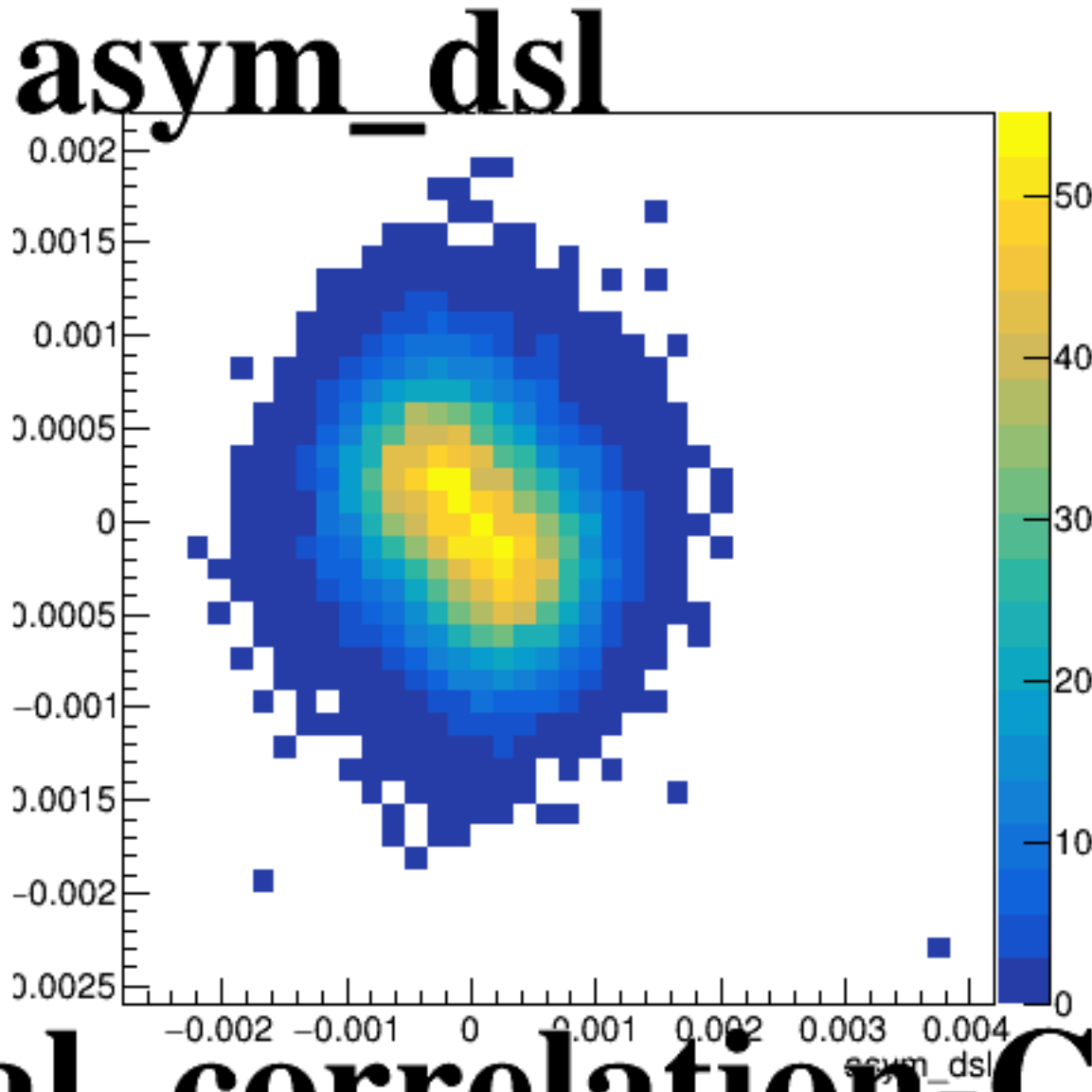
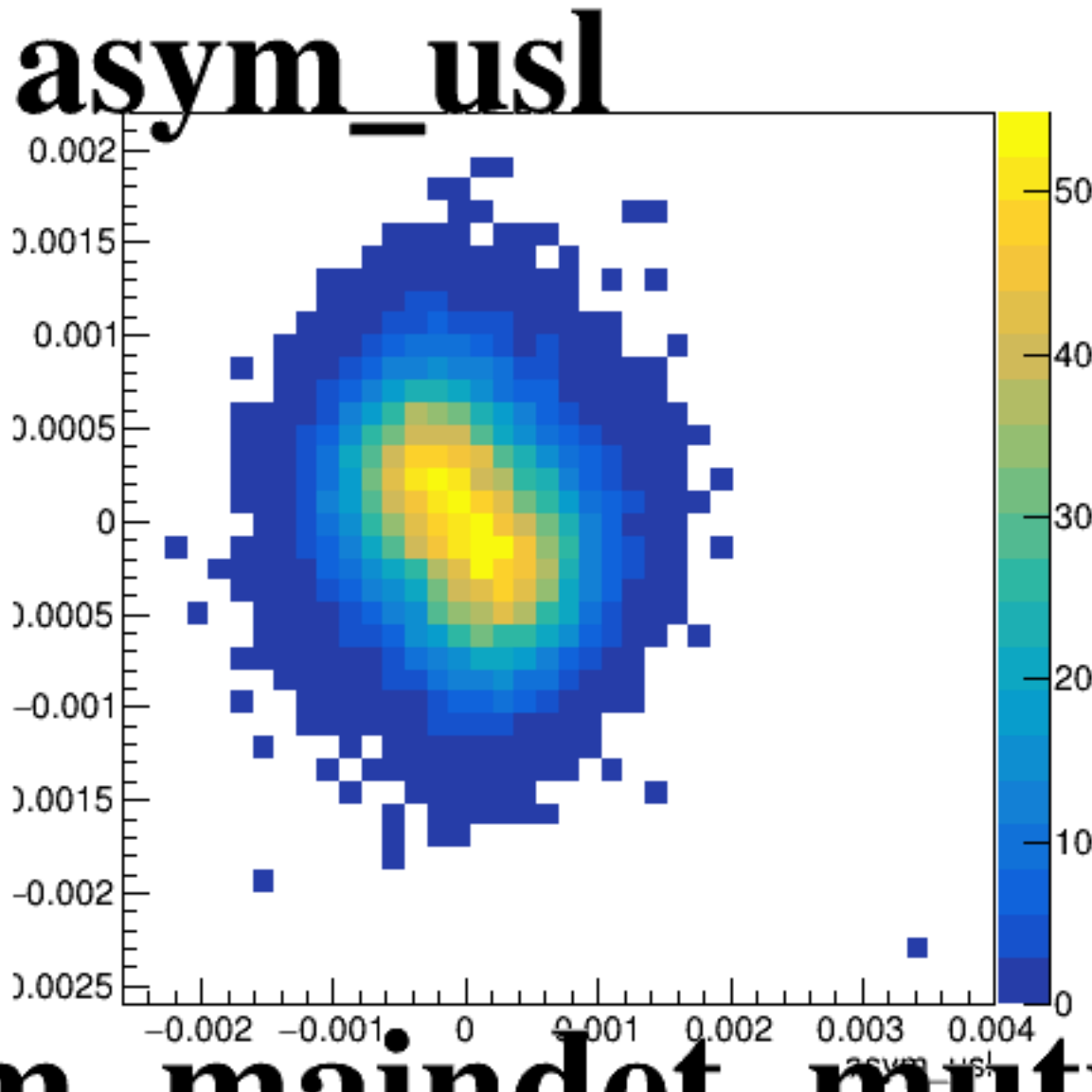
asym_dsl



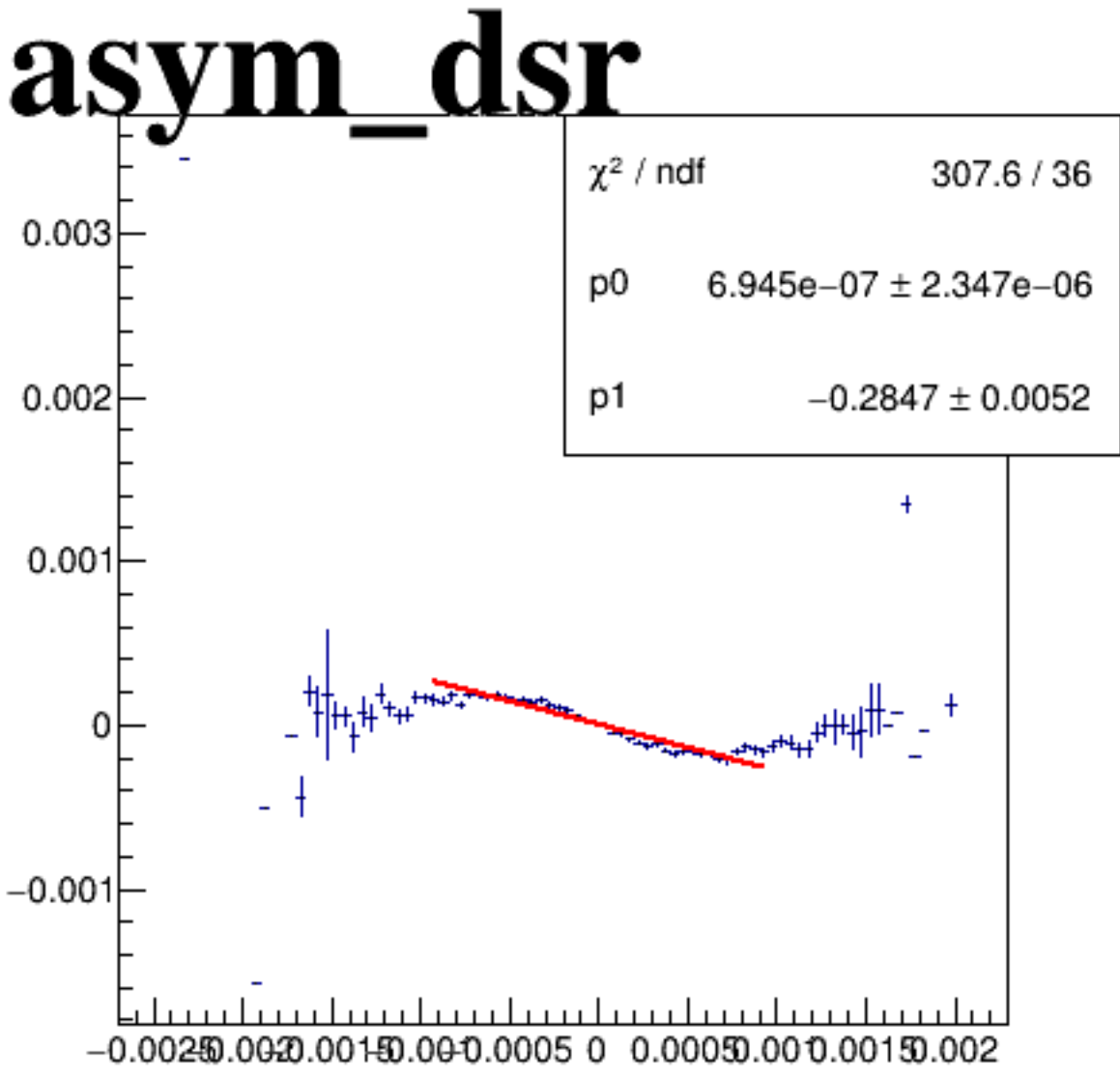
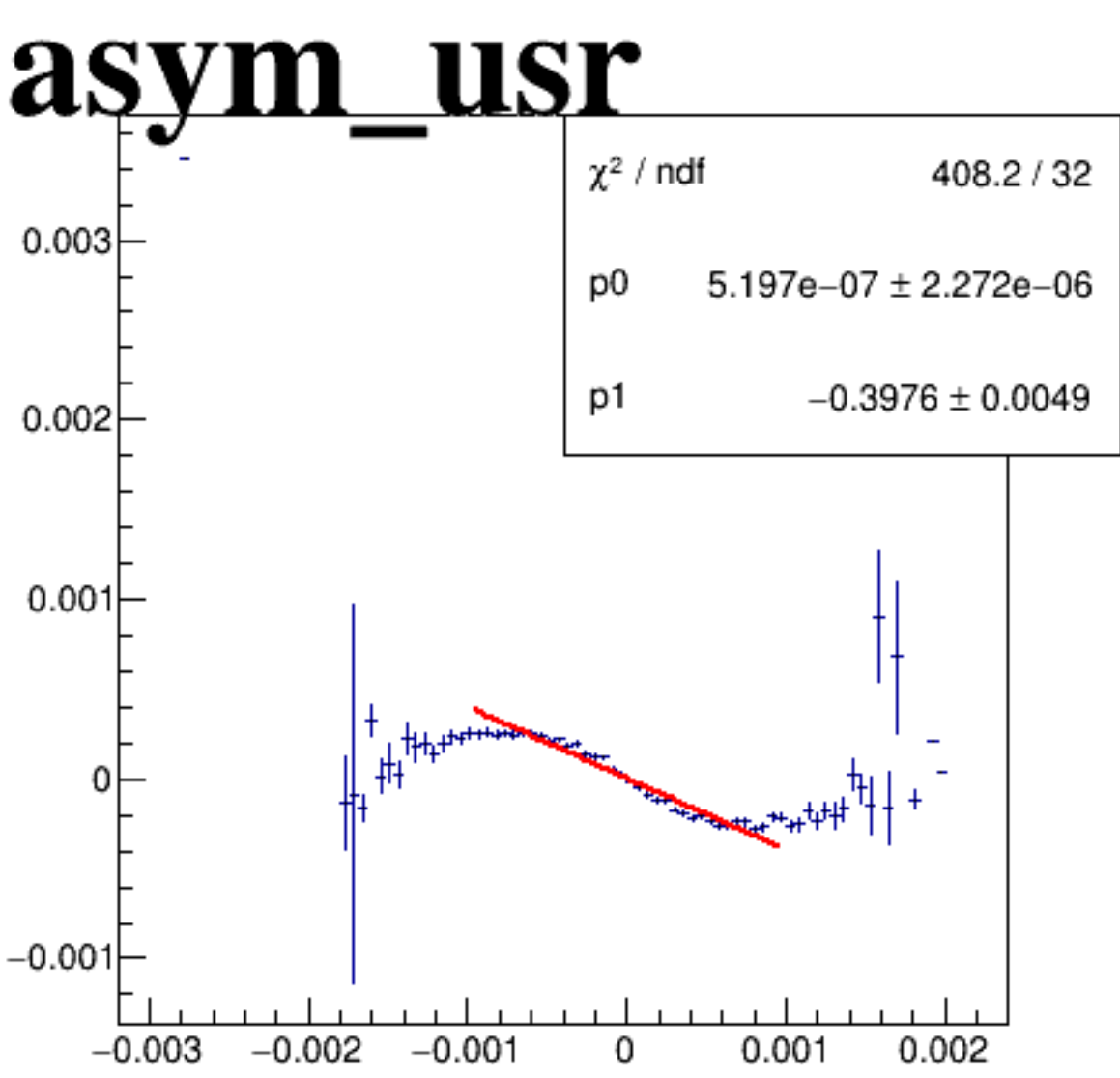
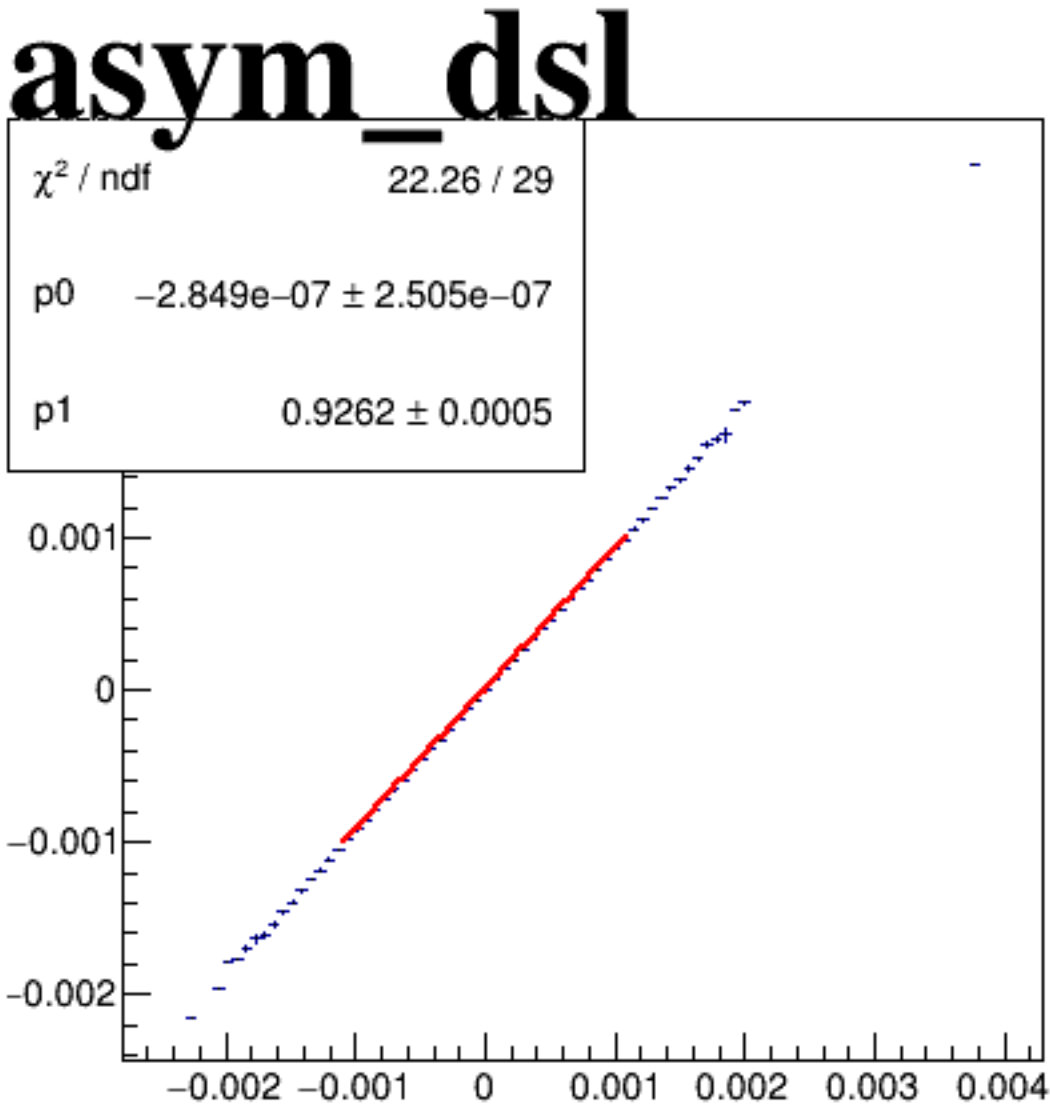
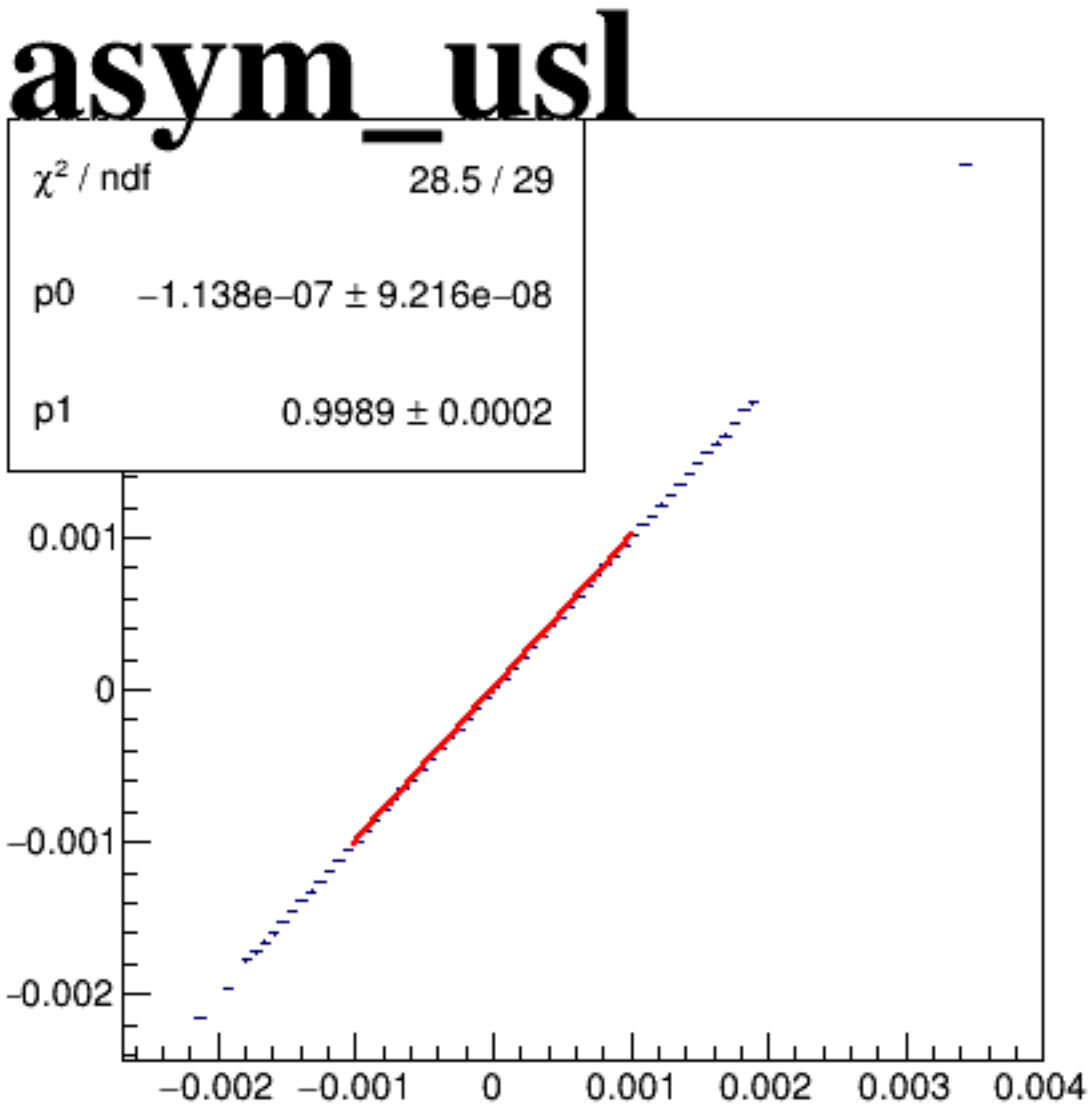
asym_usr



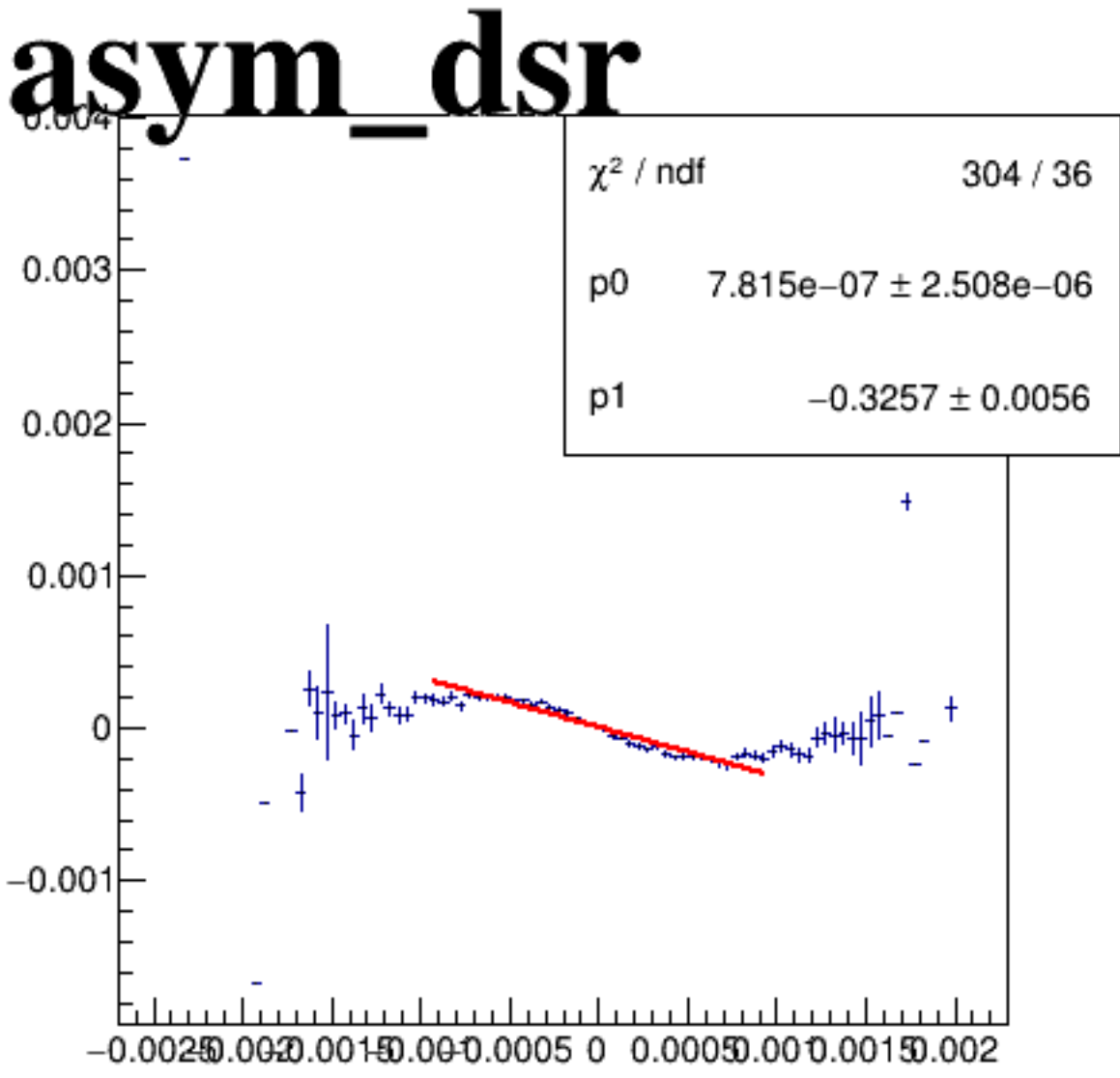
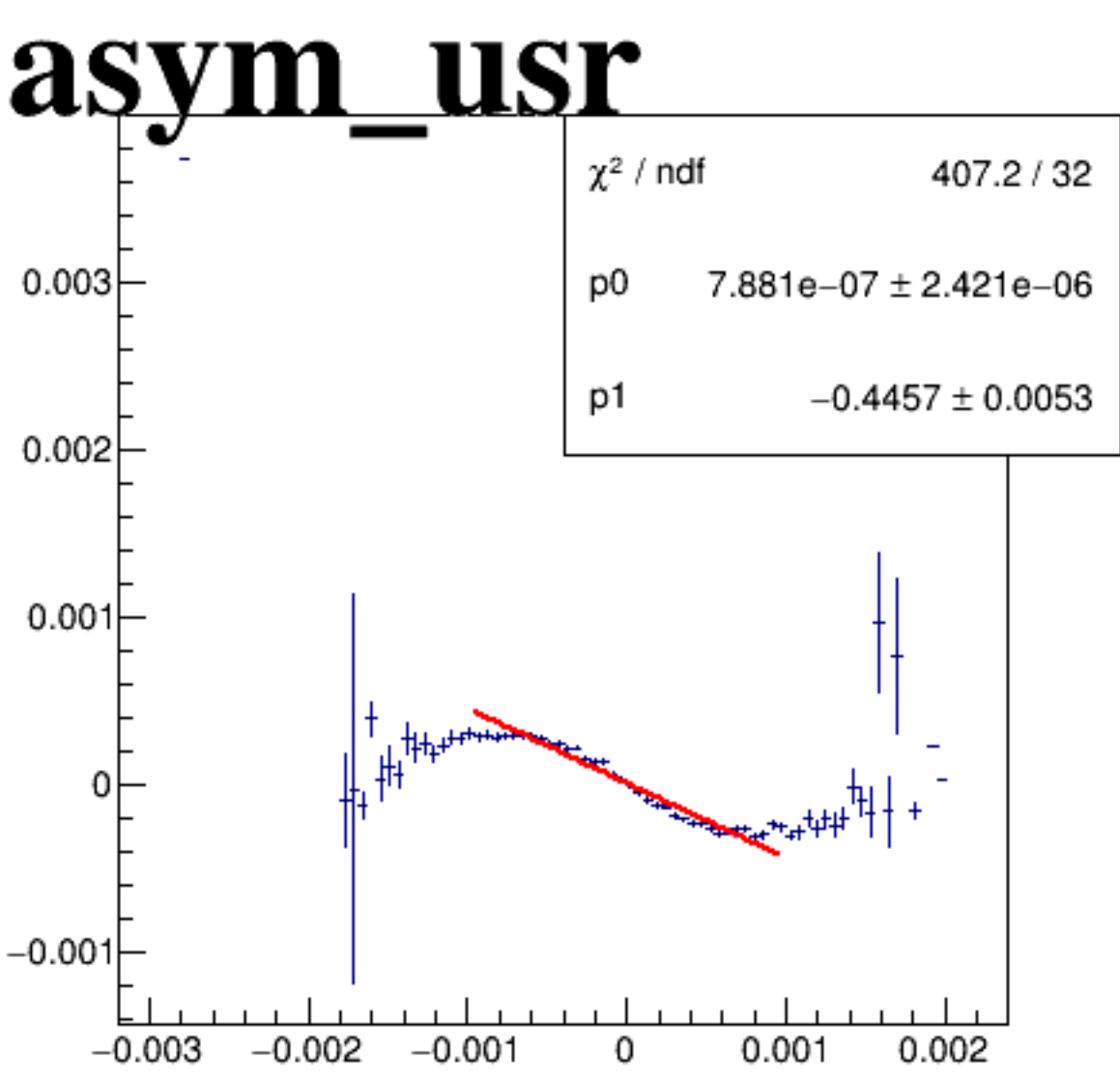
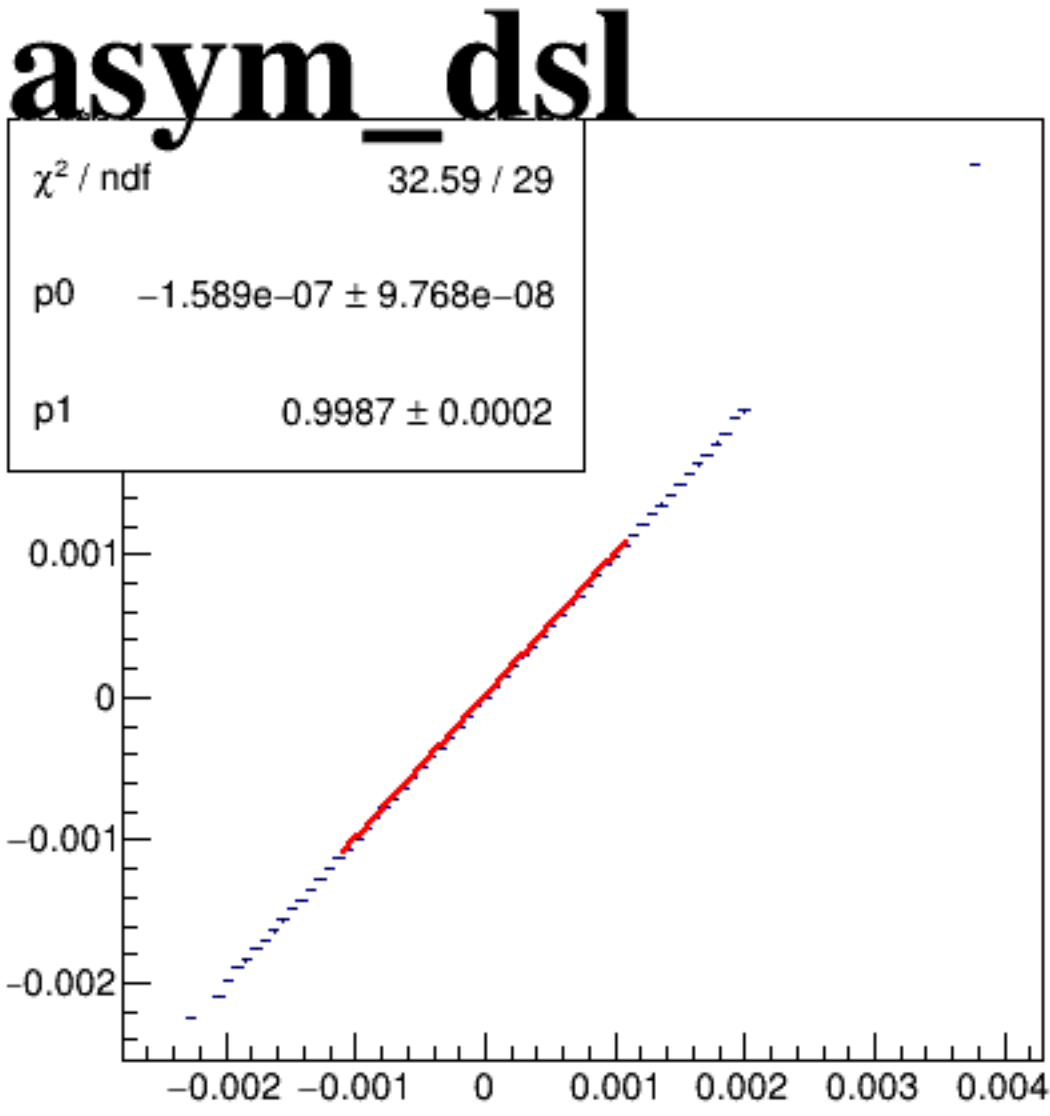
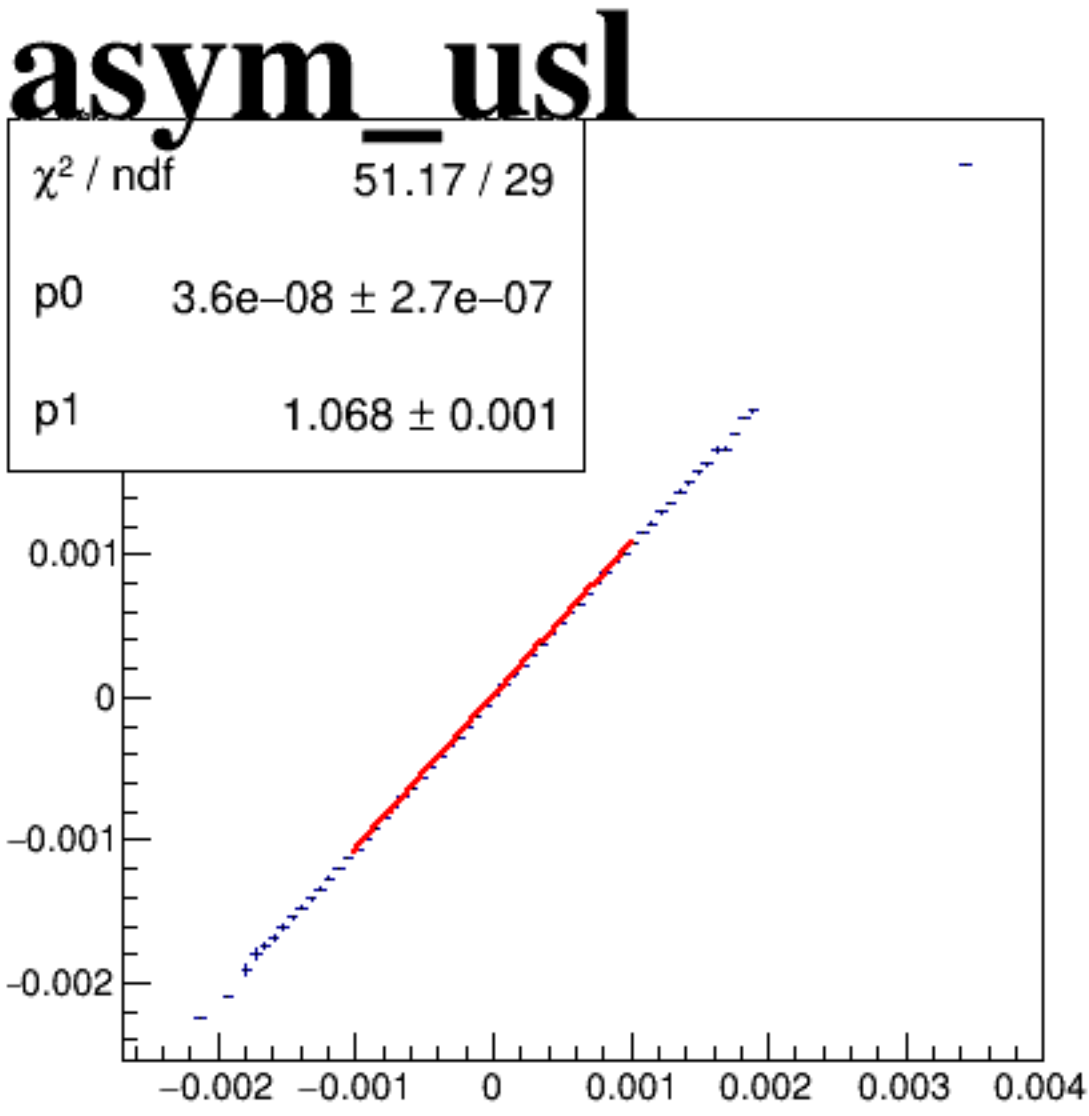
asym_dsr



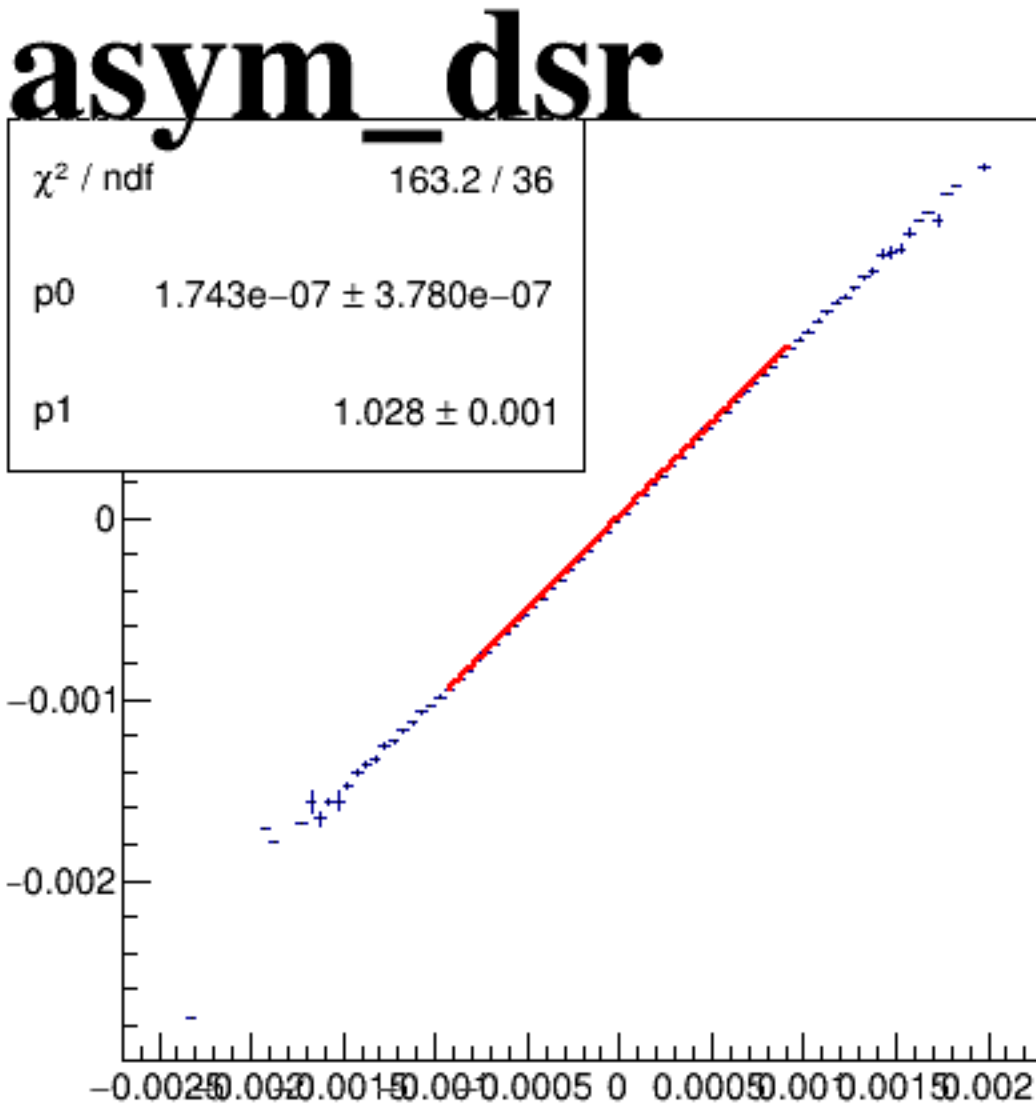
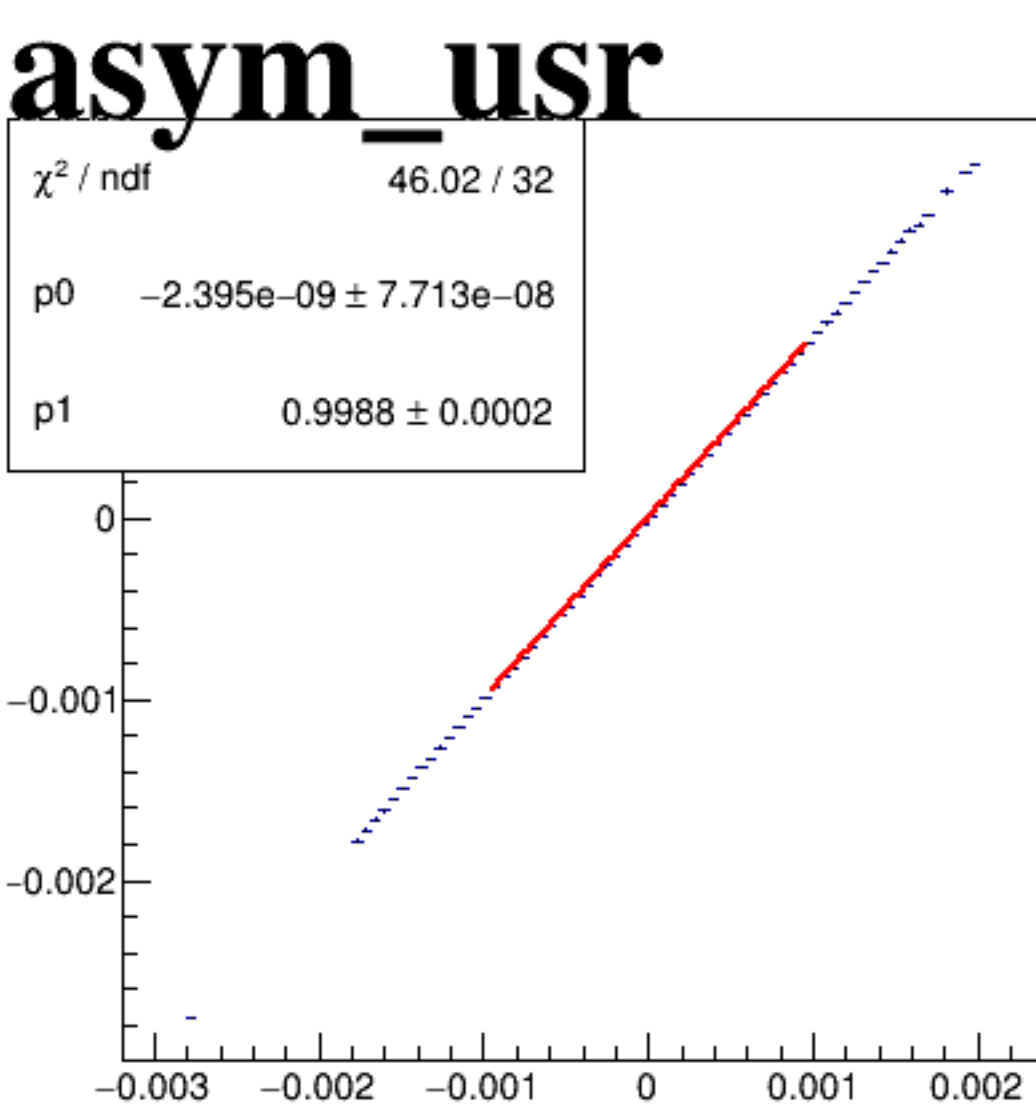
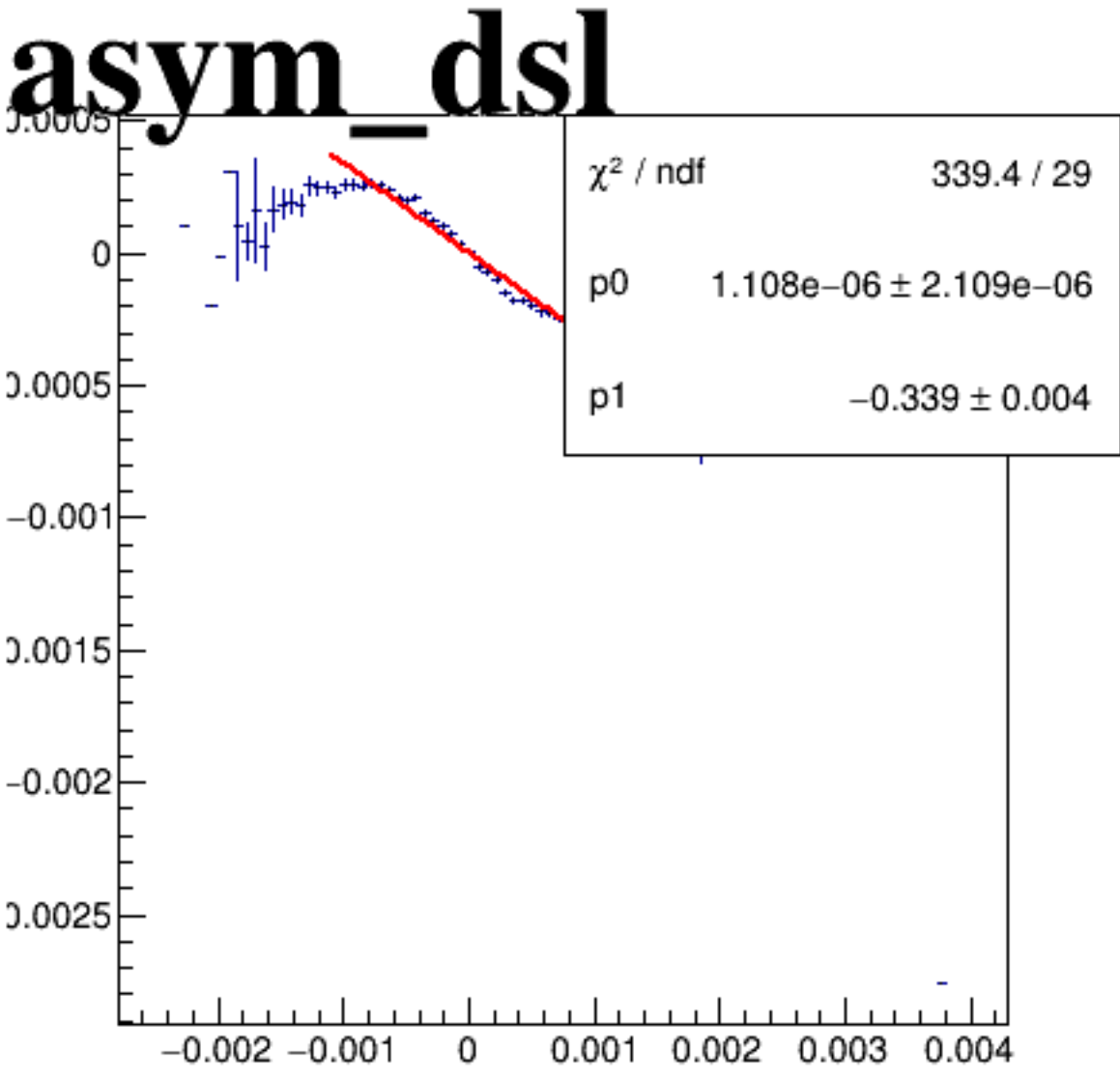
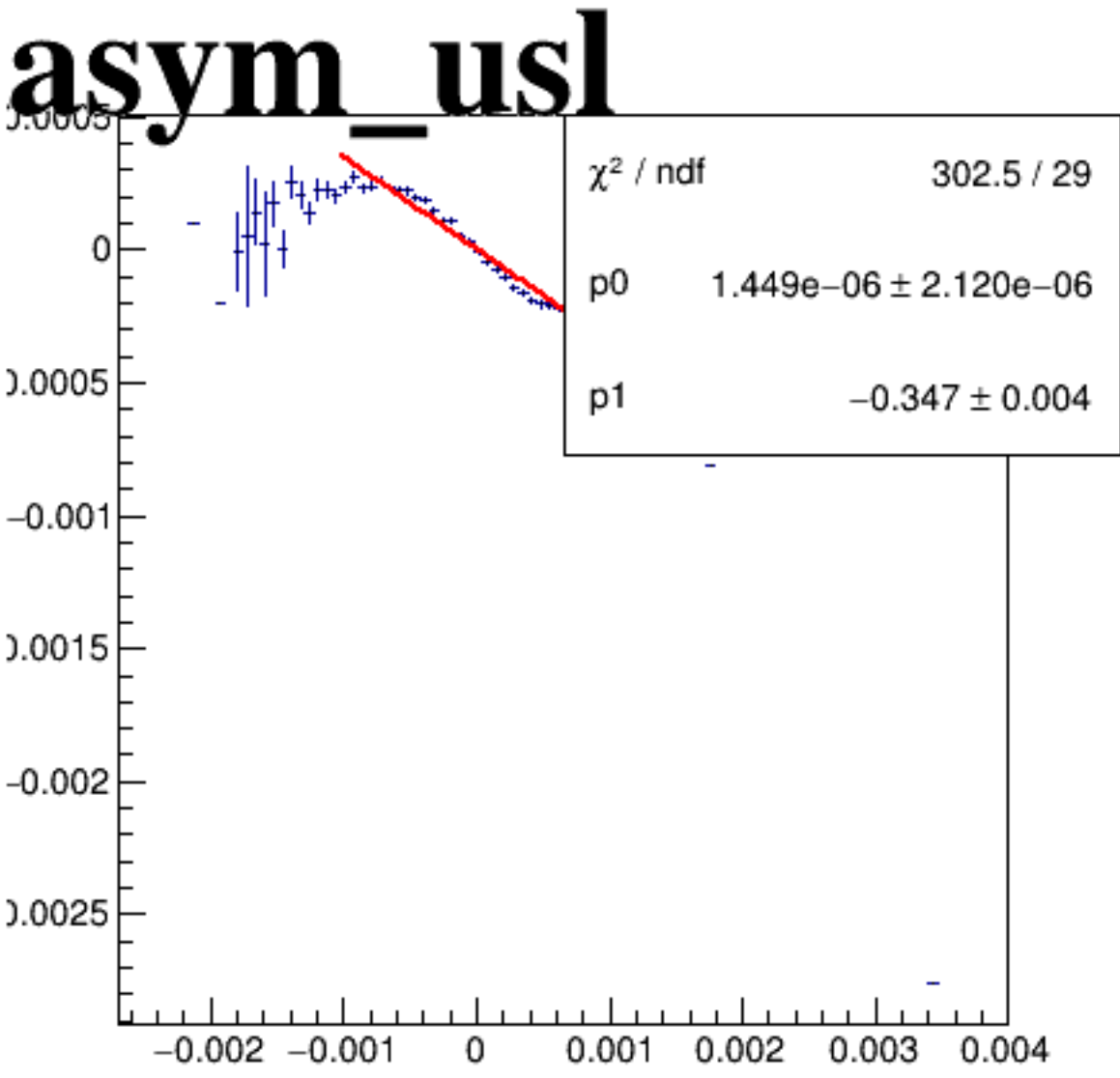
asym_usl



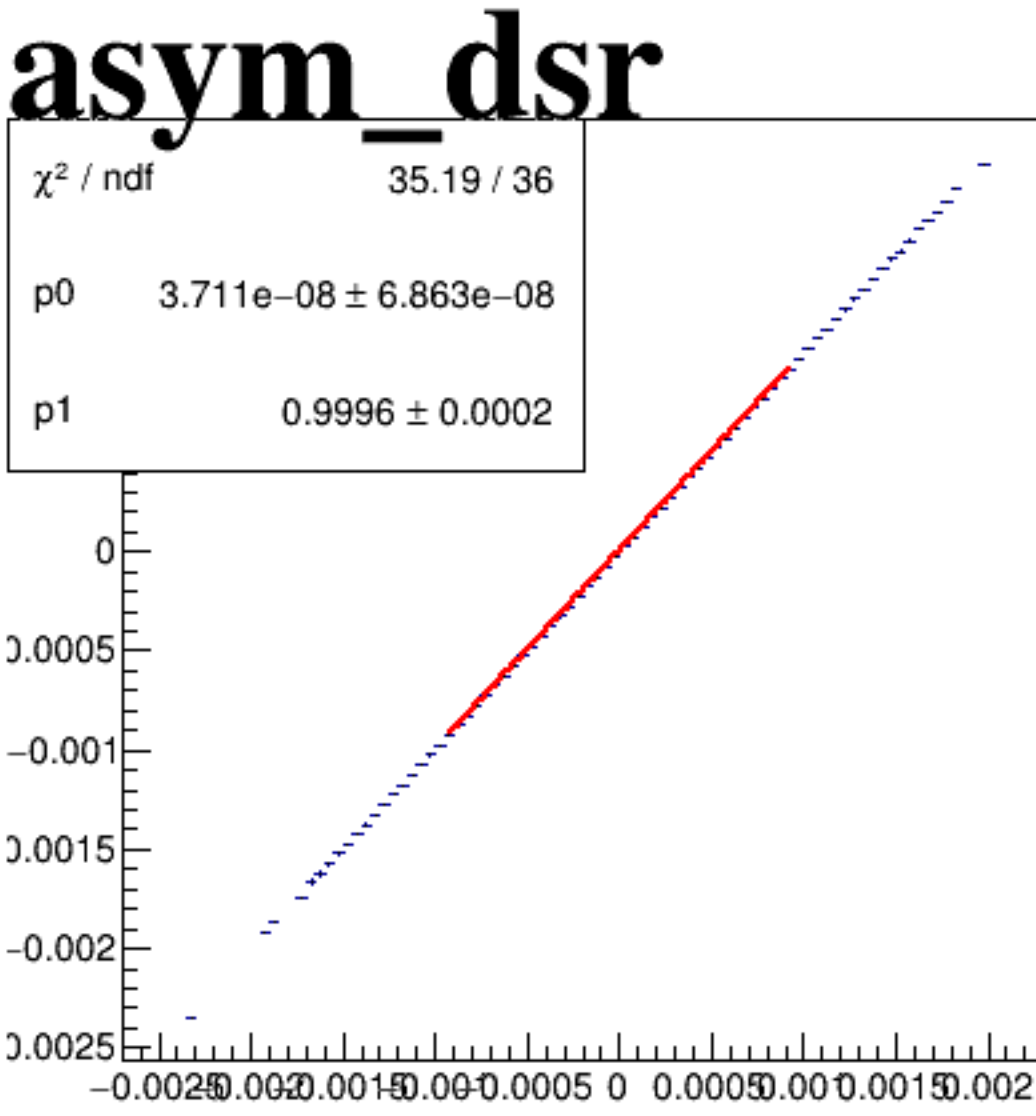
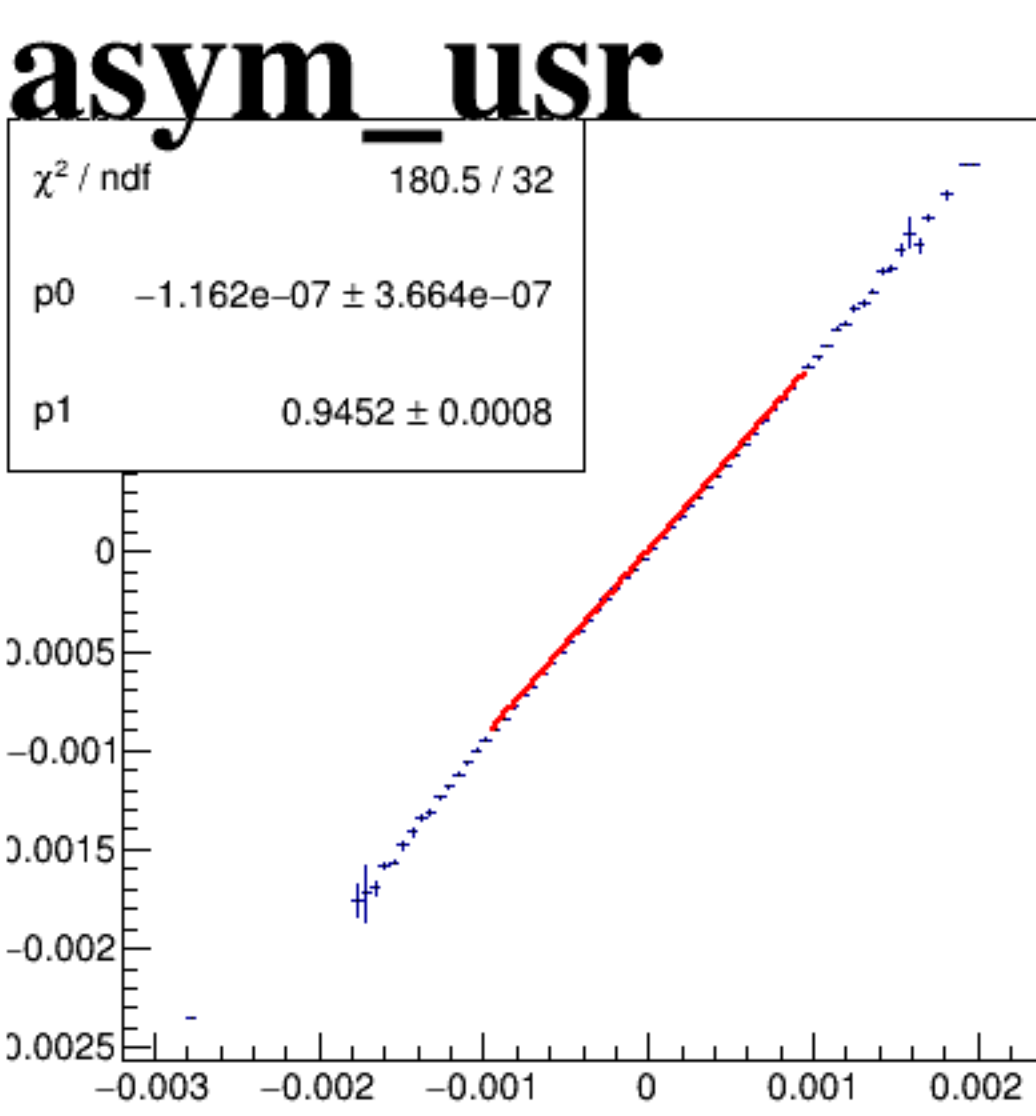
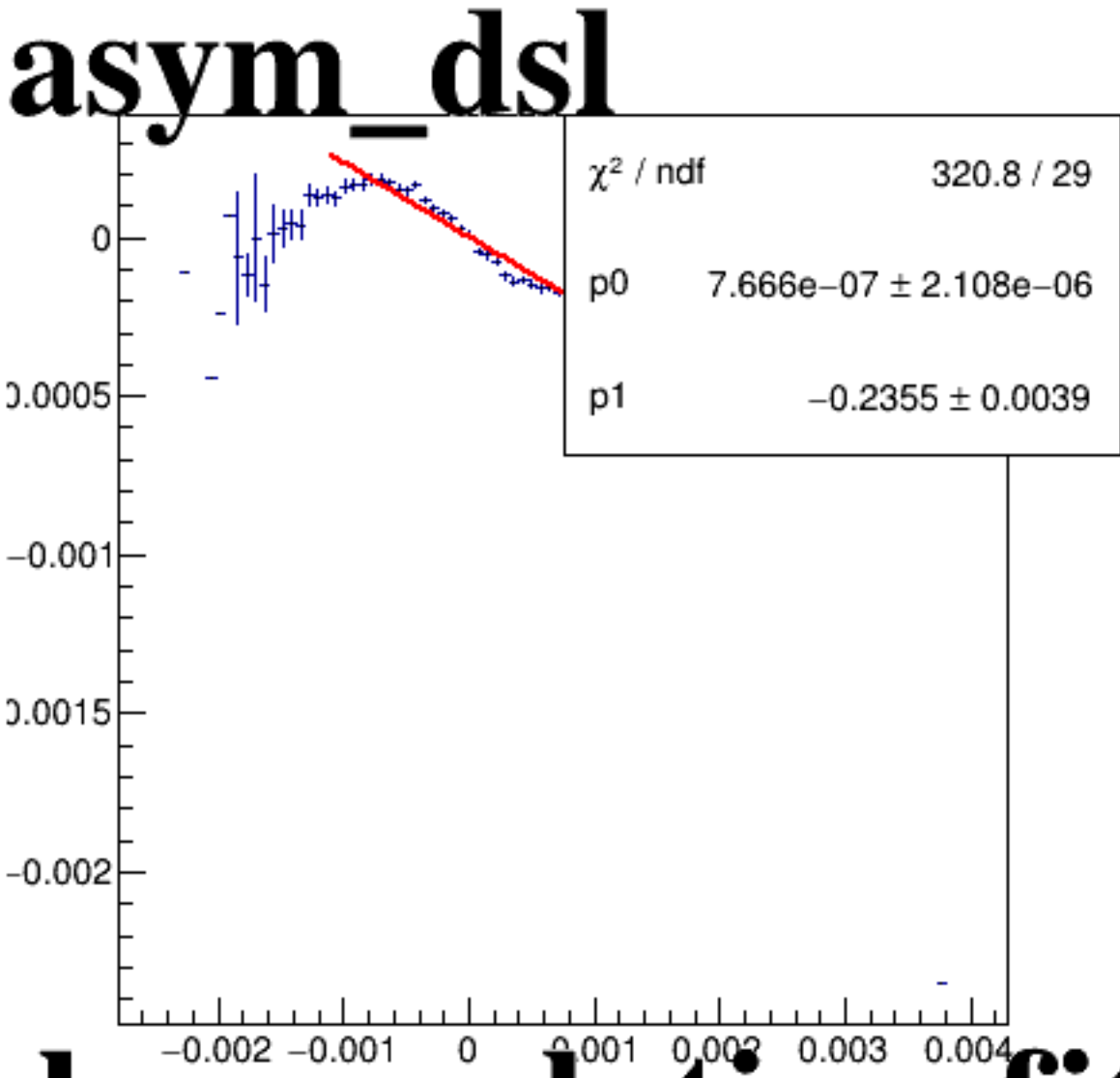
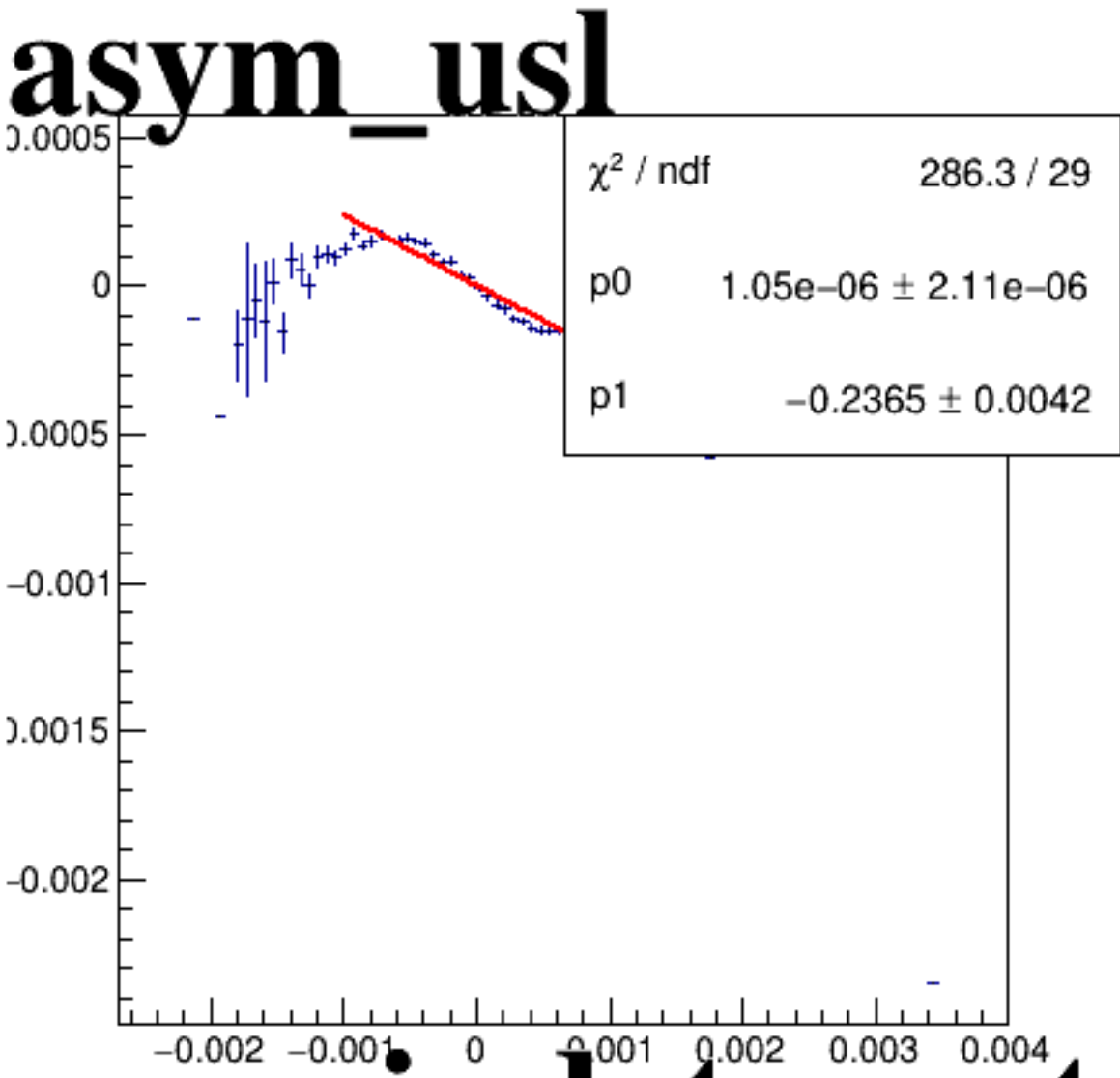
asym_dsl



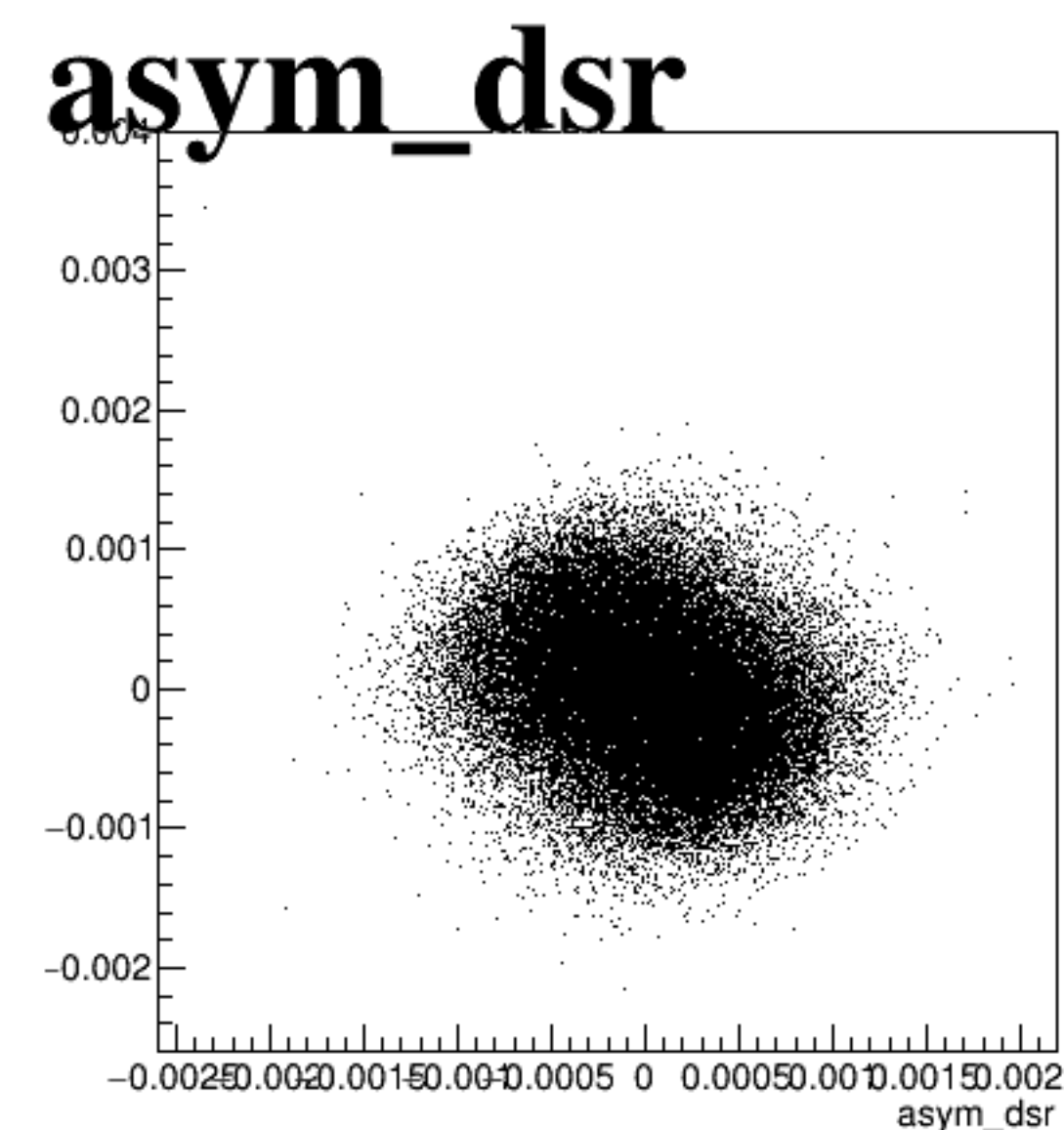
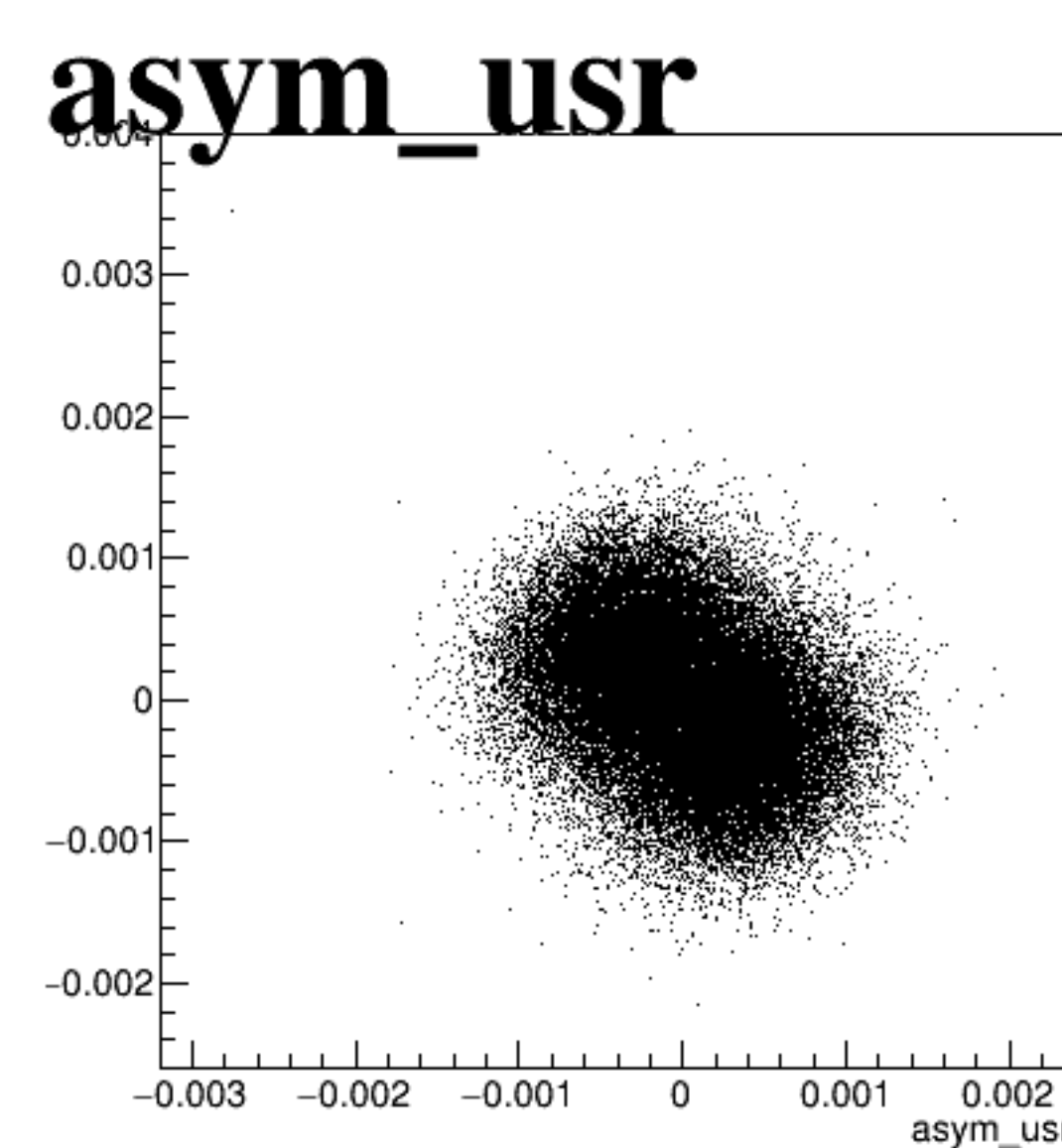
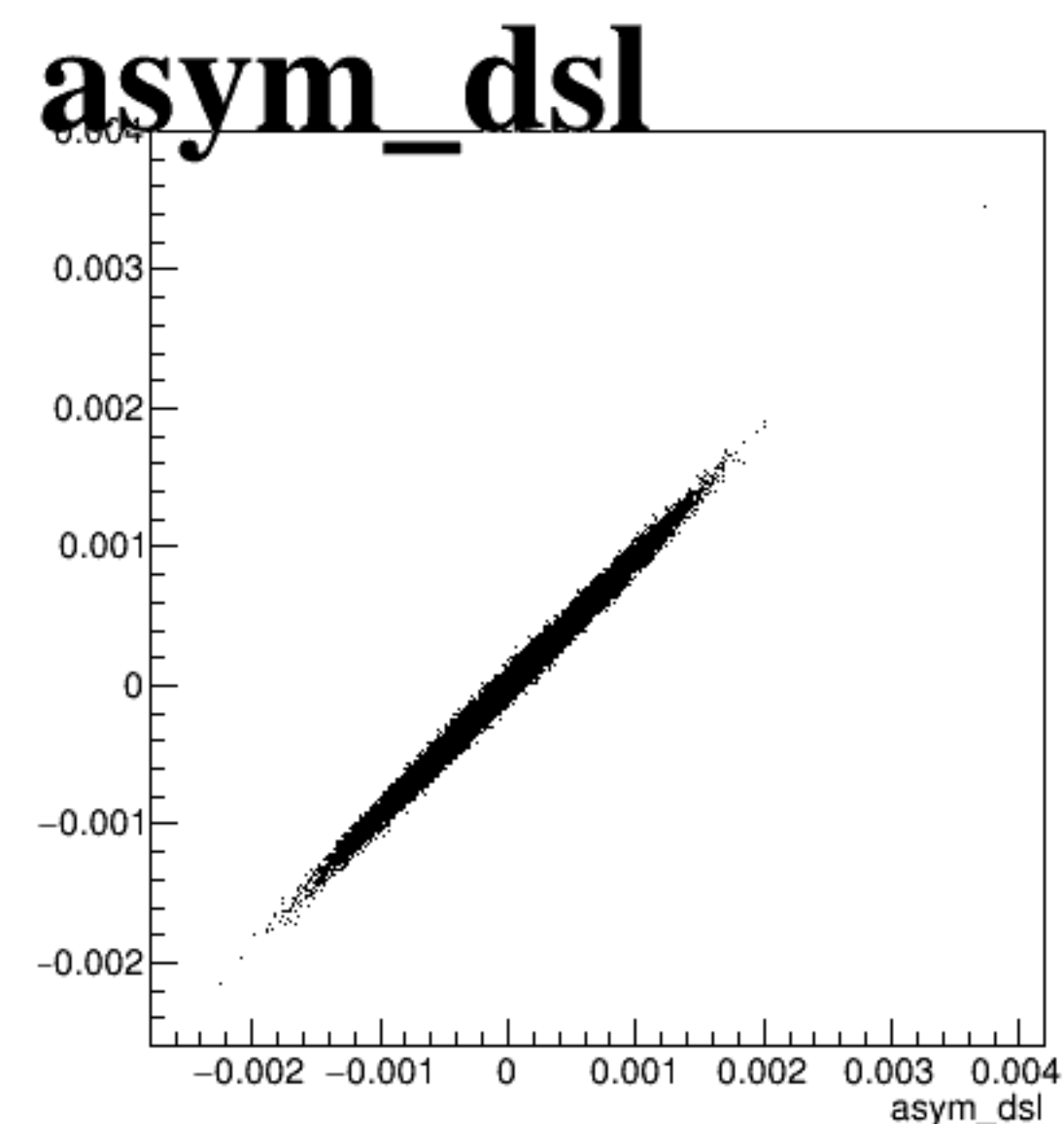
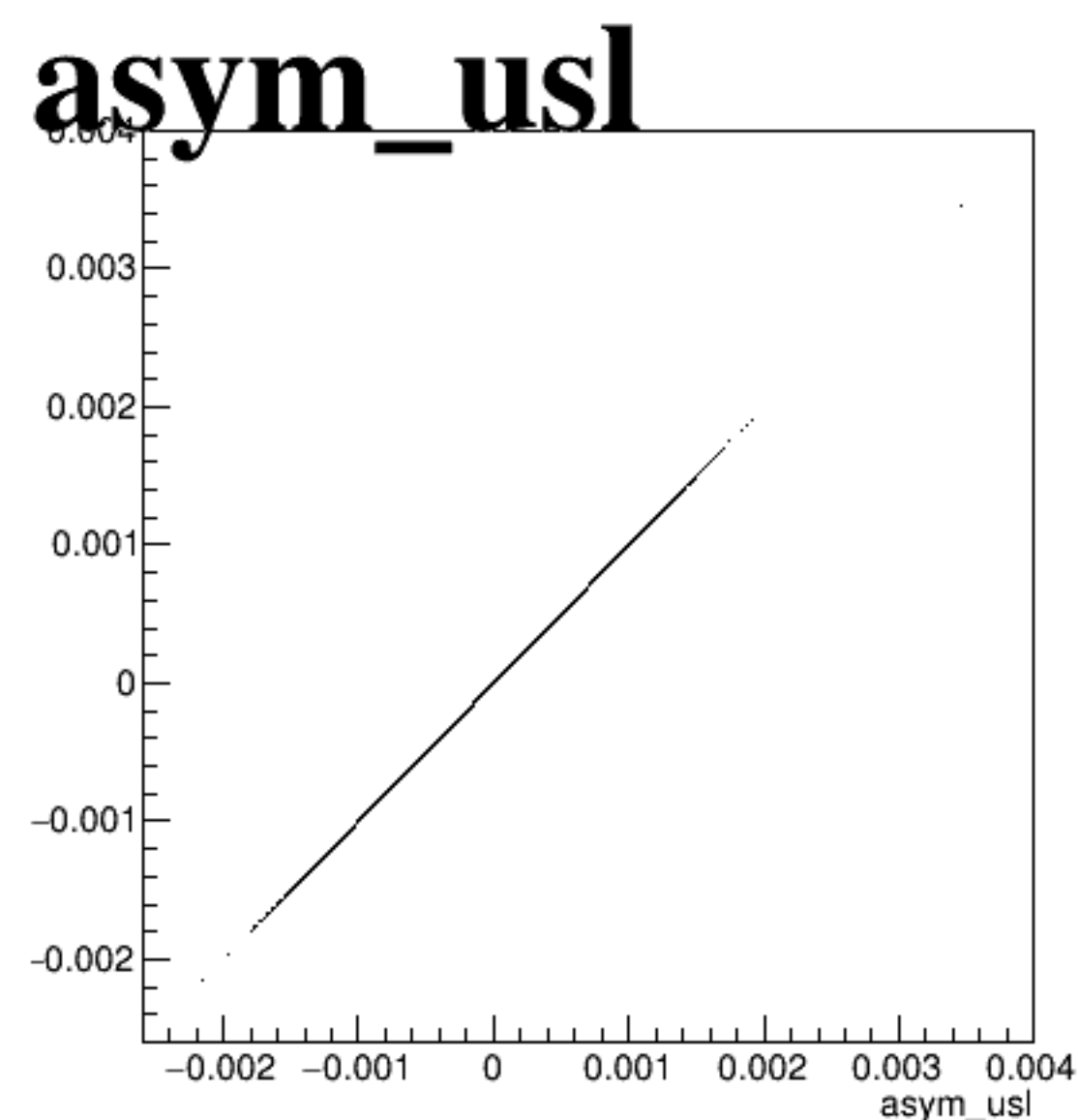
asym_usr



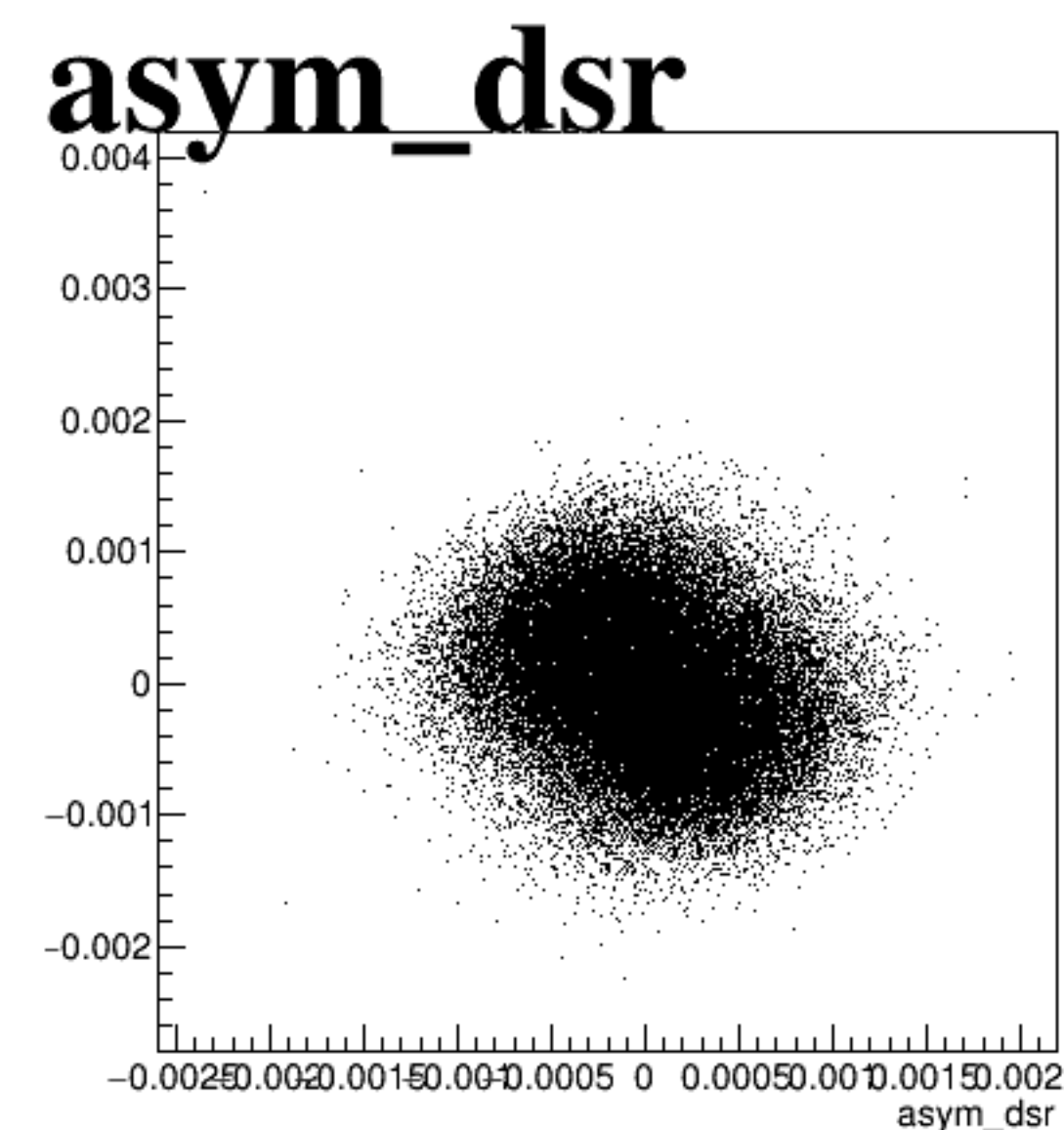
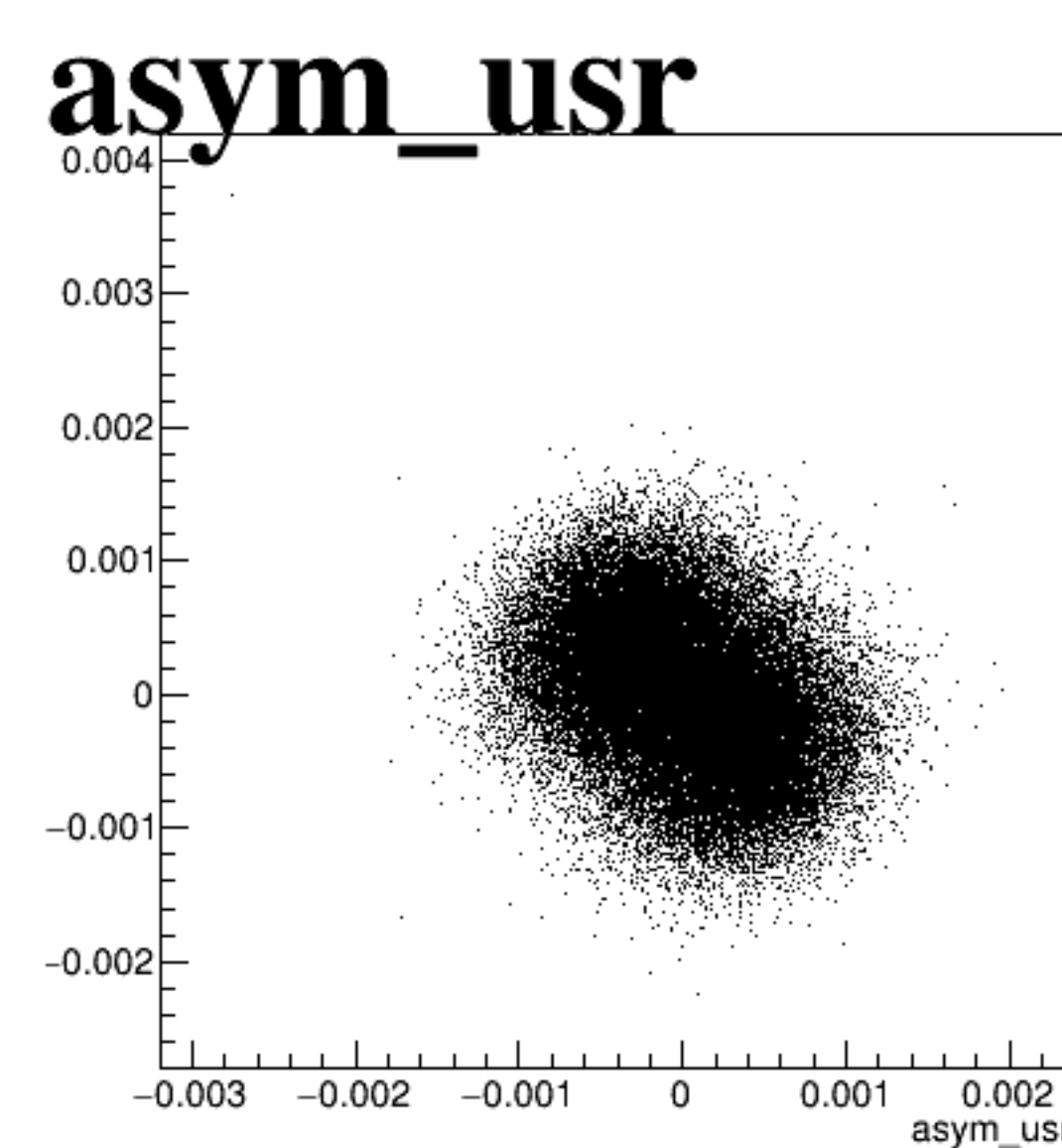
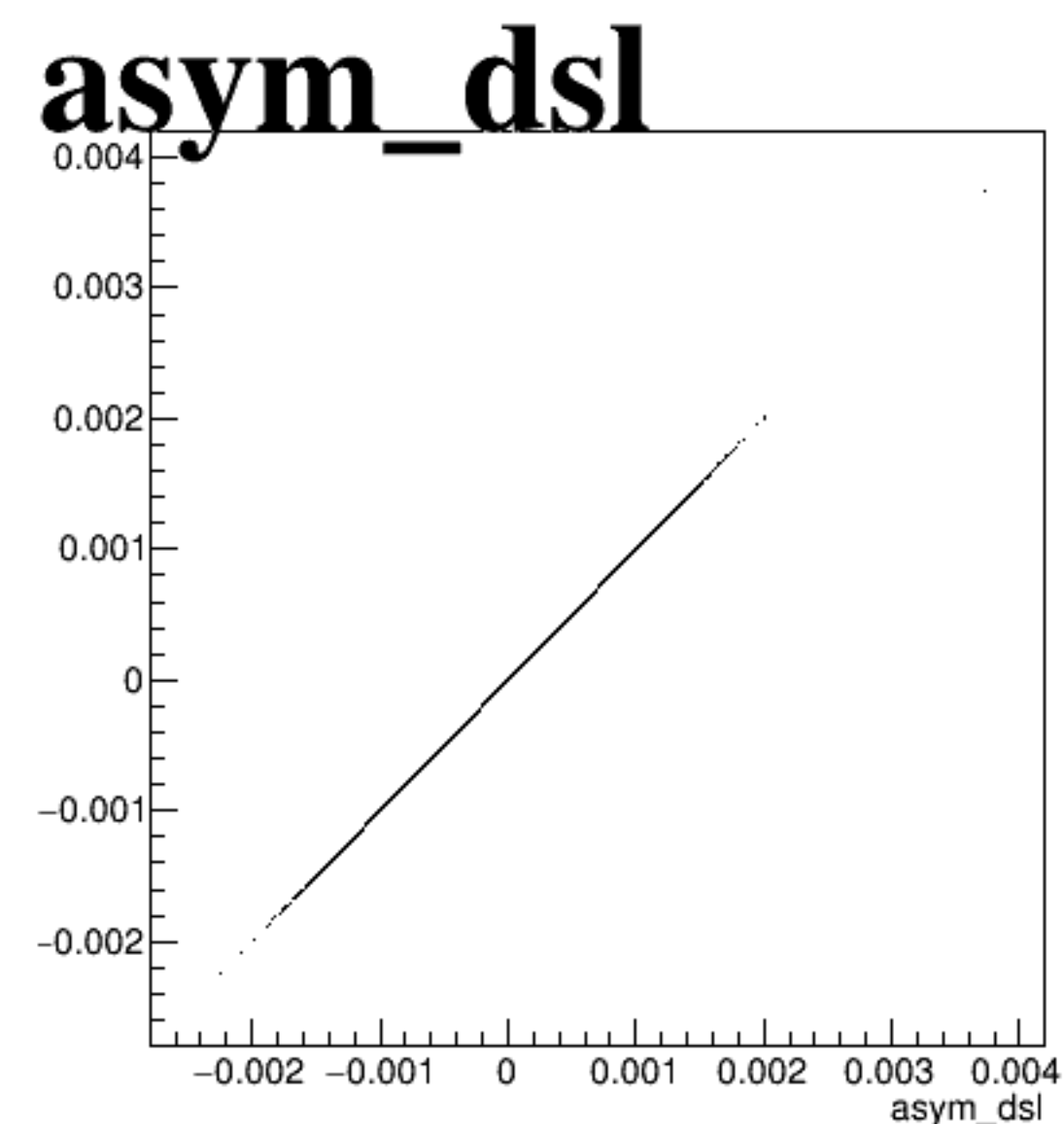
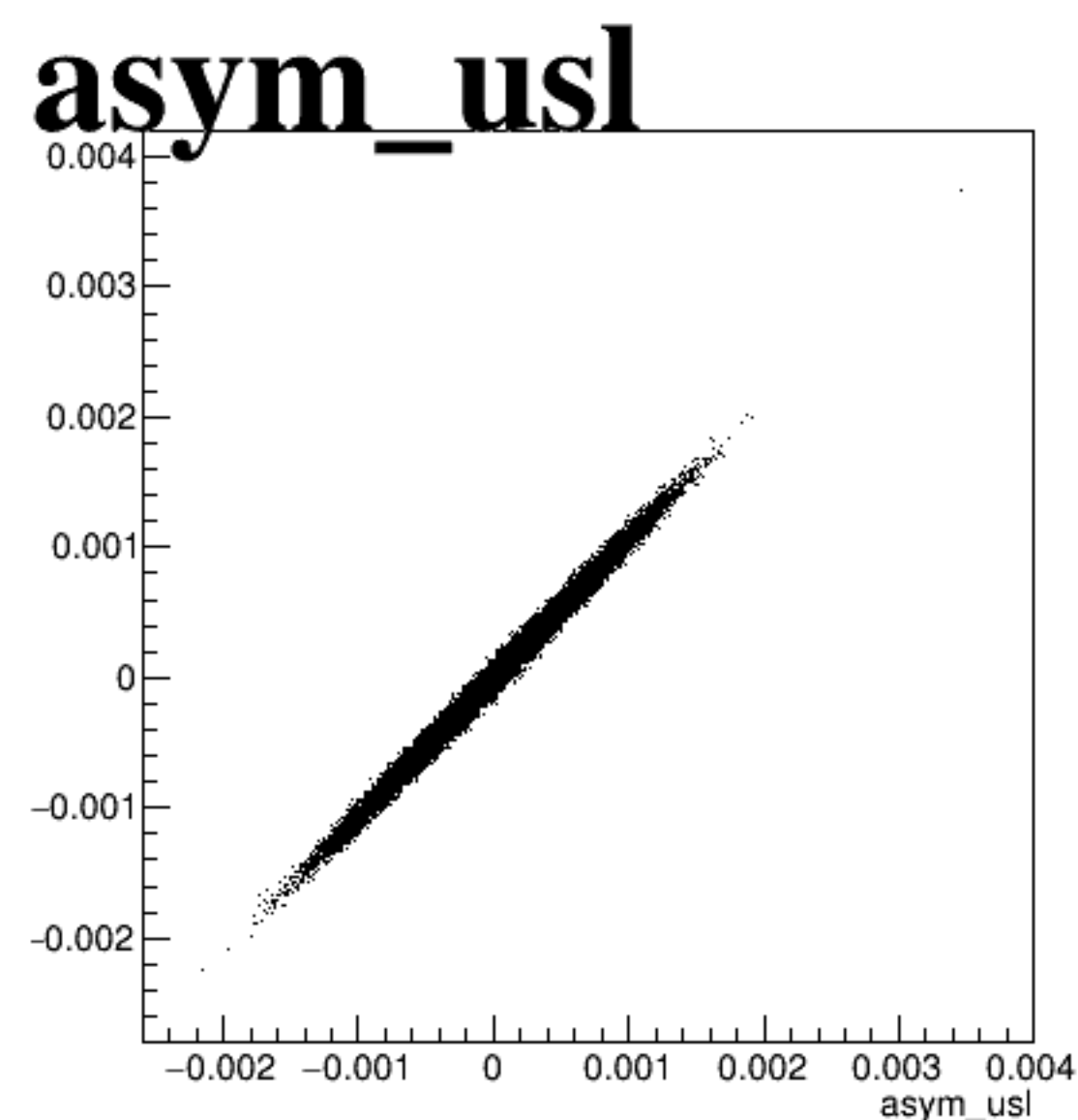
asym_dsr



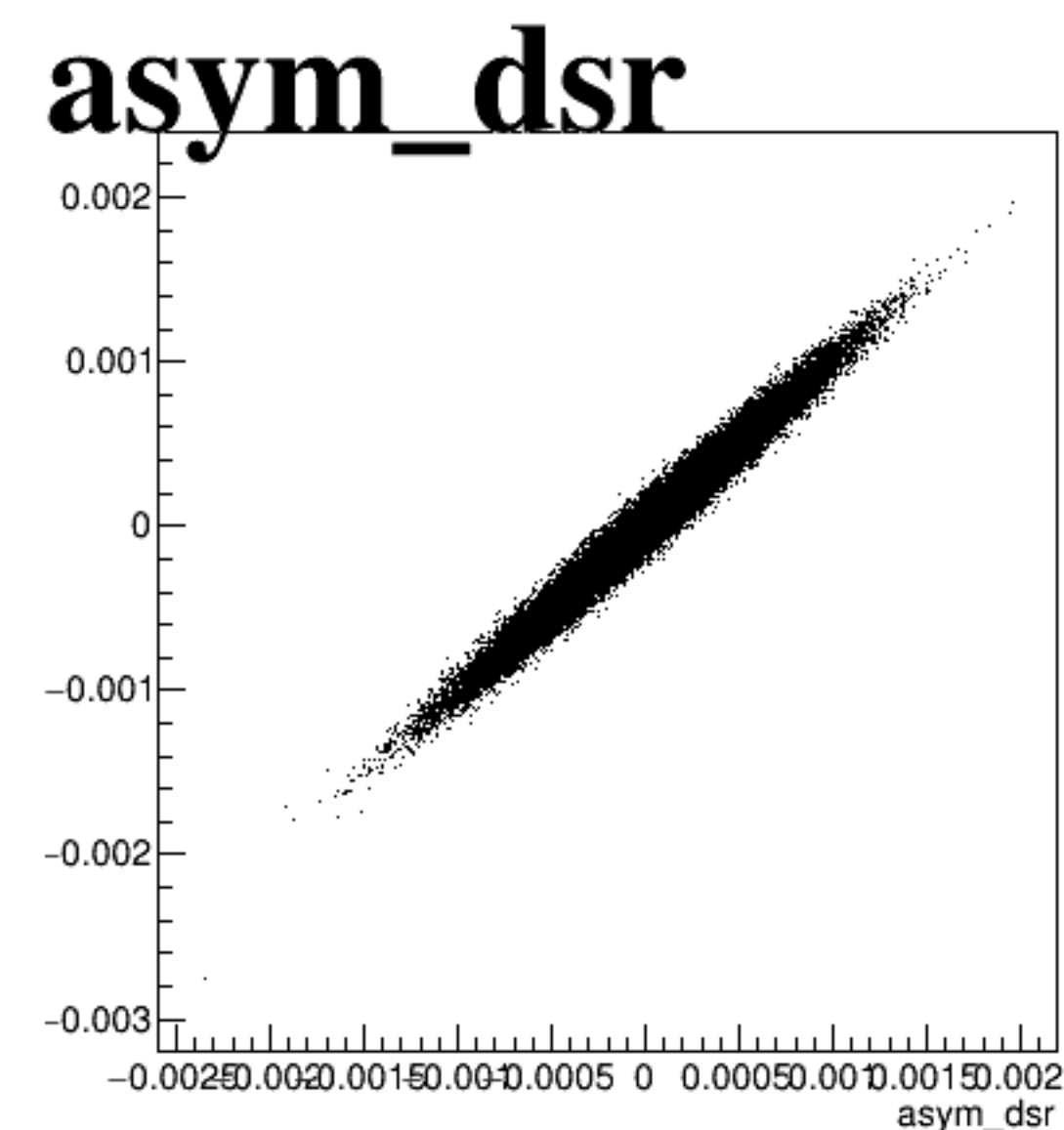
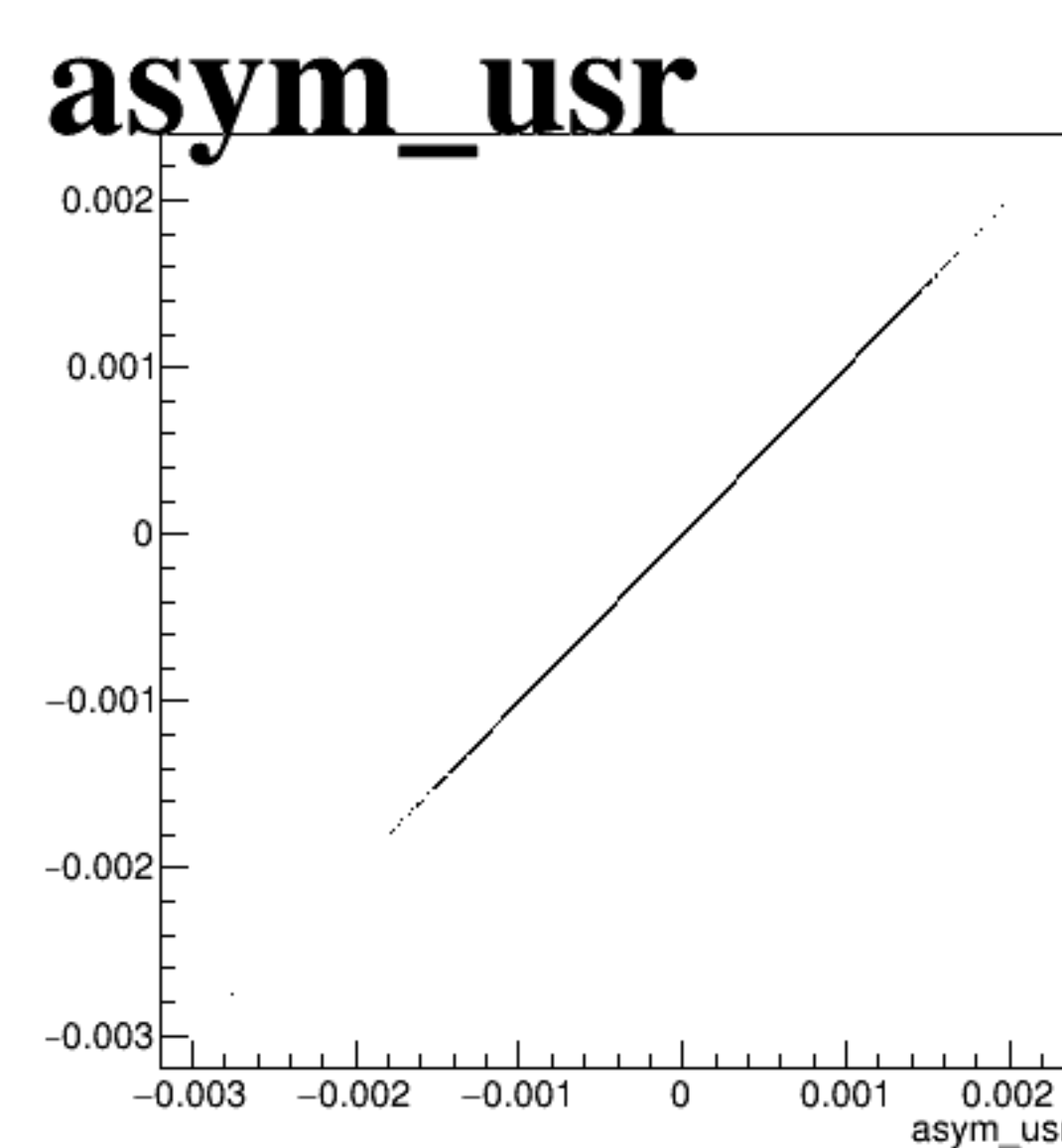
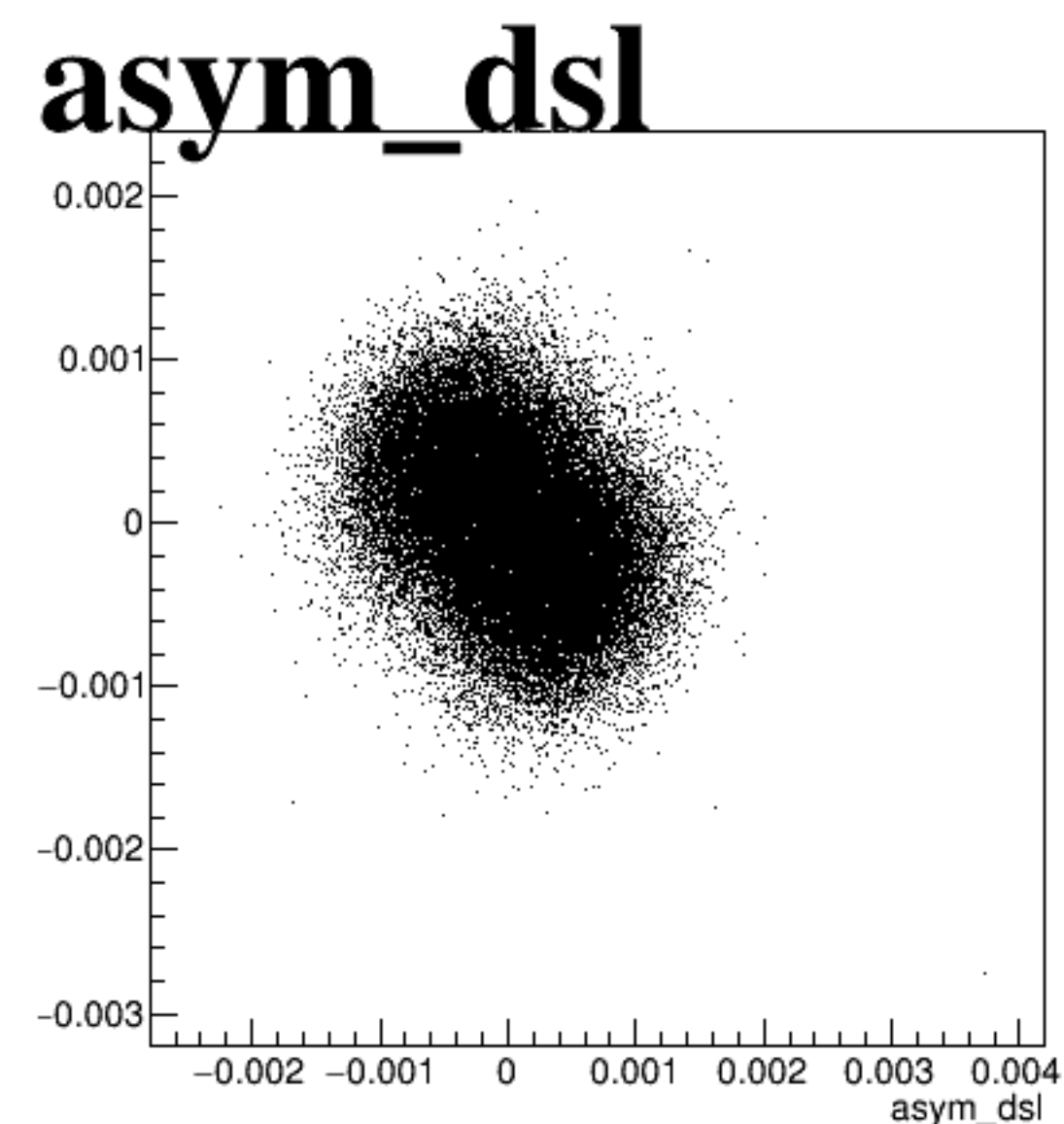
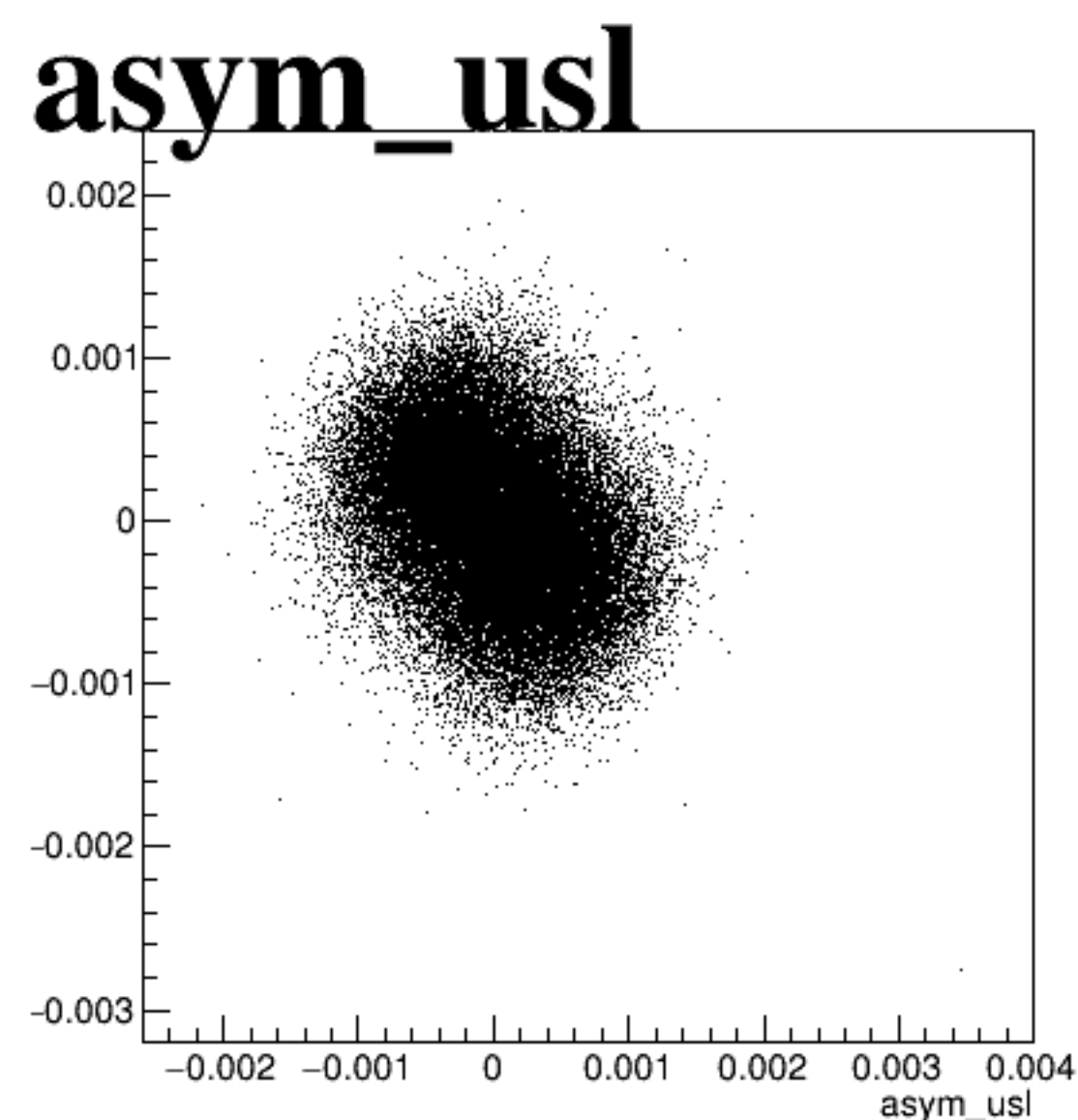
asym_usl



asym_dsl



asym_usr



asym_dsr

