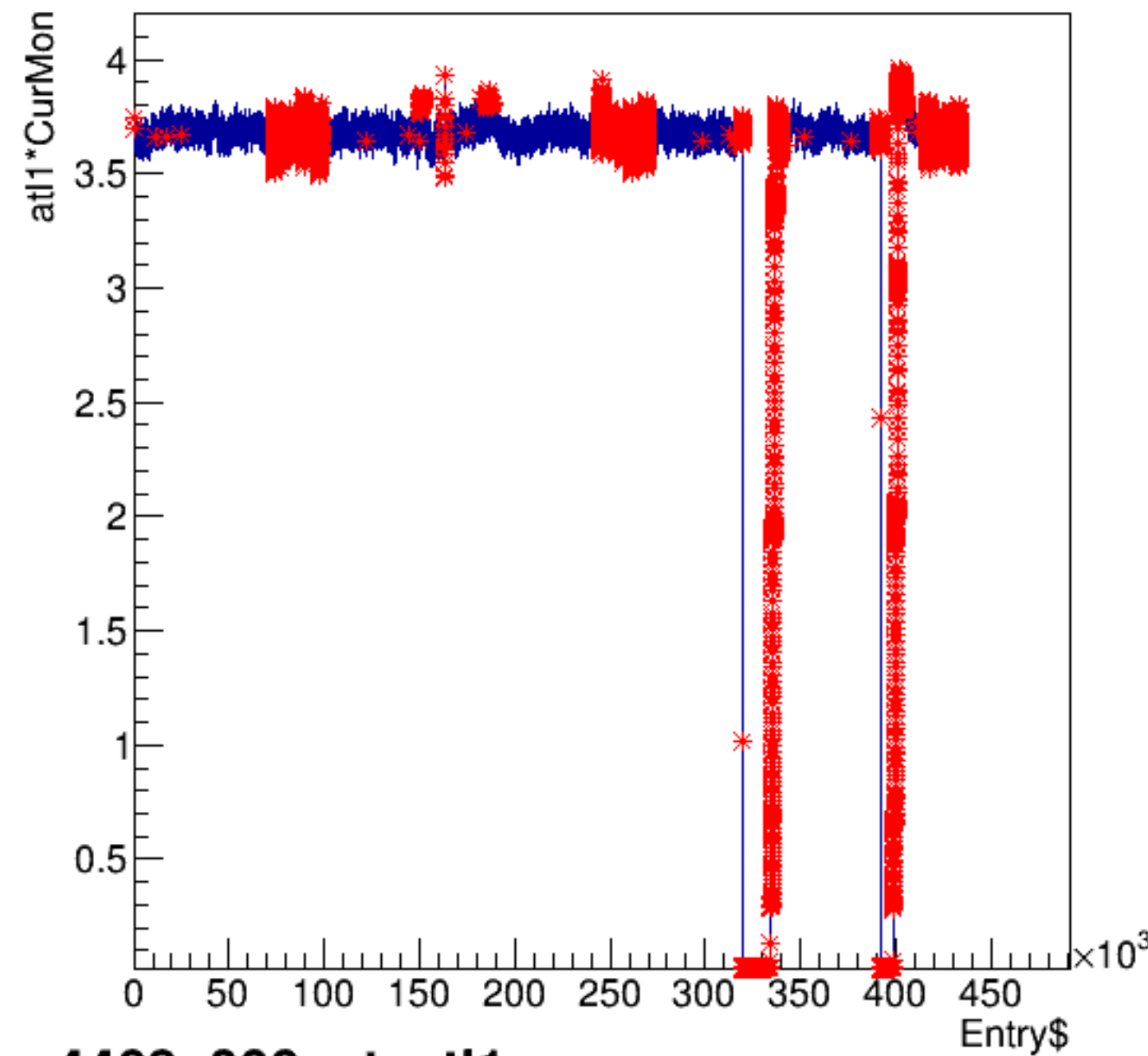
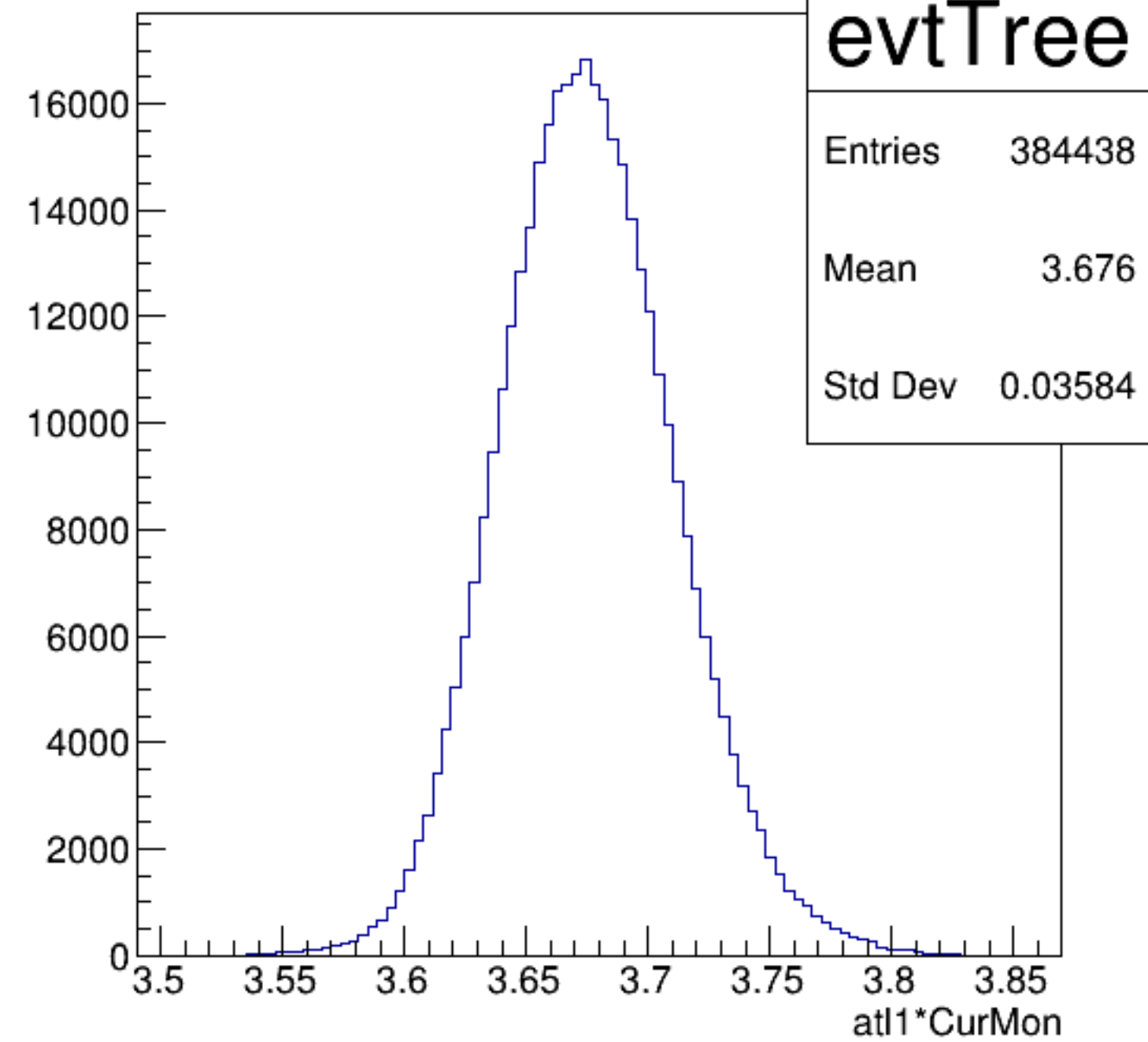


atl1*CurMon:Entry\$

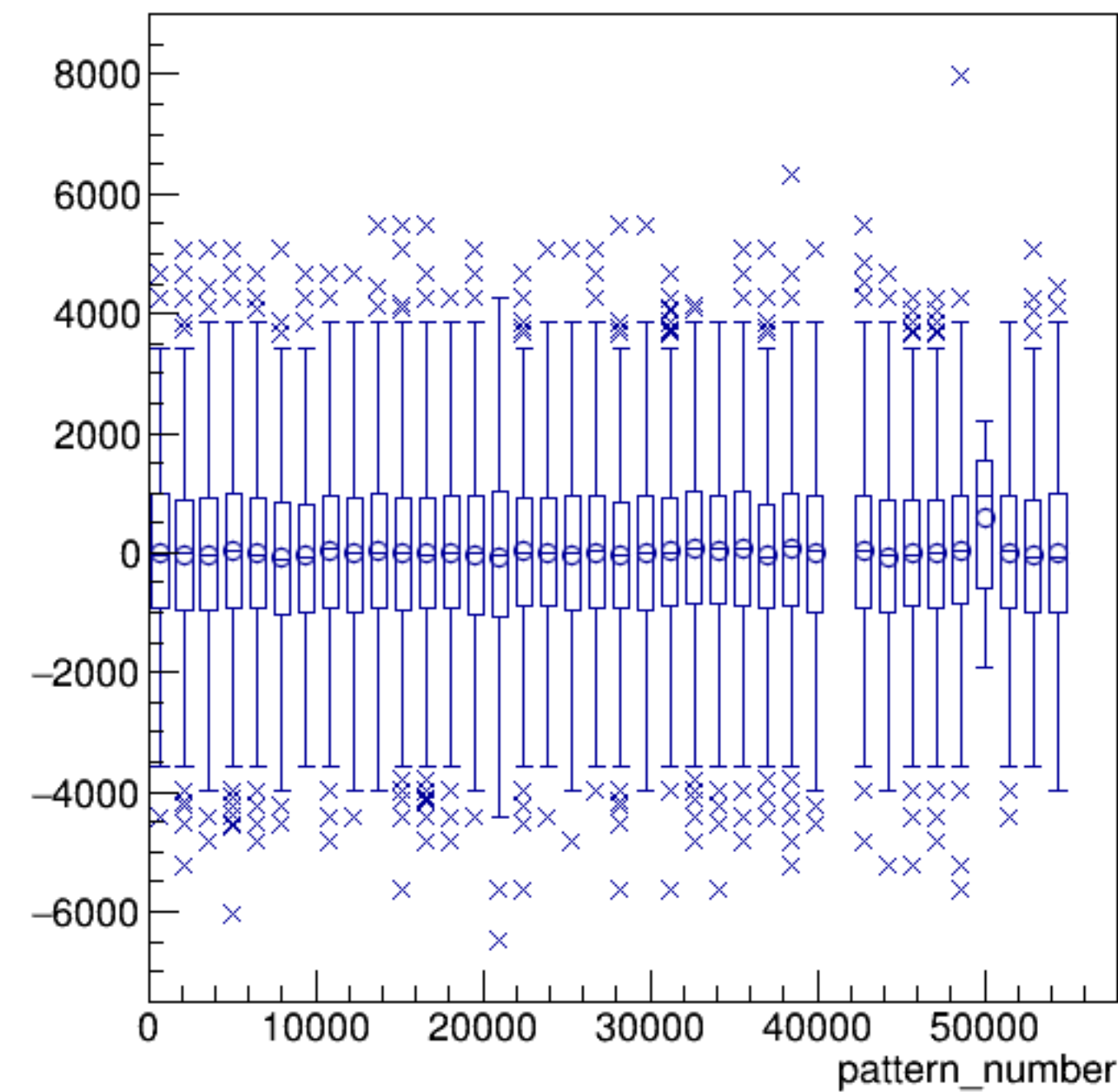


run4468_000_at_atl1.png

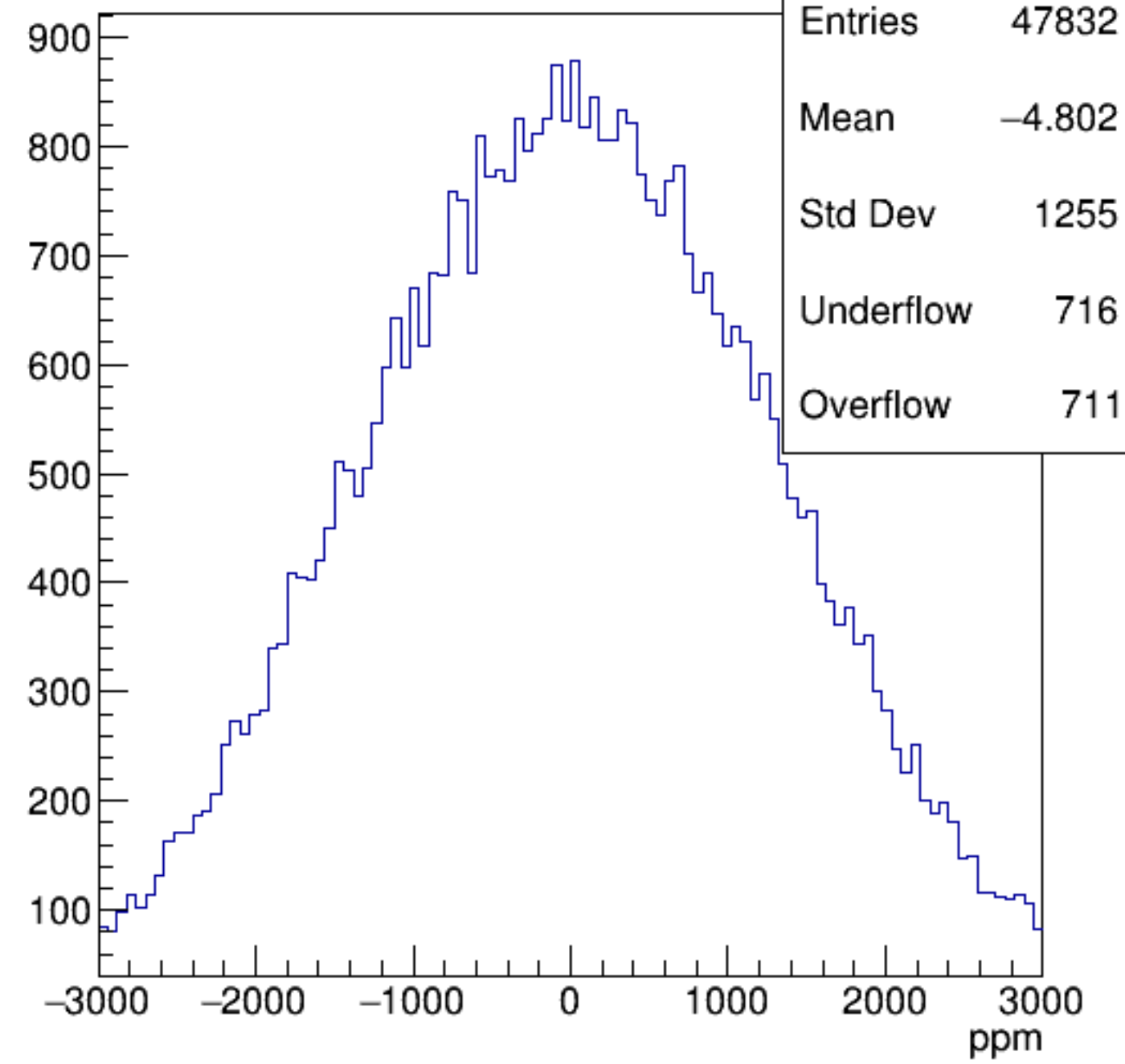
atl1*CurMon {ErrorFlag==0}



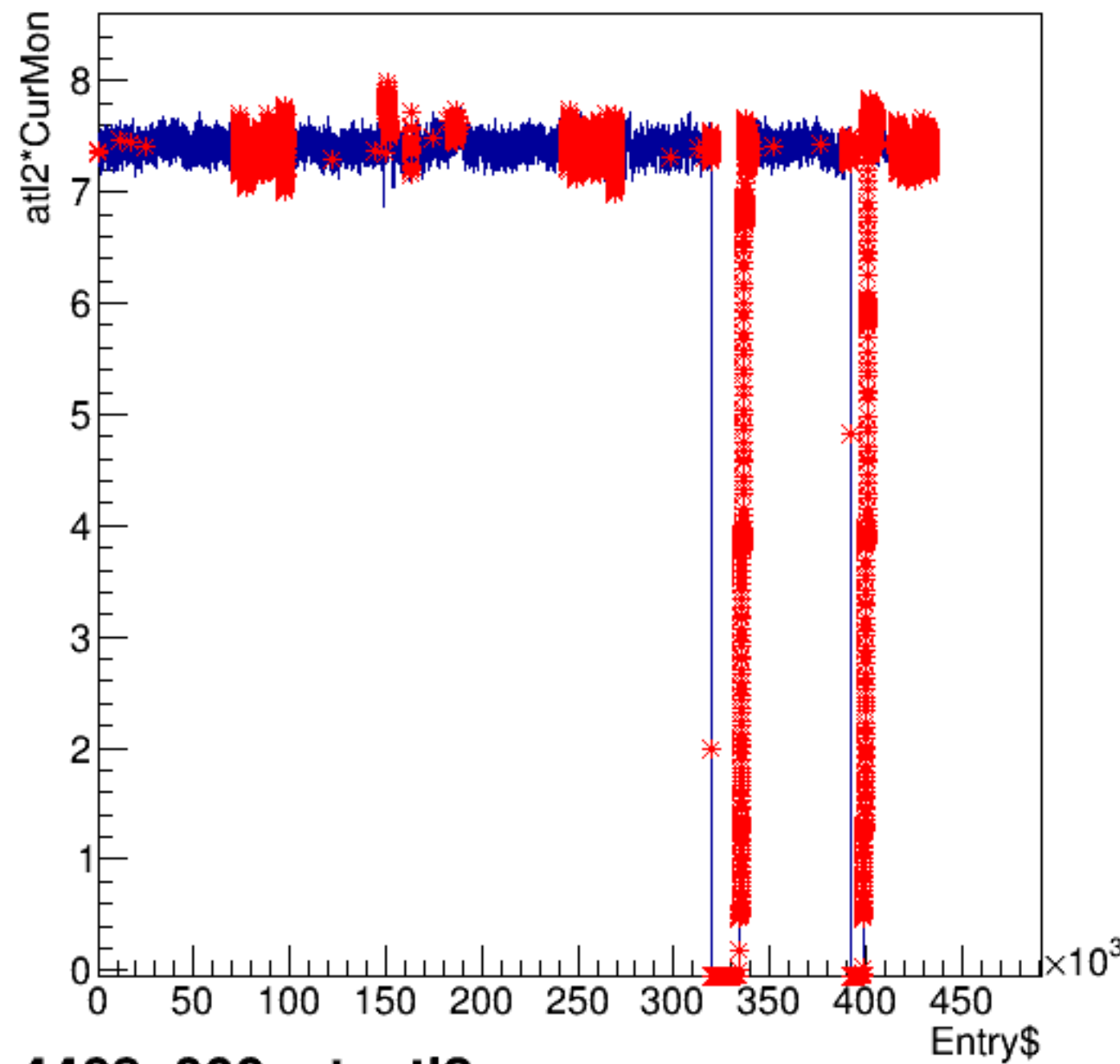
asym_atl1/ppm:pattern_number {ErrorFlag==0}



asym_atl1

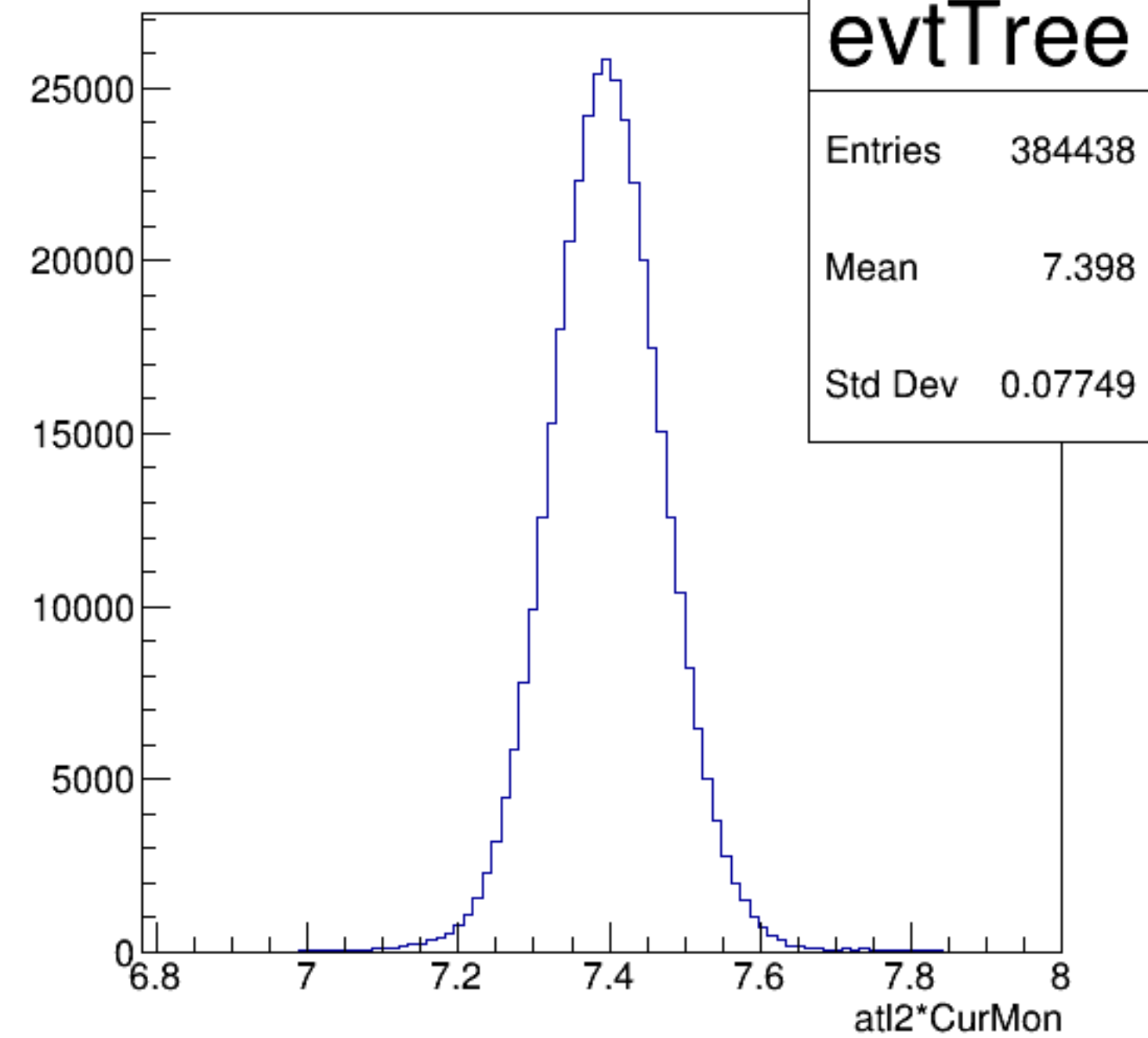


atl2*CurMon:Entry\$

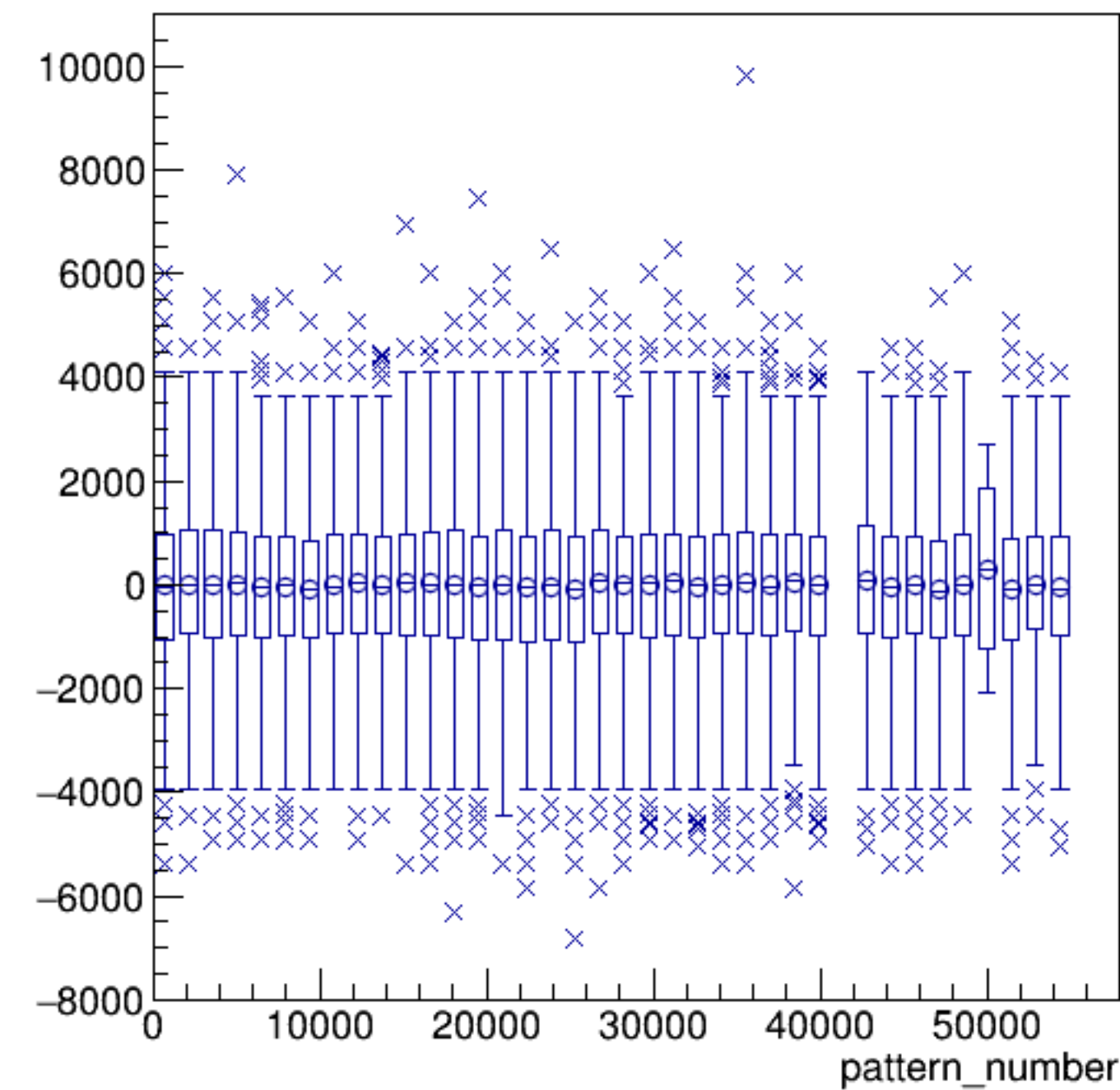


run4468_000_at_atl2.png

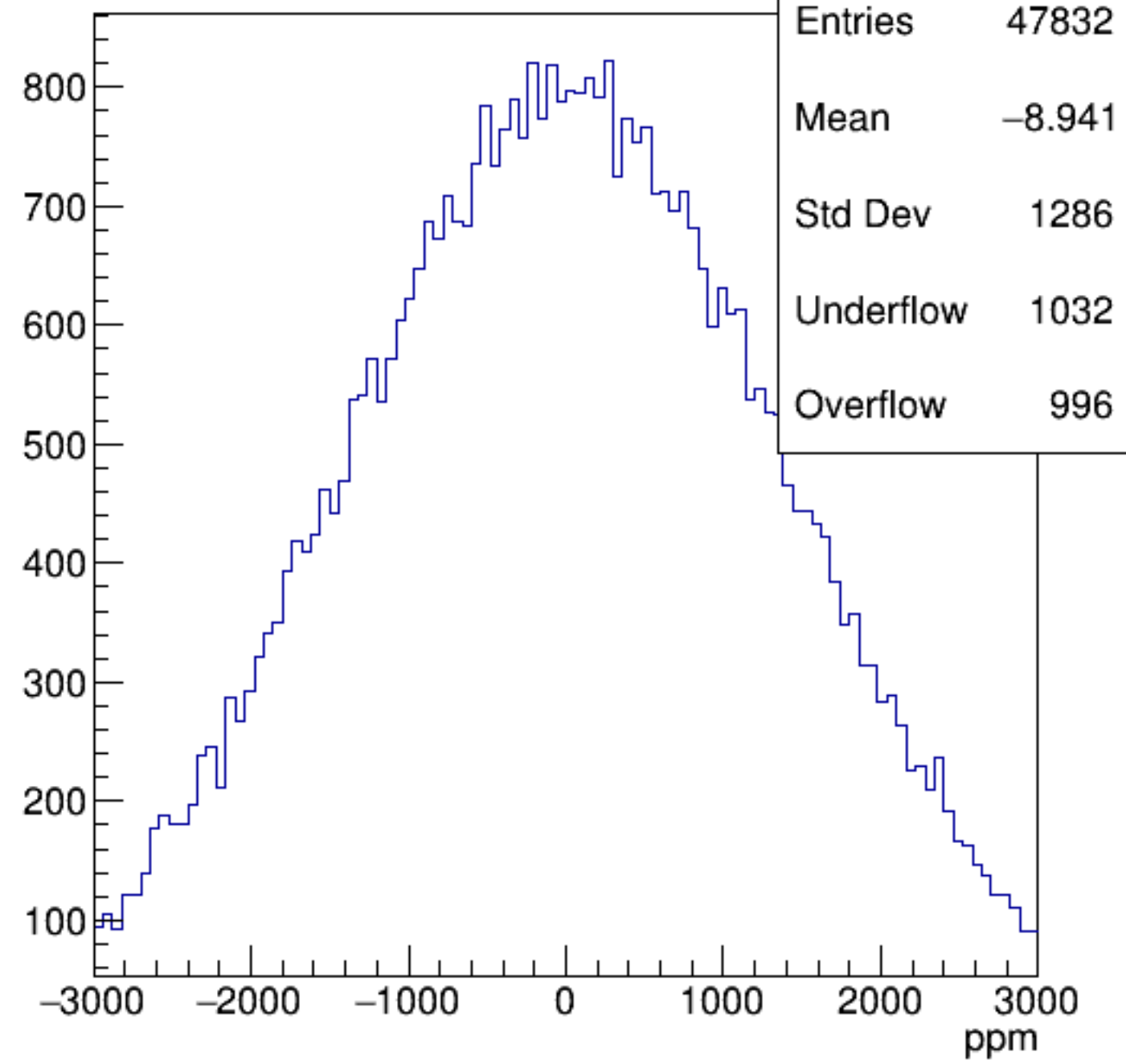
atl2*CurMon {ErrorFlag==0}



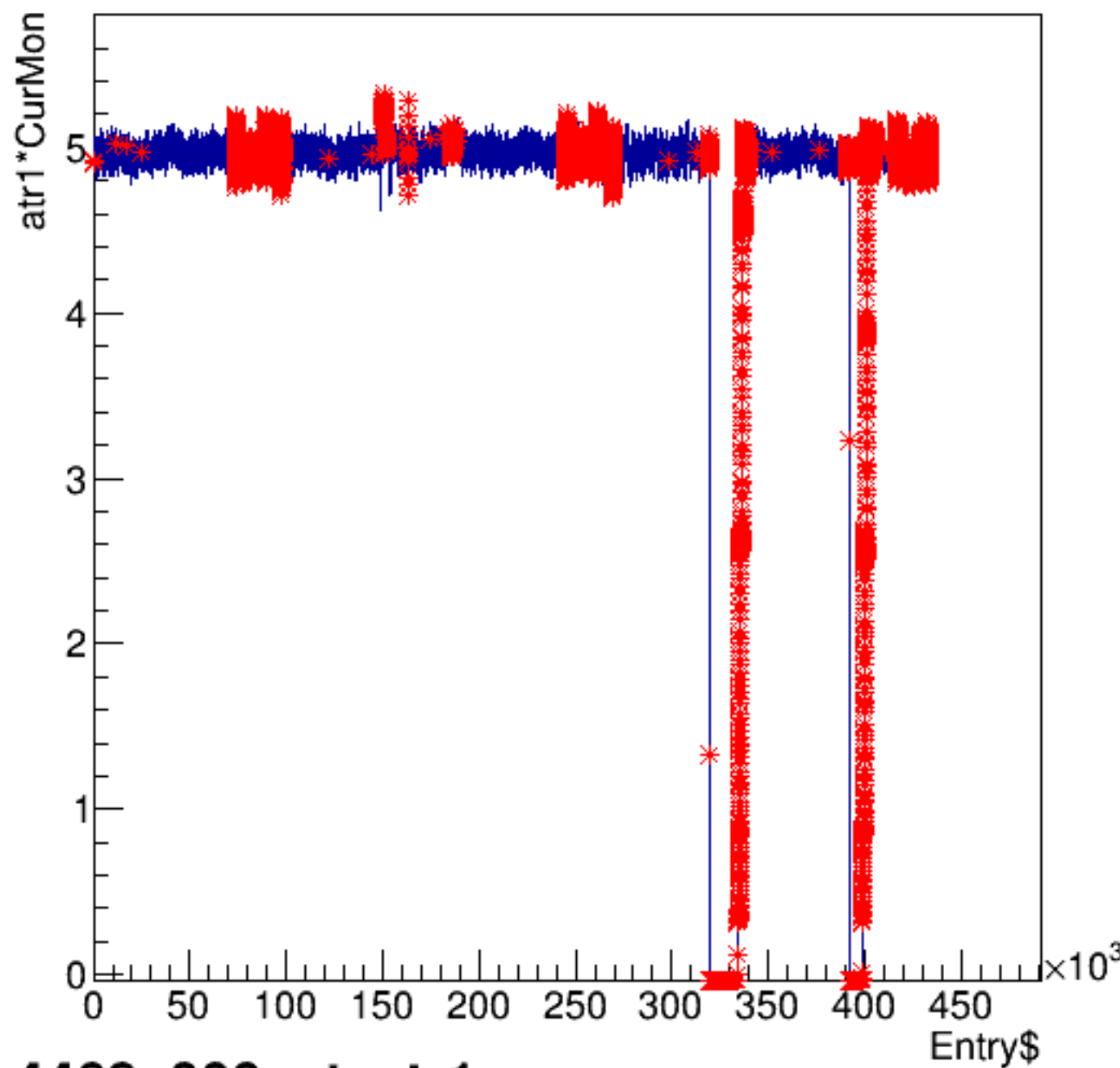
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asym_atl2

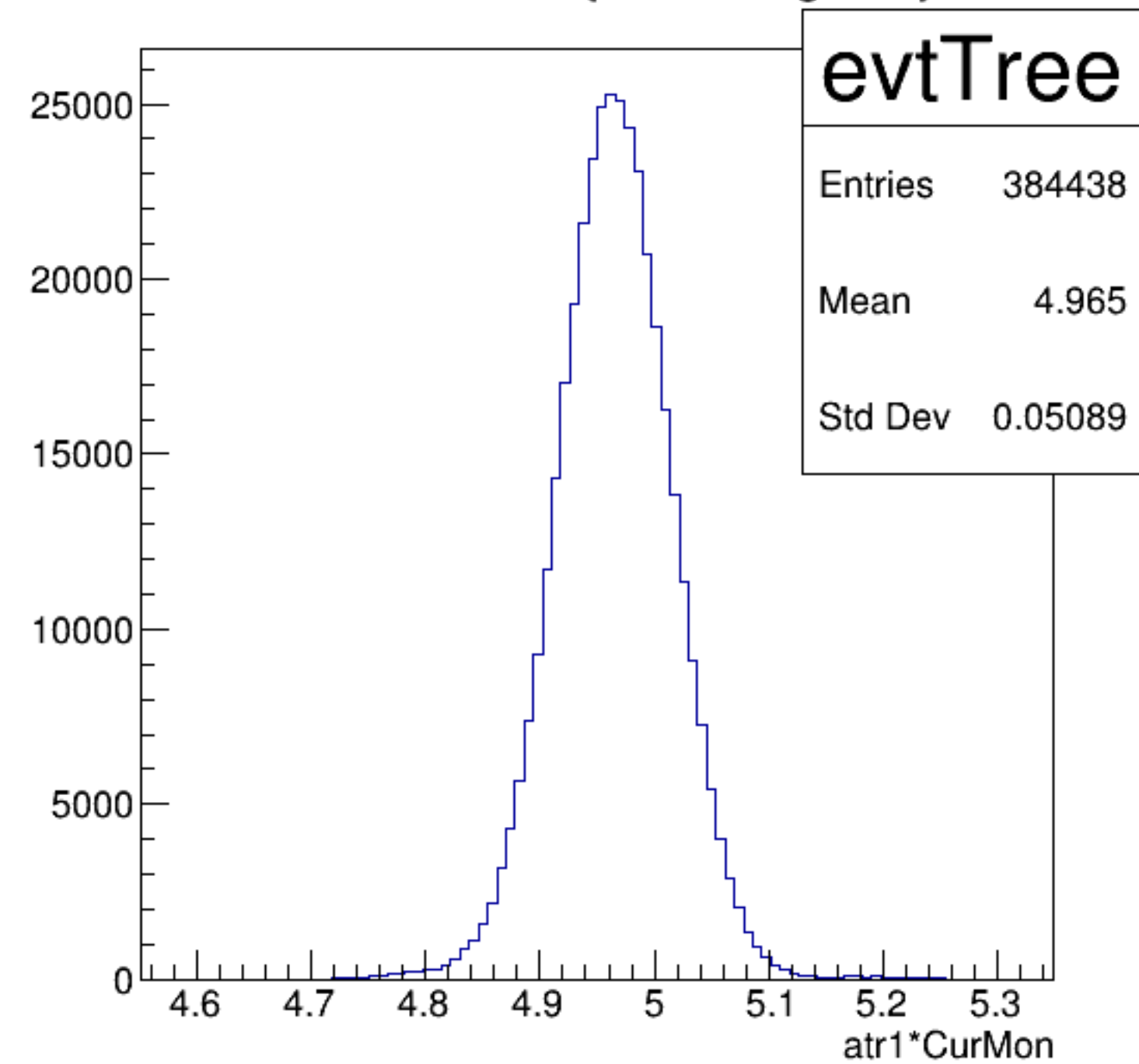


atr1*CurMon:Entry\$

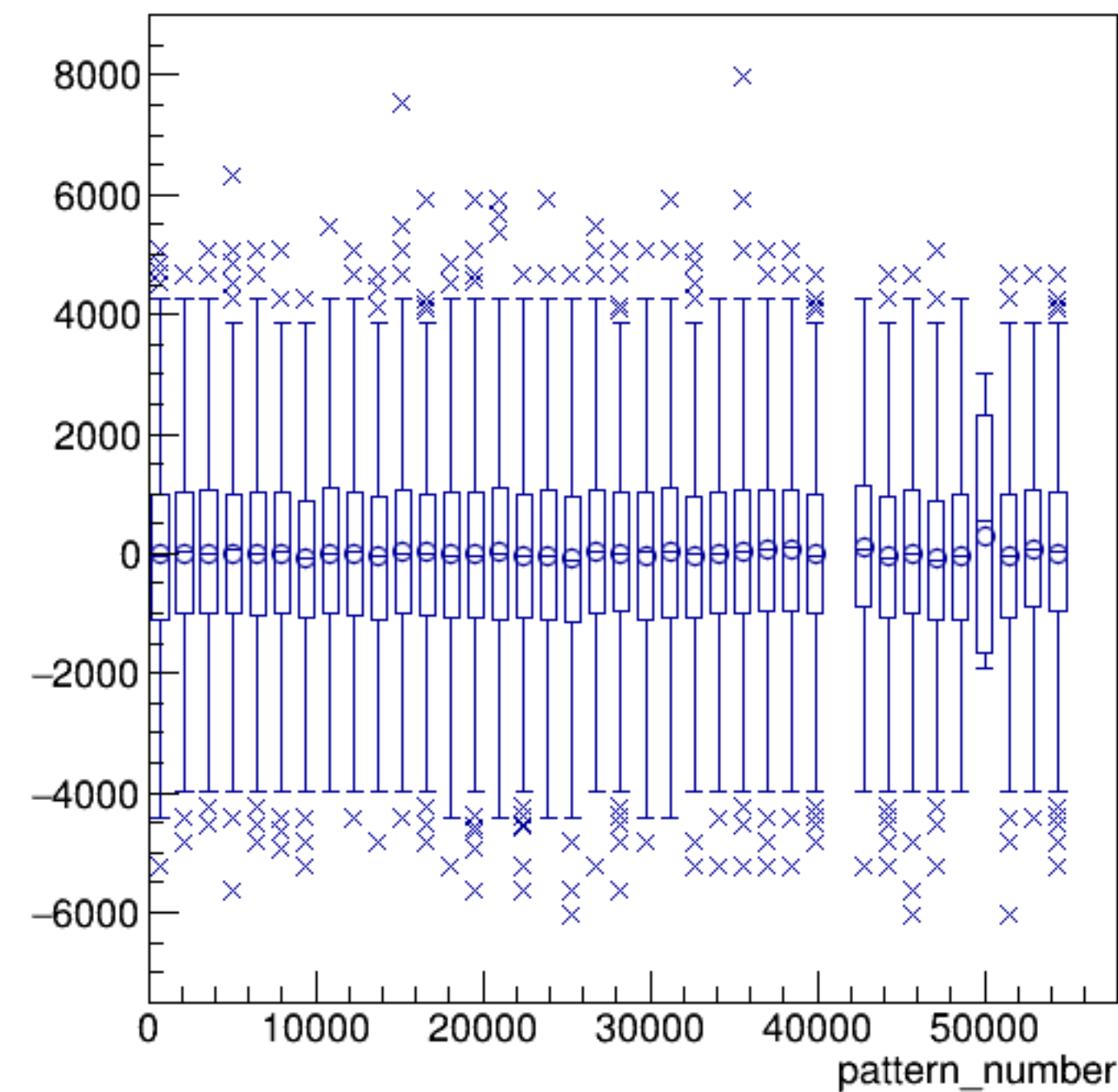


run4468_000_at_atr1.png

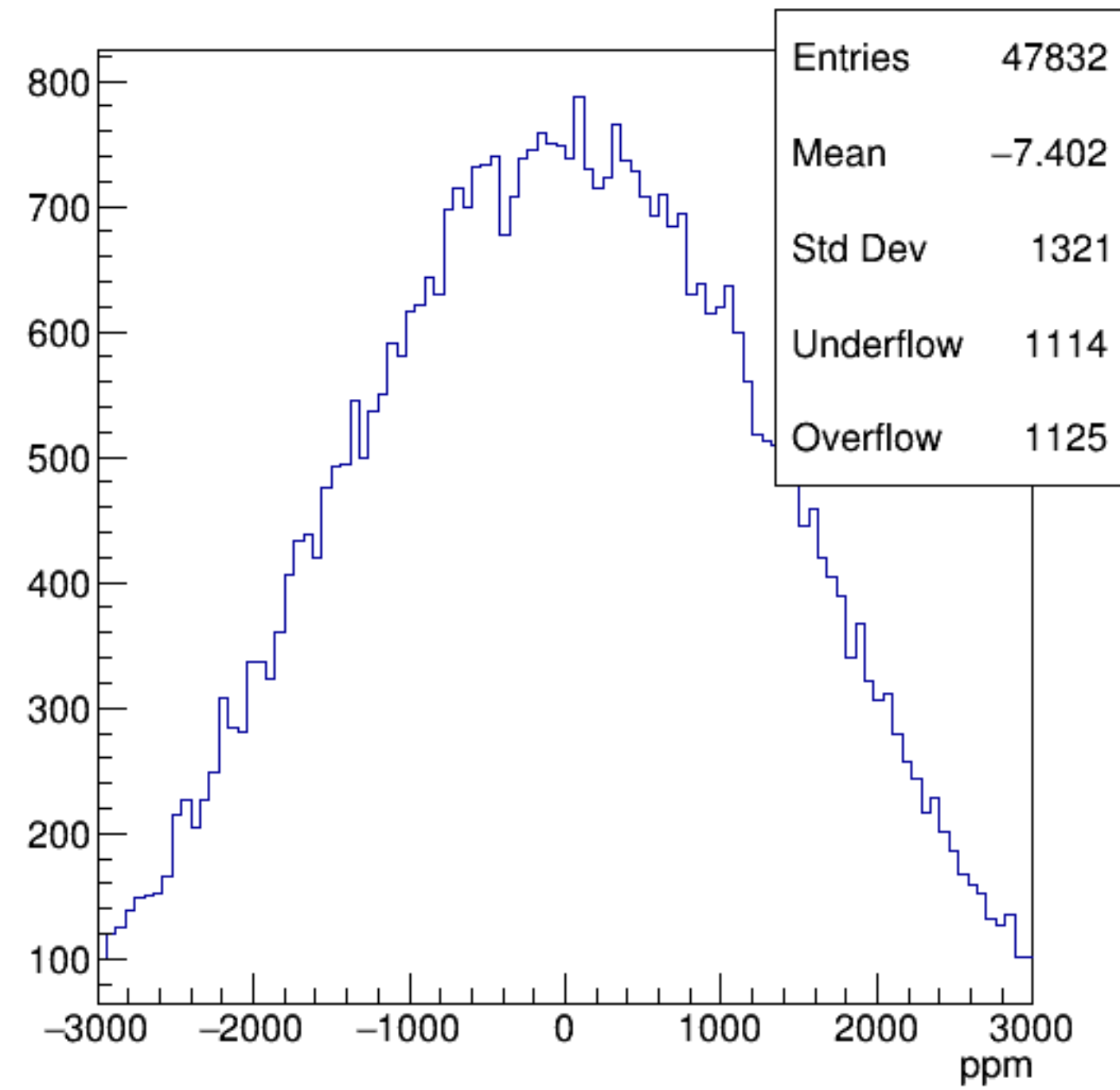
atr1*CurMon {ErrorFlag==0}



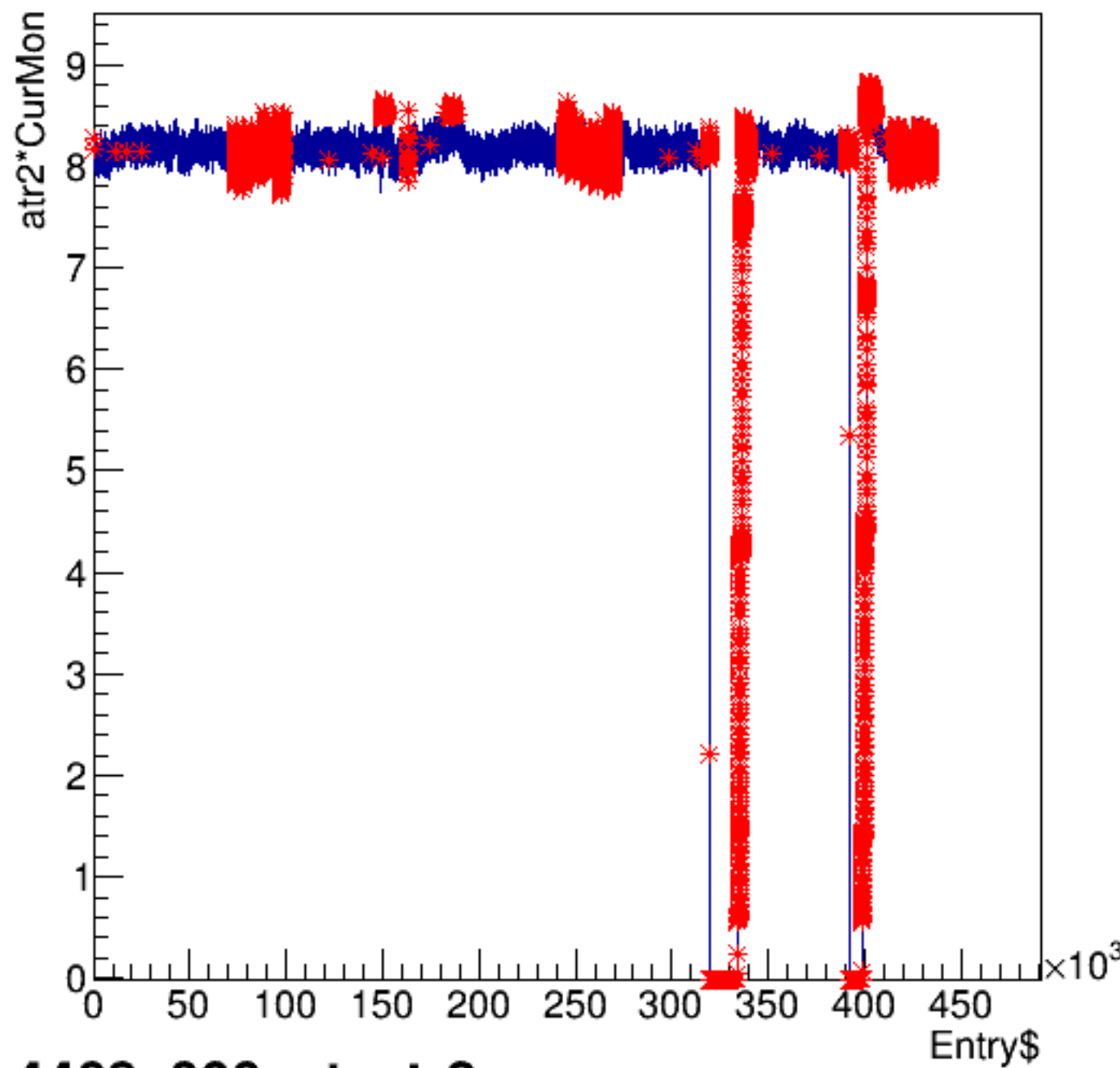
asym_atr1/ppm:pattern_number {ErrorFlag==0}



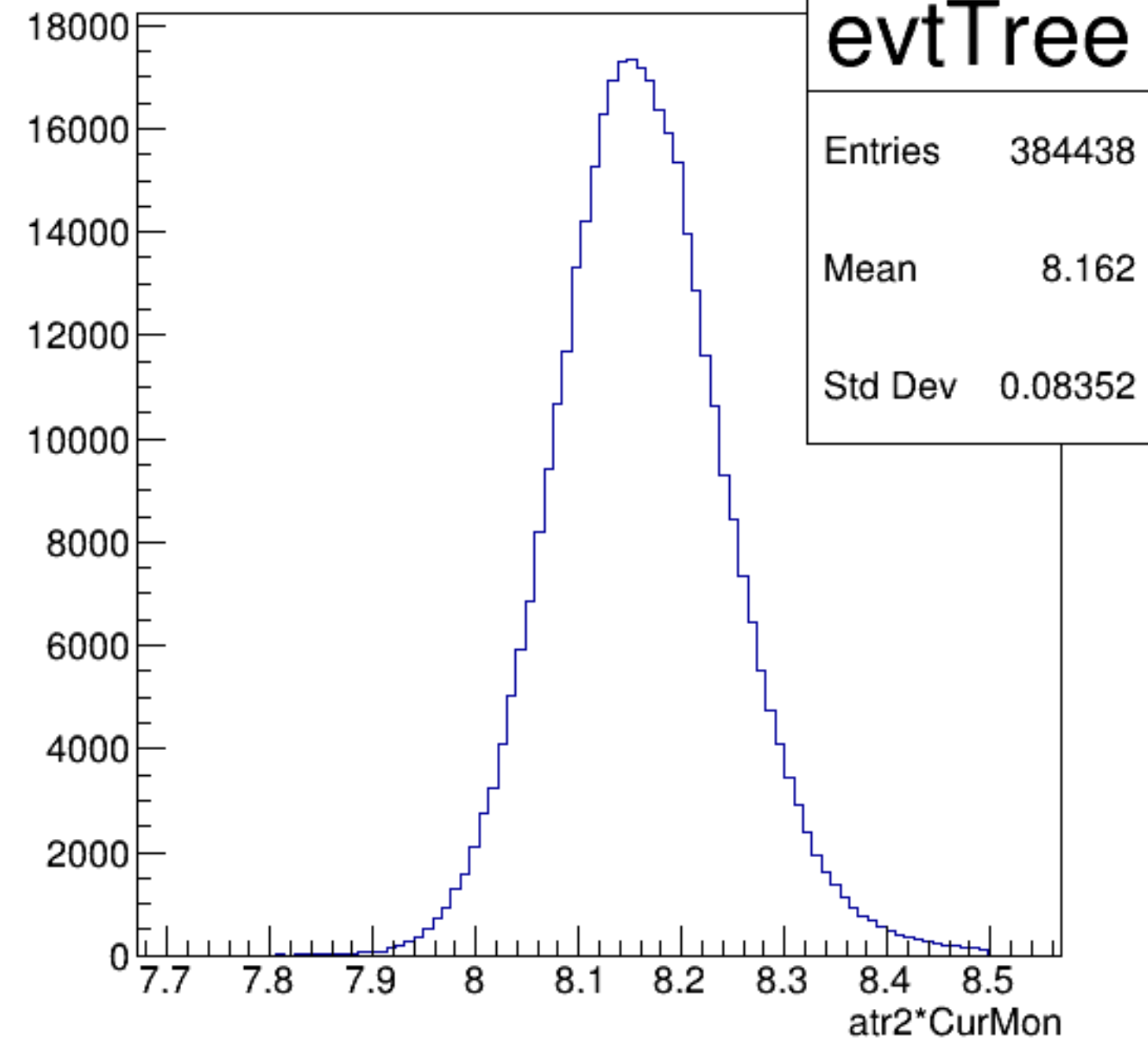
asym_atr1



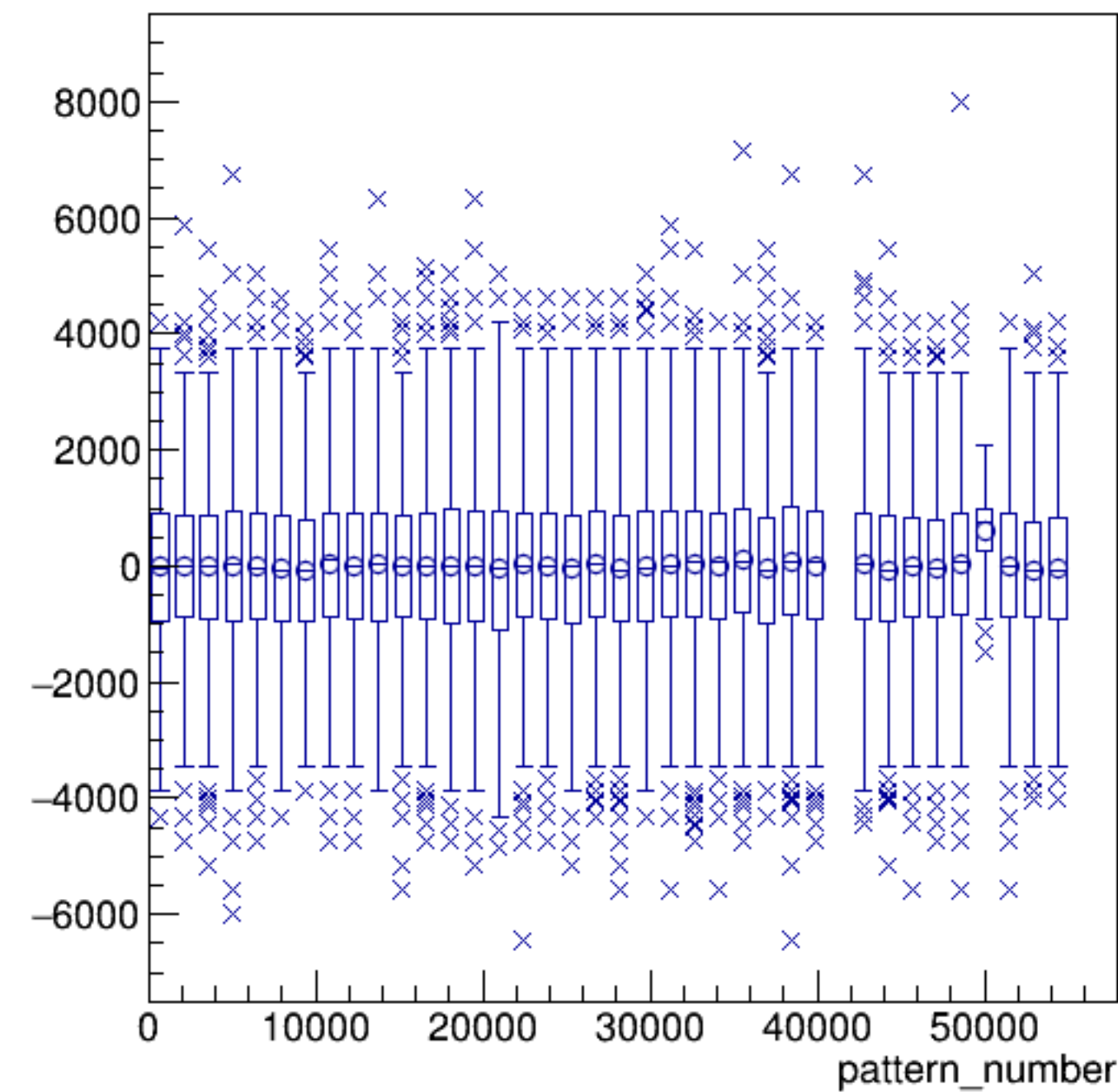
atr2*CurMon:Entry\$



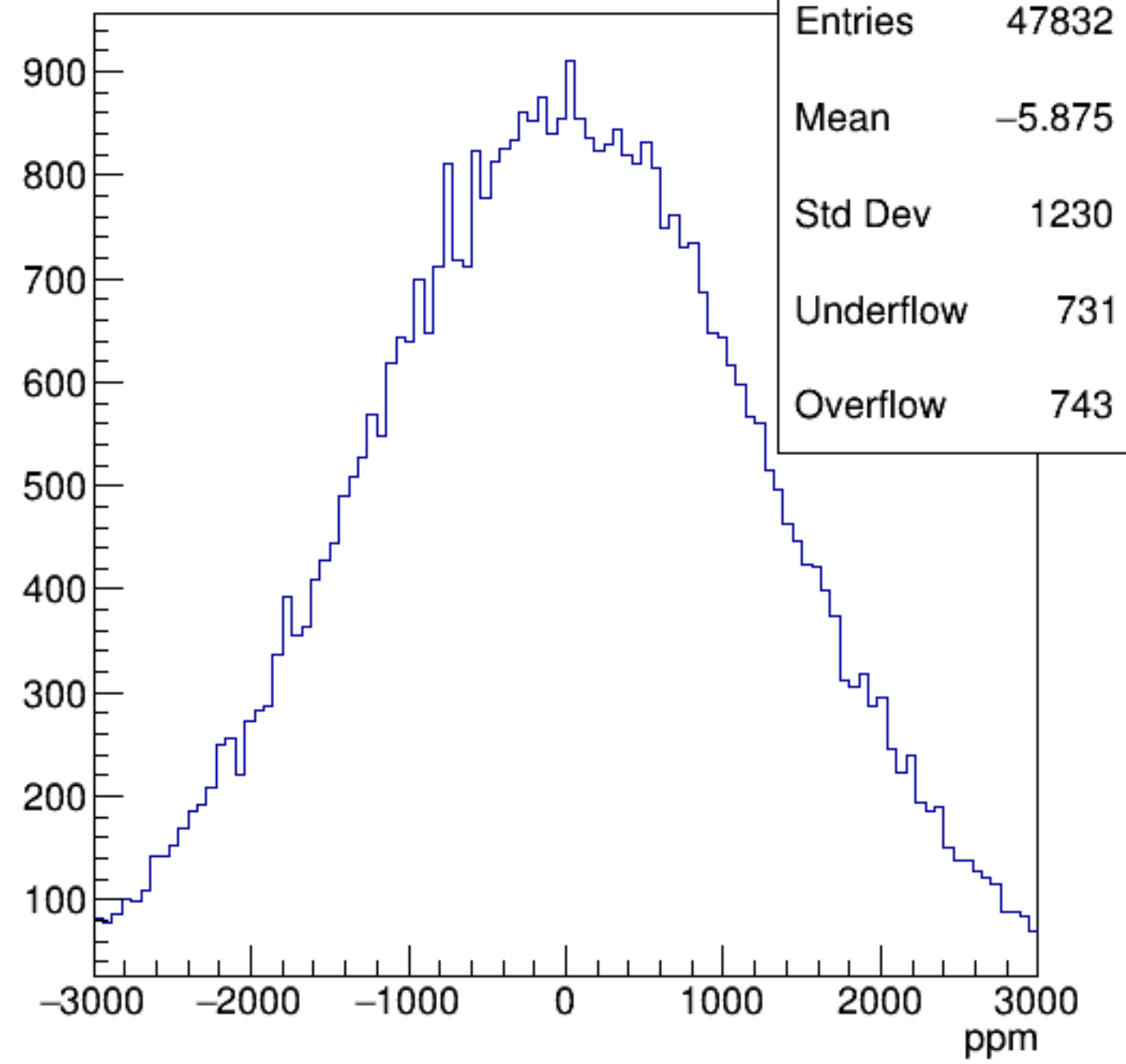
atr2*CurMon {ErrorFlag==0}



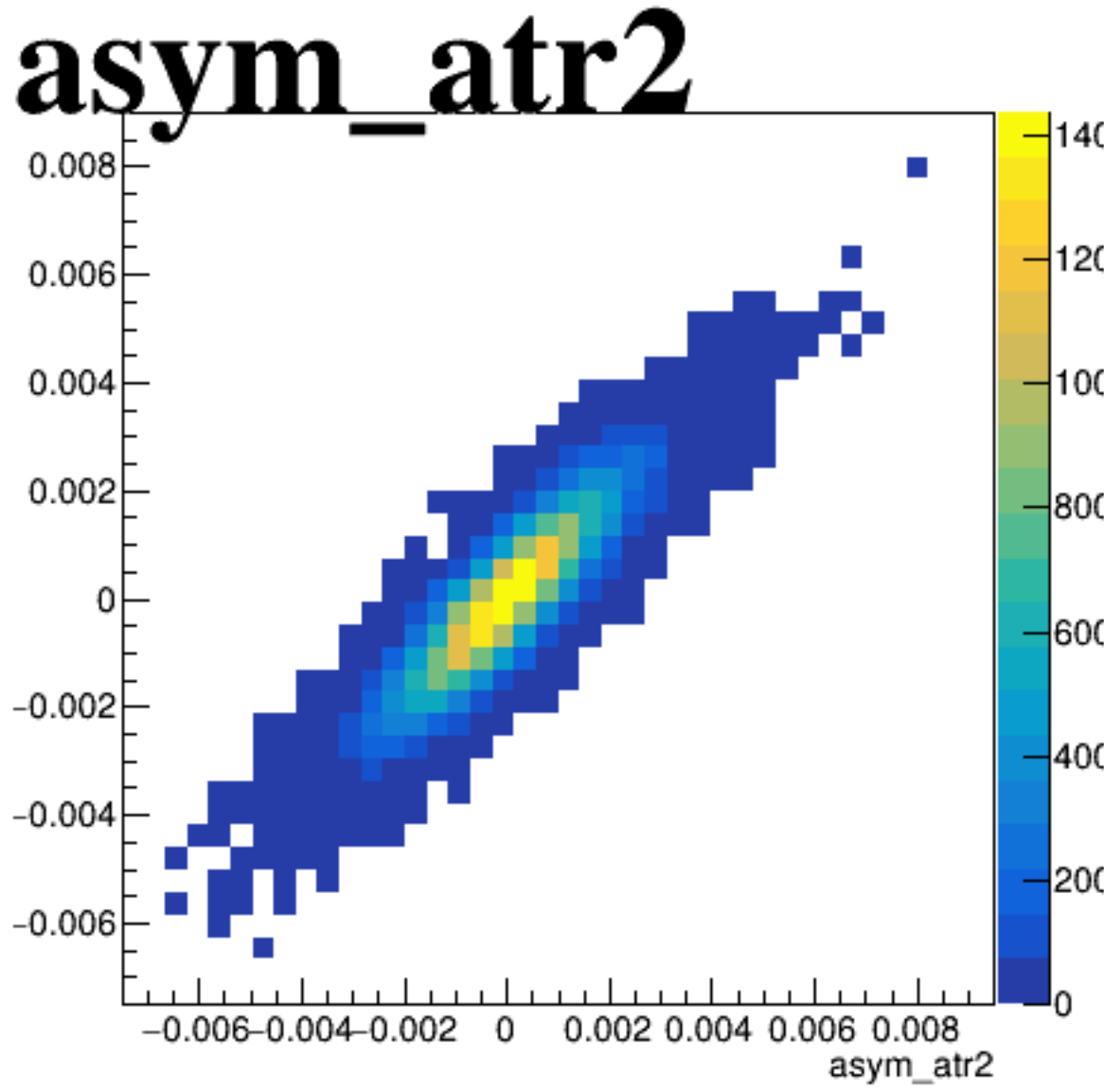
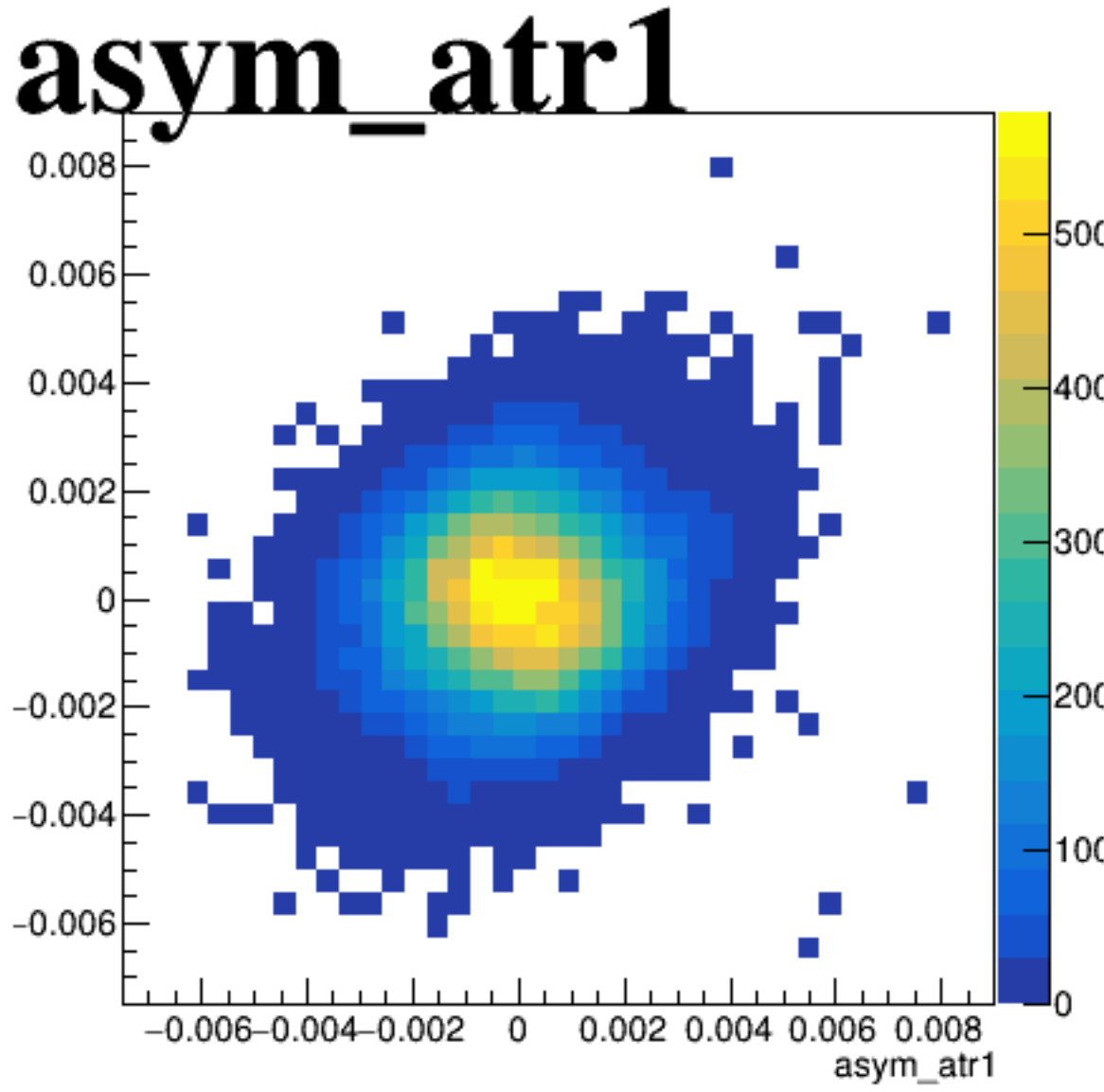
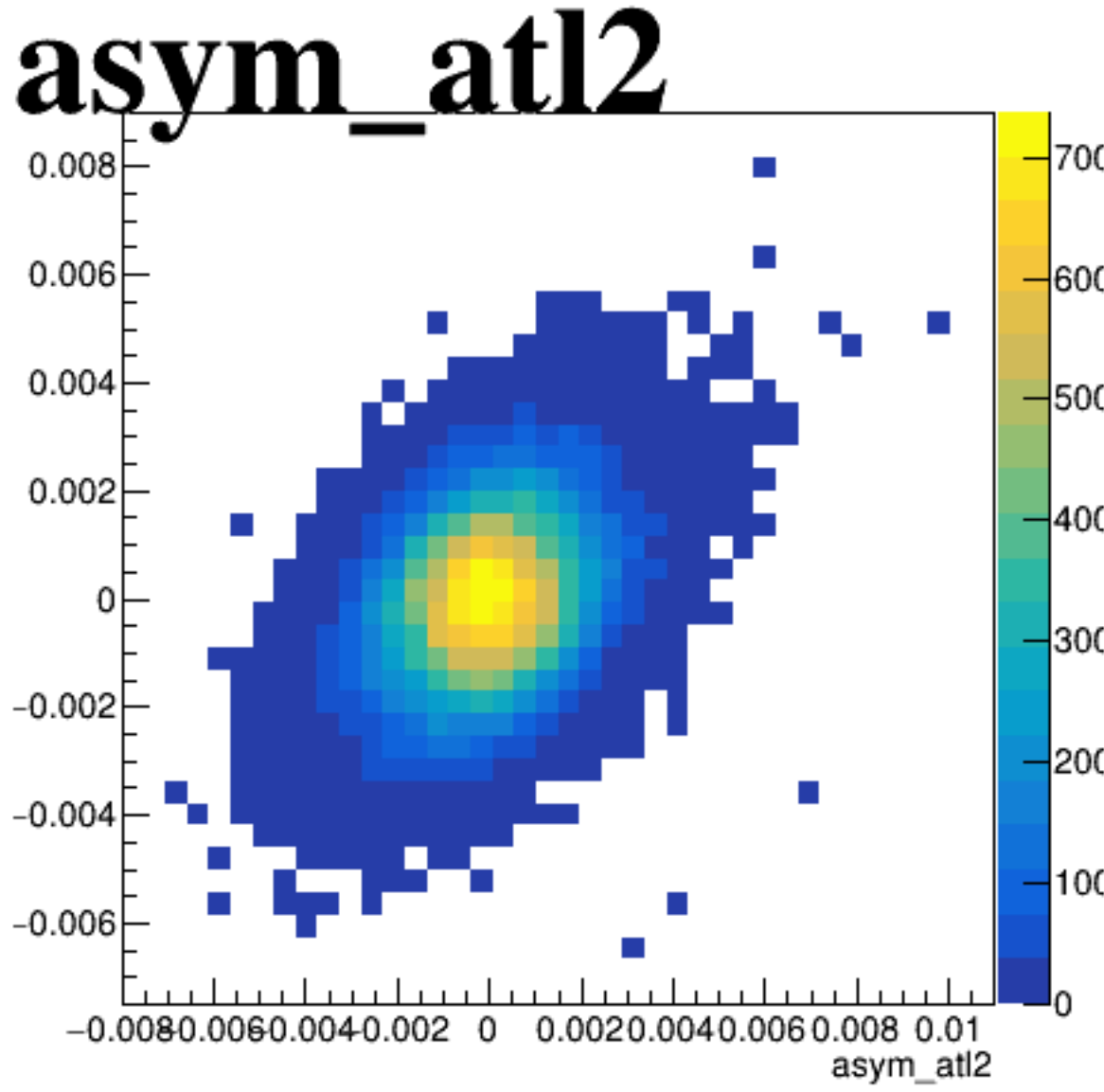
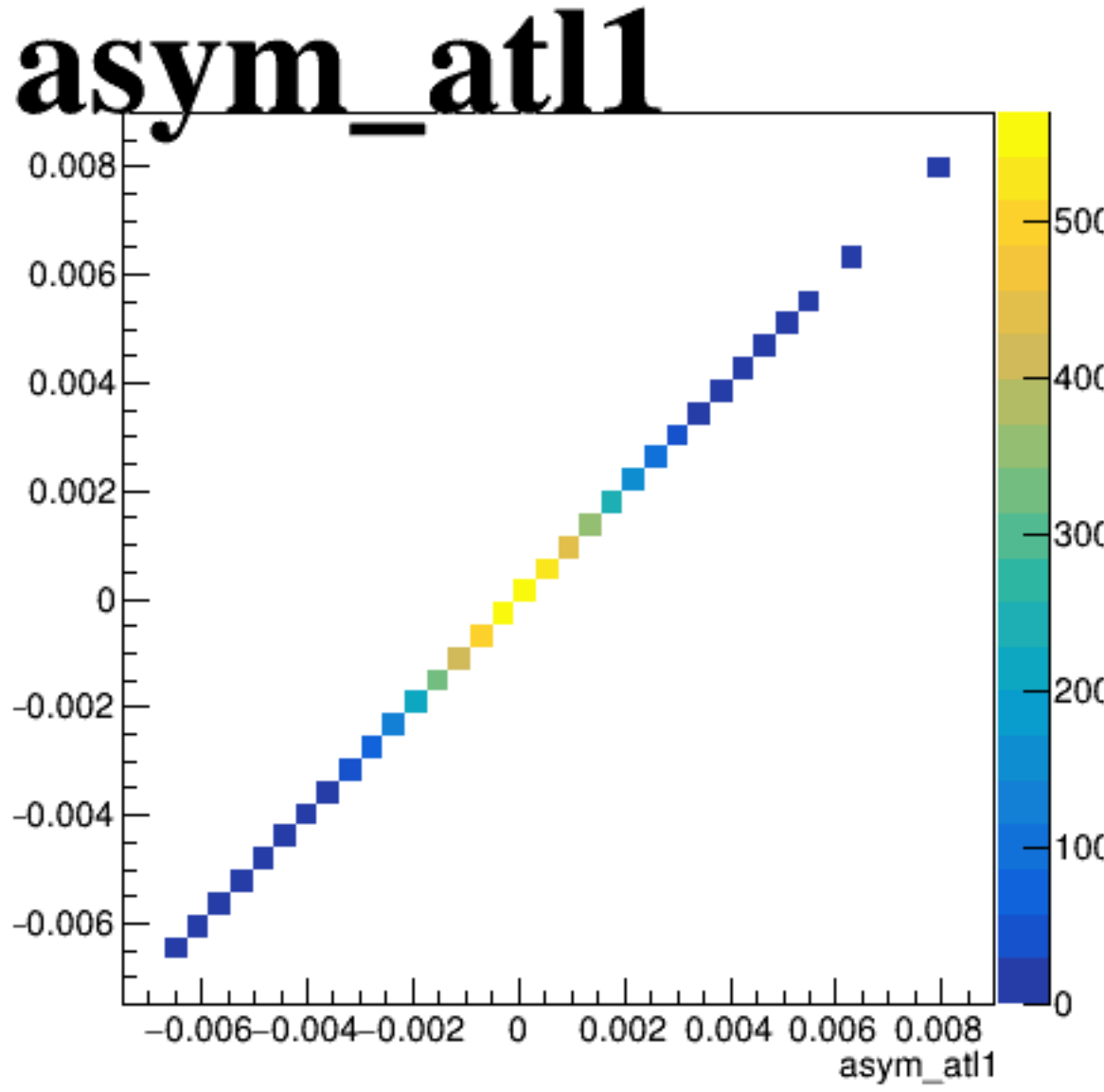
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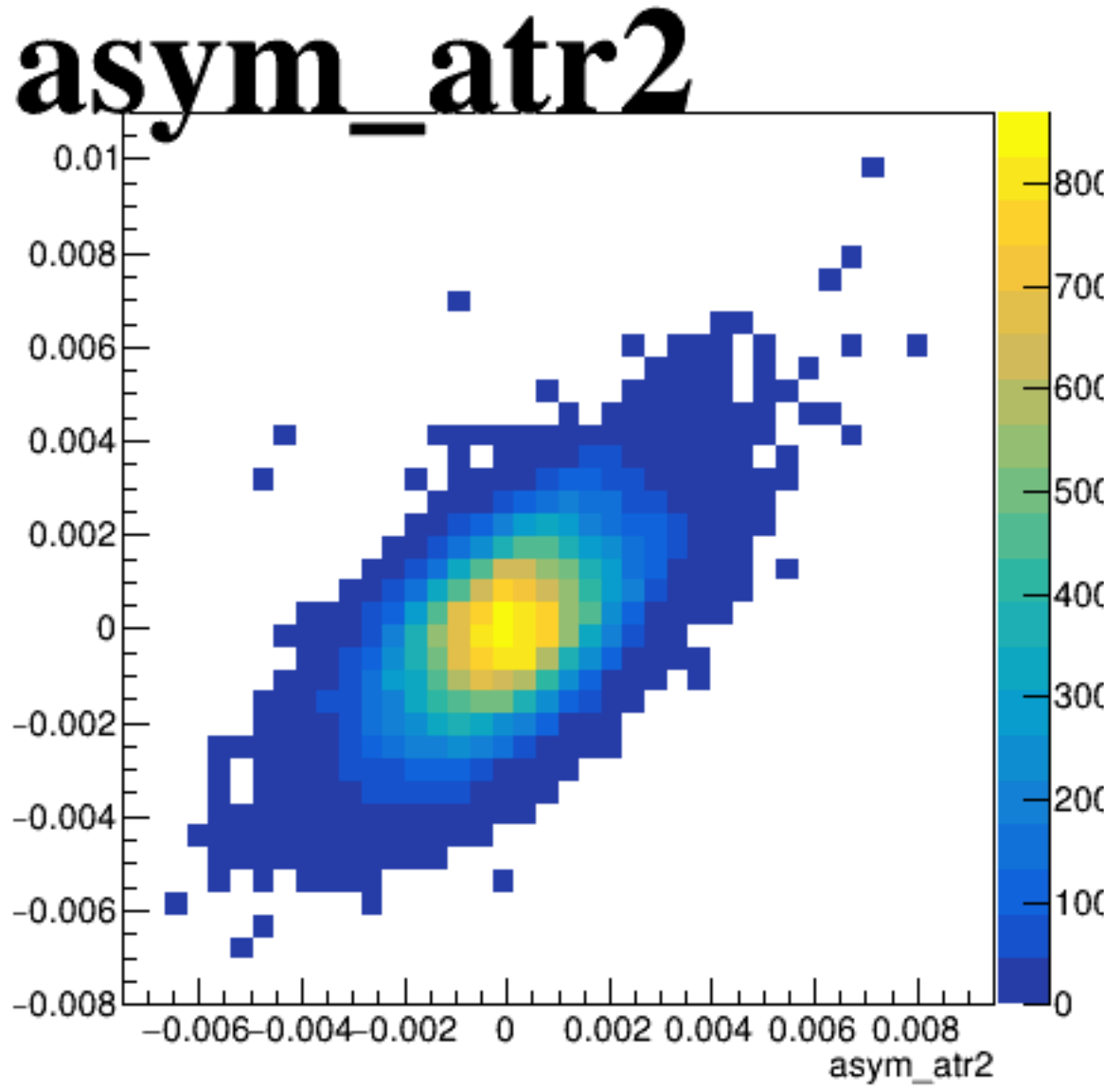
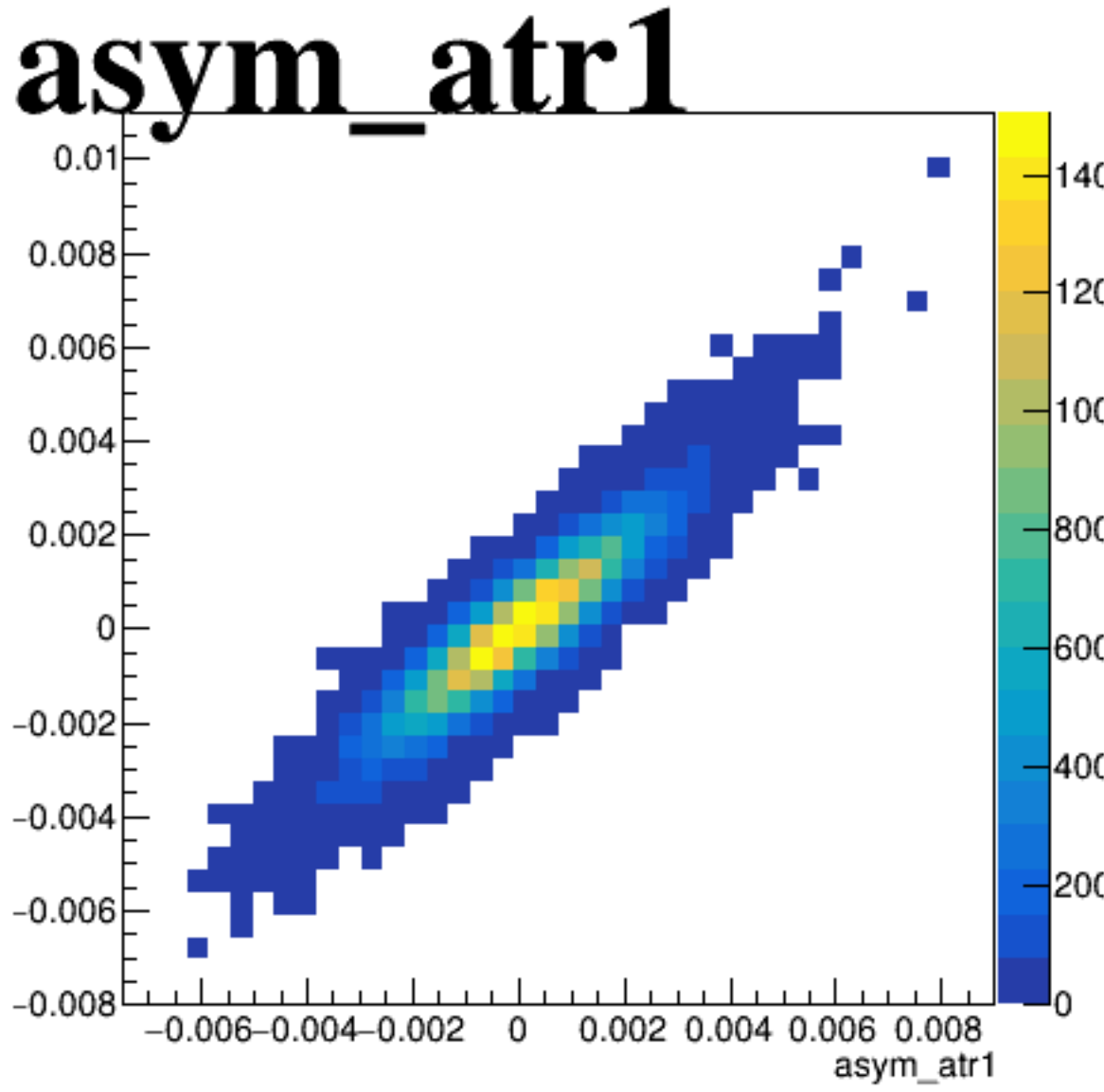
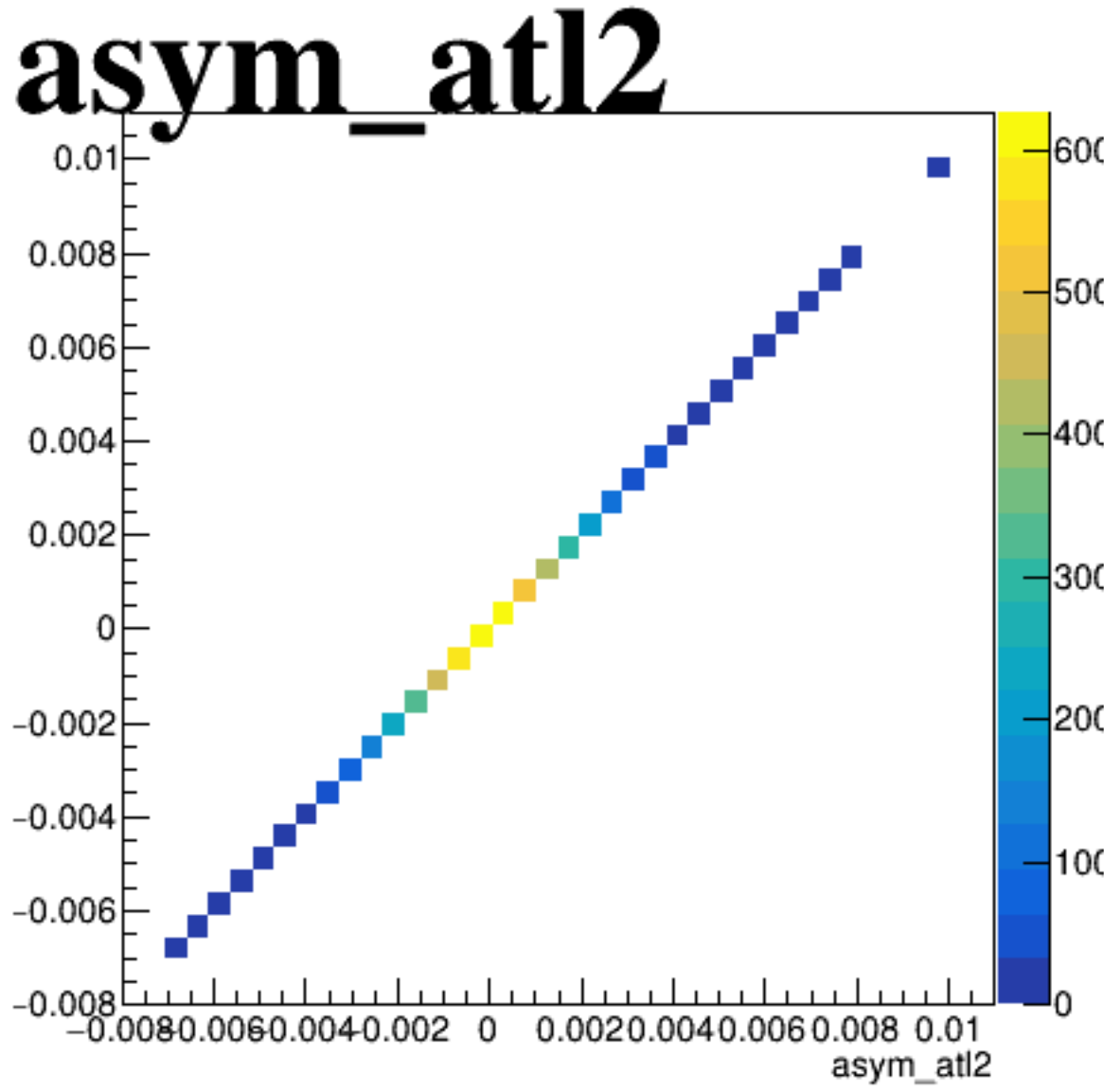
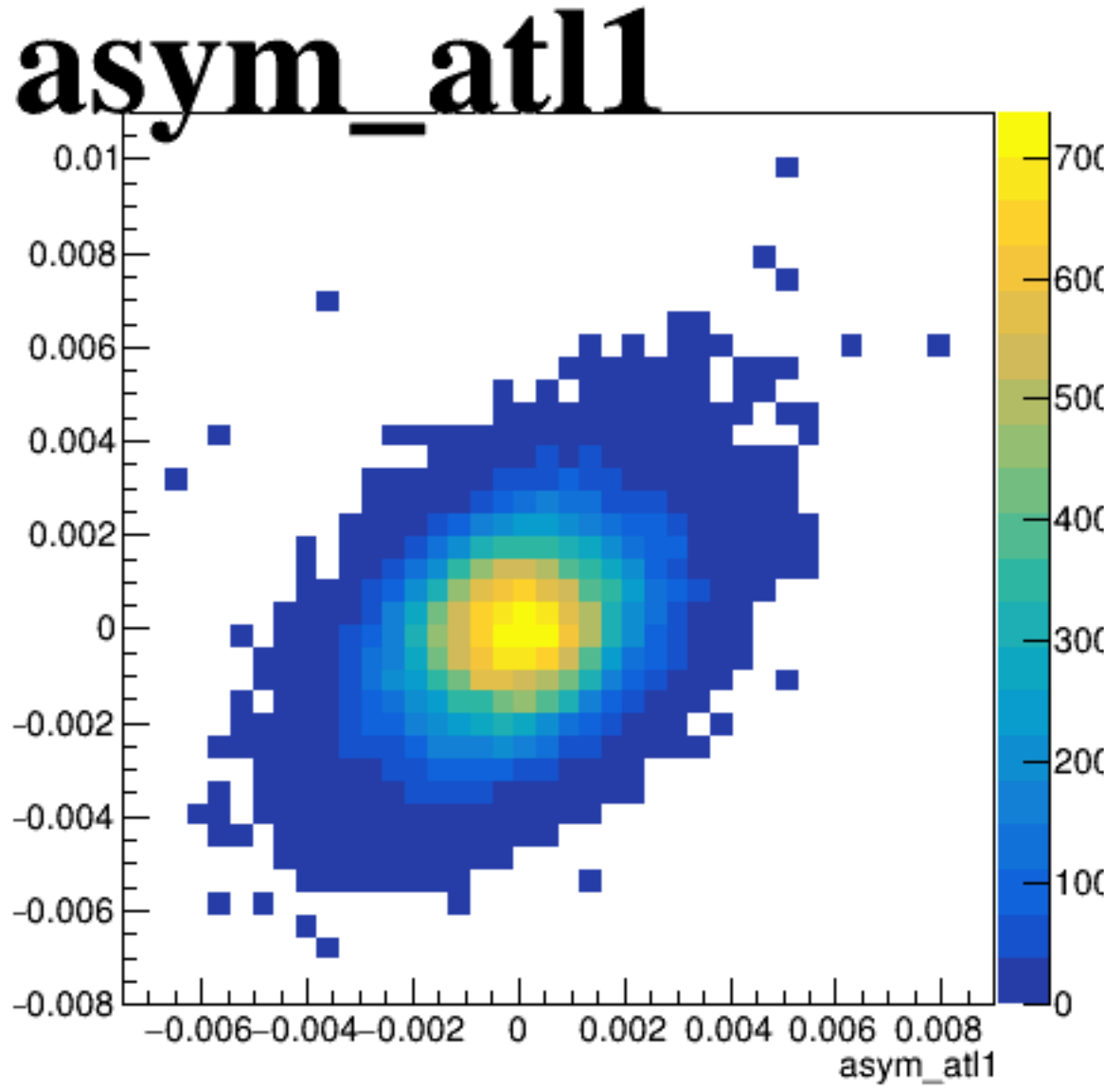
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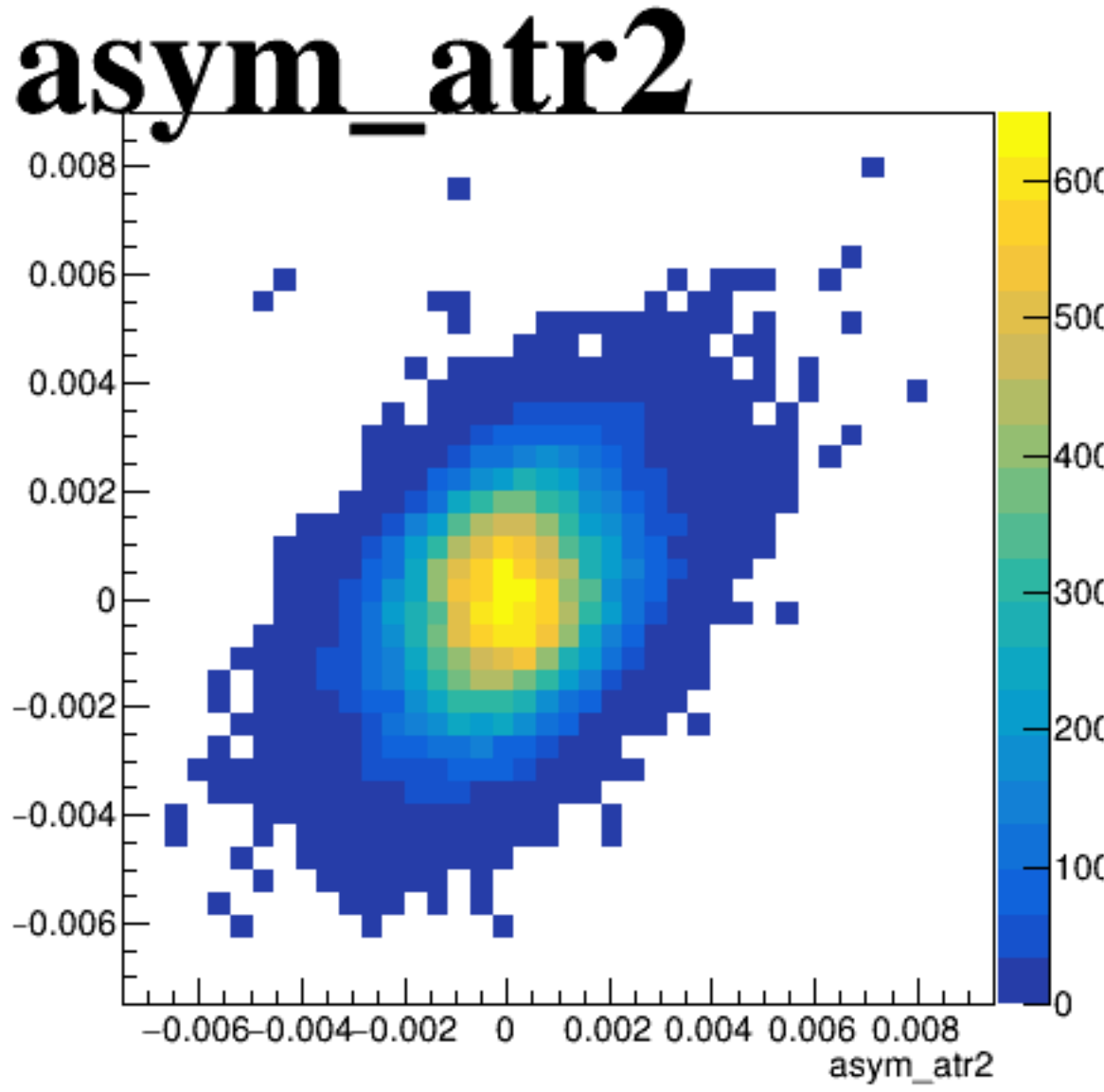
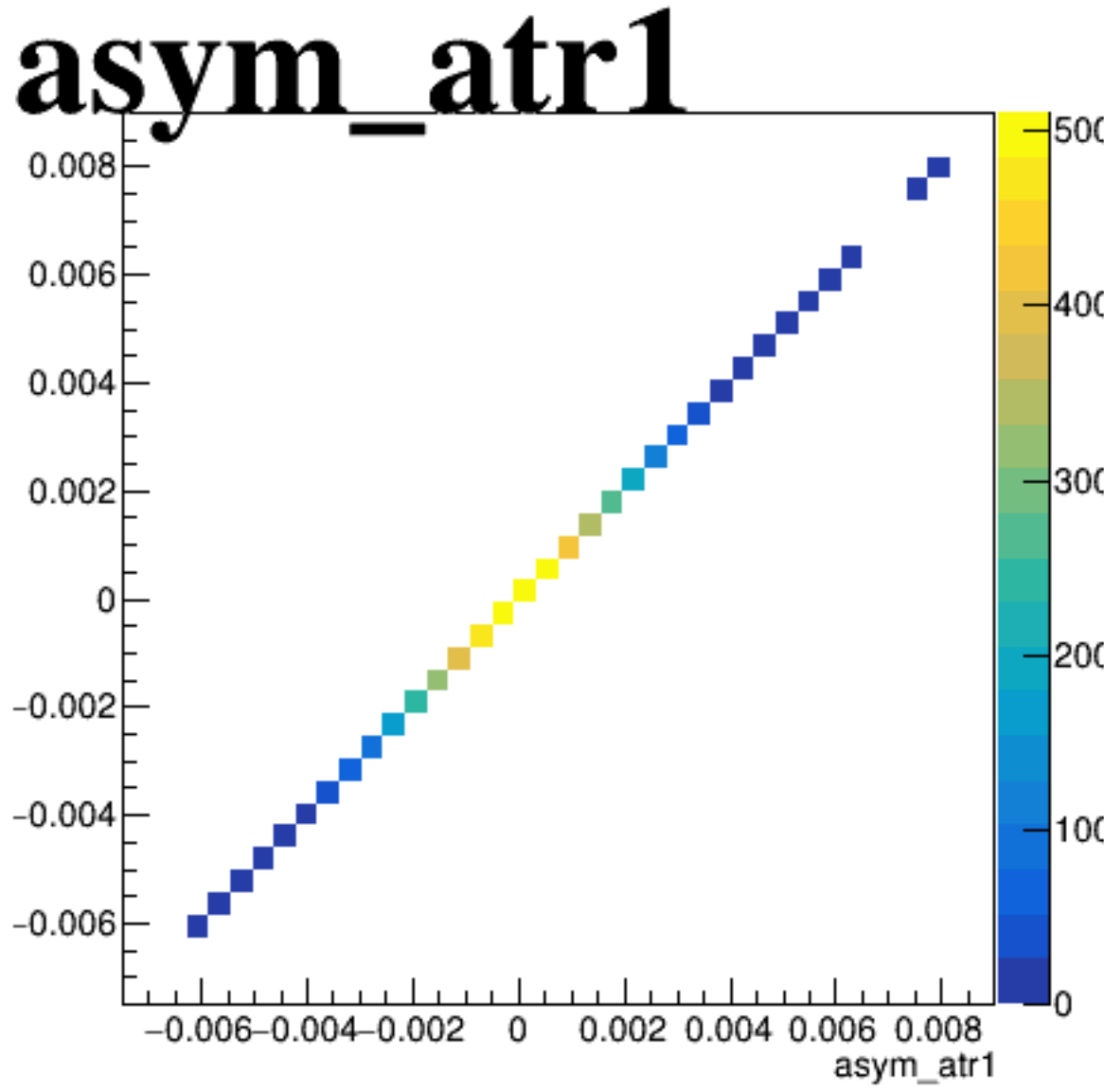
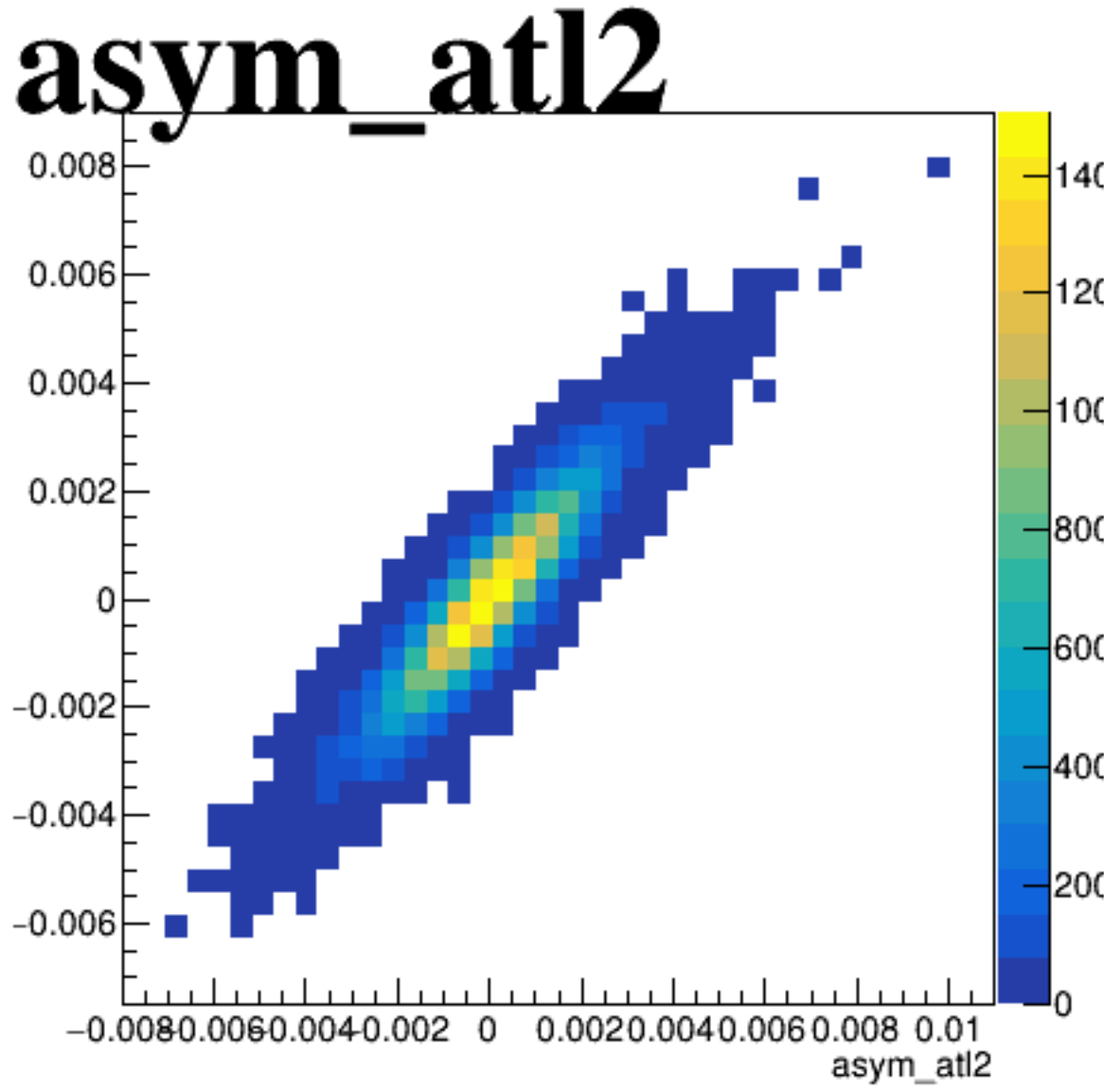
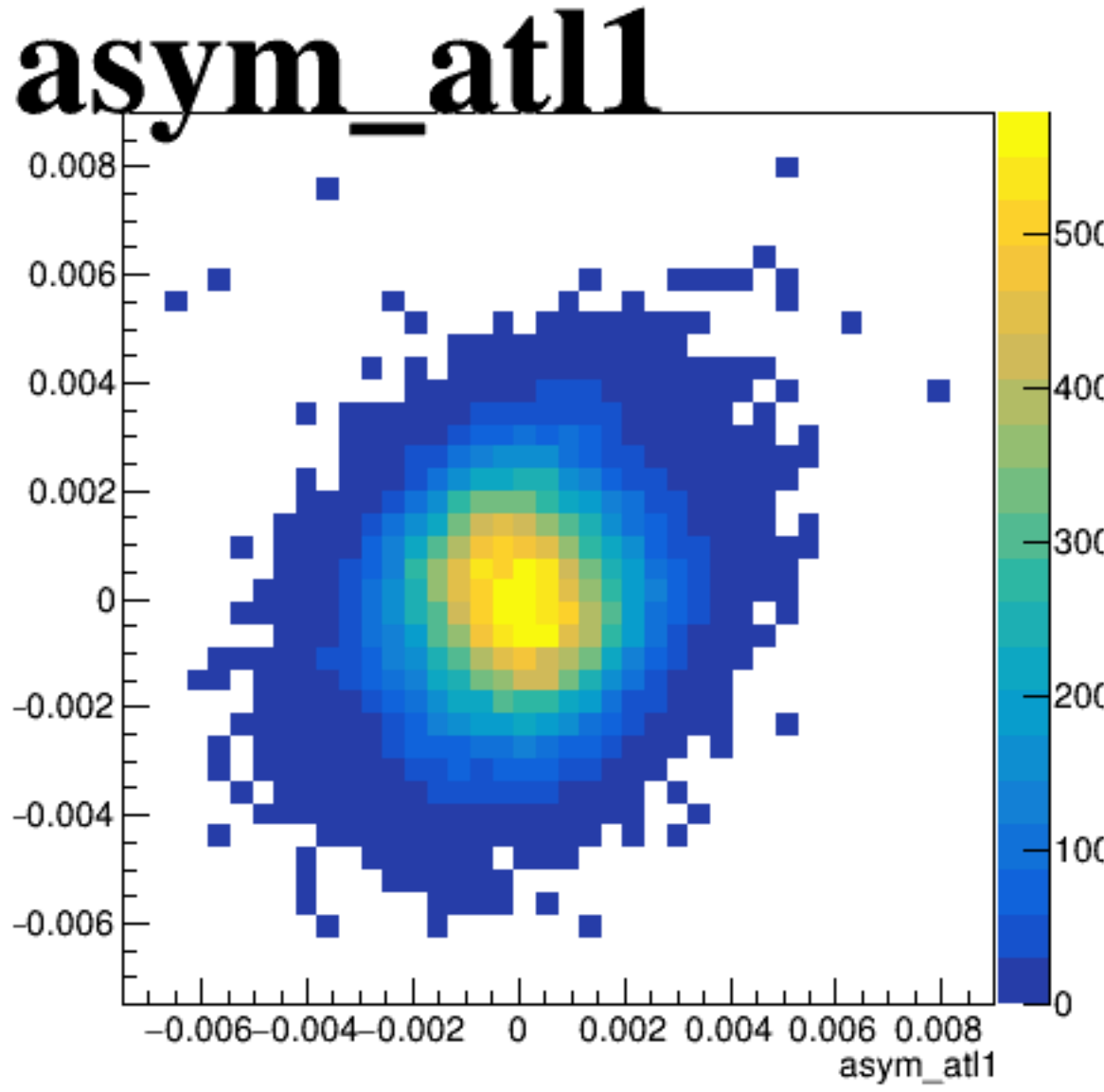
asym_atl1



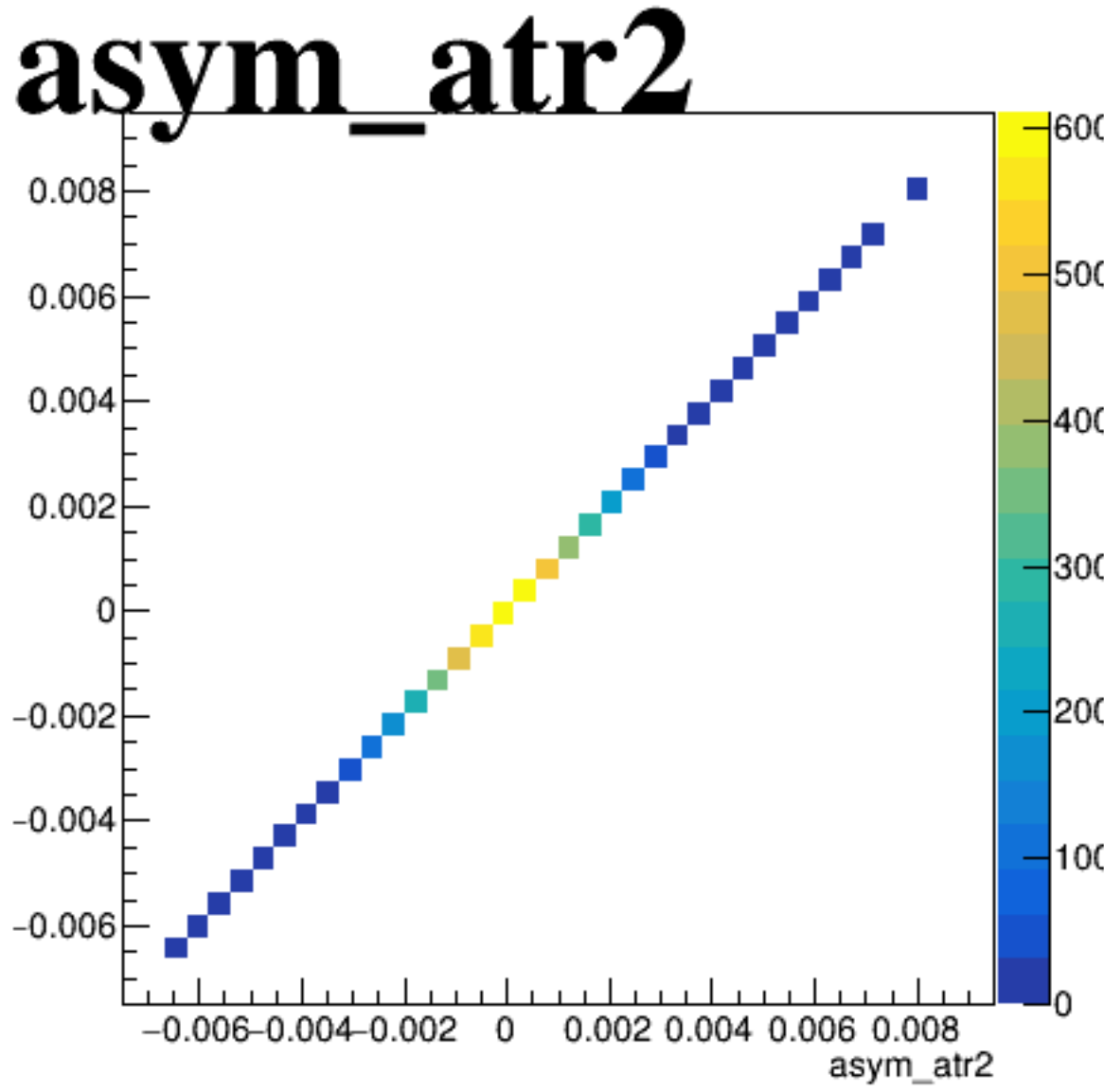
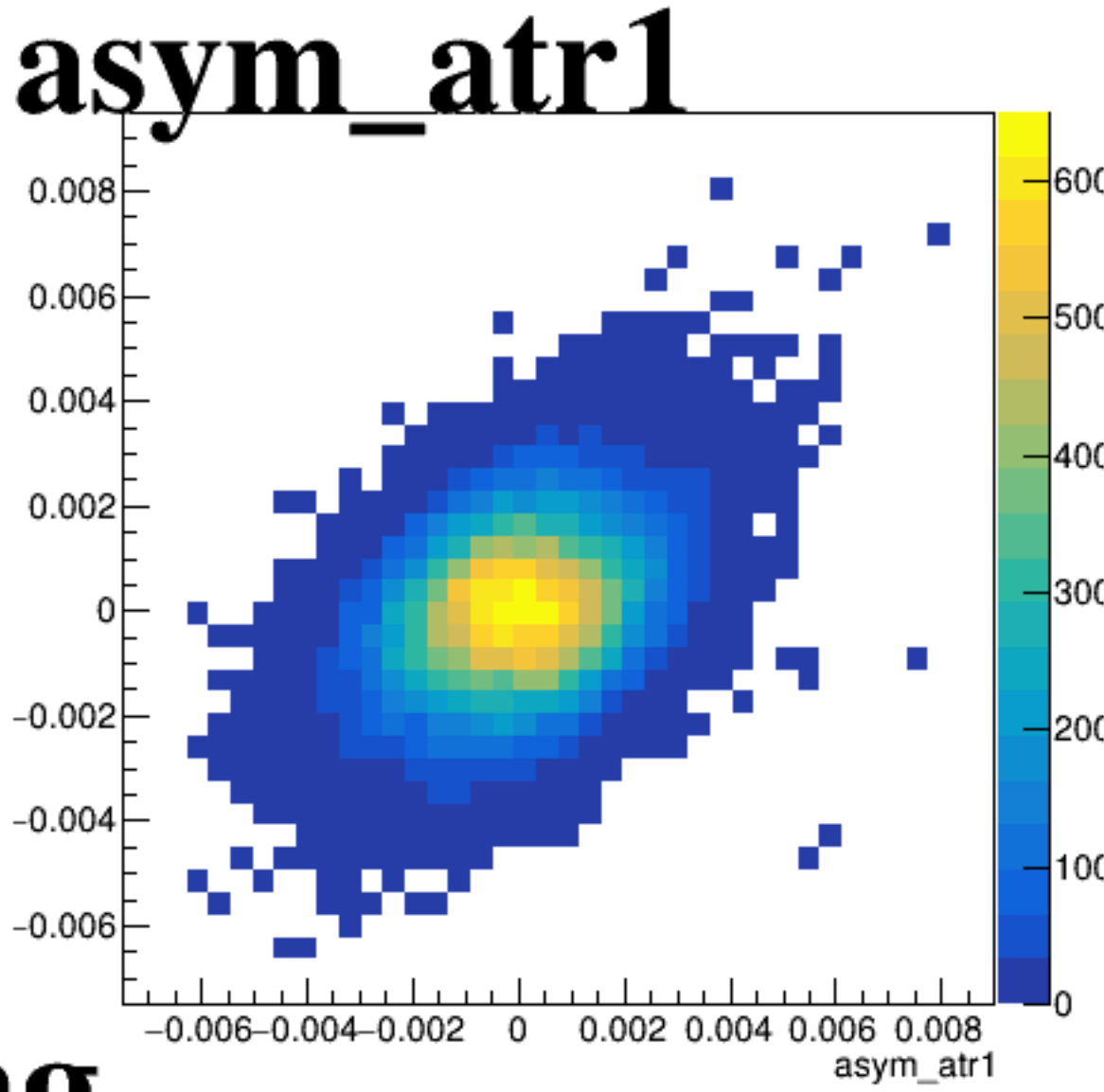
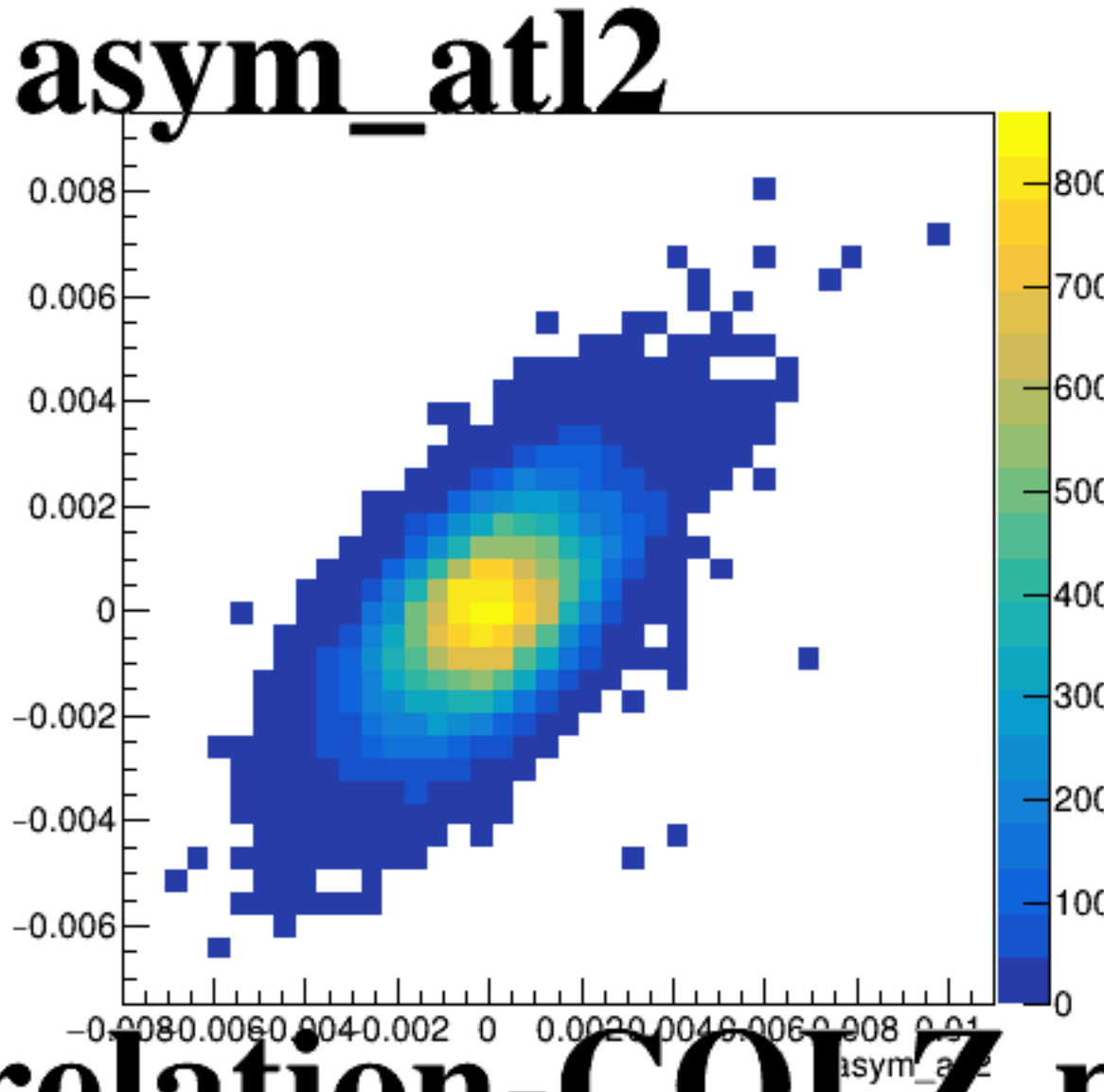
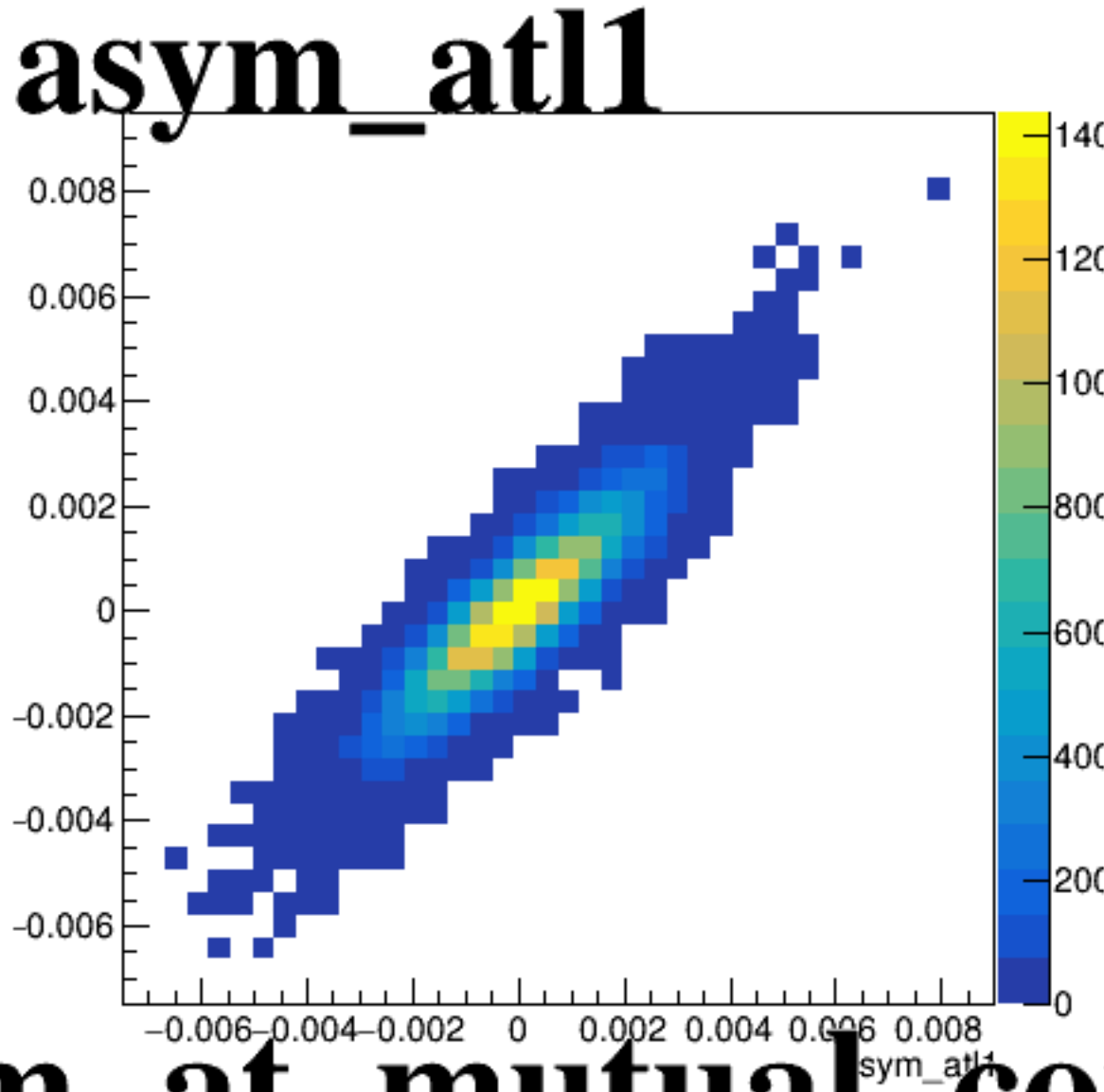
asym_atl2



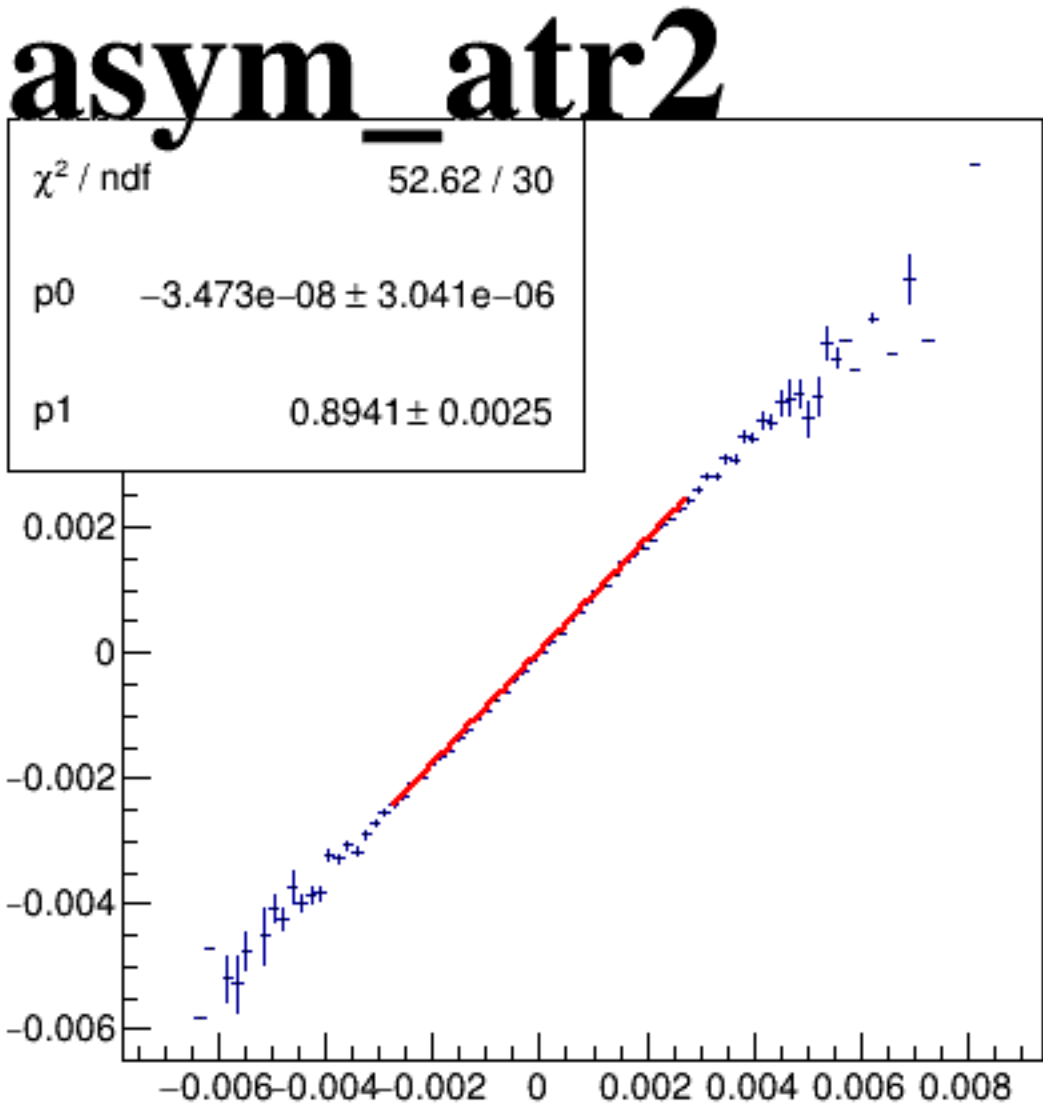
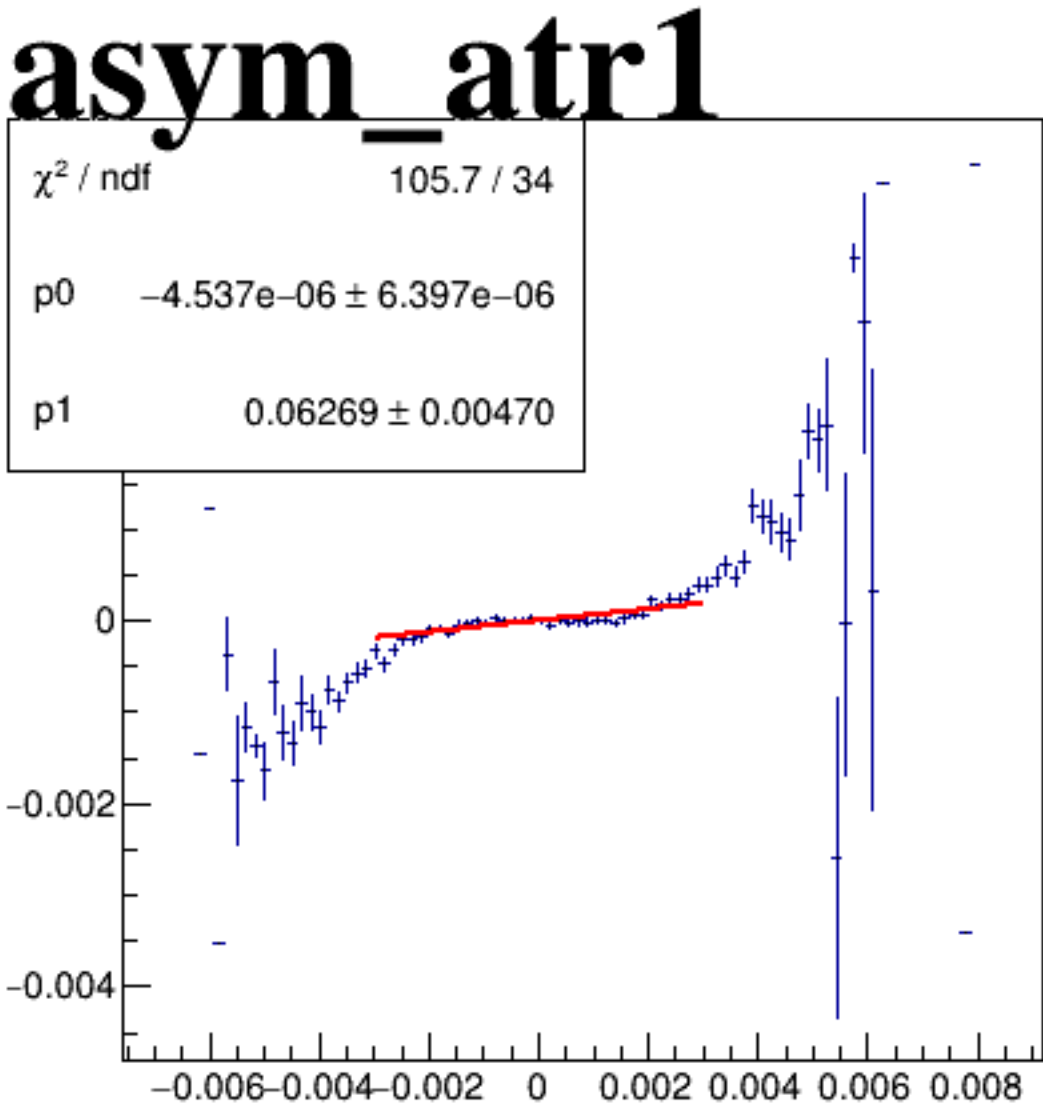
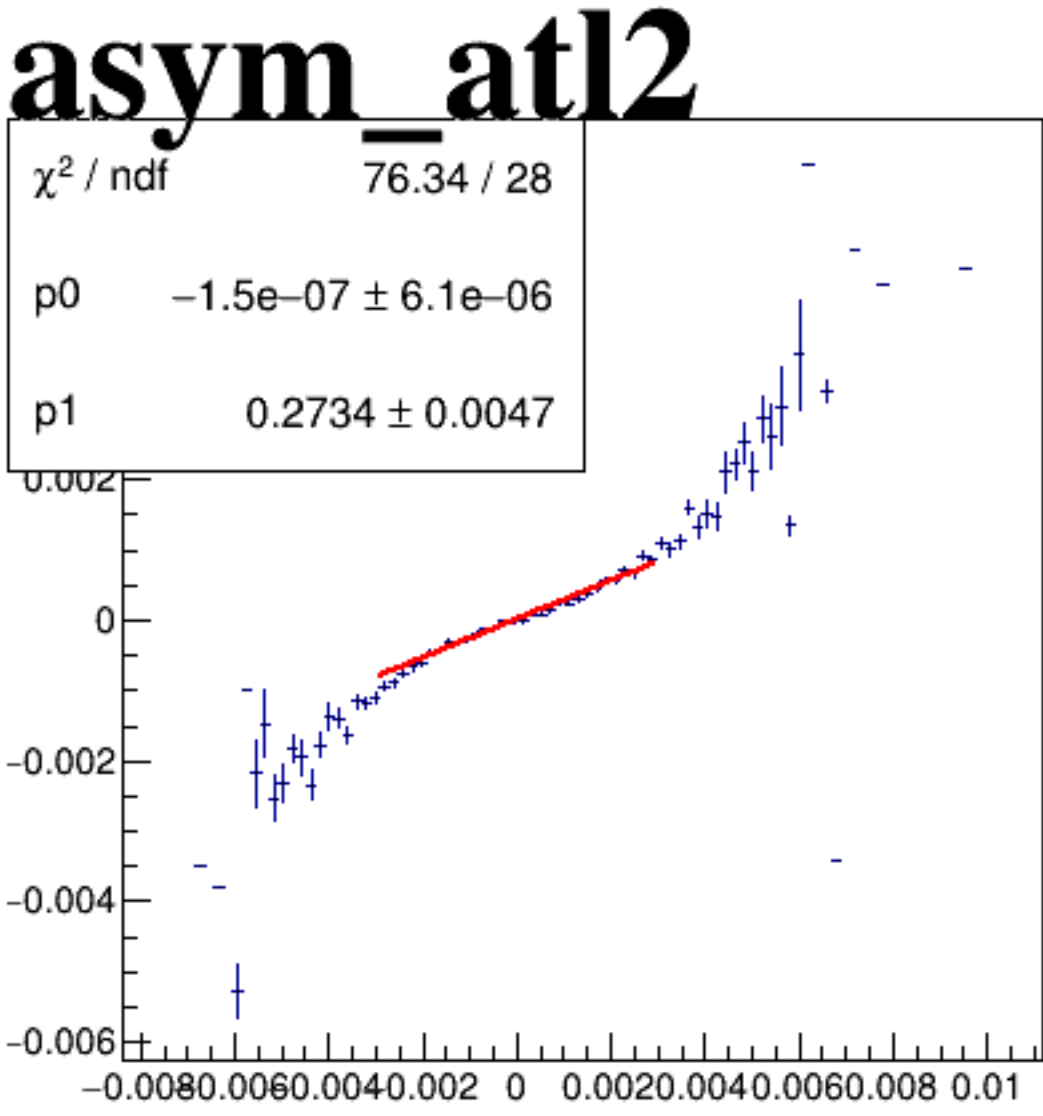
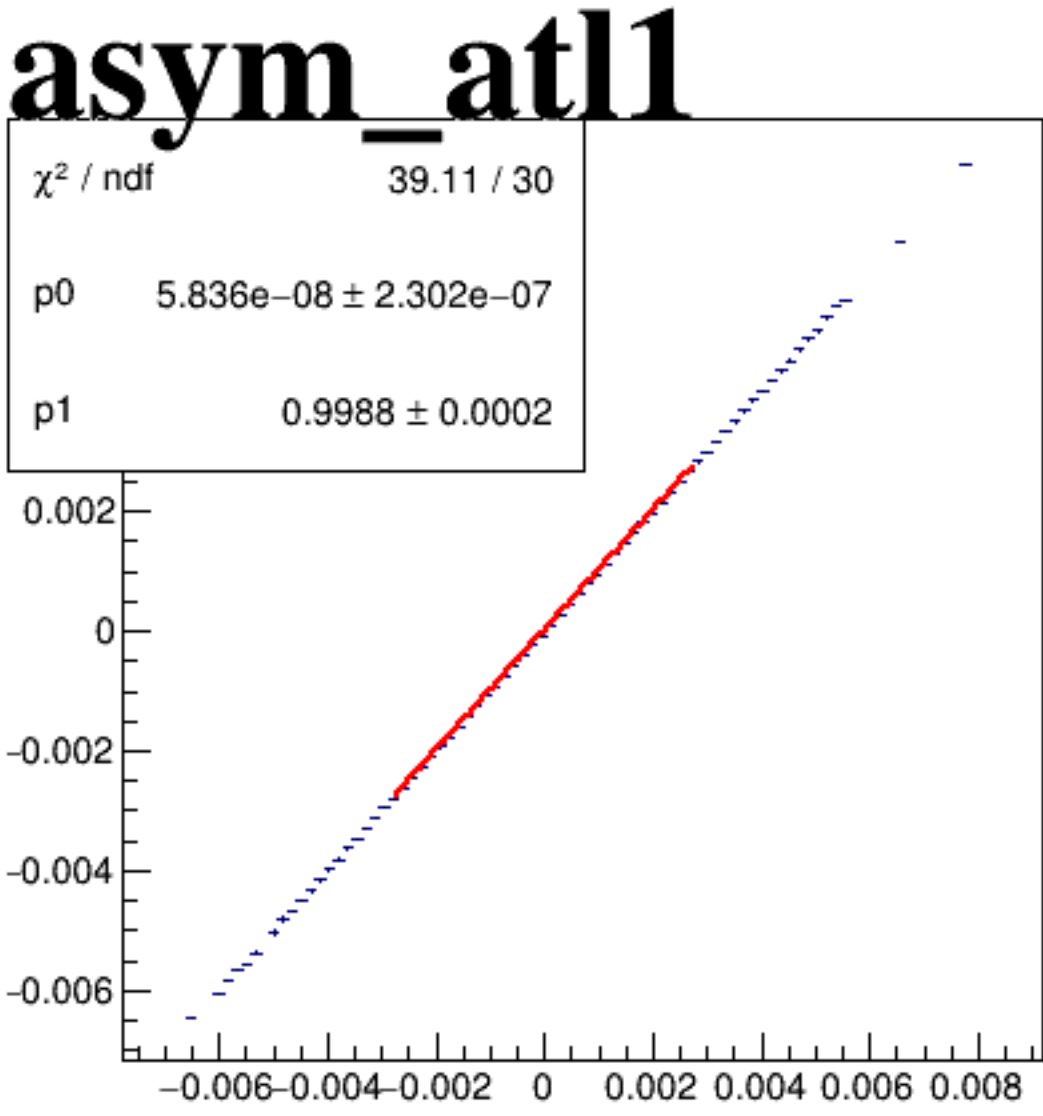
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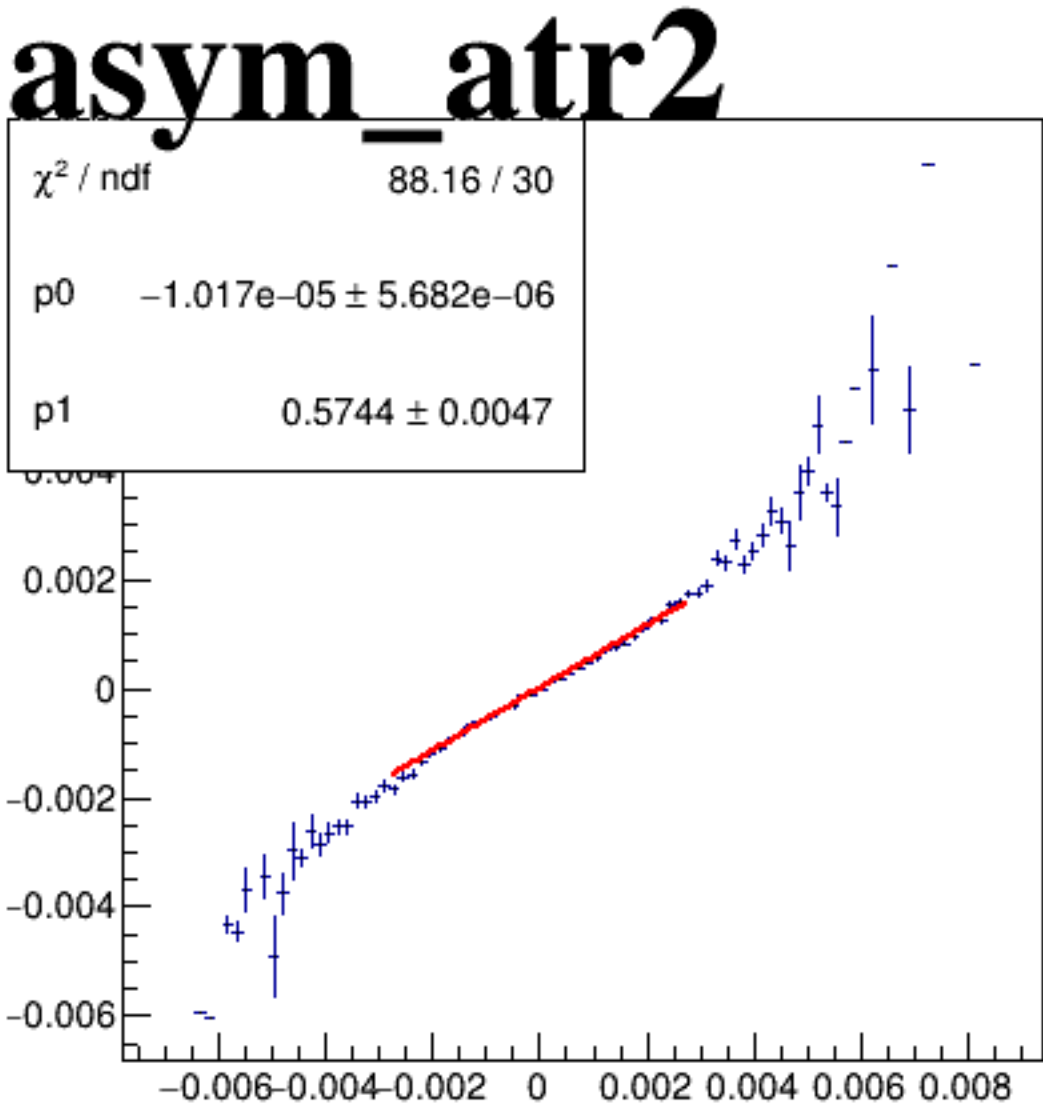
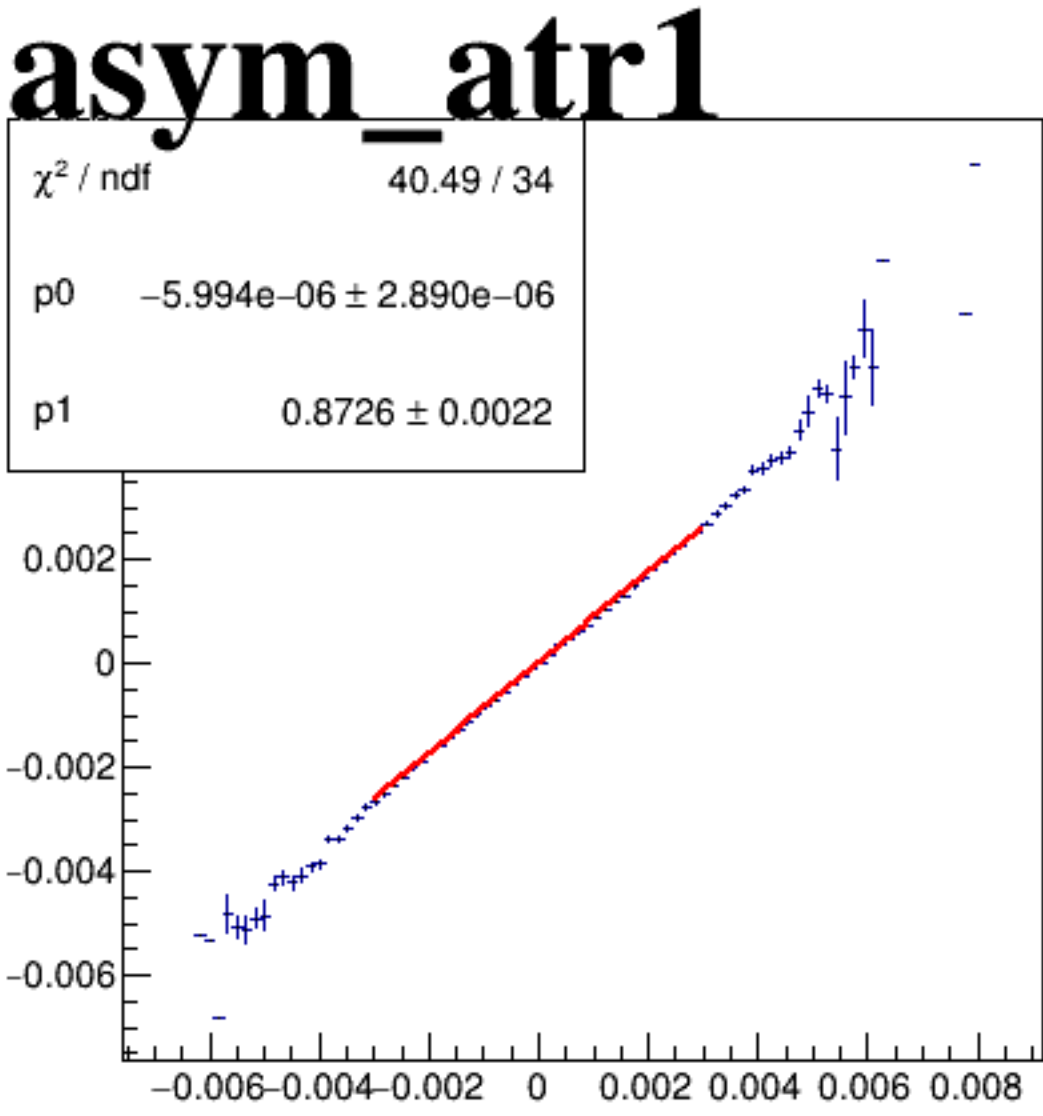
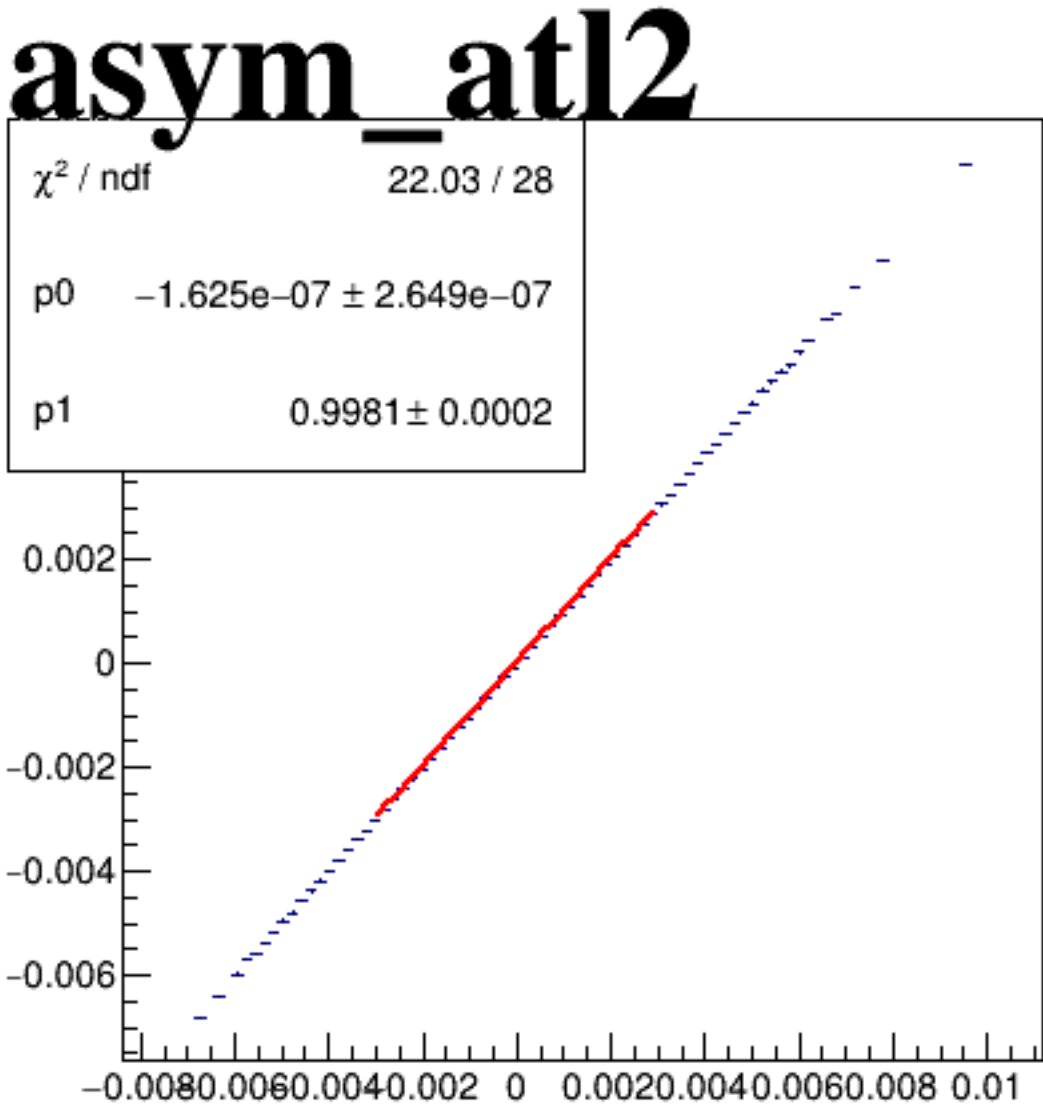
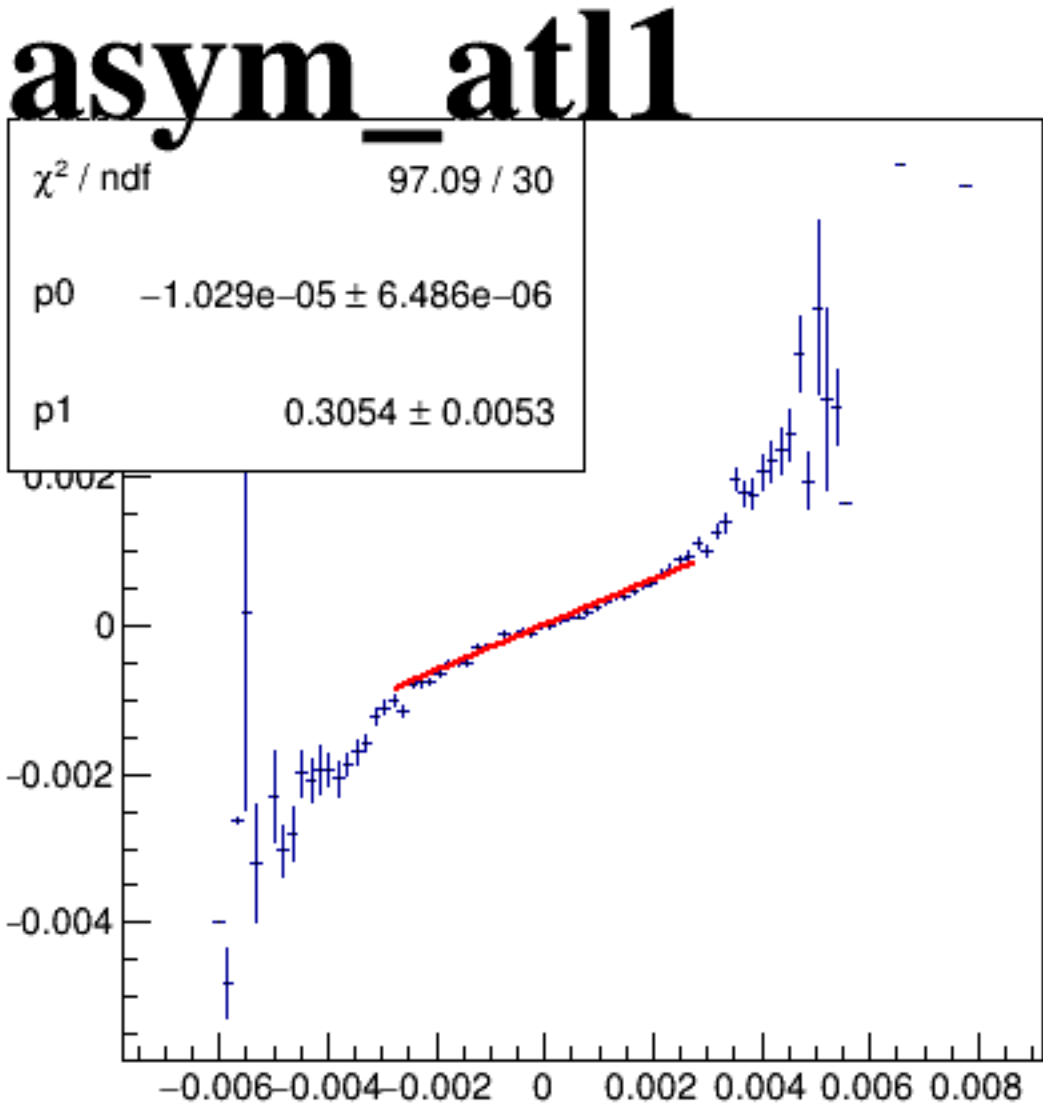
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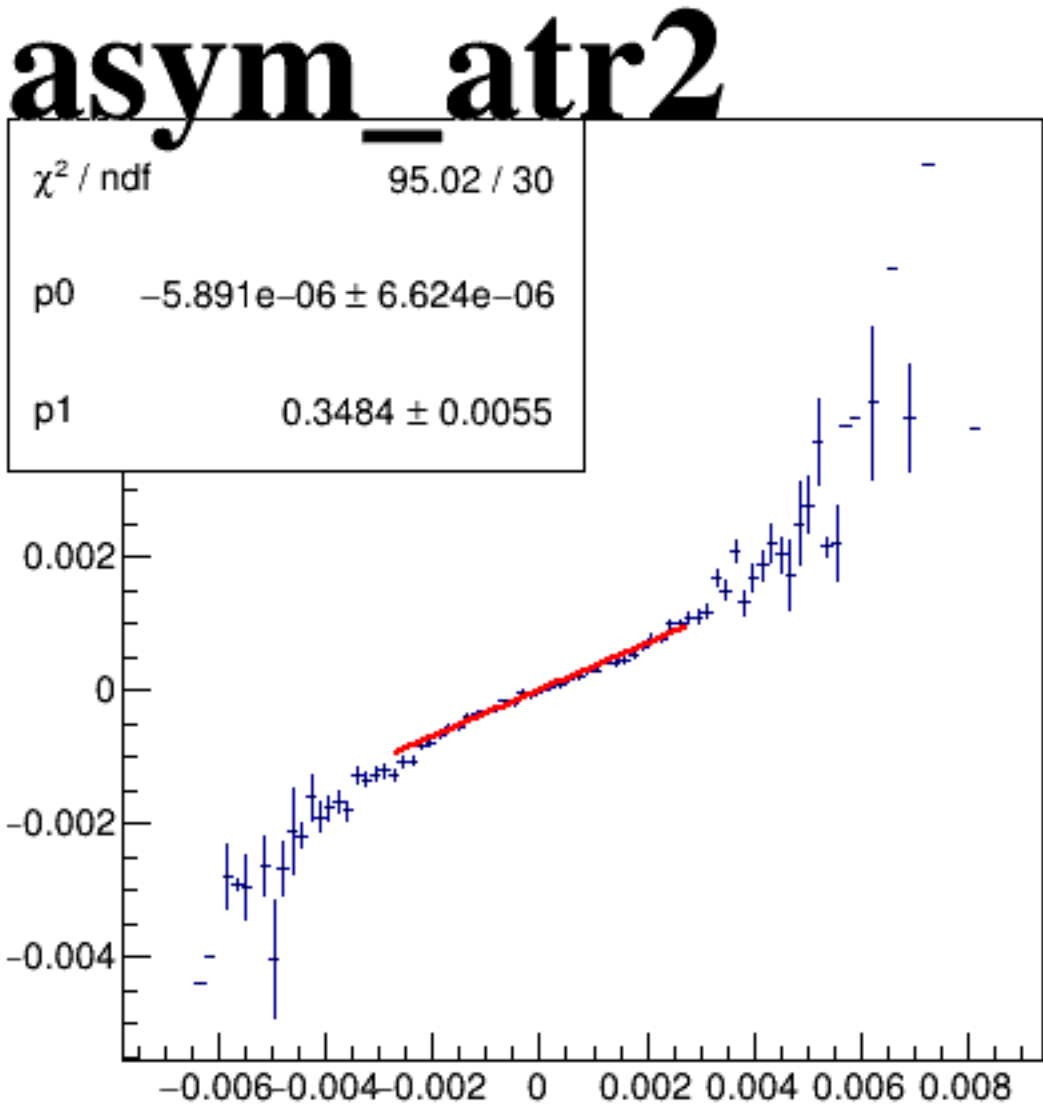
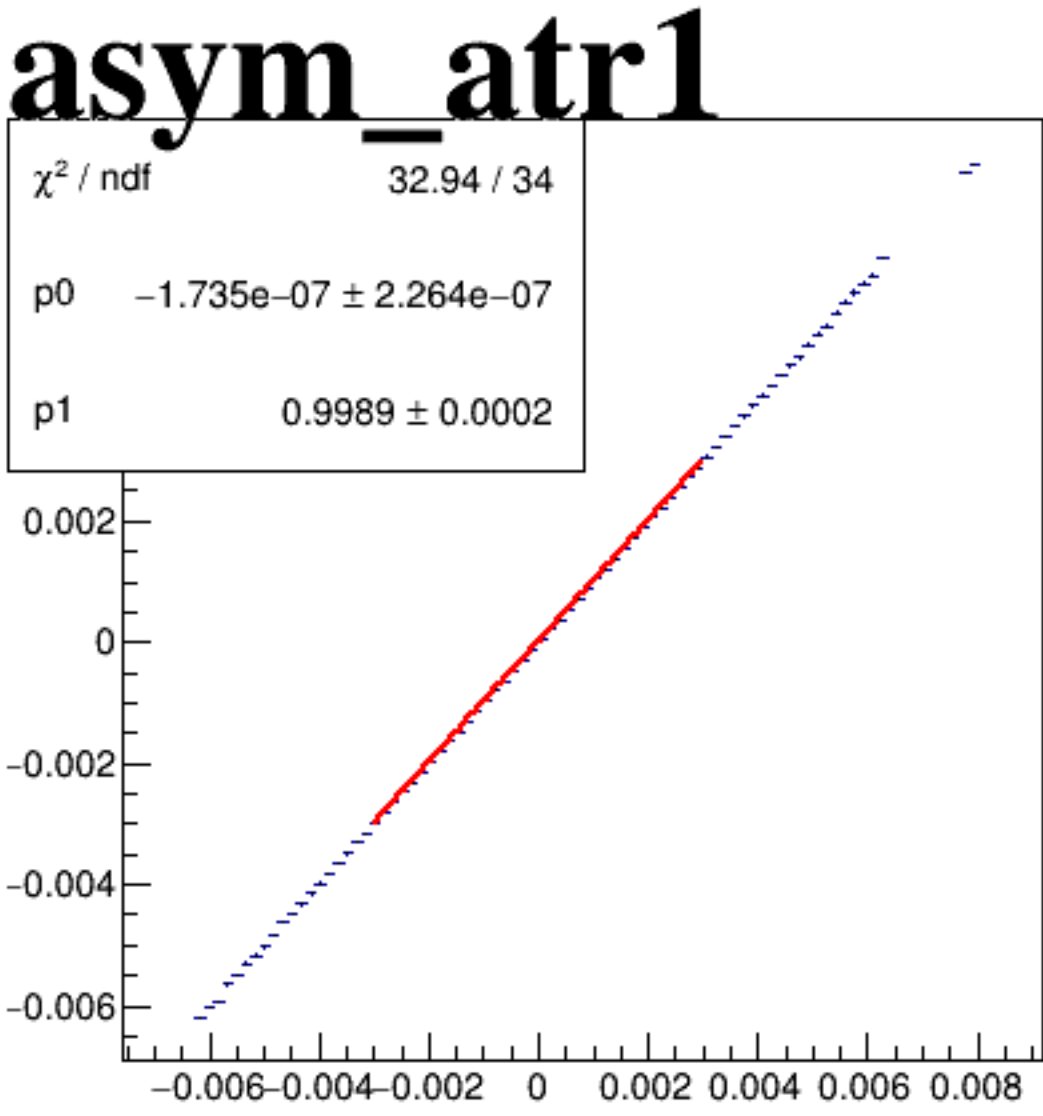
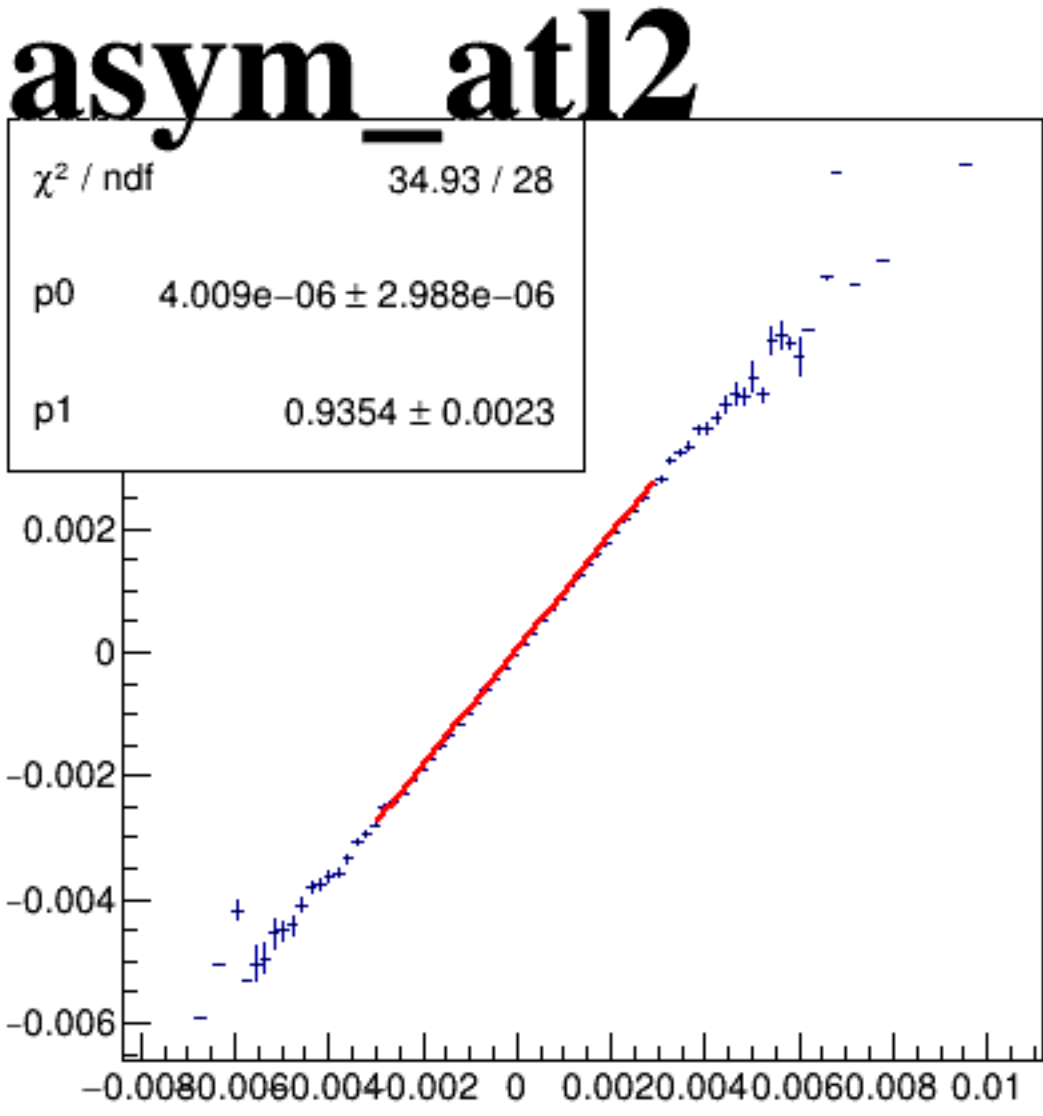
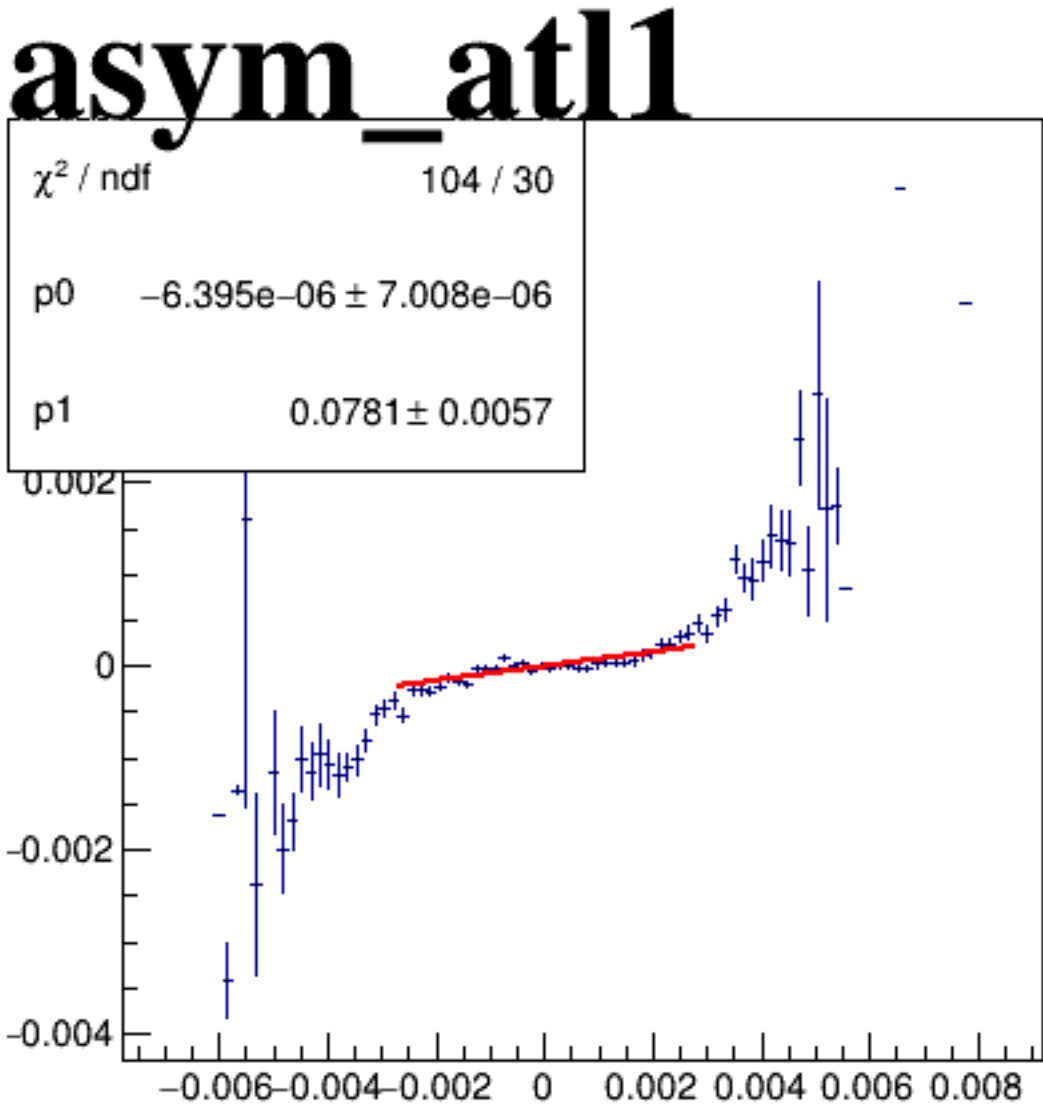
asym_atl1



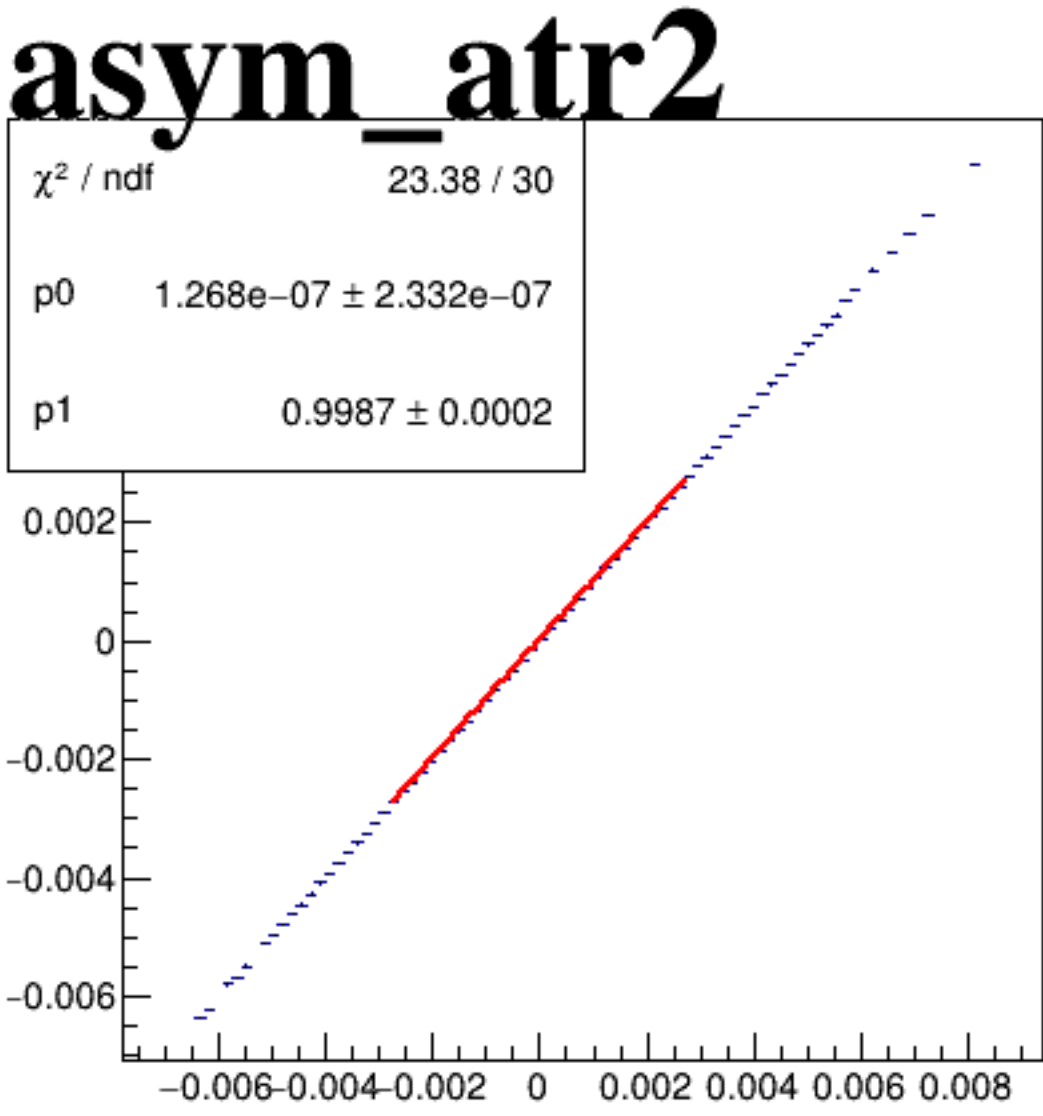
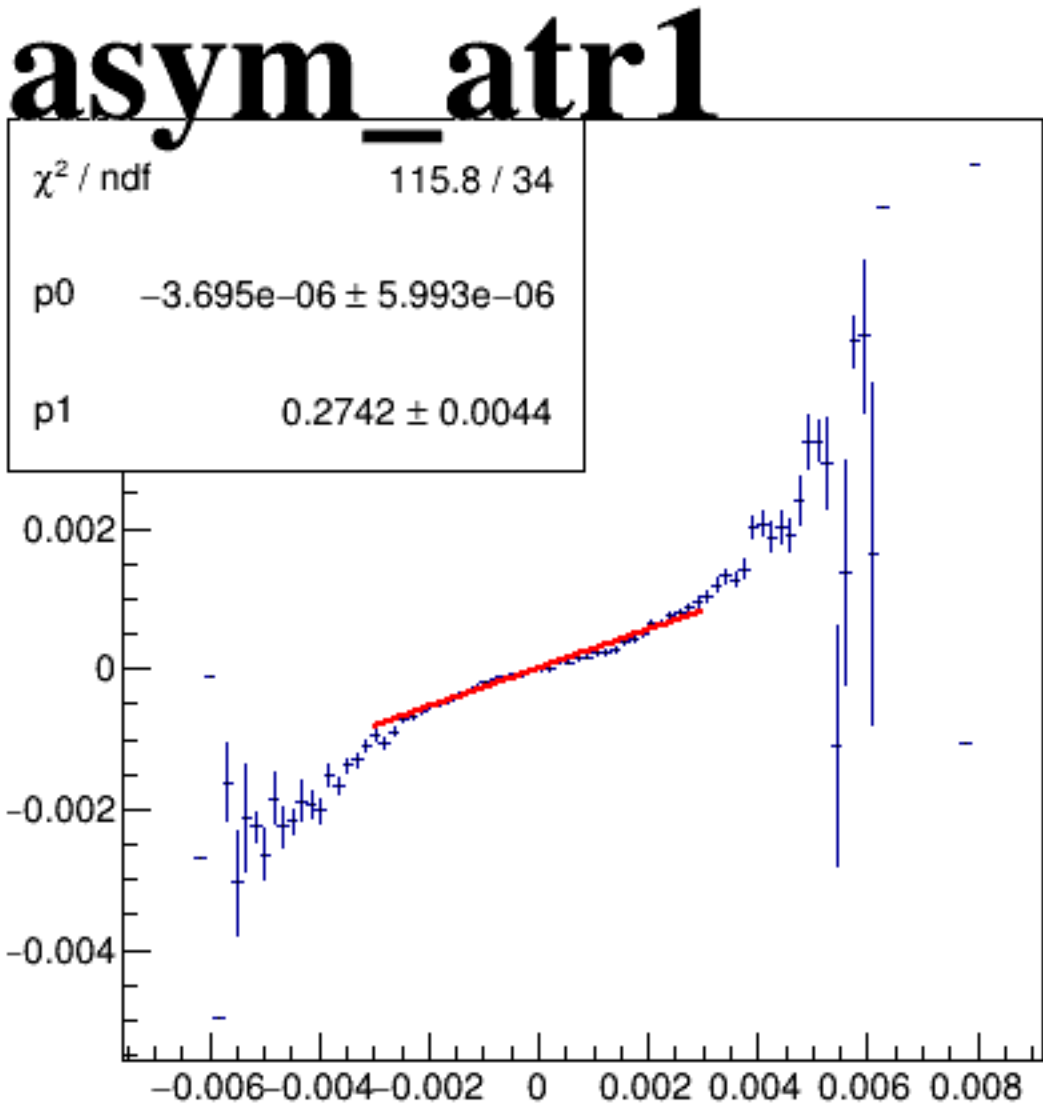
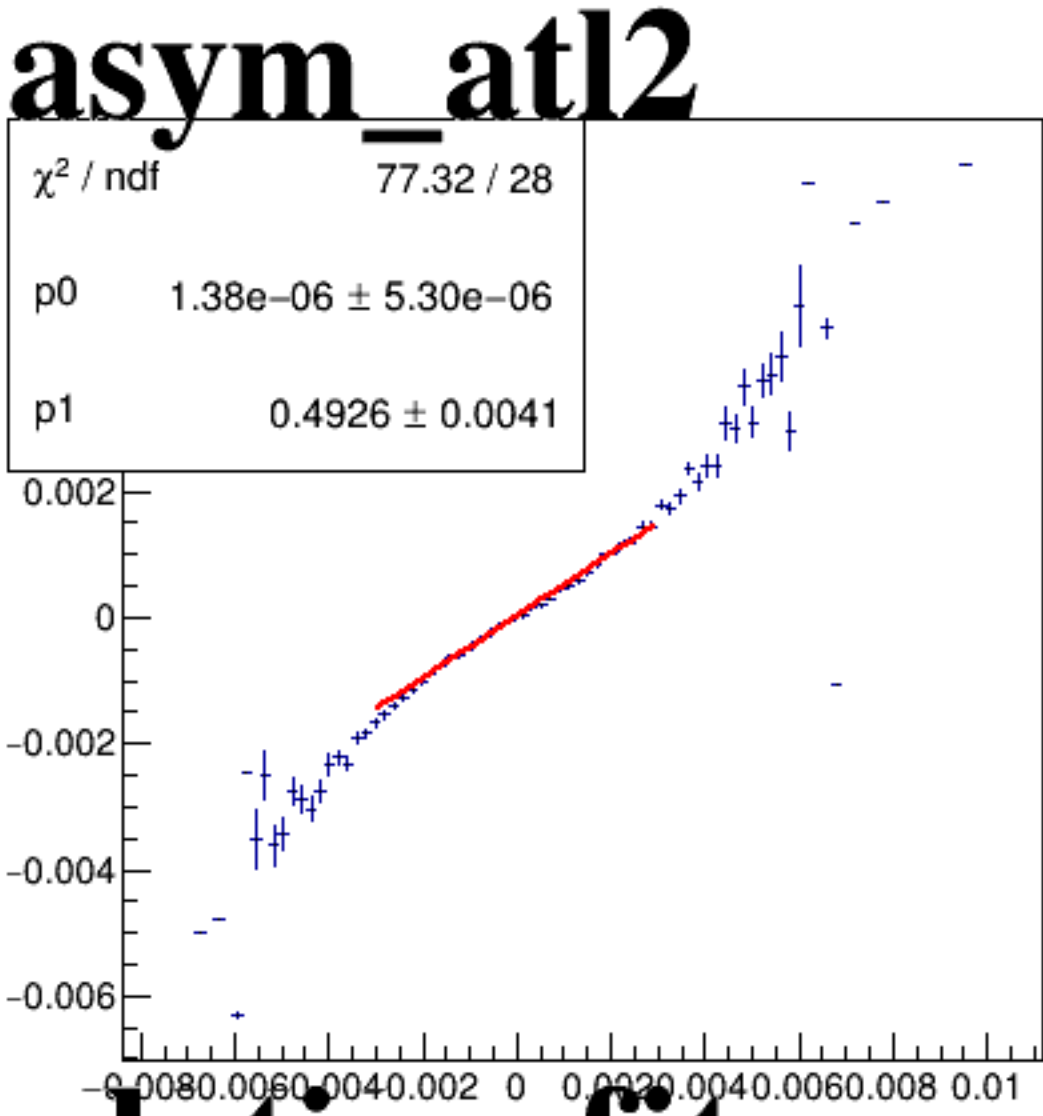
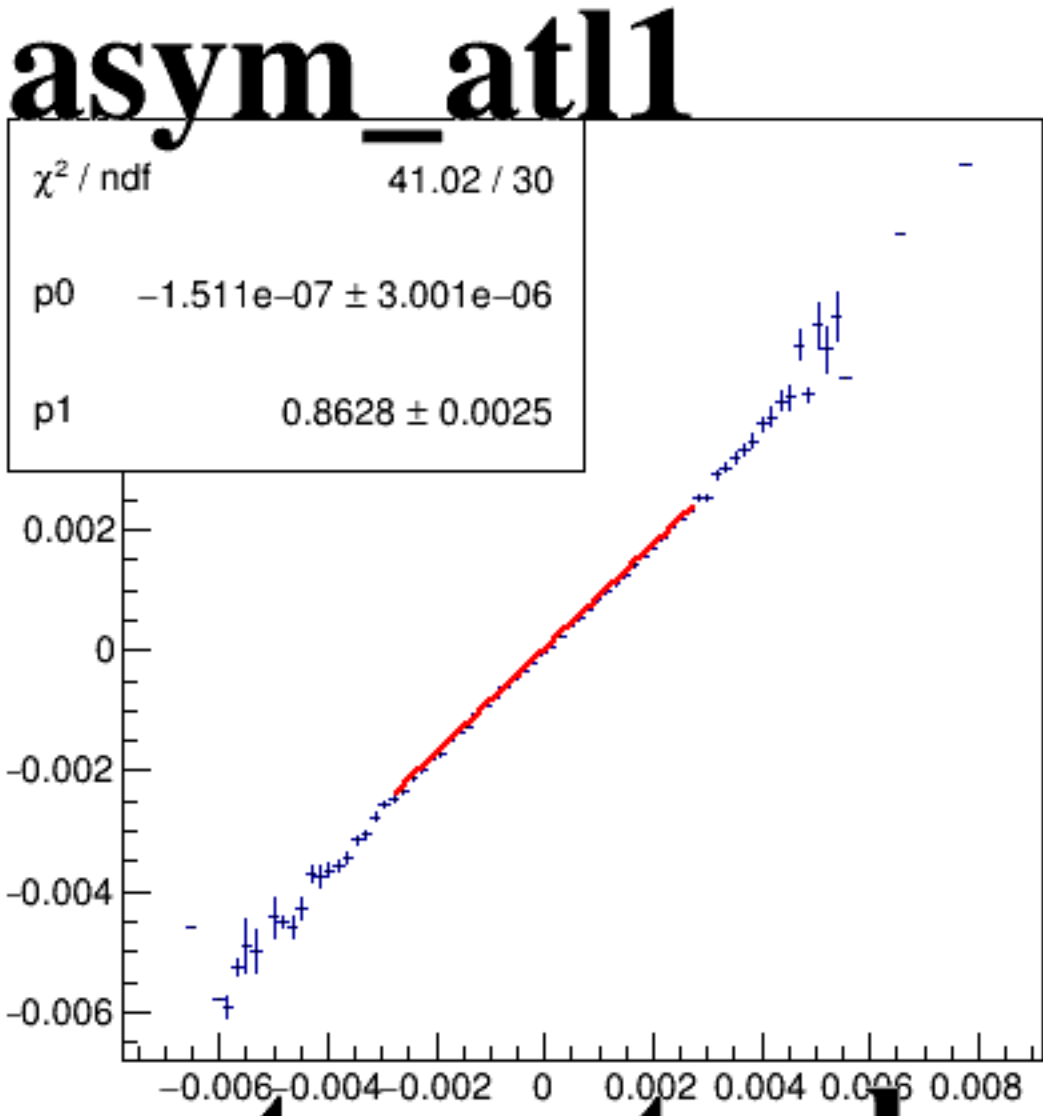
asym_atl2



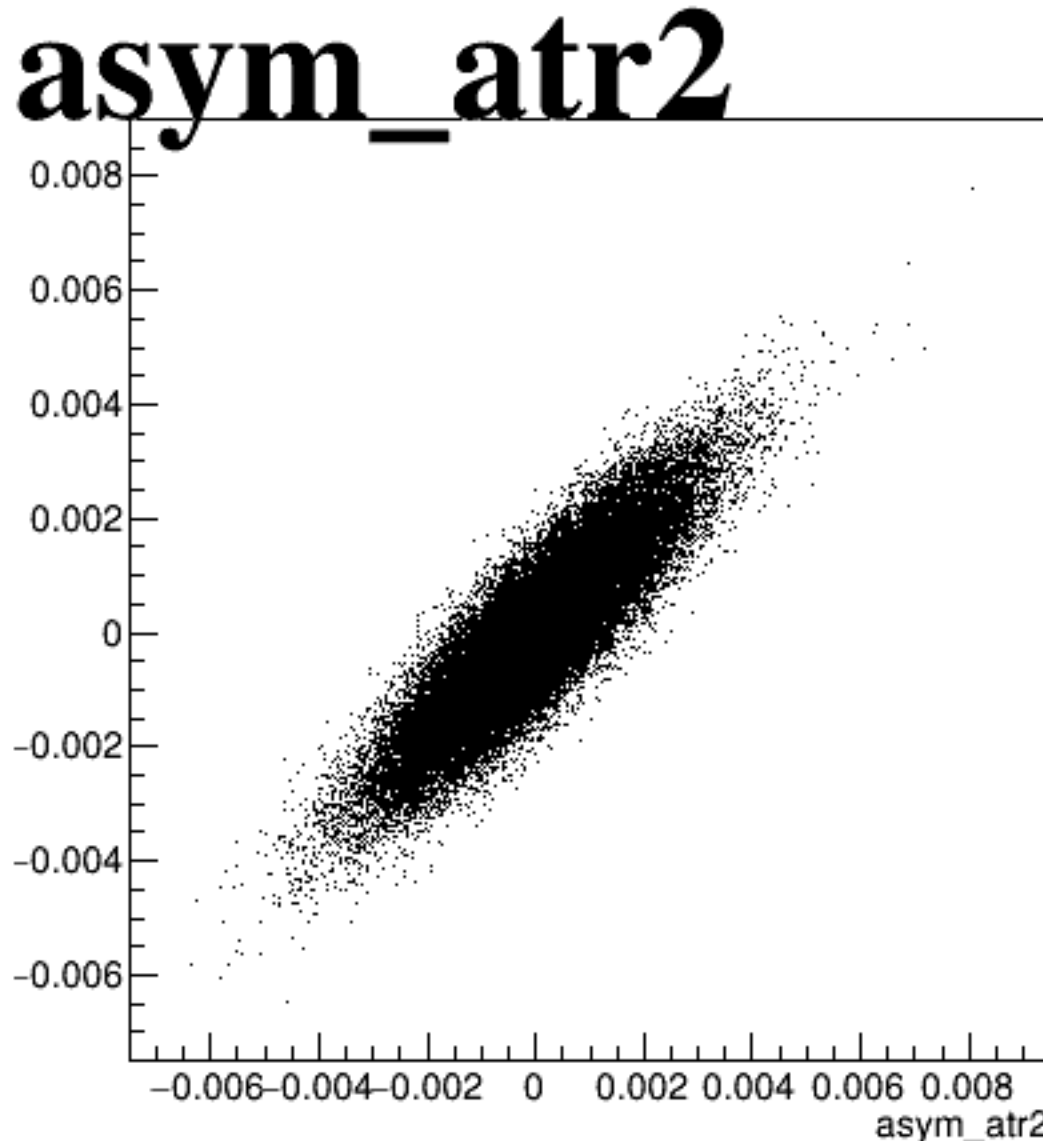
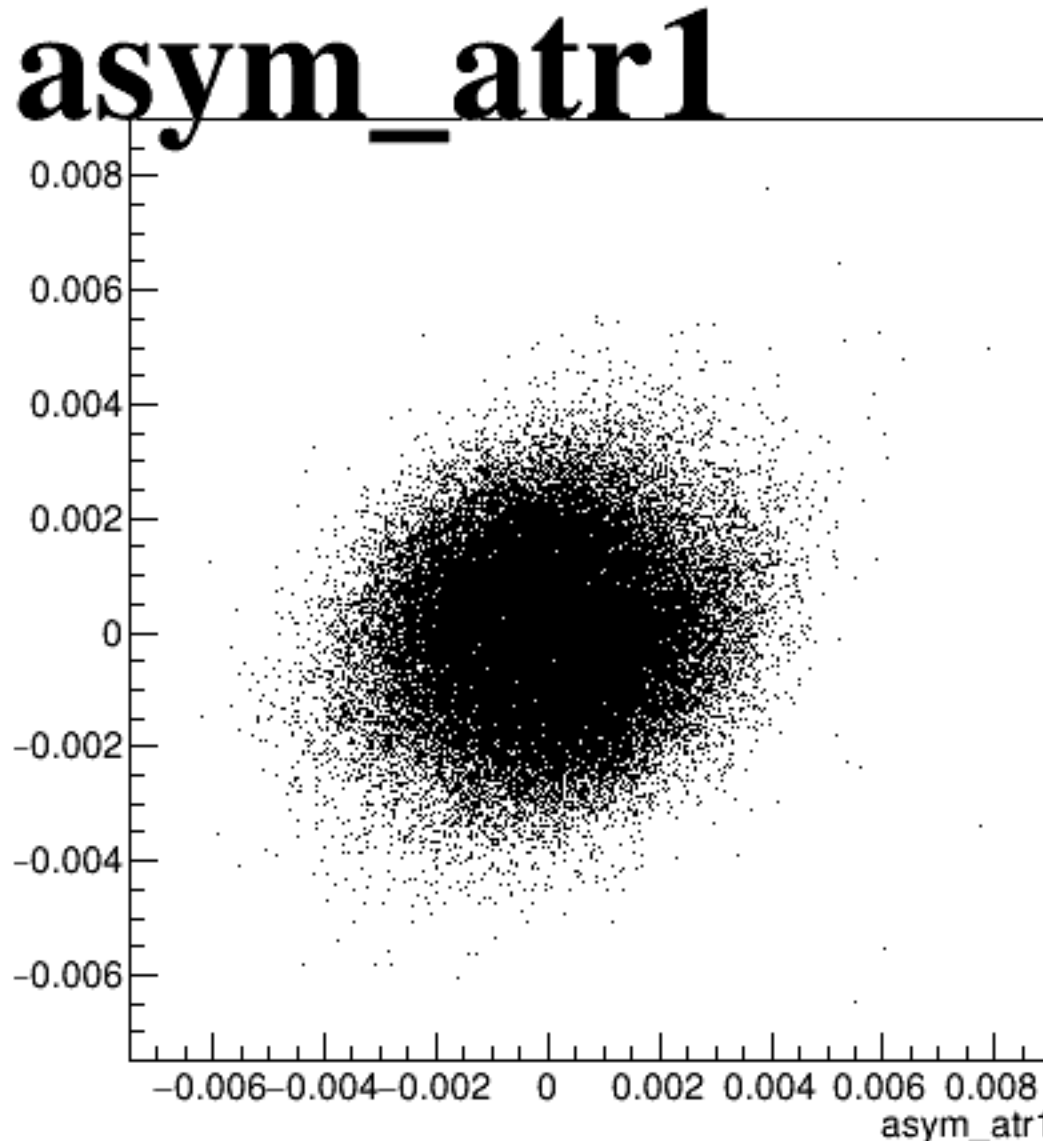
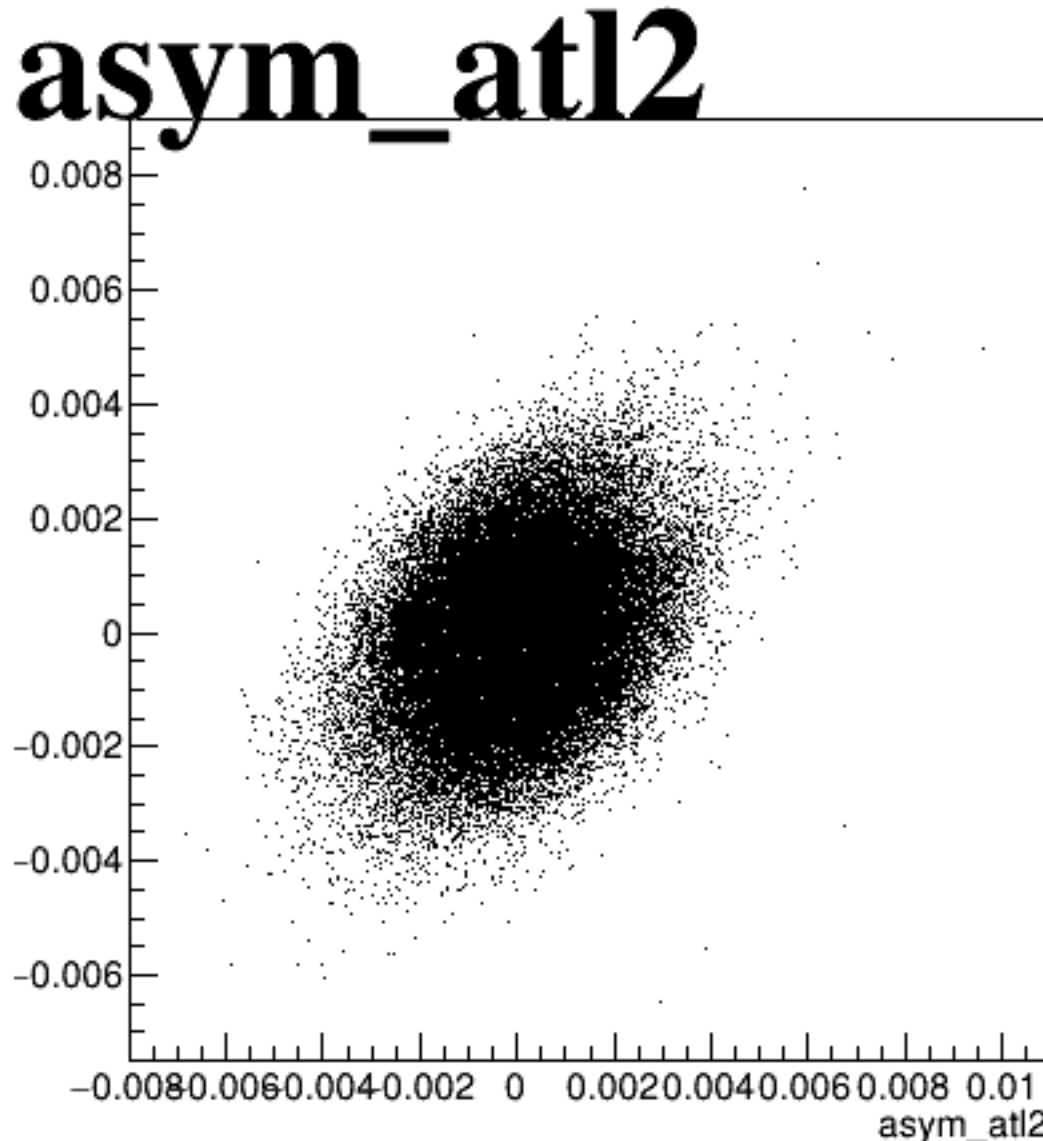
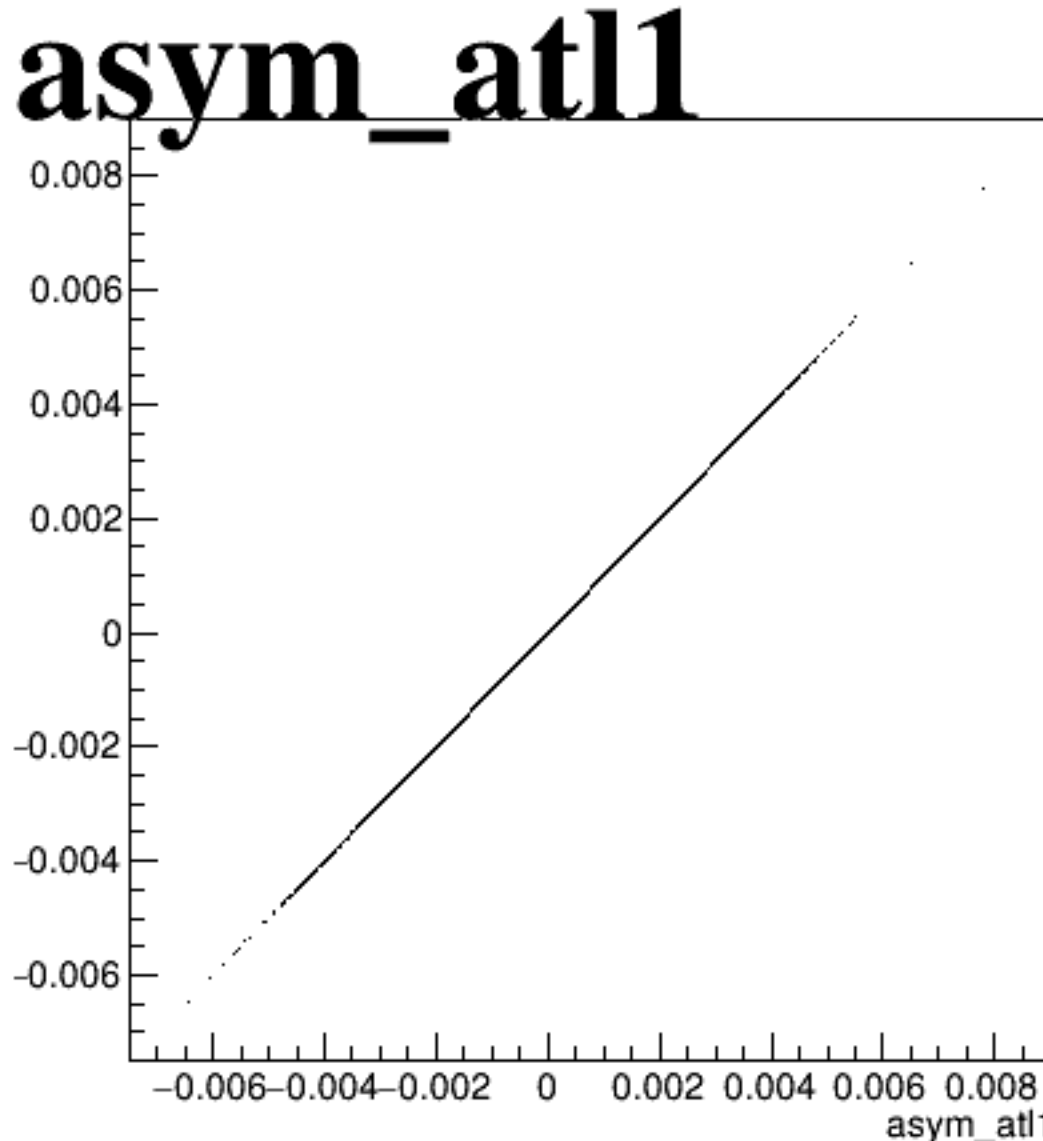
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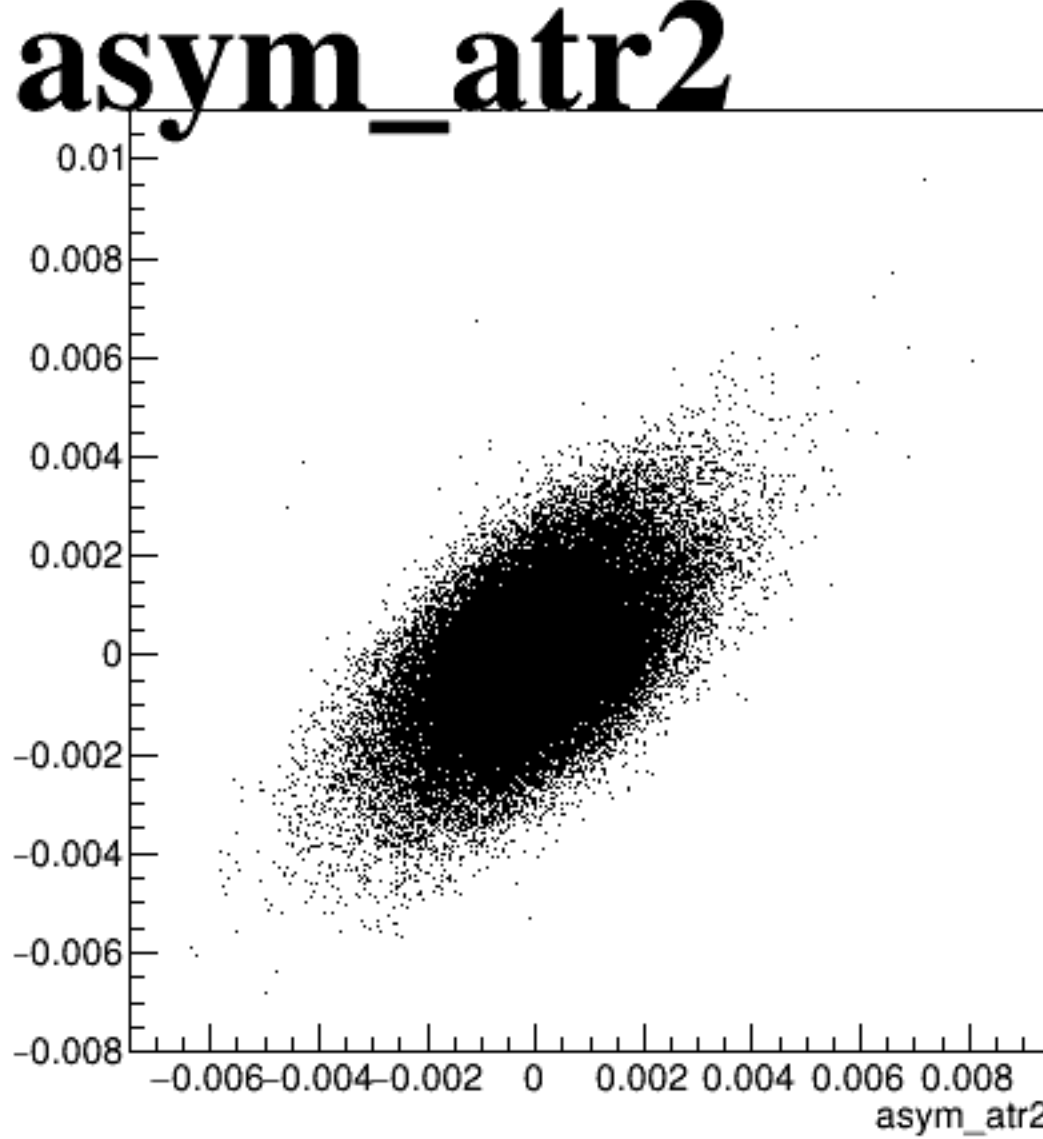
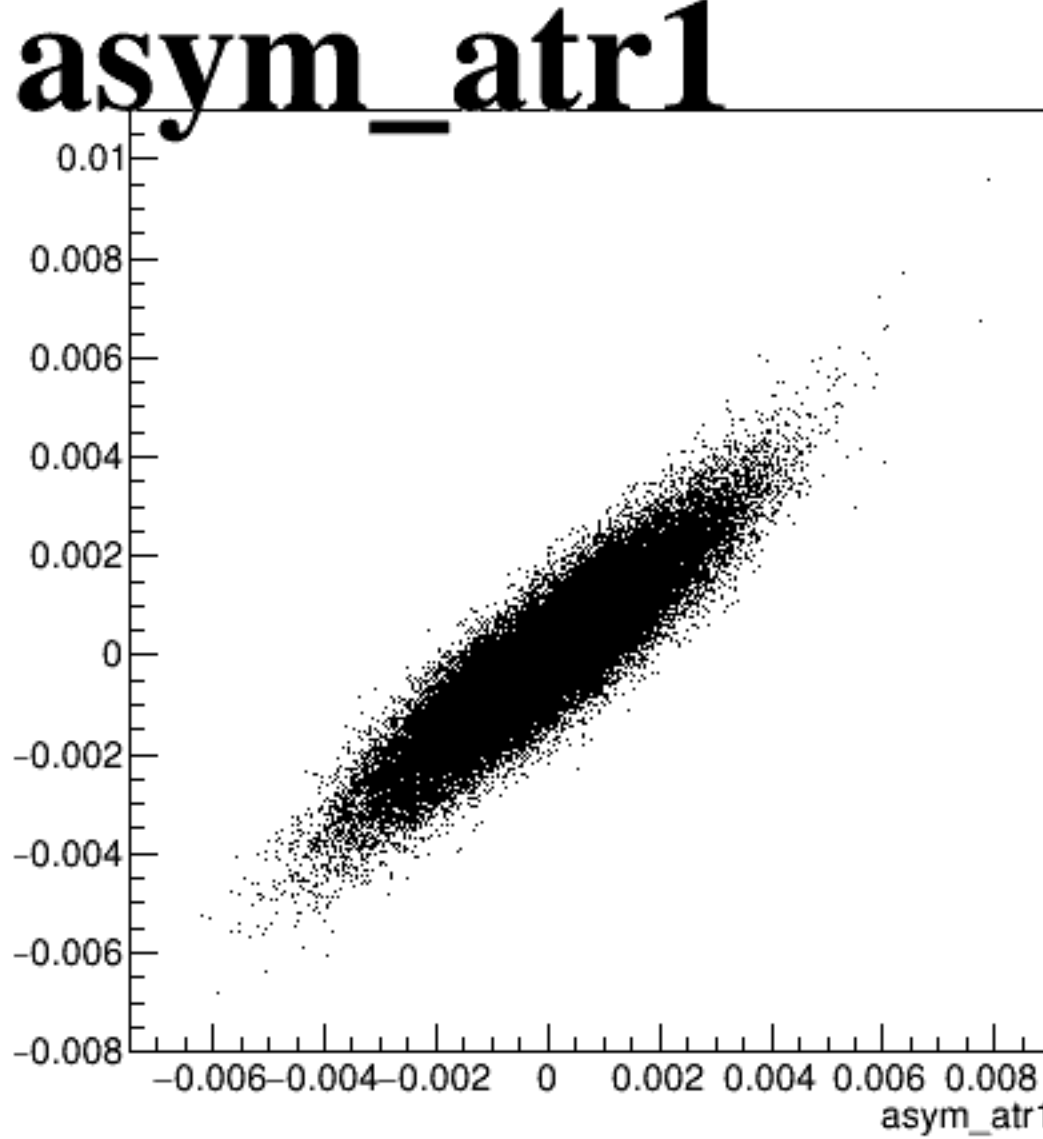
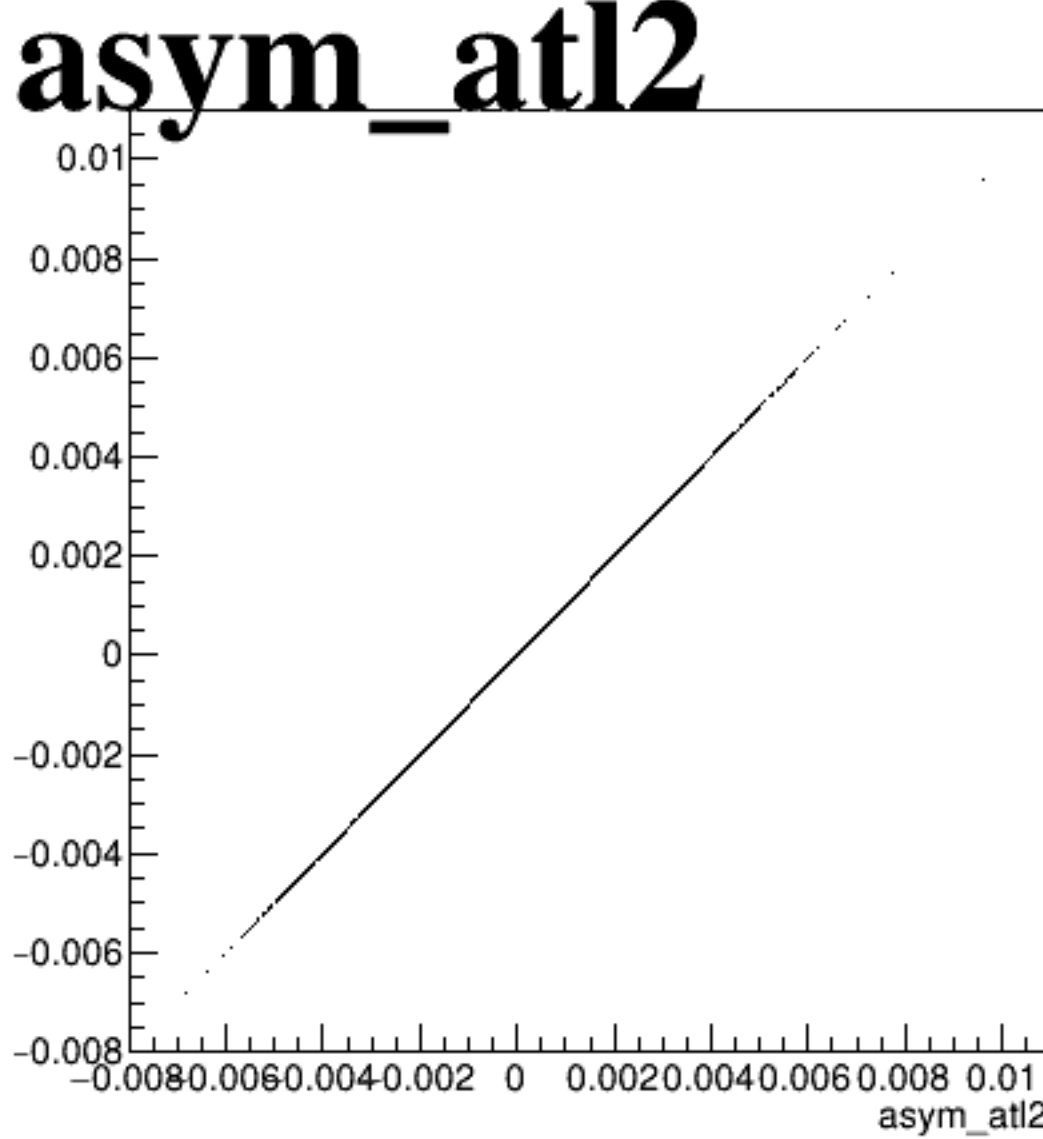
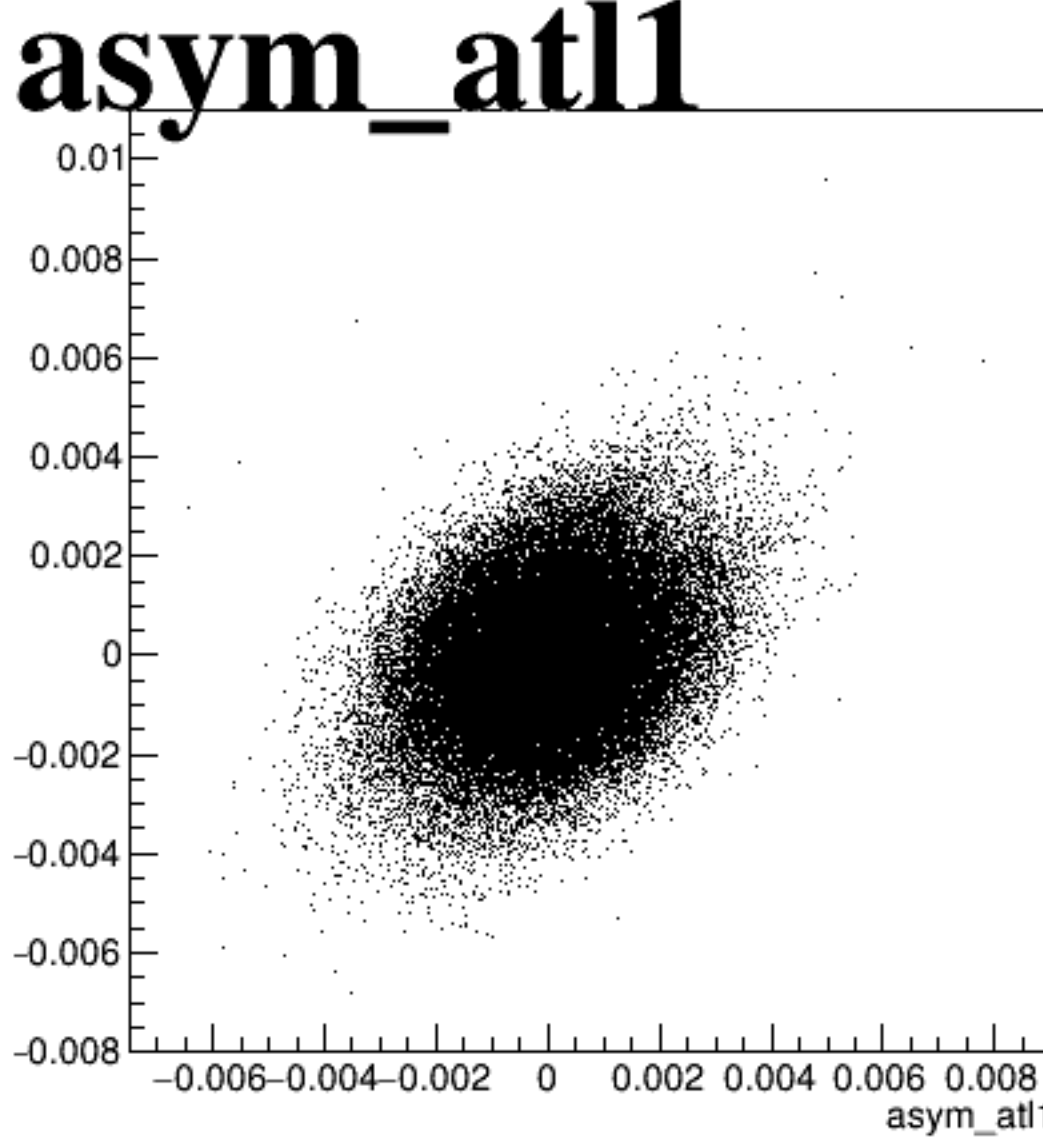
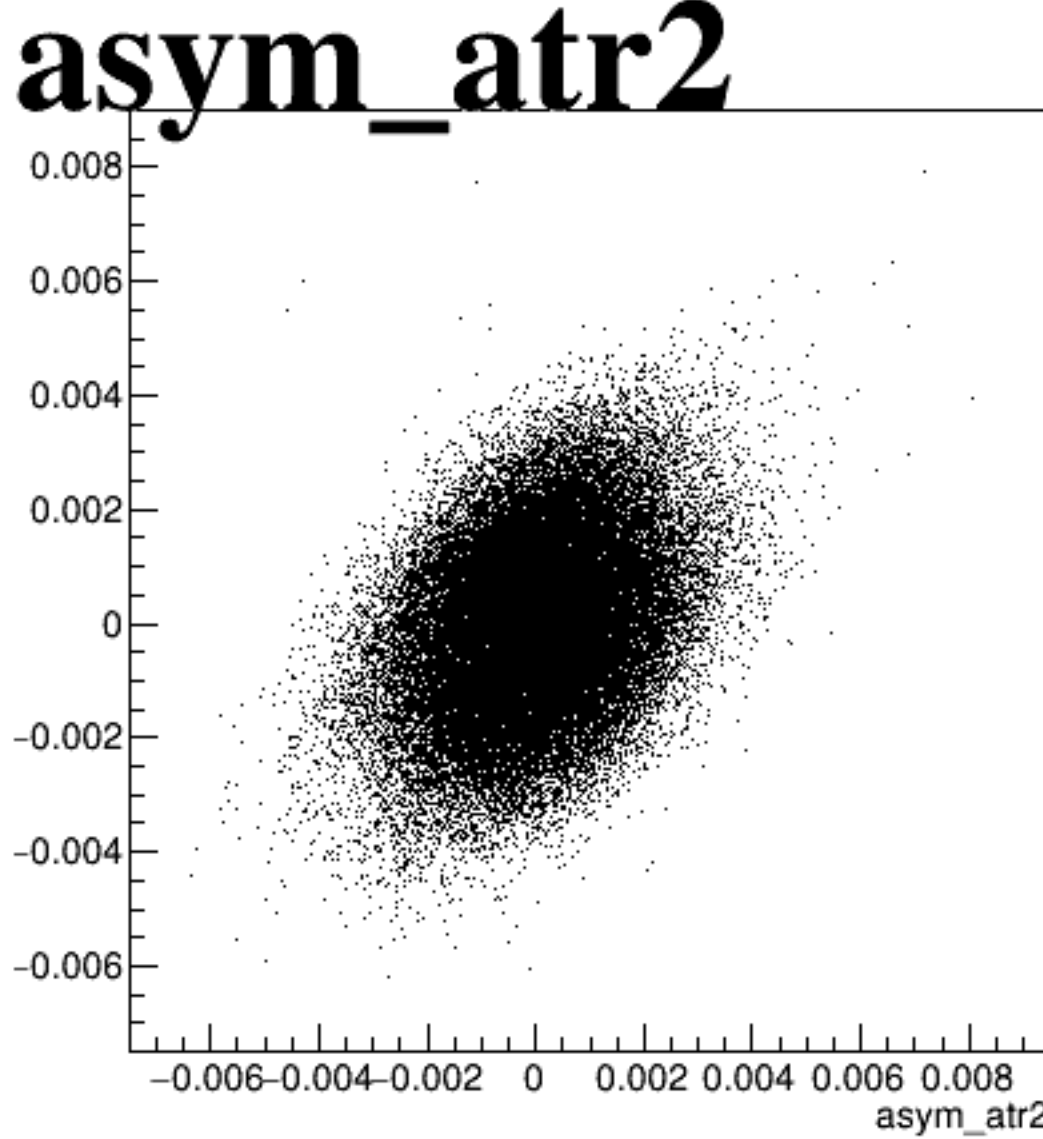
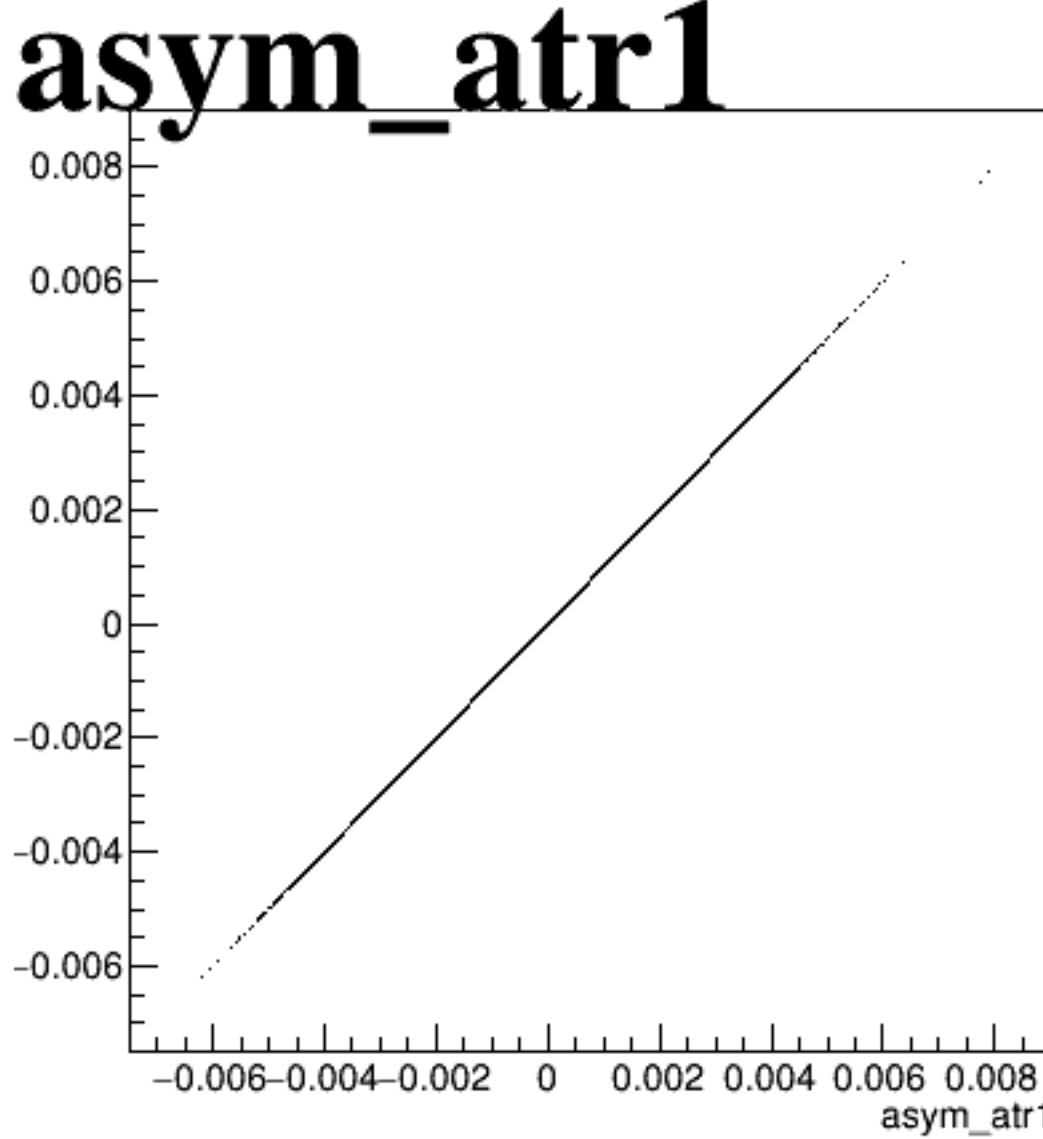
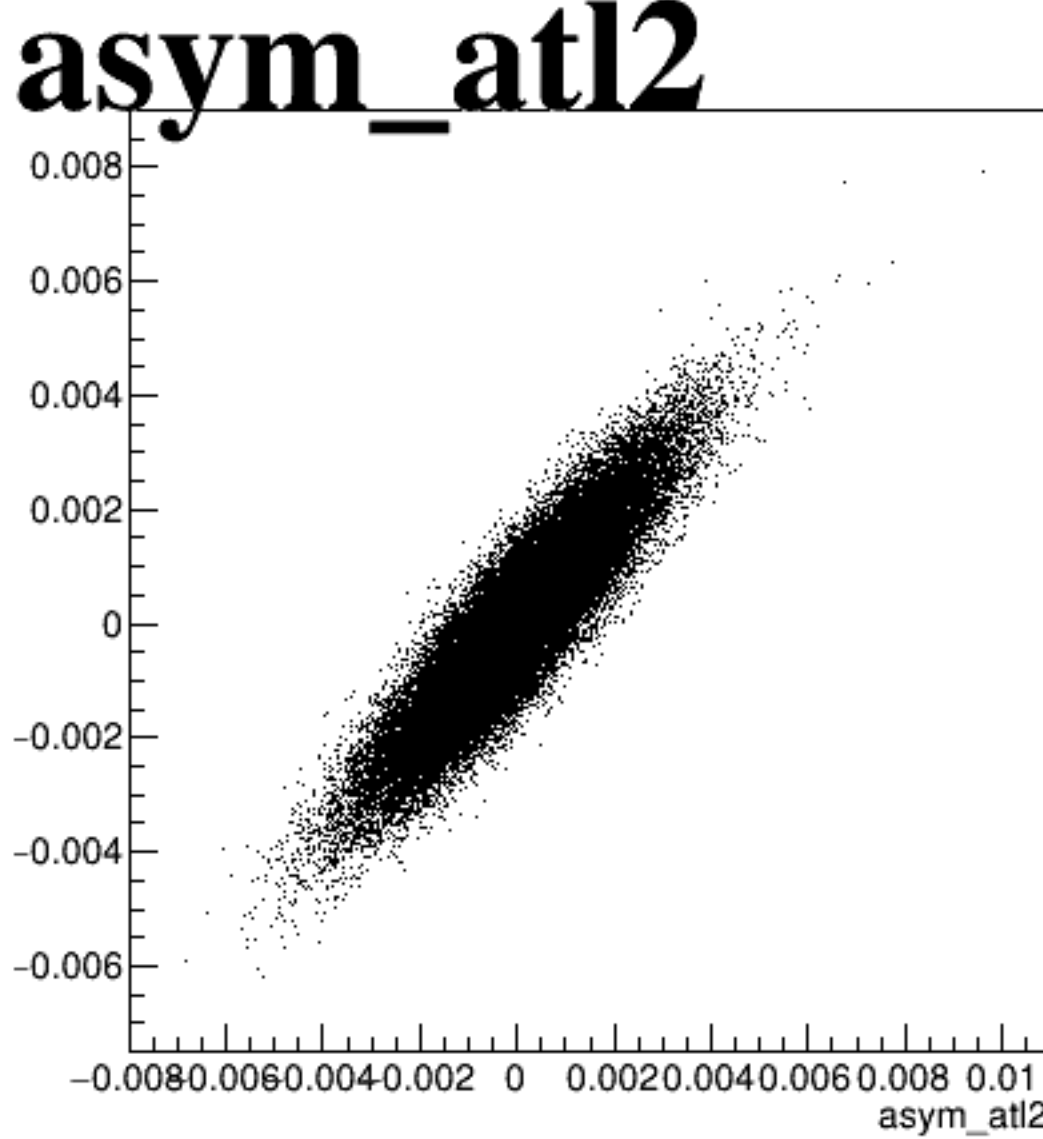
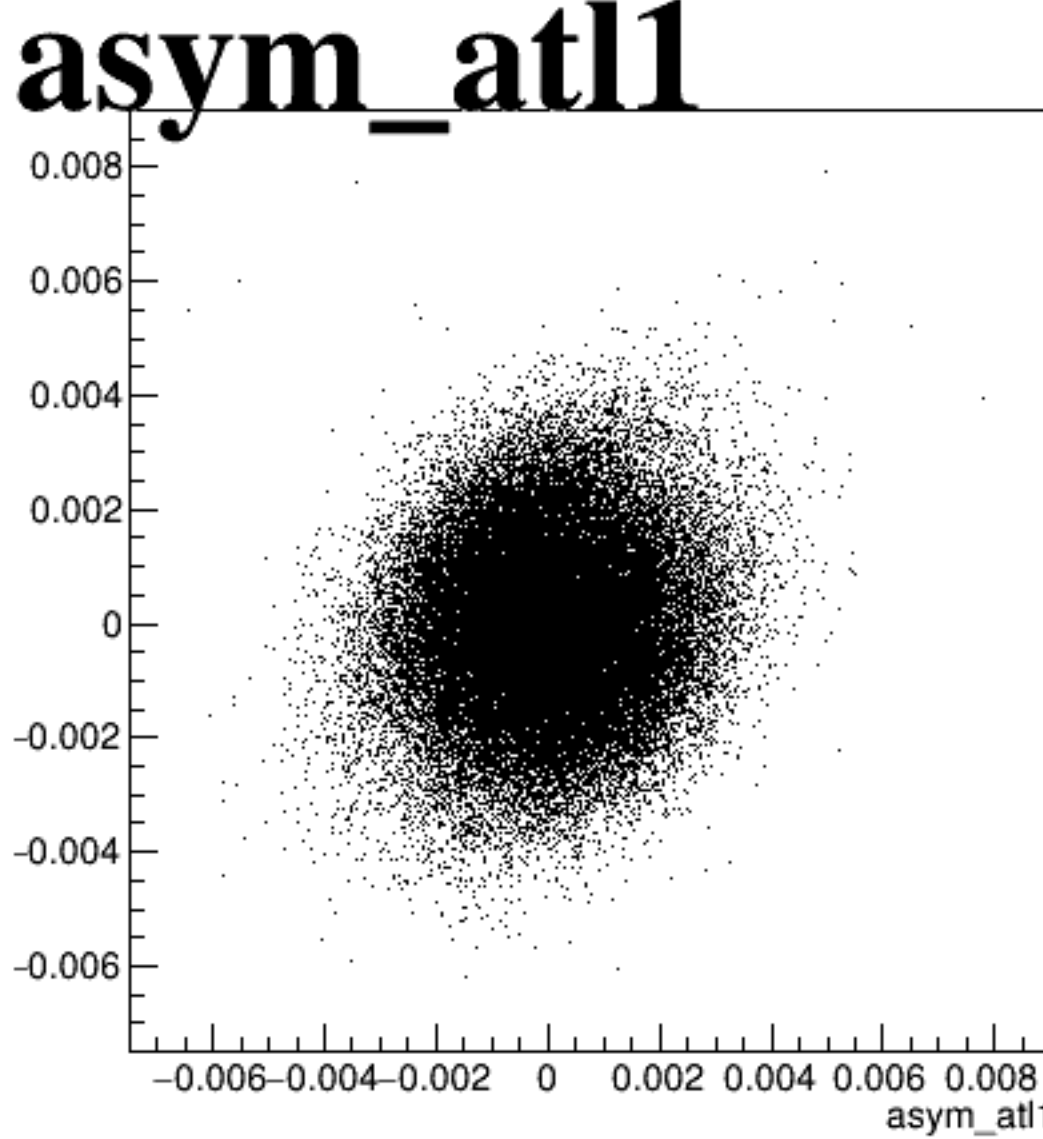
asym_atr2



asym_atl1



asym_atl2

**asym_atr1**

asym_atr2

