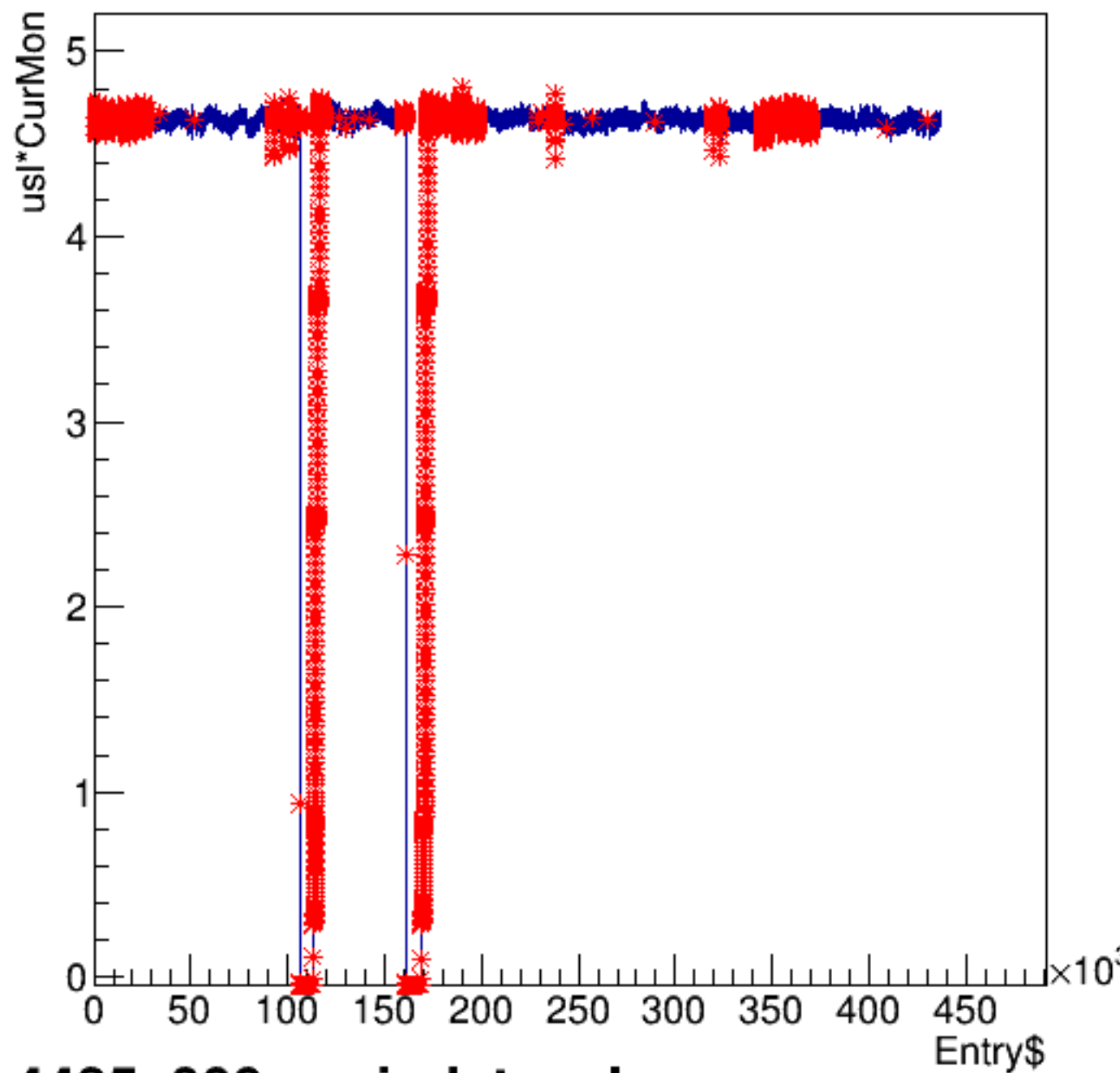
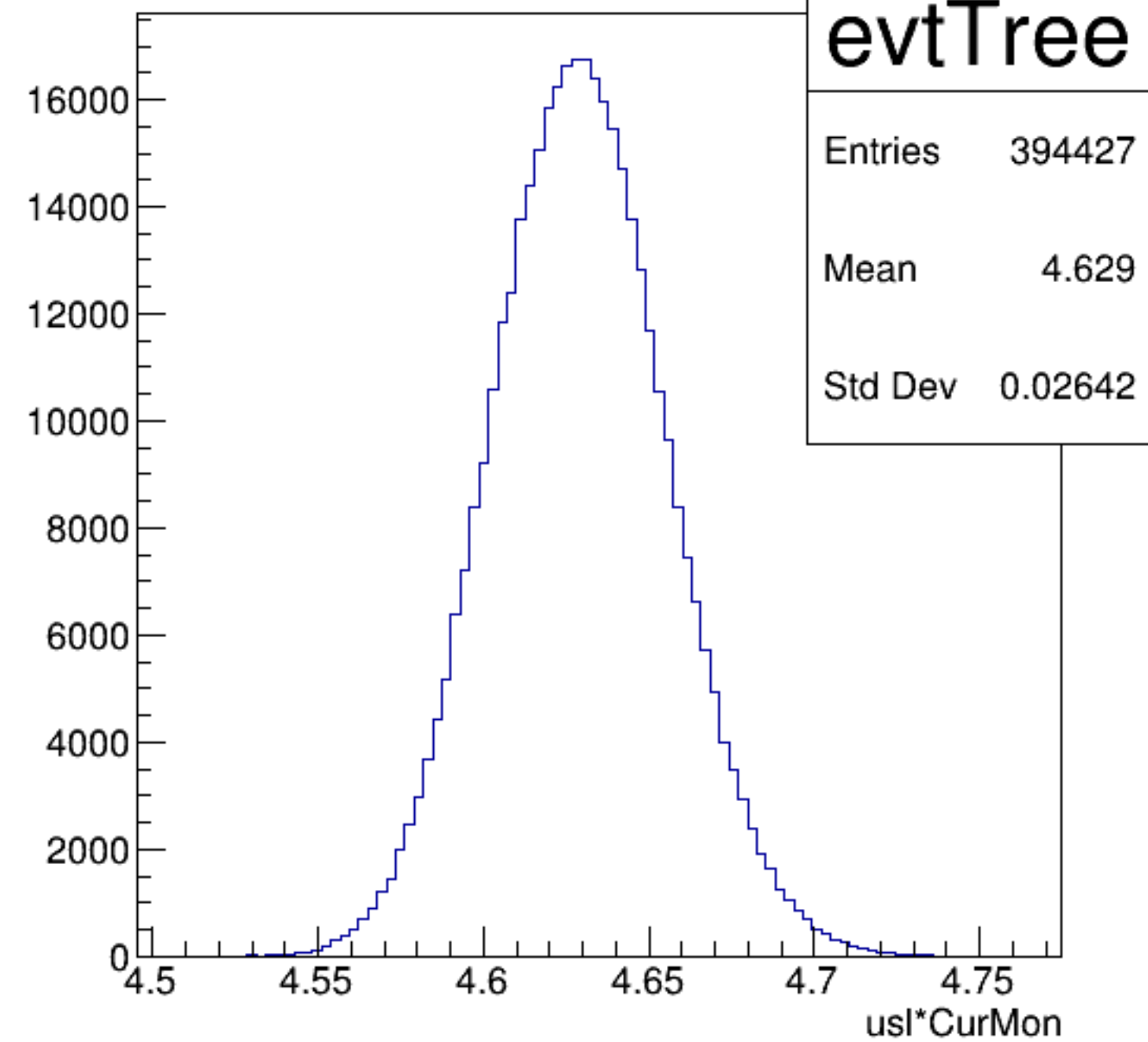


usl*CurMon:Entry\$



run4485_000_maindet_usl.png

usl*CurMon {ErrorFlag==0}



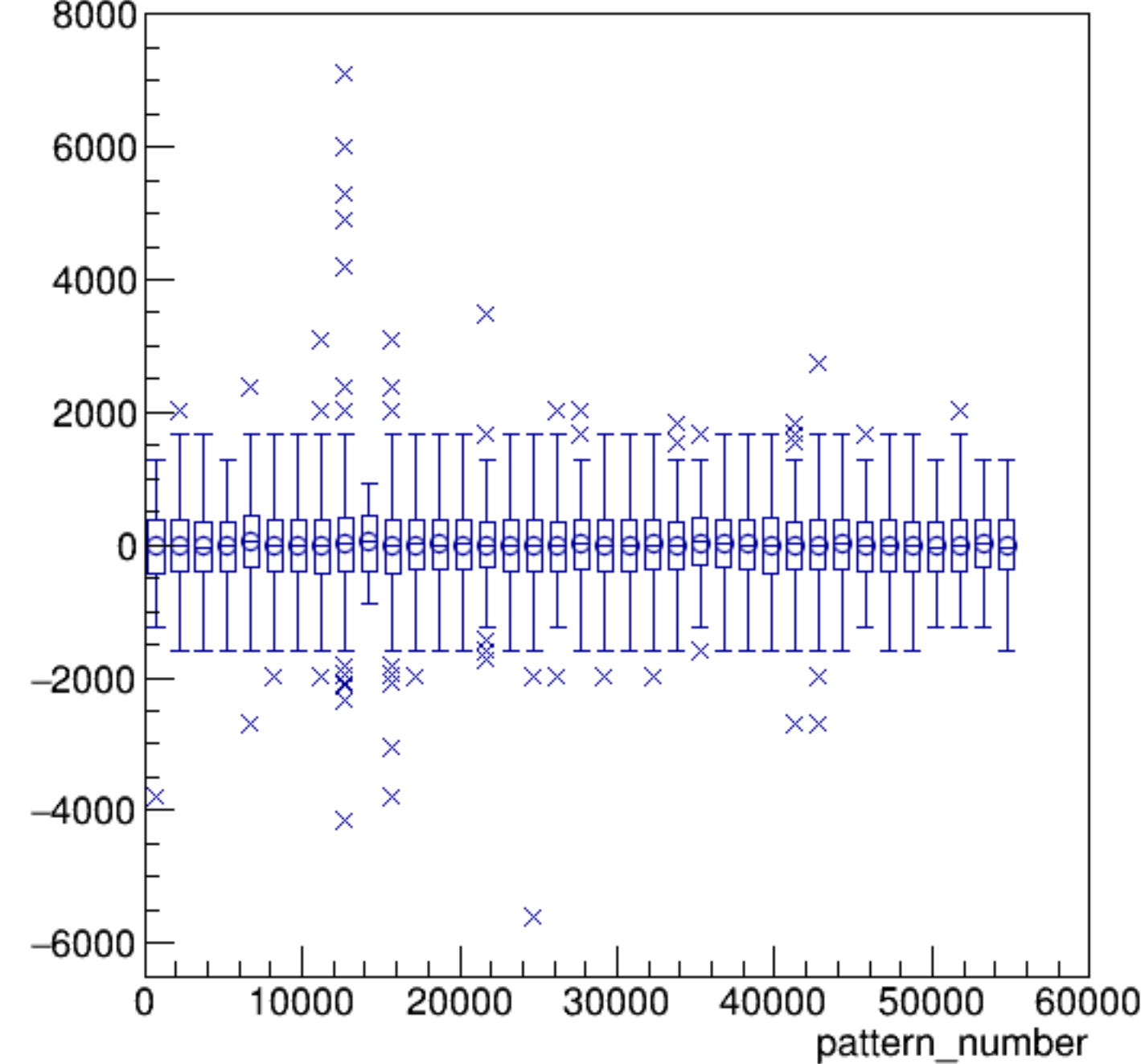
evtTree

Entries 394427

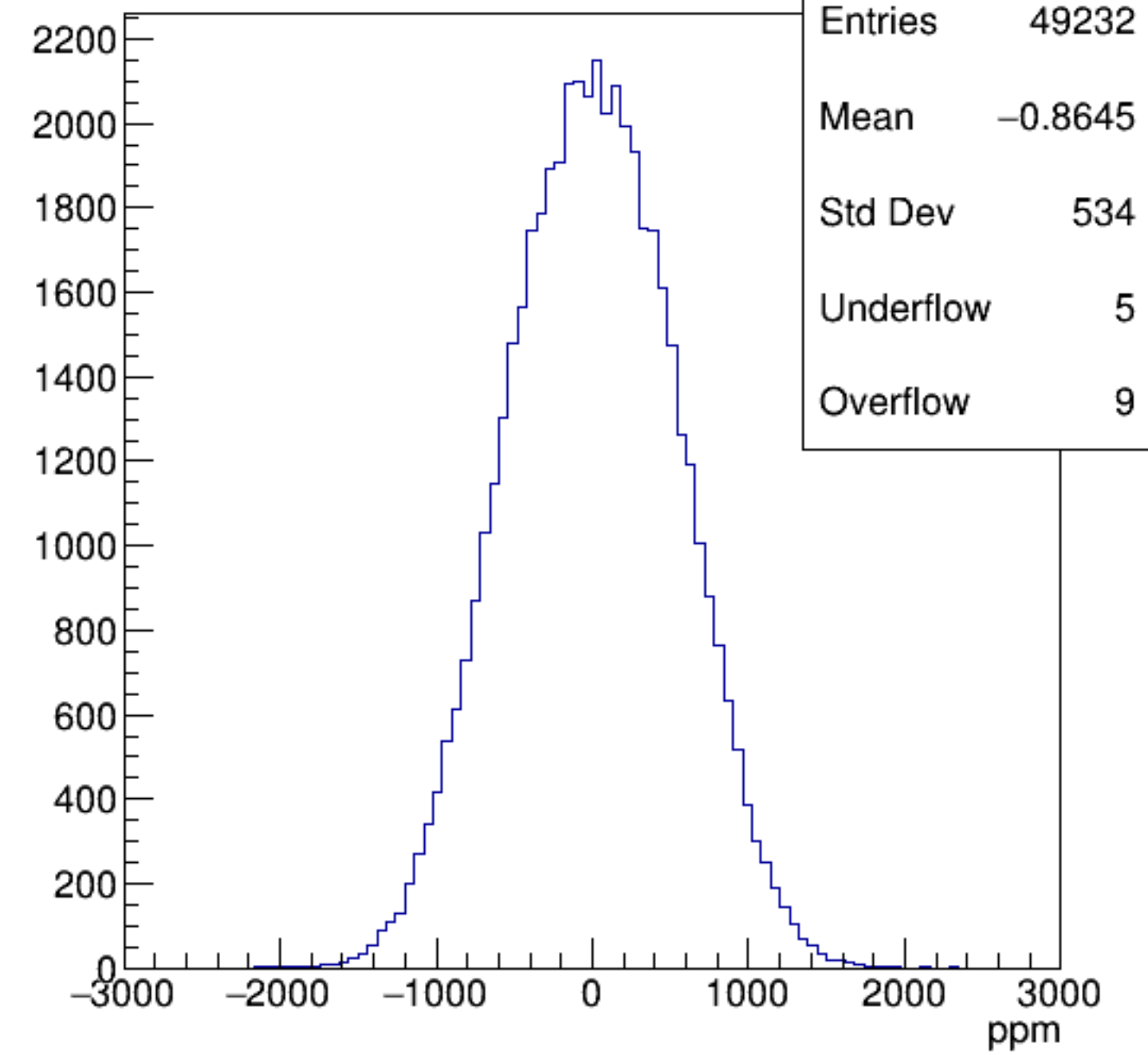
Mean 4.629

Std Dev 0.02642

asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl



Entries 49232

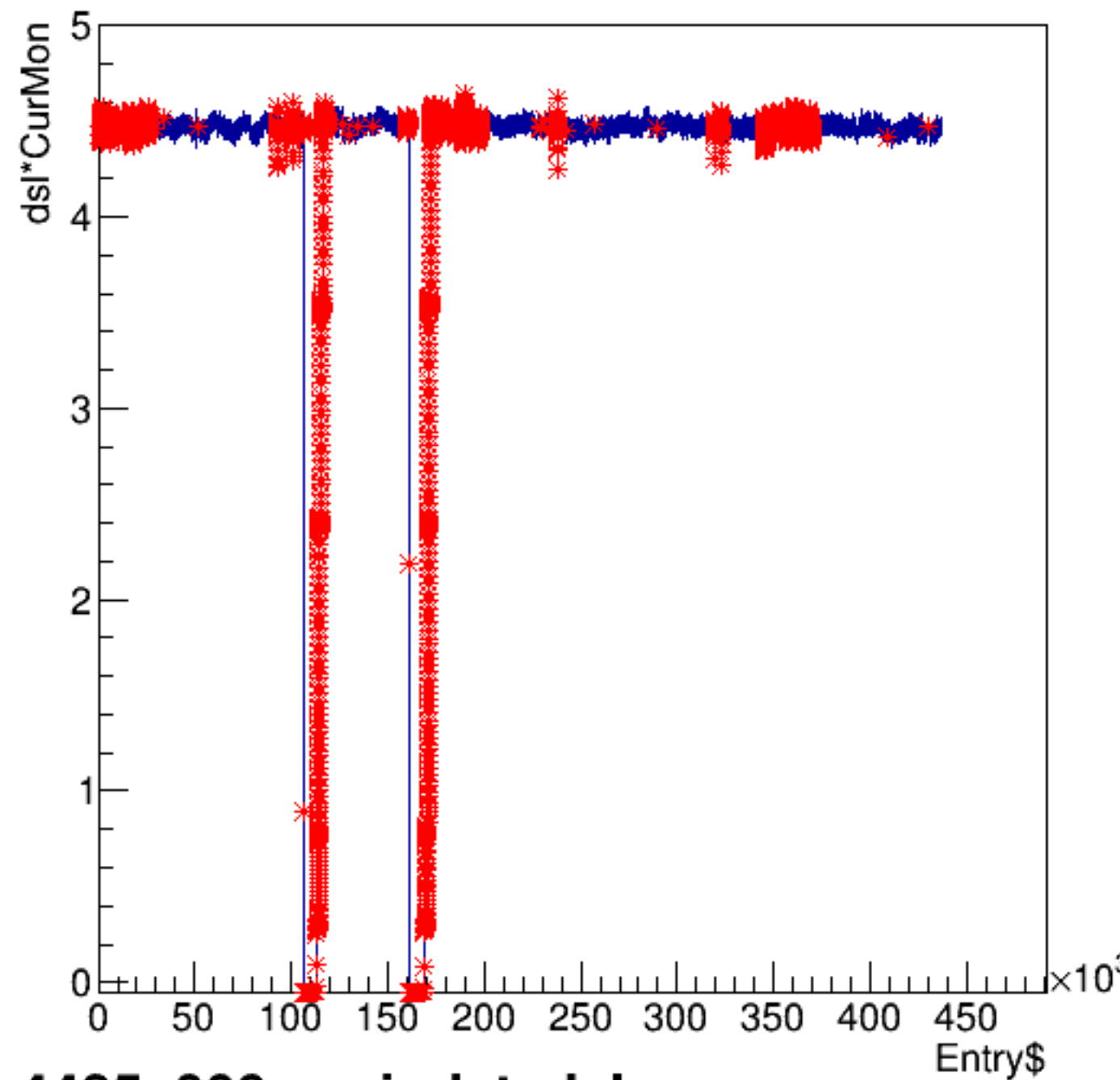
Mean -0.8645

Std Dev 534

Underflow 5

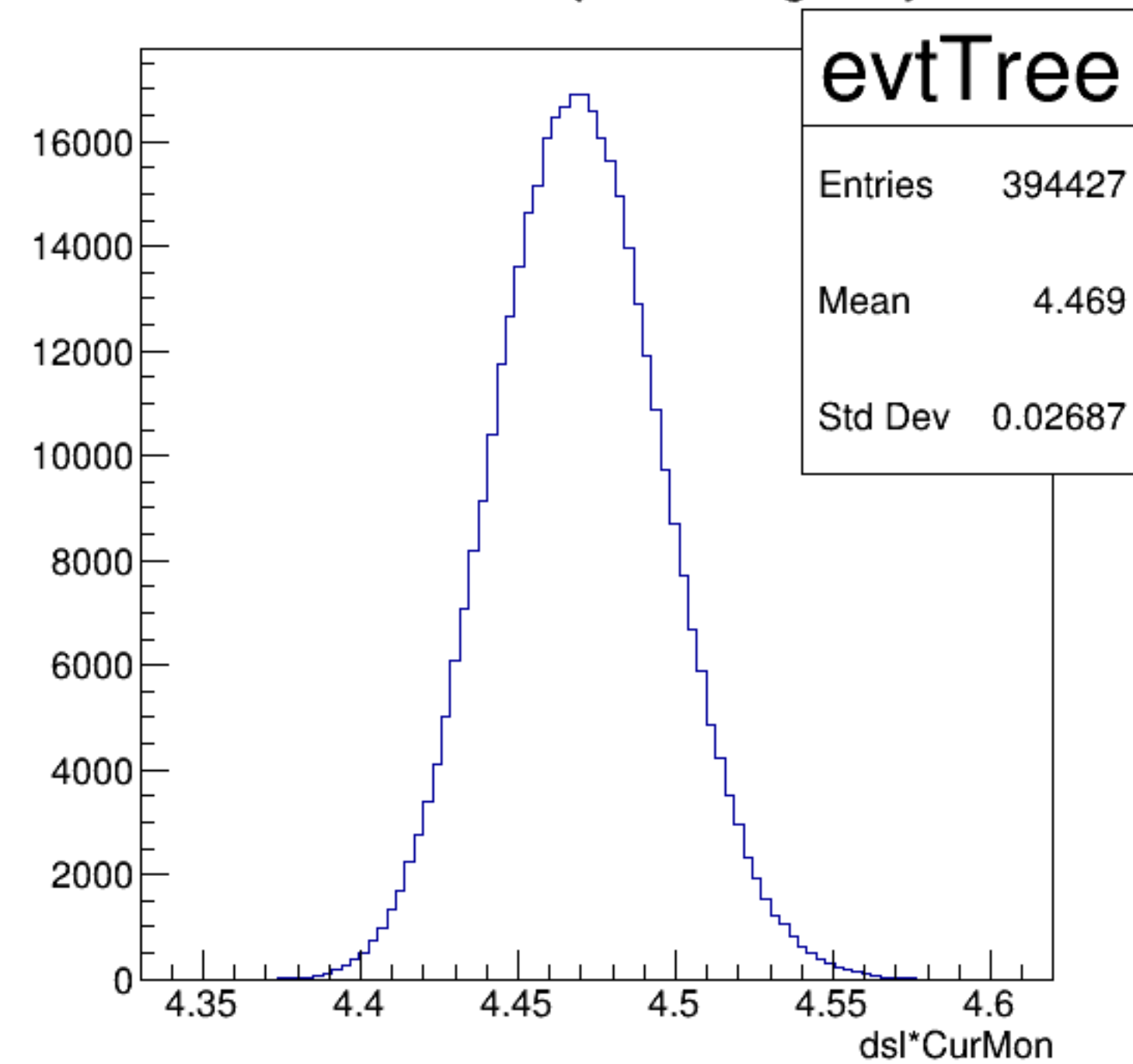
Overflow 9

dsl*CurMon:Entry\$

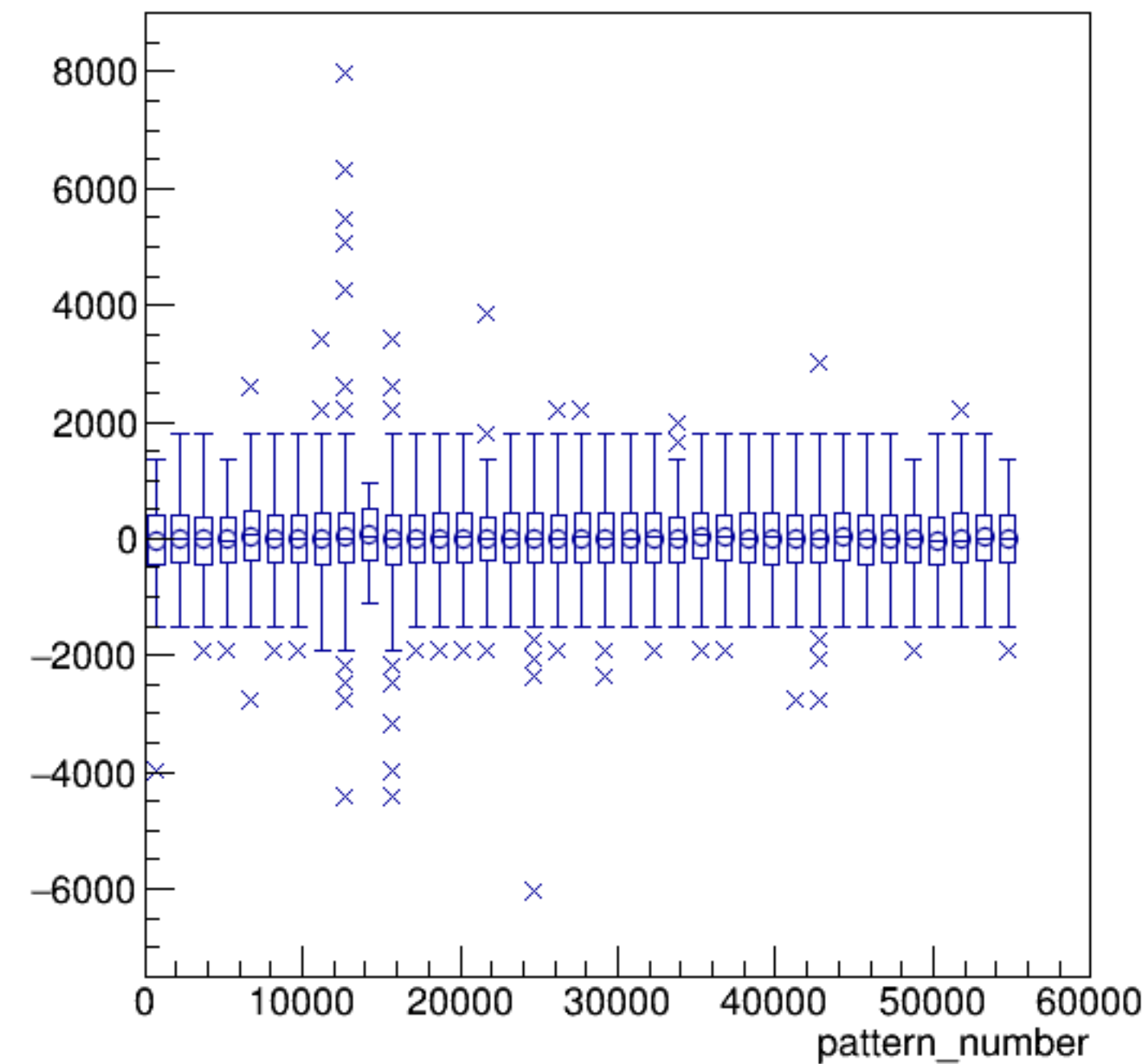


run4485_000_maindet_dsl.png

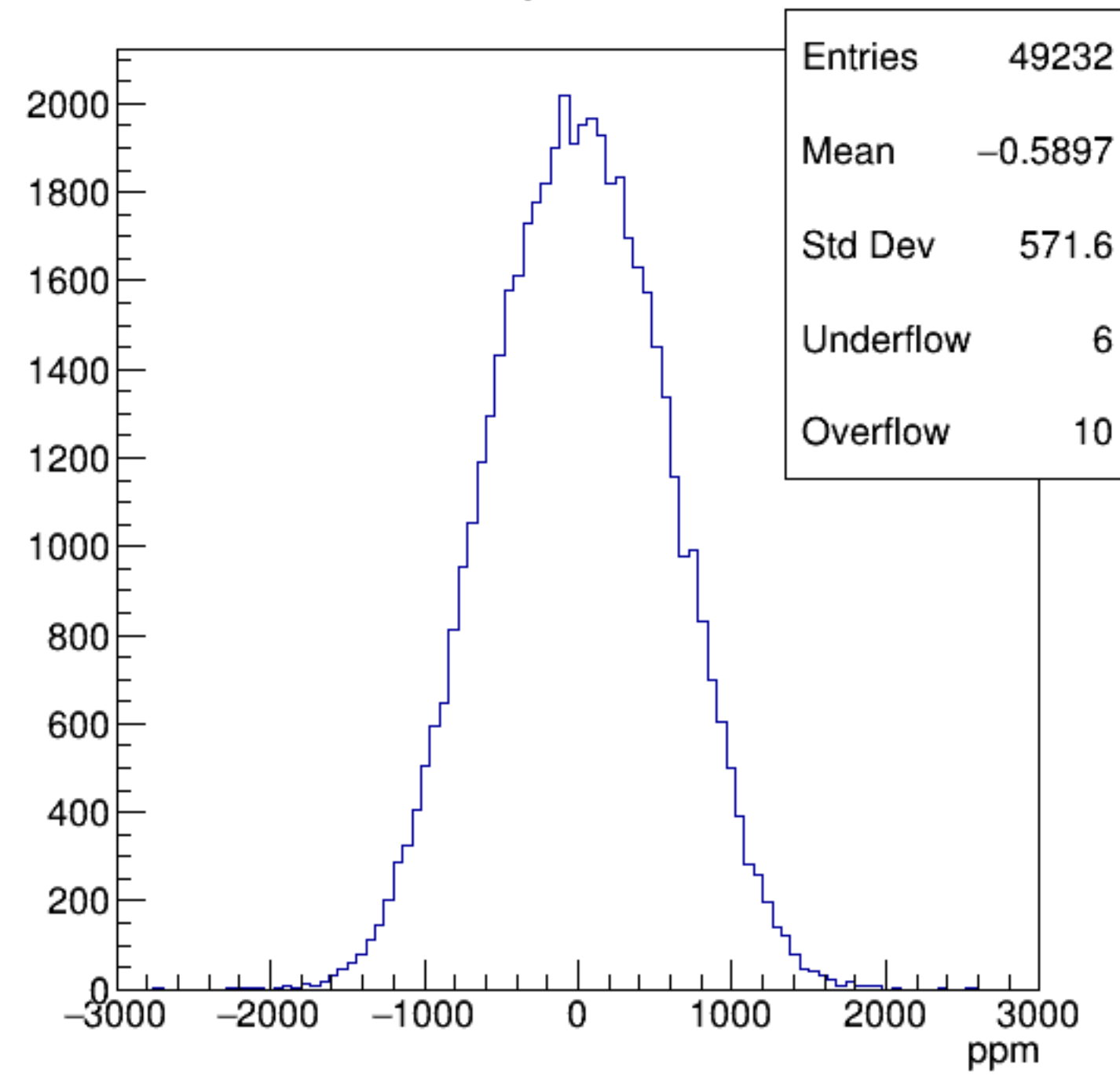
dsl*CurMon {ErrorFlag==0}



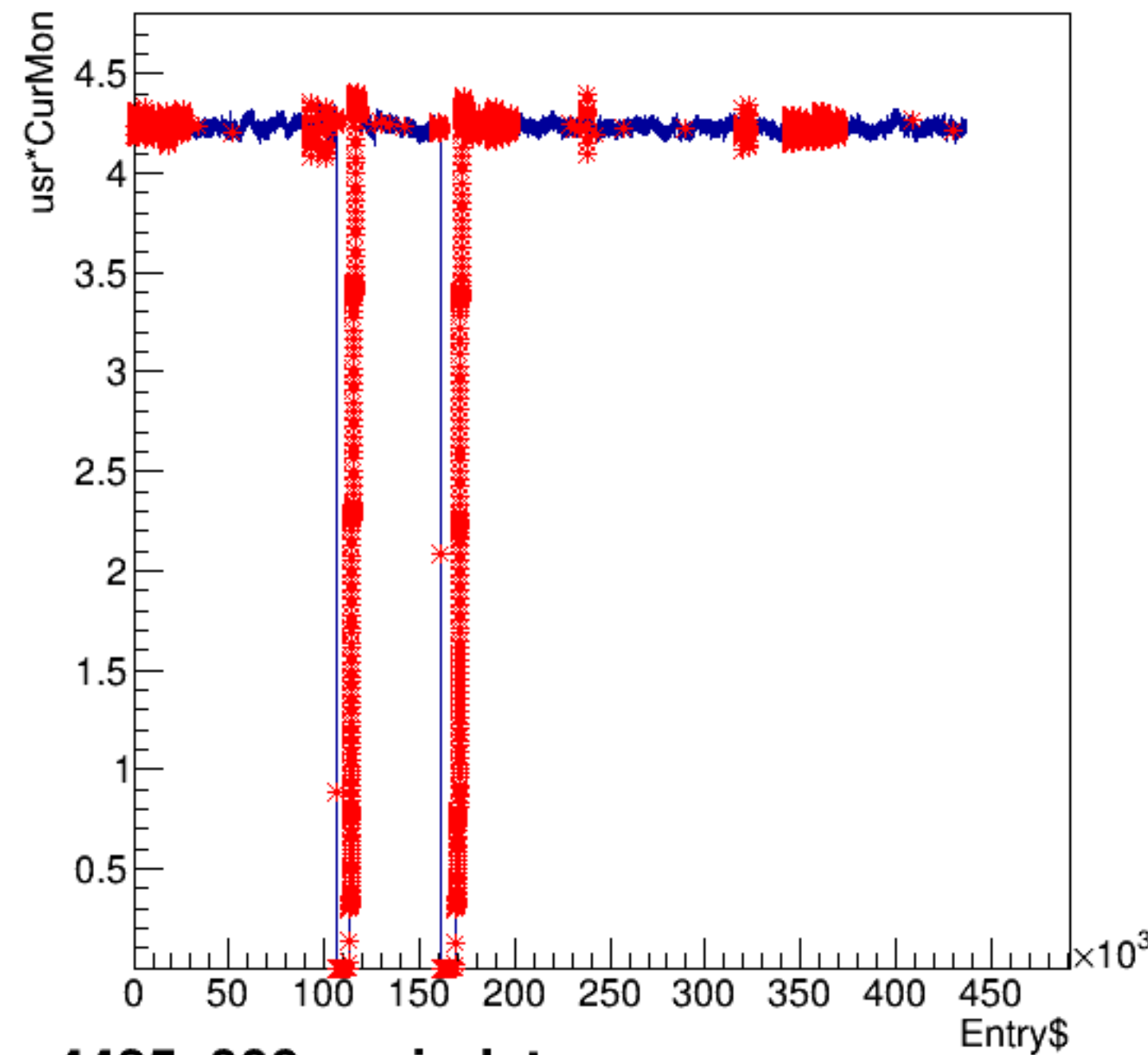
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl

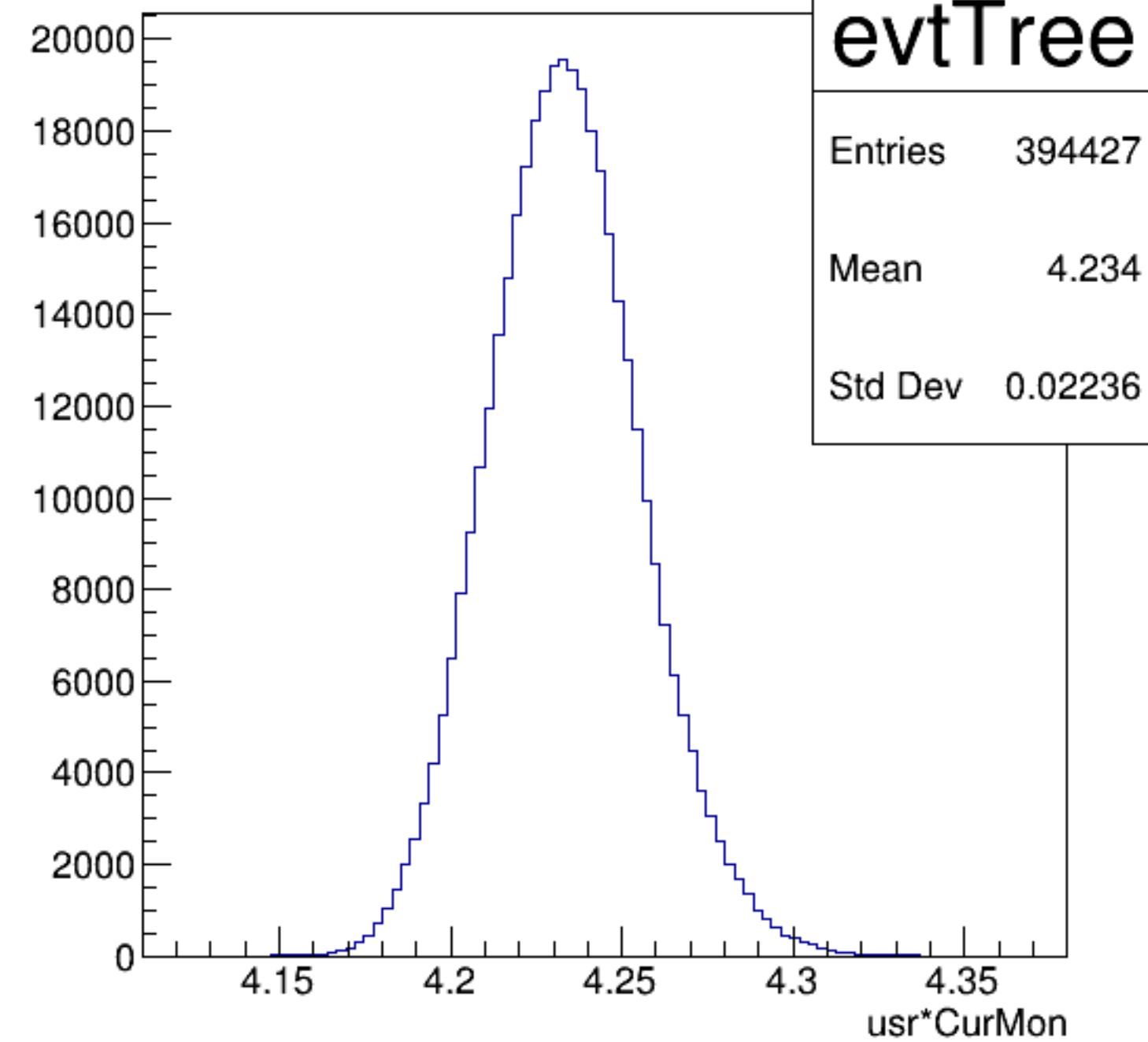


usr*CurMon:Entry\$

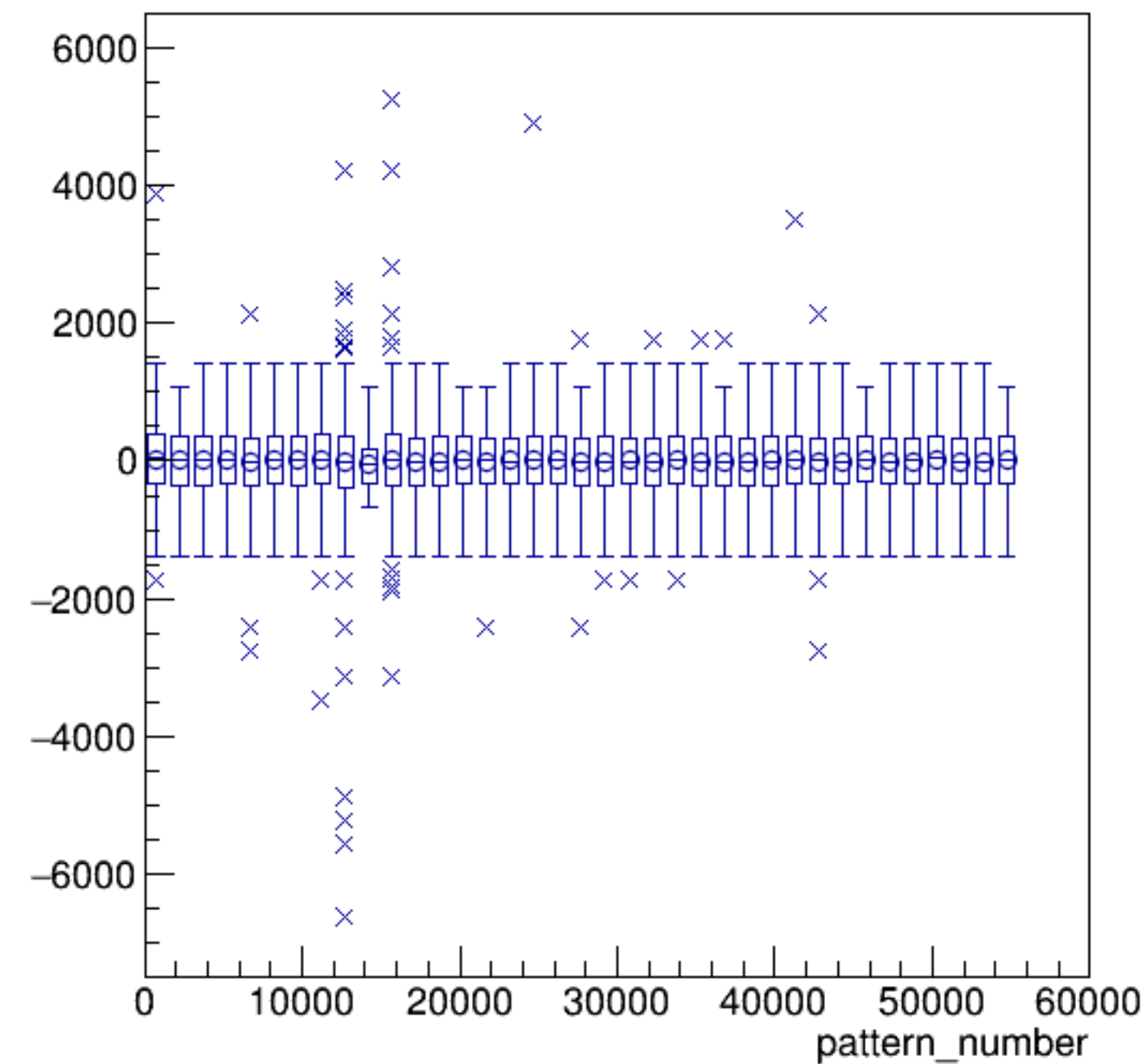


run4485_000_maindet_usr.png

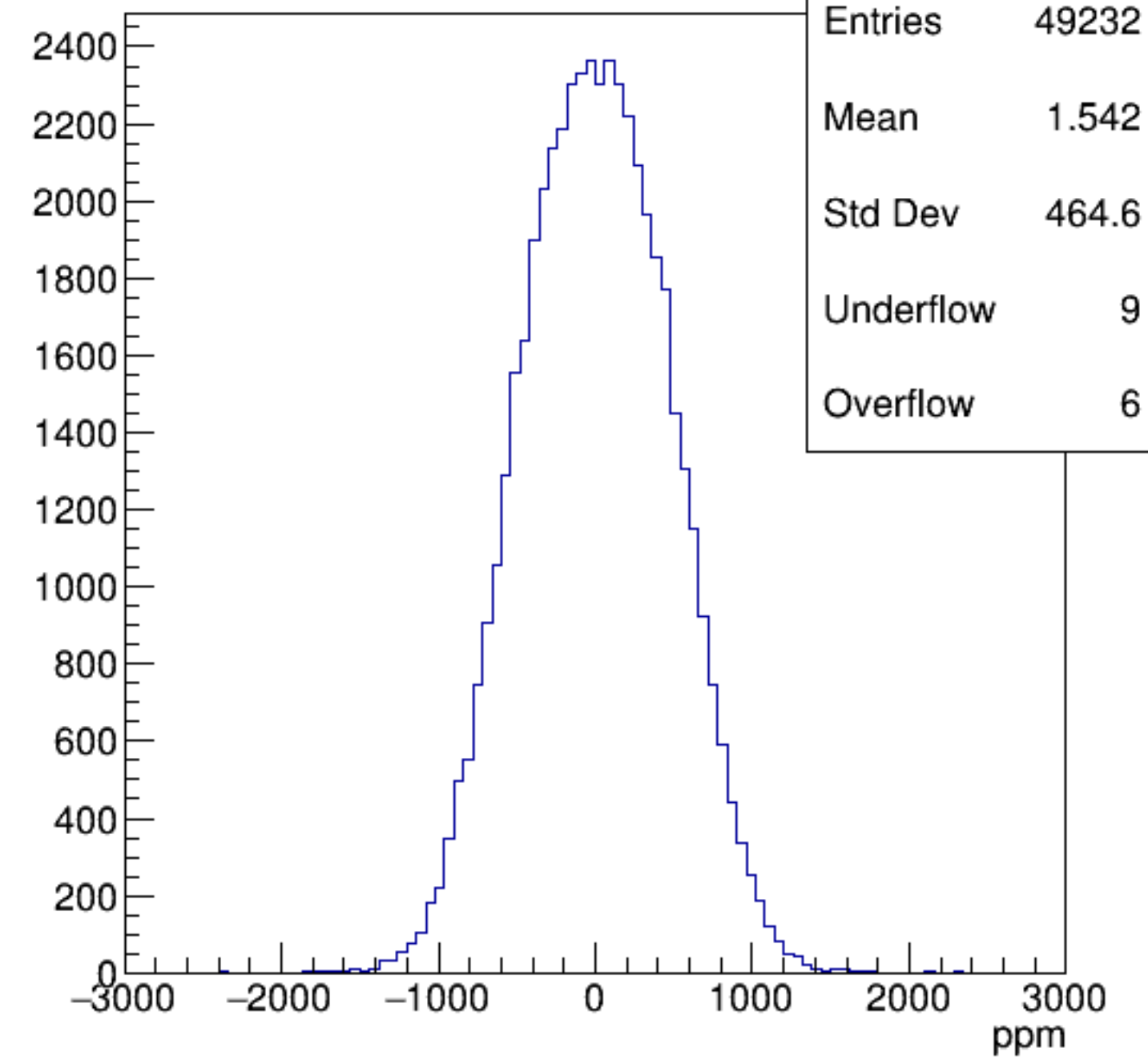
usr*CurMon {ErrorFlag==0}



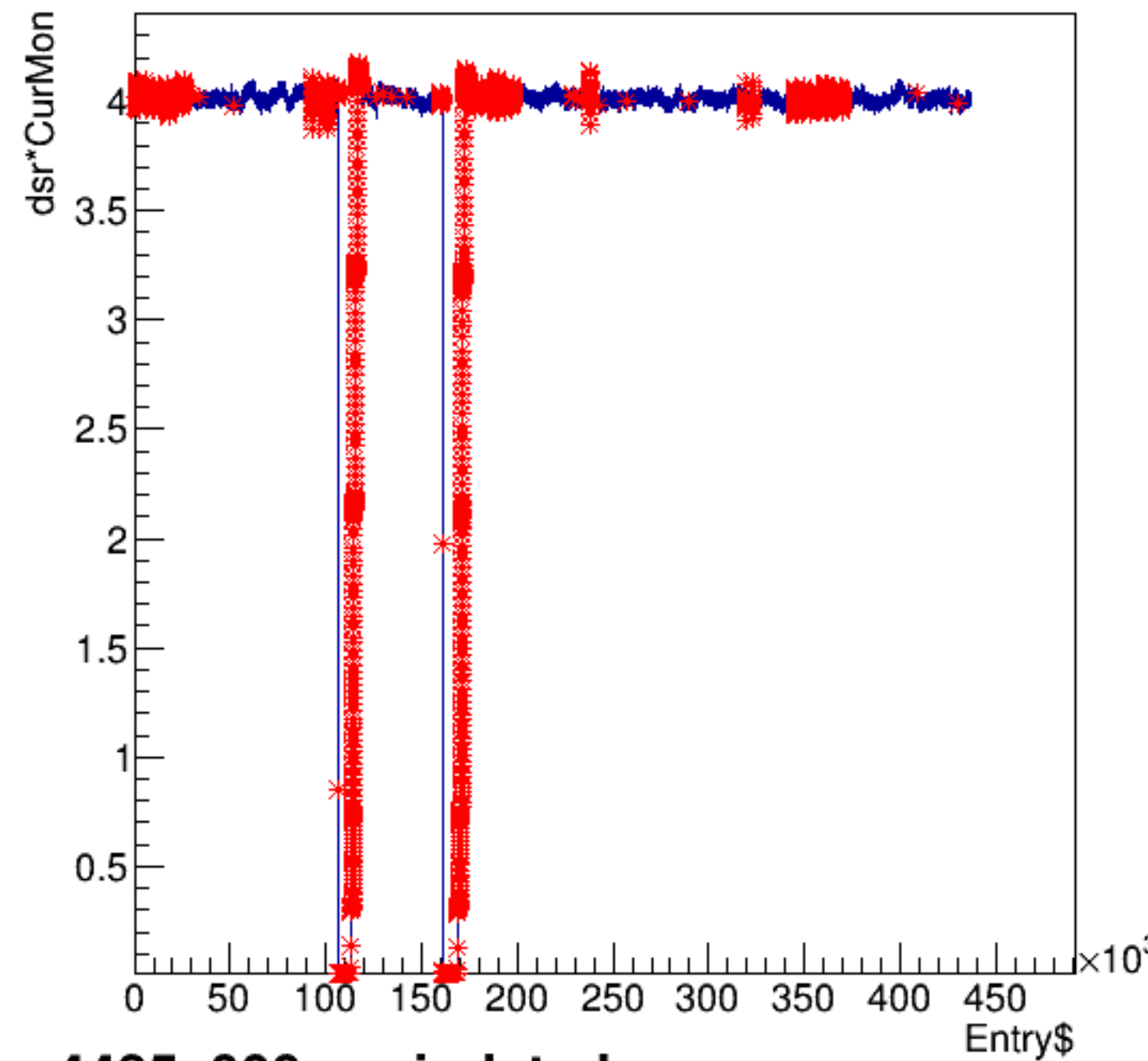
asym_usr/ppm:pattern_number {ErrorFlag==0}



asym_usr

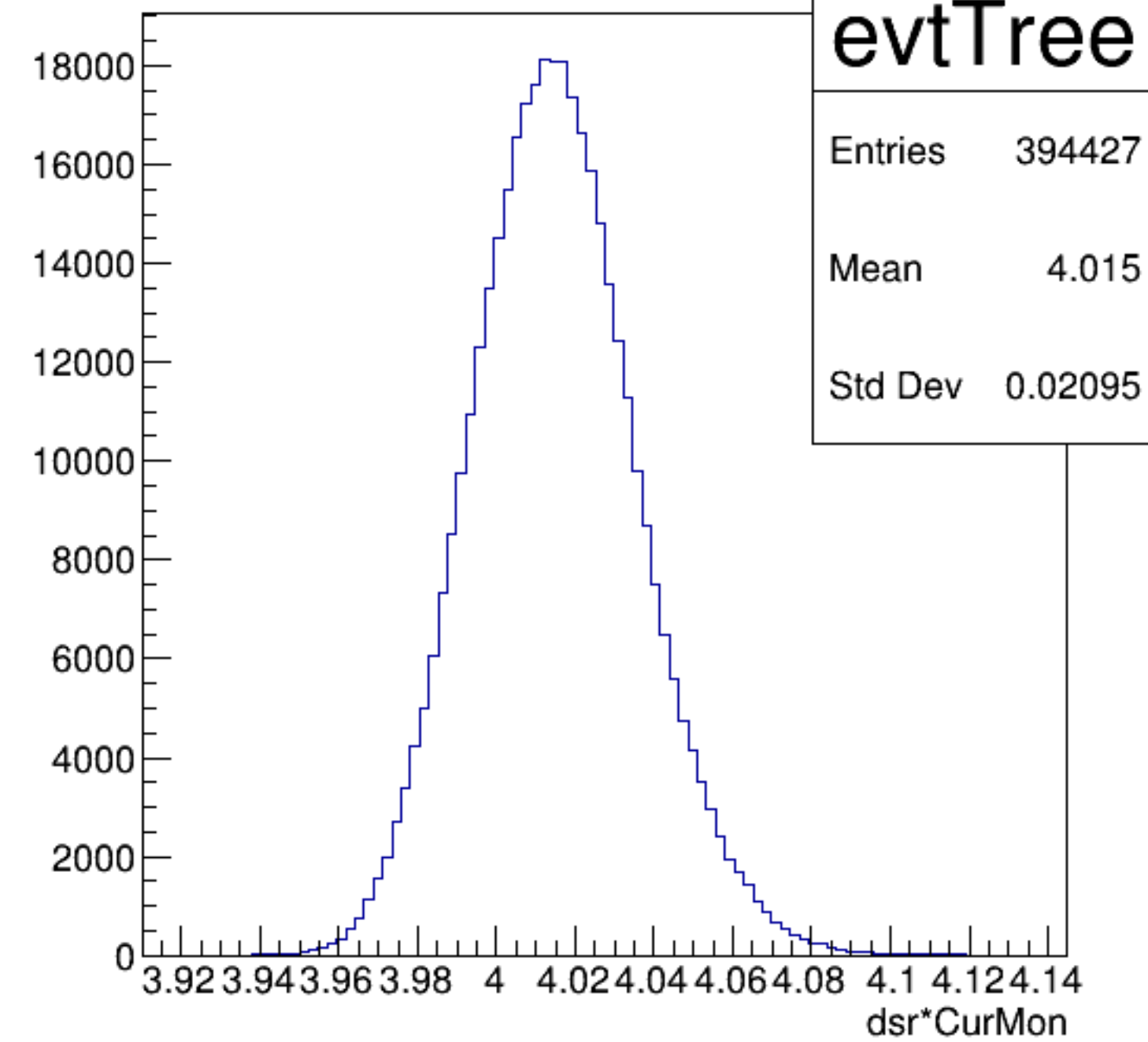


dsr*CurMon:Entry\$



run4485_000_maindet_dsr.png

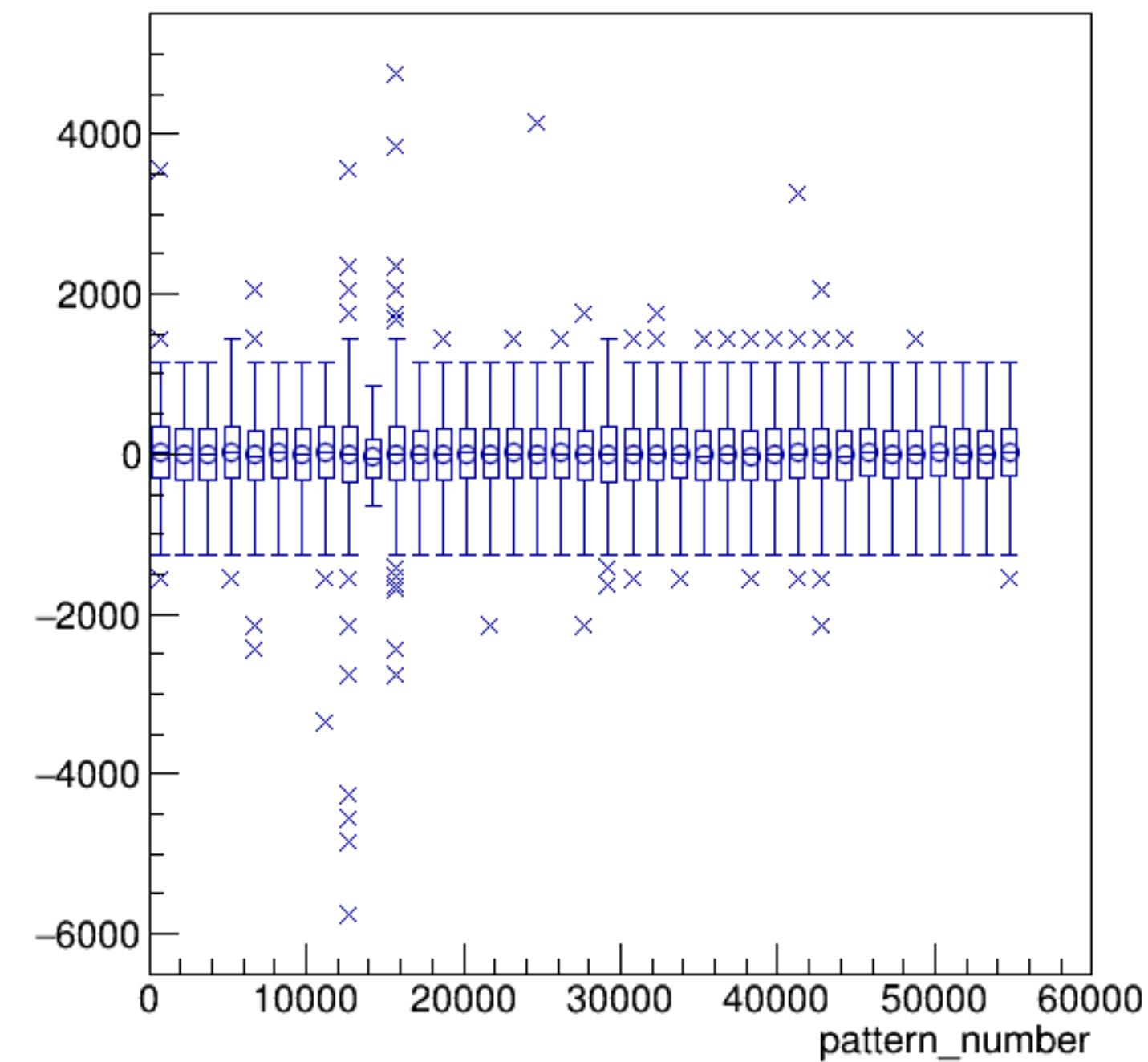
dsr*CurMon {ErrorFlag==0}



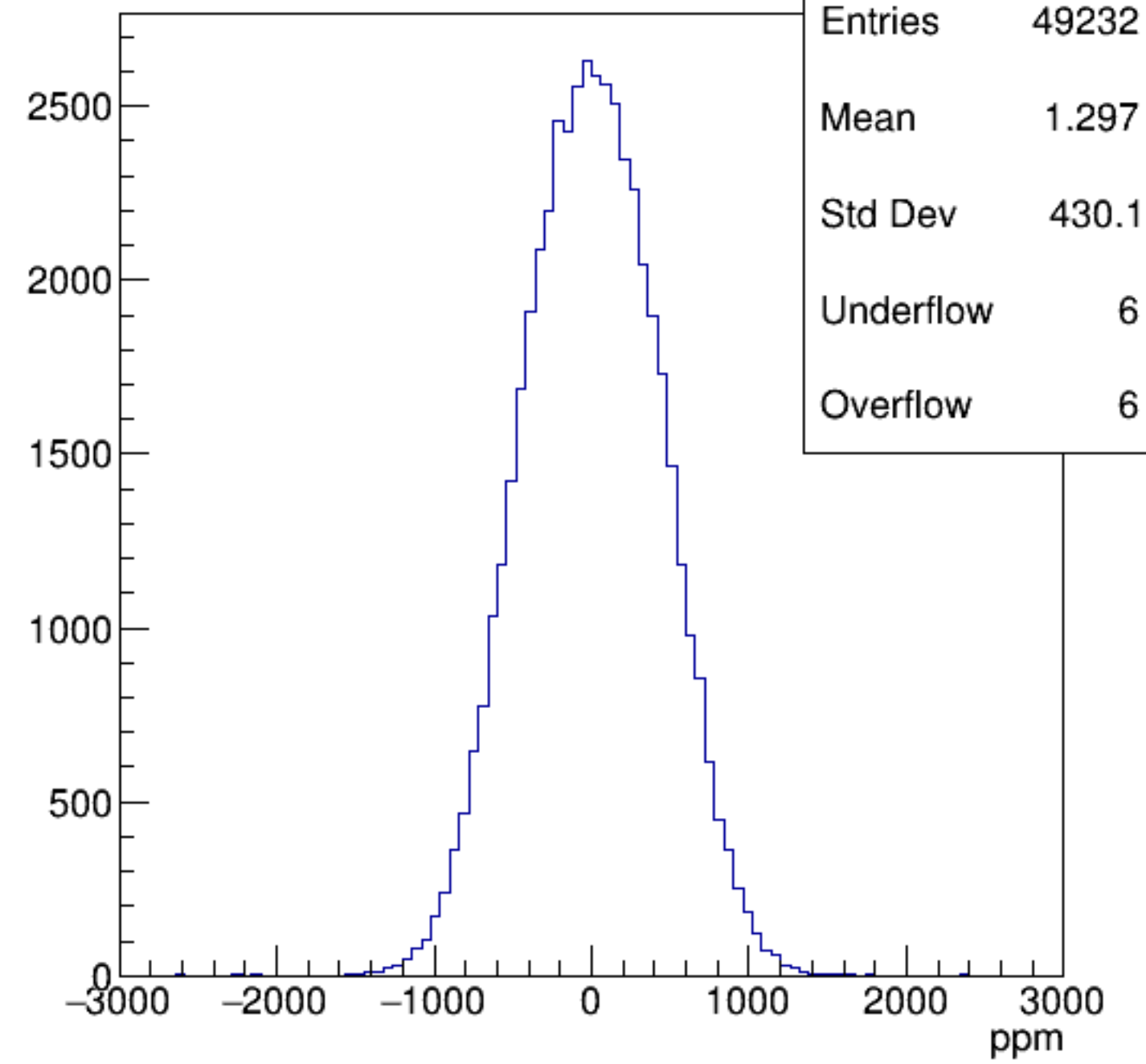
evtTree

Entries	394427
Mean	4.015
Std Dev	0.02095

asym_dsr/ppm:pattern_number {ErrorFlag==0}

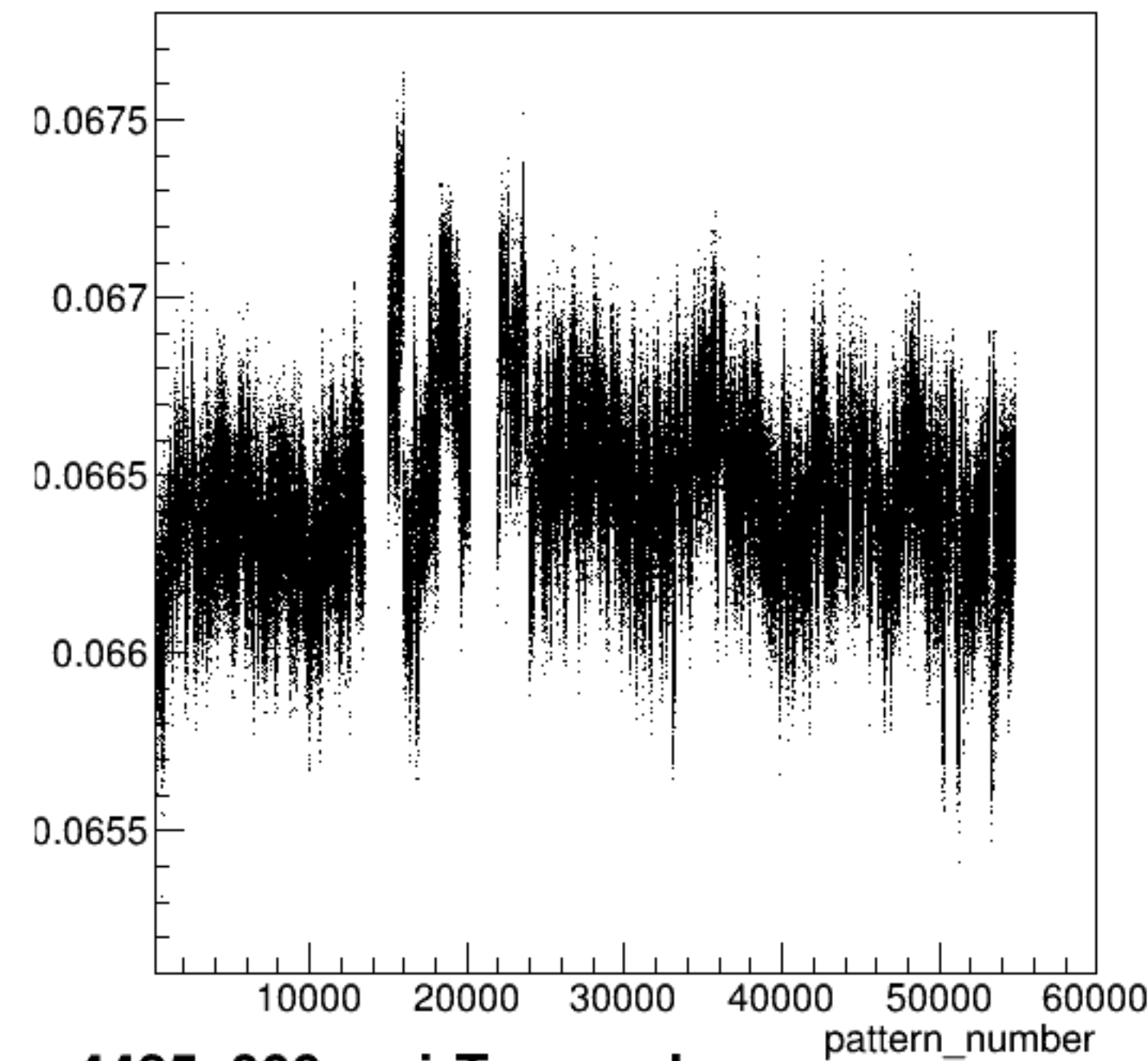


asym_dsr



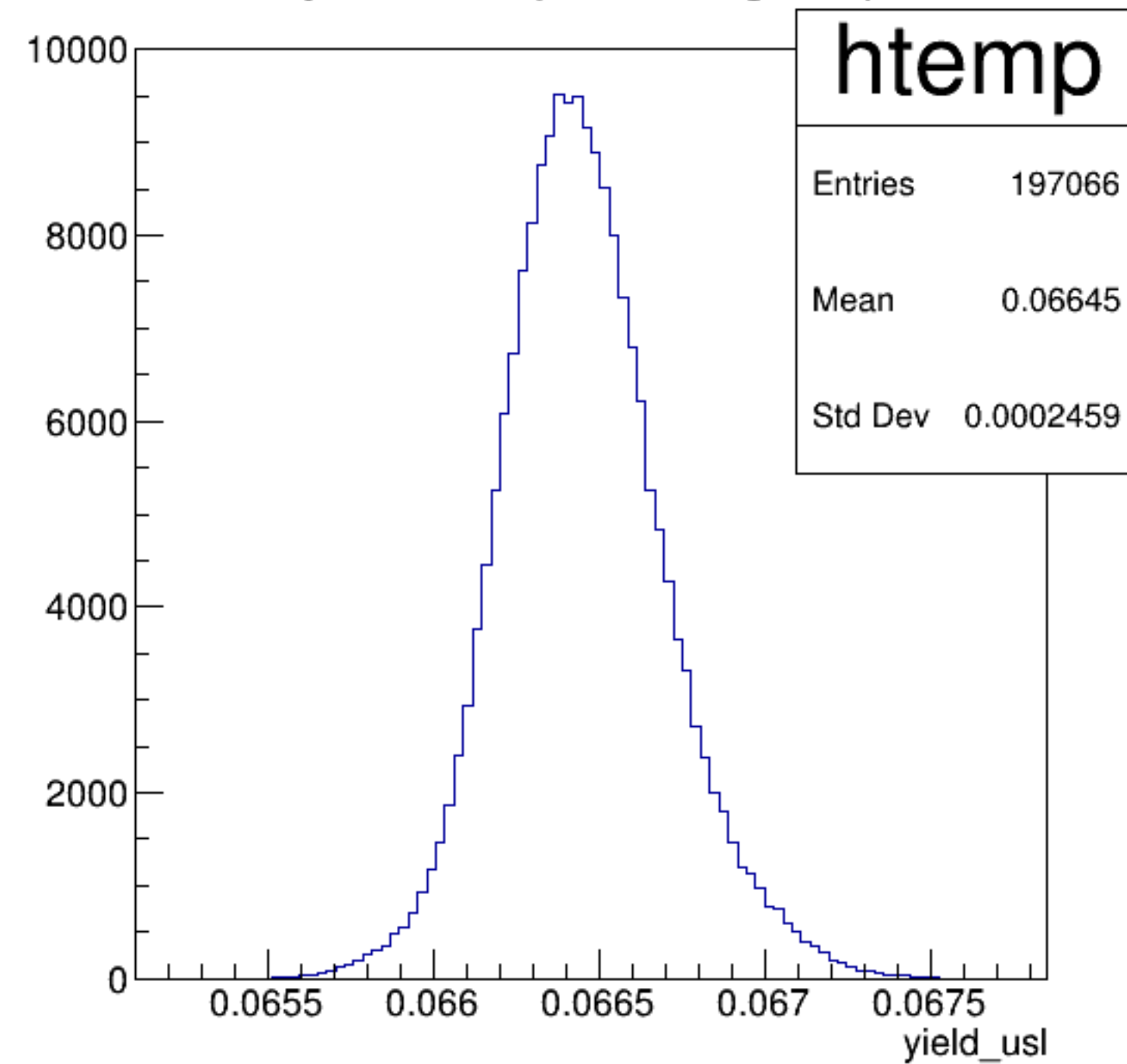
Entries	49232
Mean	1.297
Std Dev	430.1
Underflow	6
Overflow	6

yield_usl:pattern_number {ErrorFlag==0}

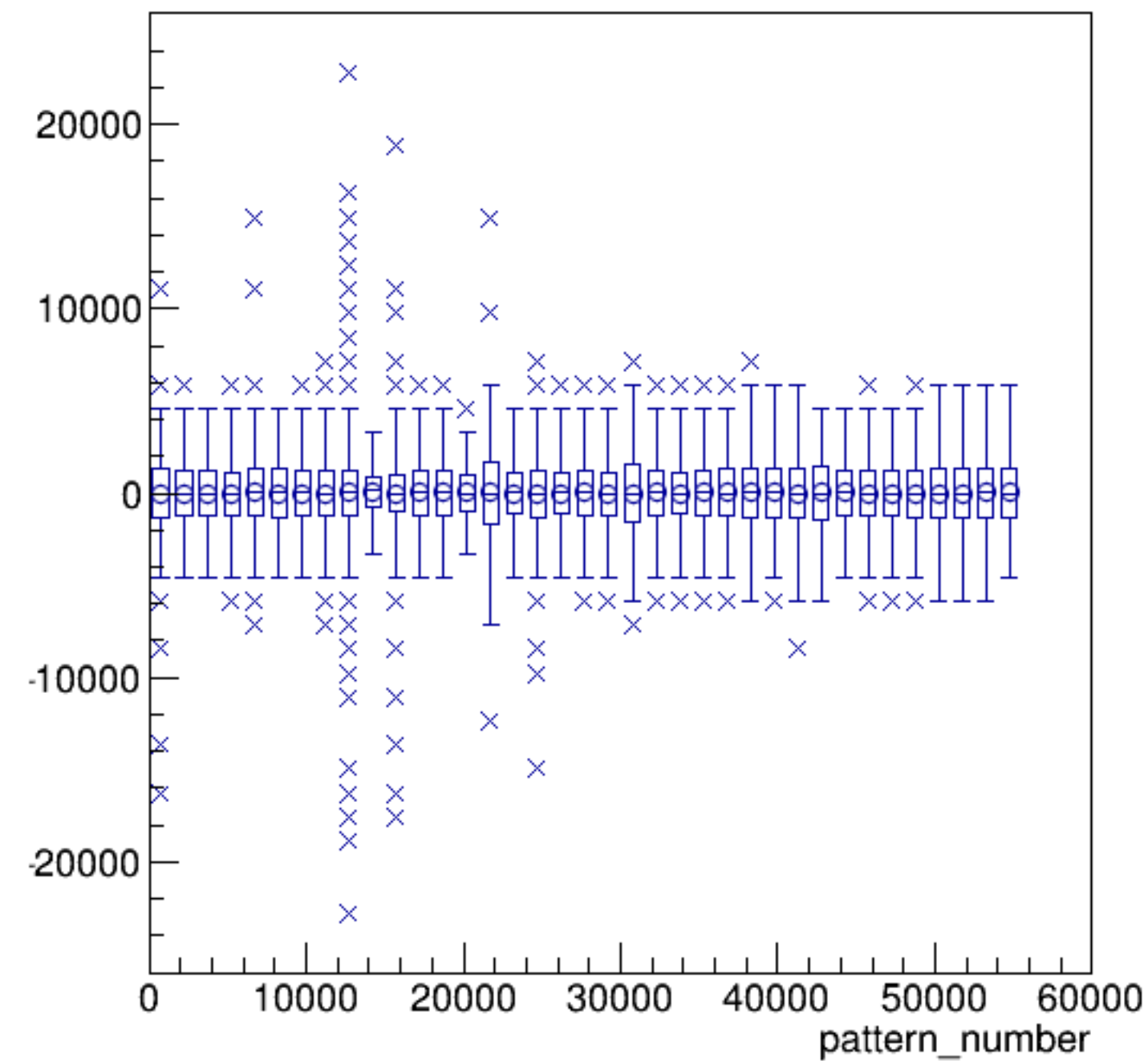


run4485_000_pairTree_usl.png

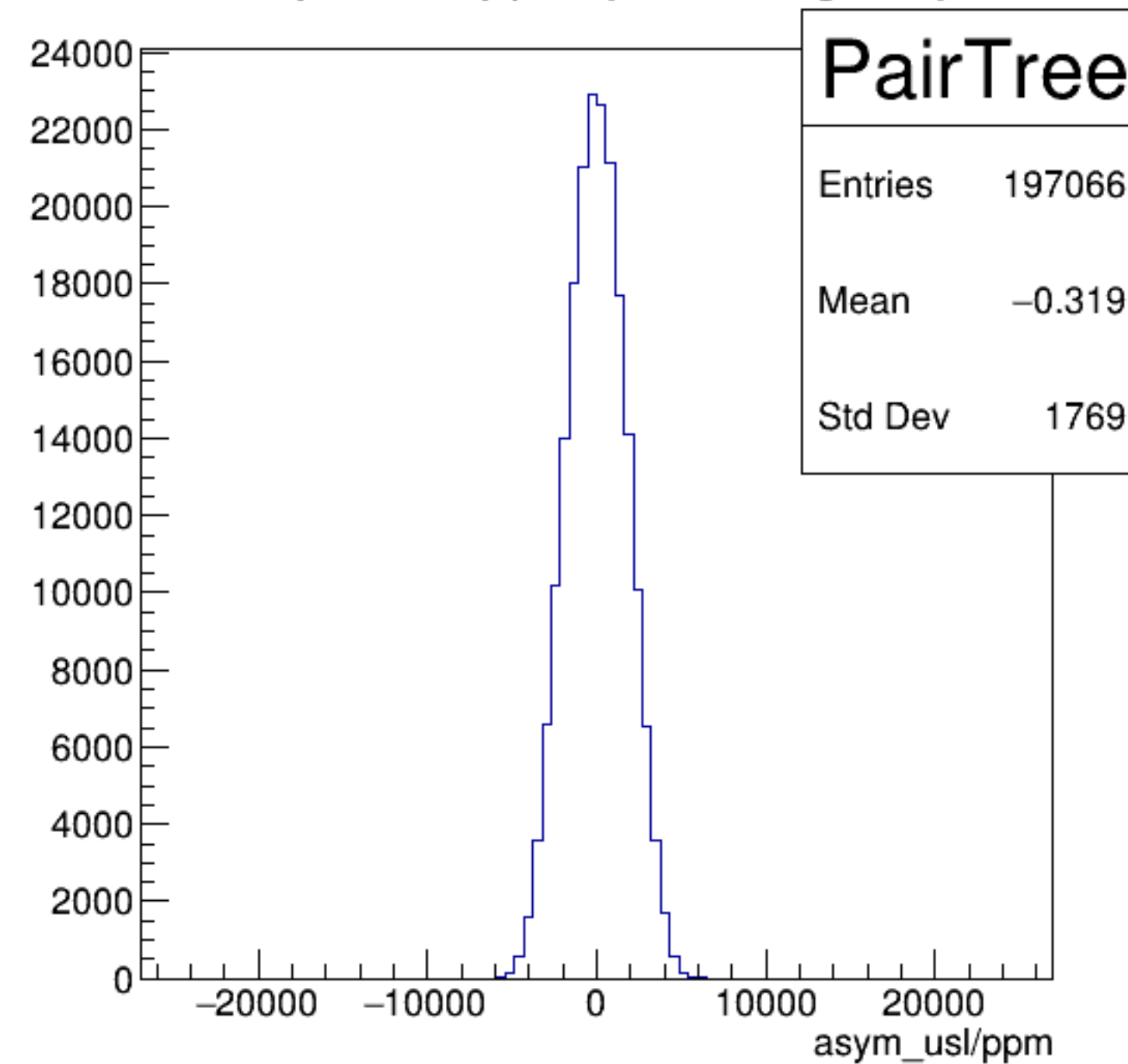
yield_usl {ErrorFlag==0}



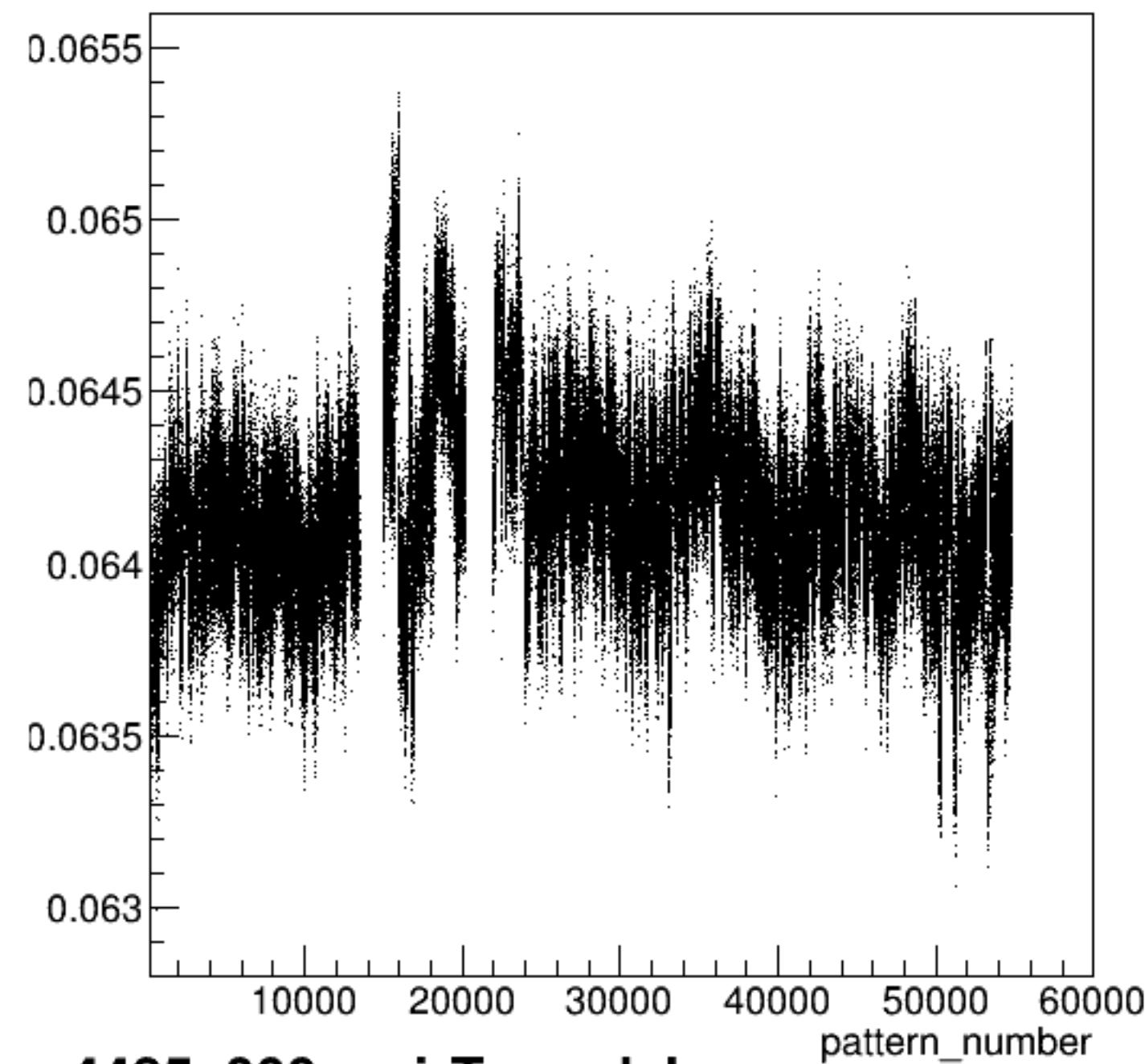
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl/ppm {ErrorFlag==0}

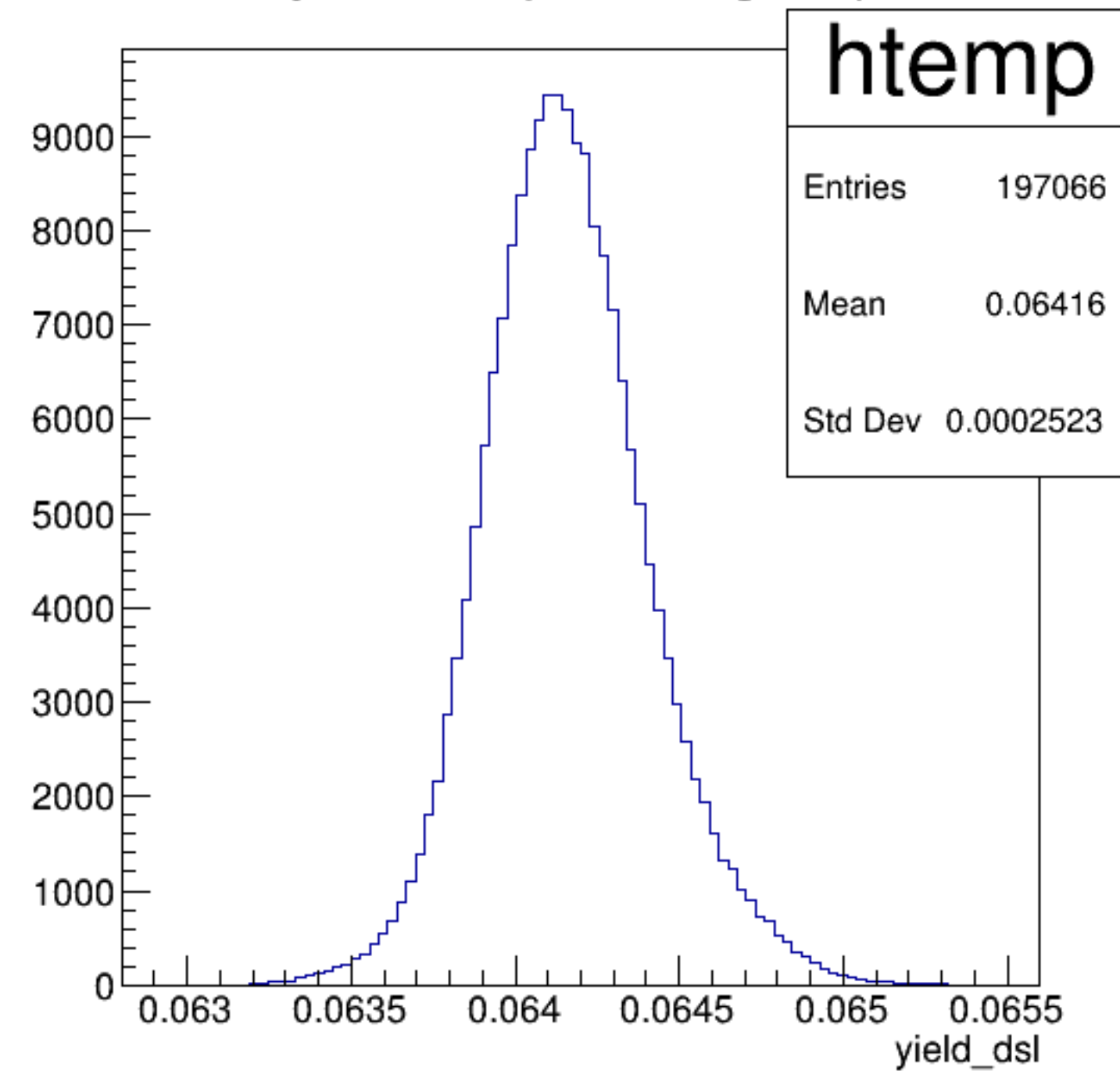


yield_dsl:pattern_number {ErrorFlag==0}

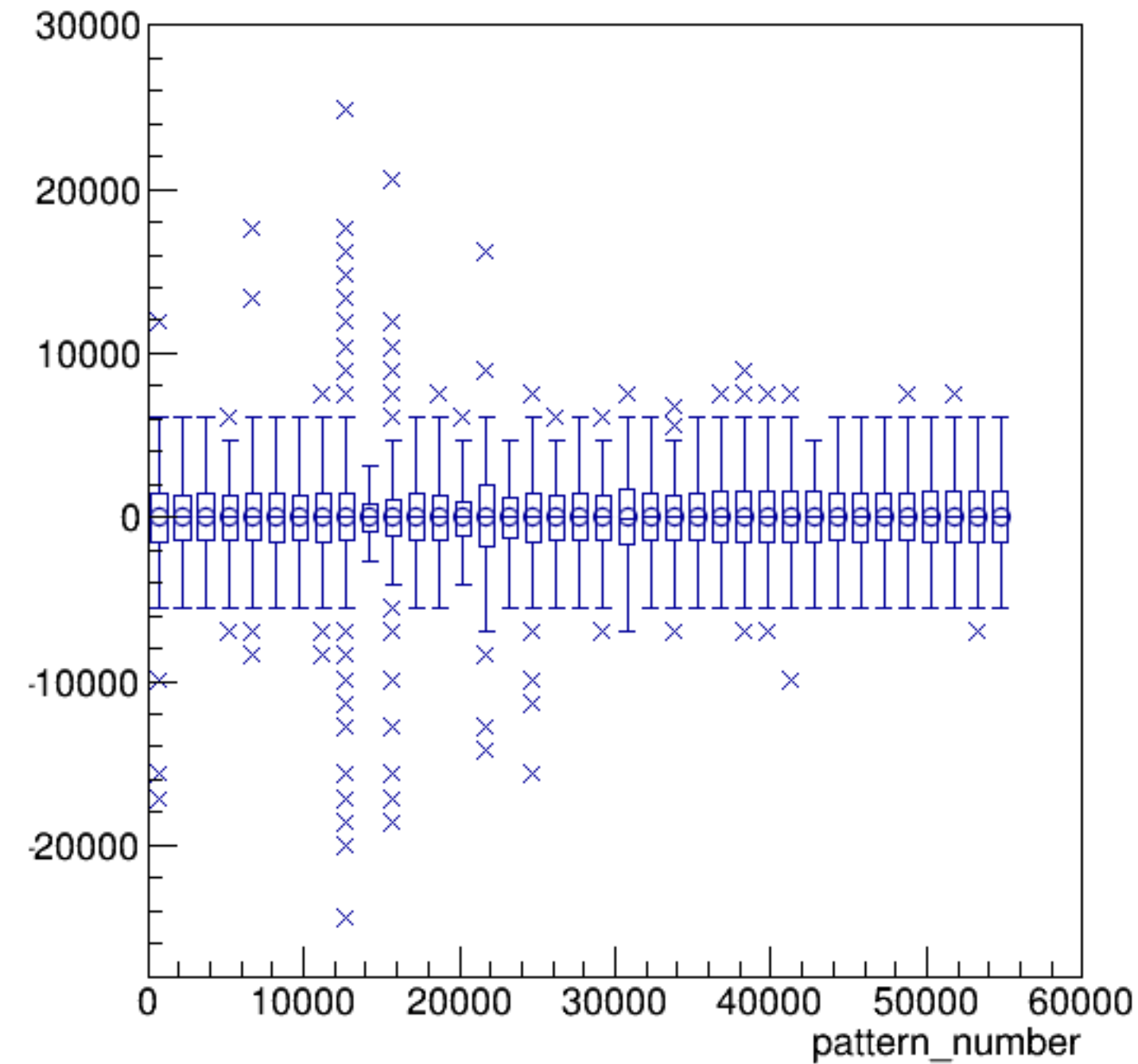


run4485_000_pairTree_dsl.png

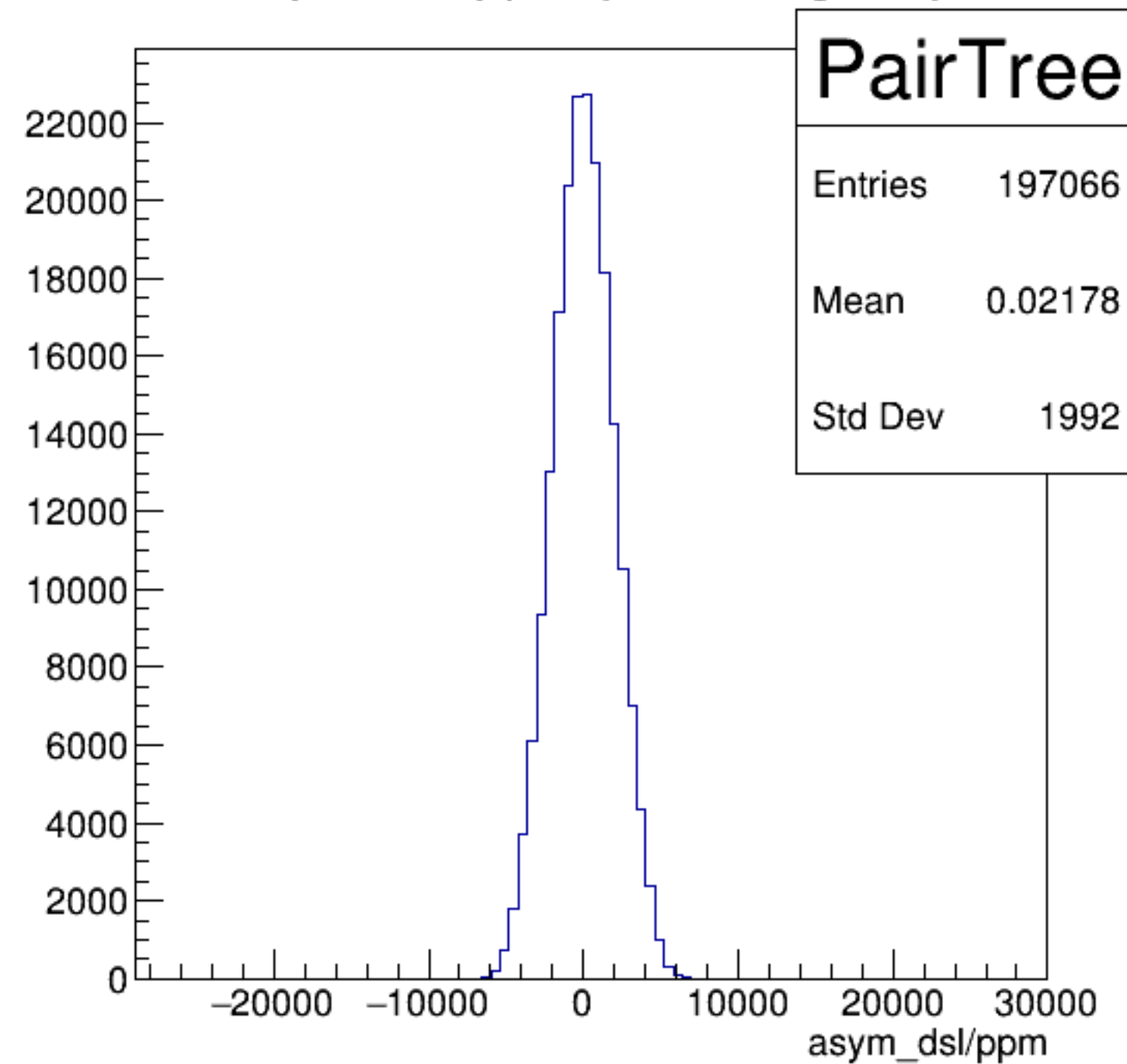
yield_dsl {ErrorFlag==0}

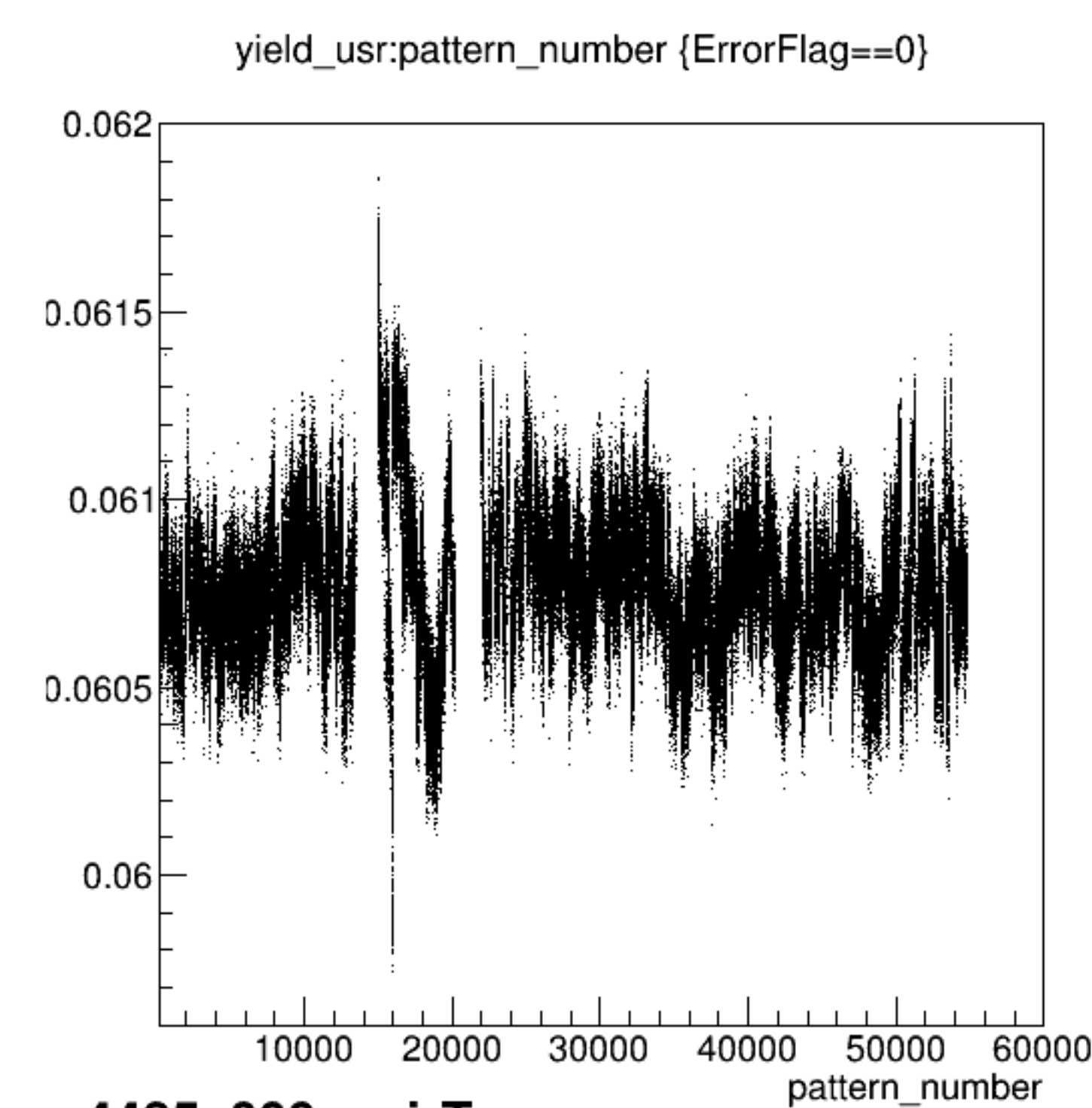


asym_dsl/ppm:pattern_number {ErrorFlag==0}

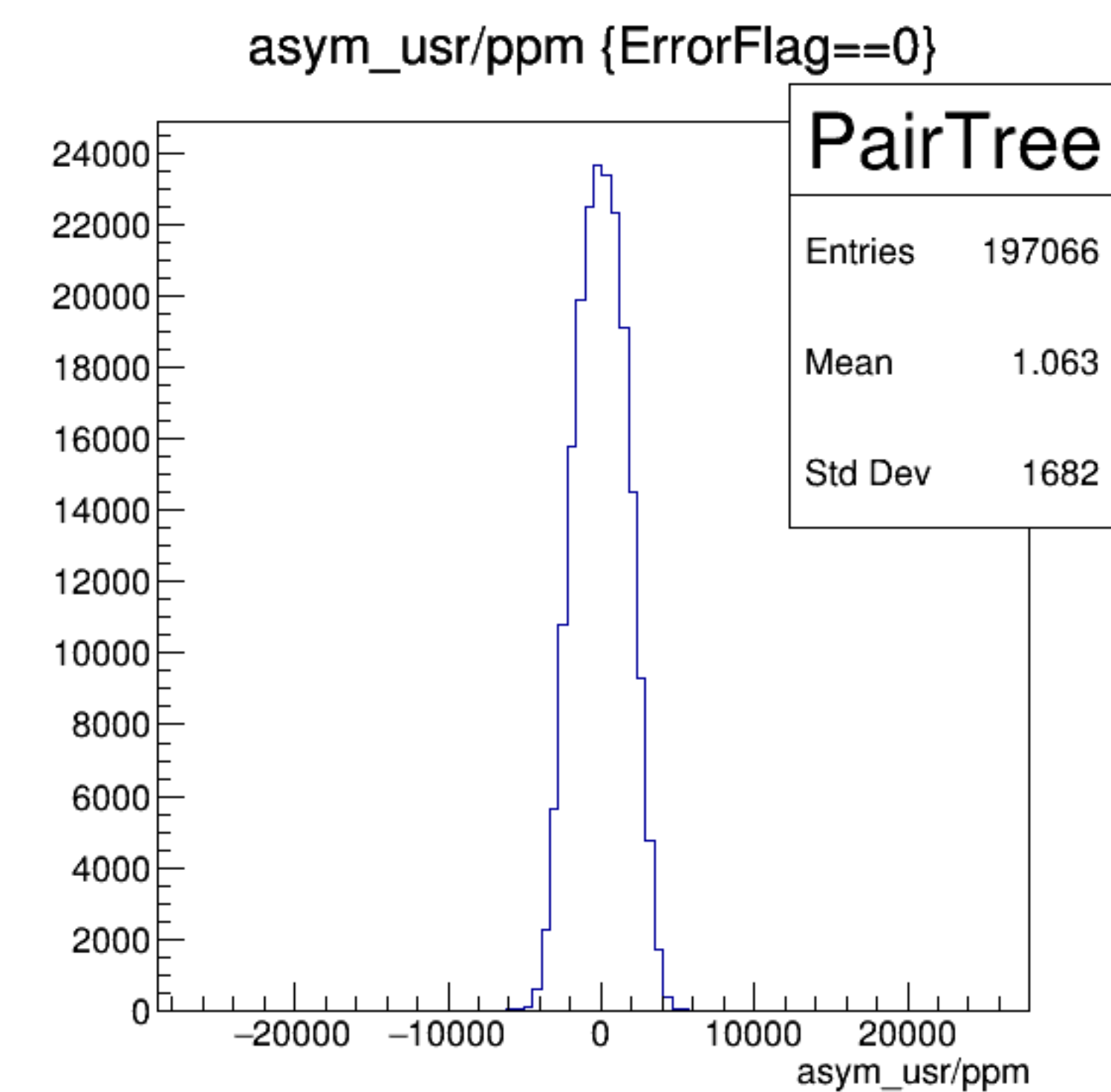
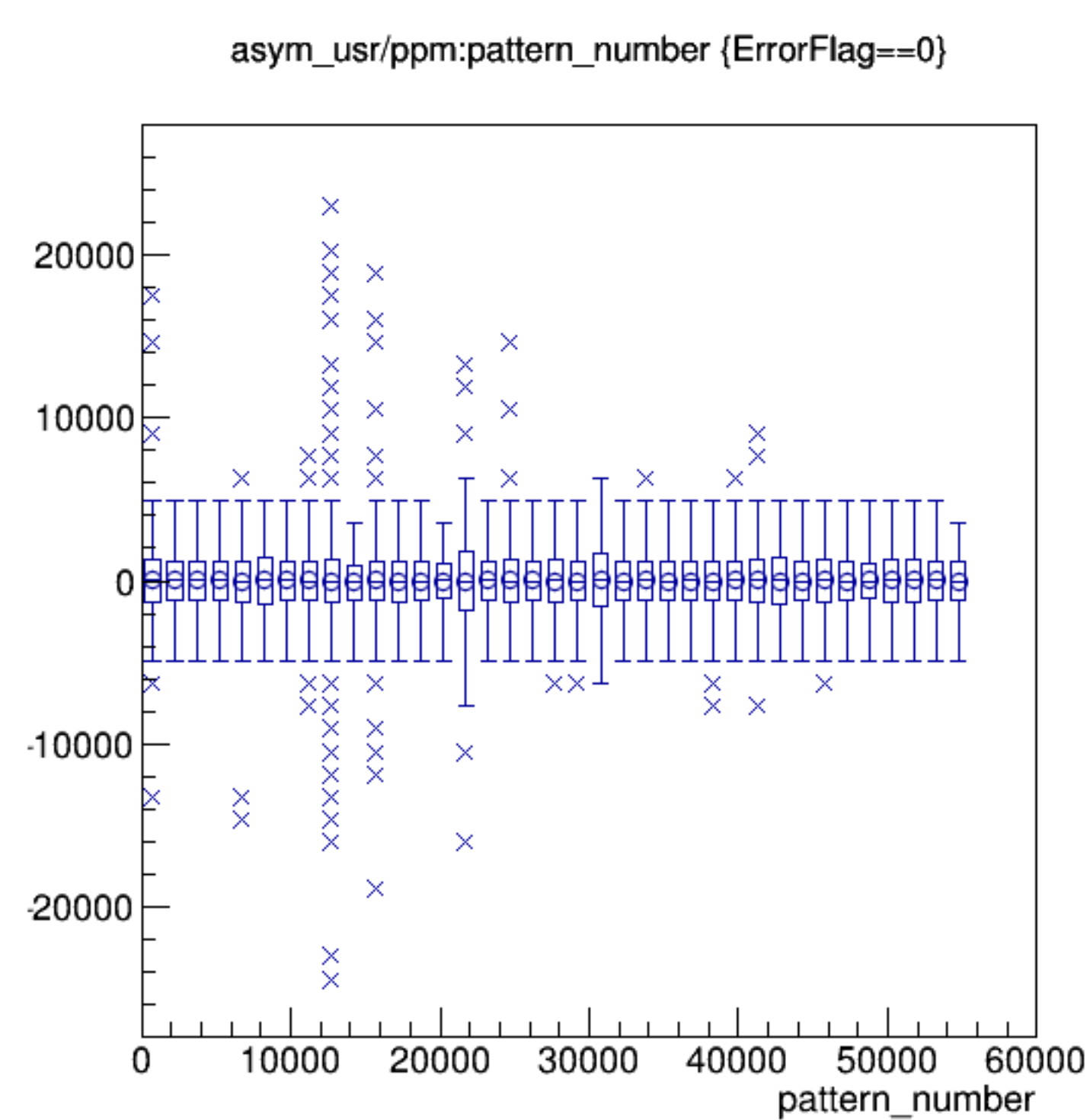
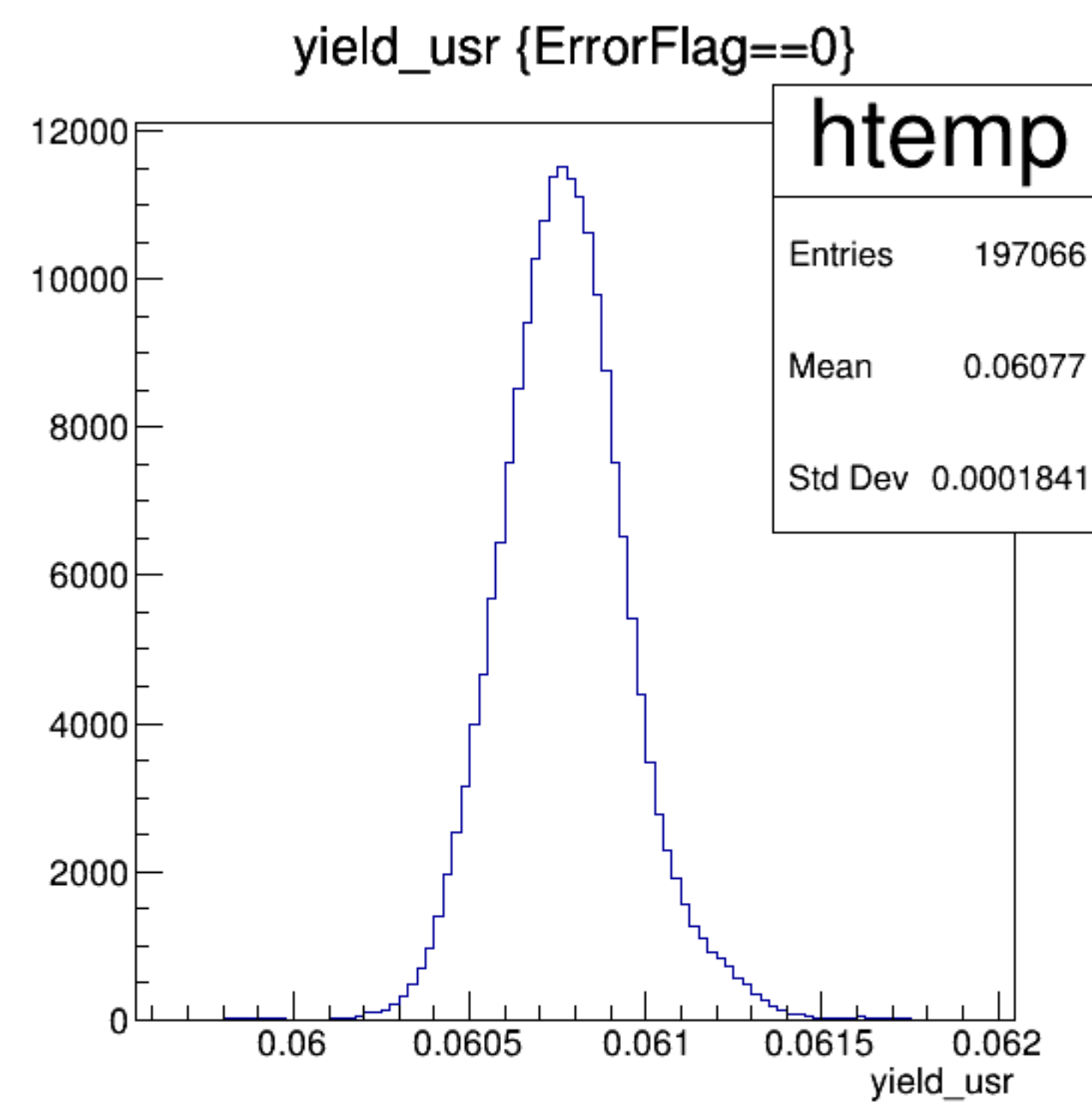


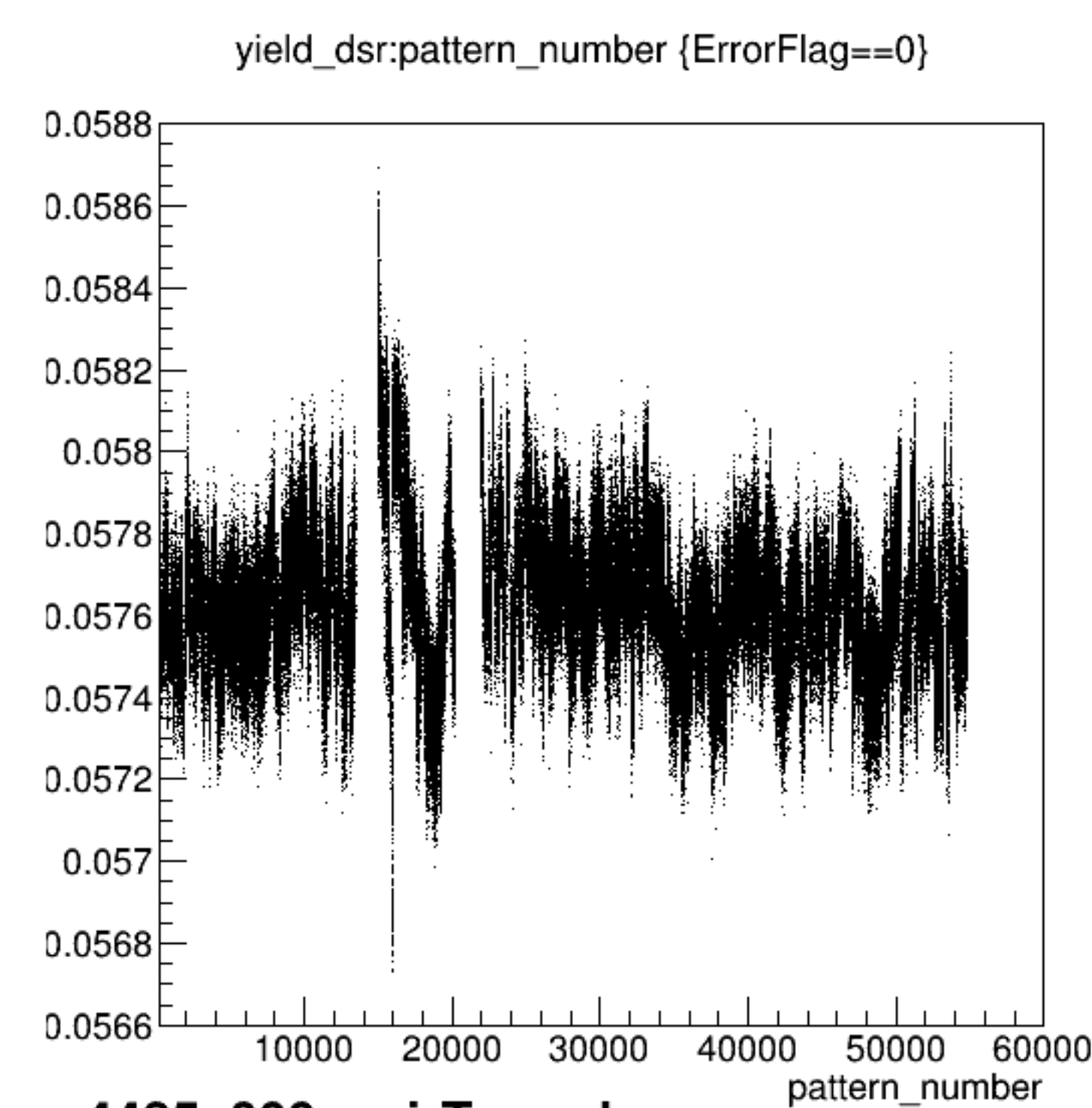
asym_dsl/ppm {ErrorFlag==0}



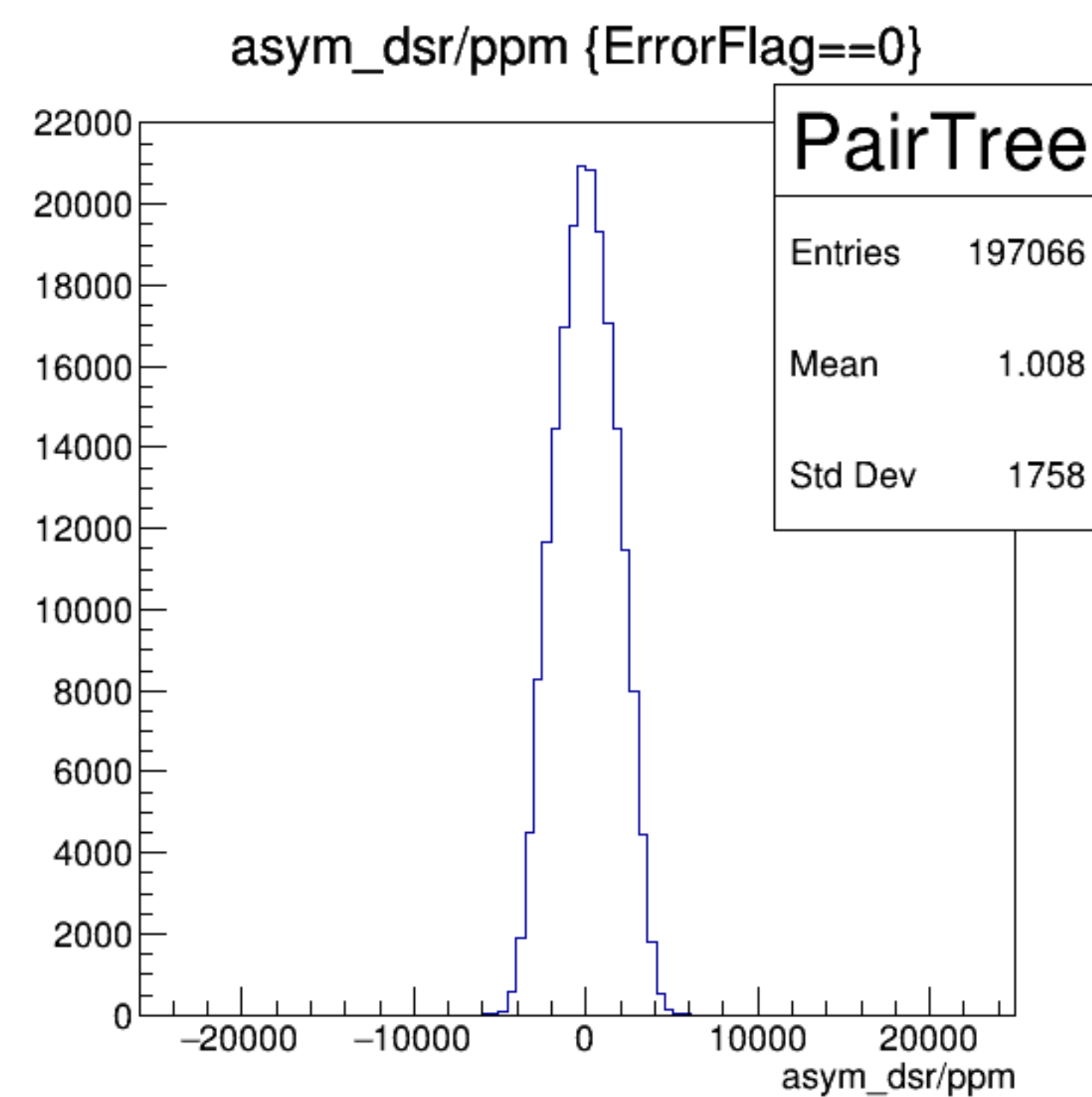
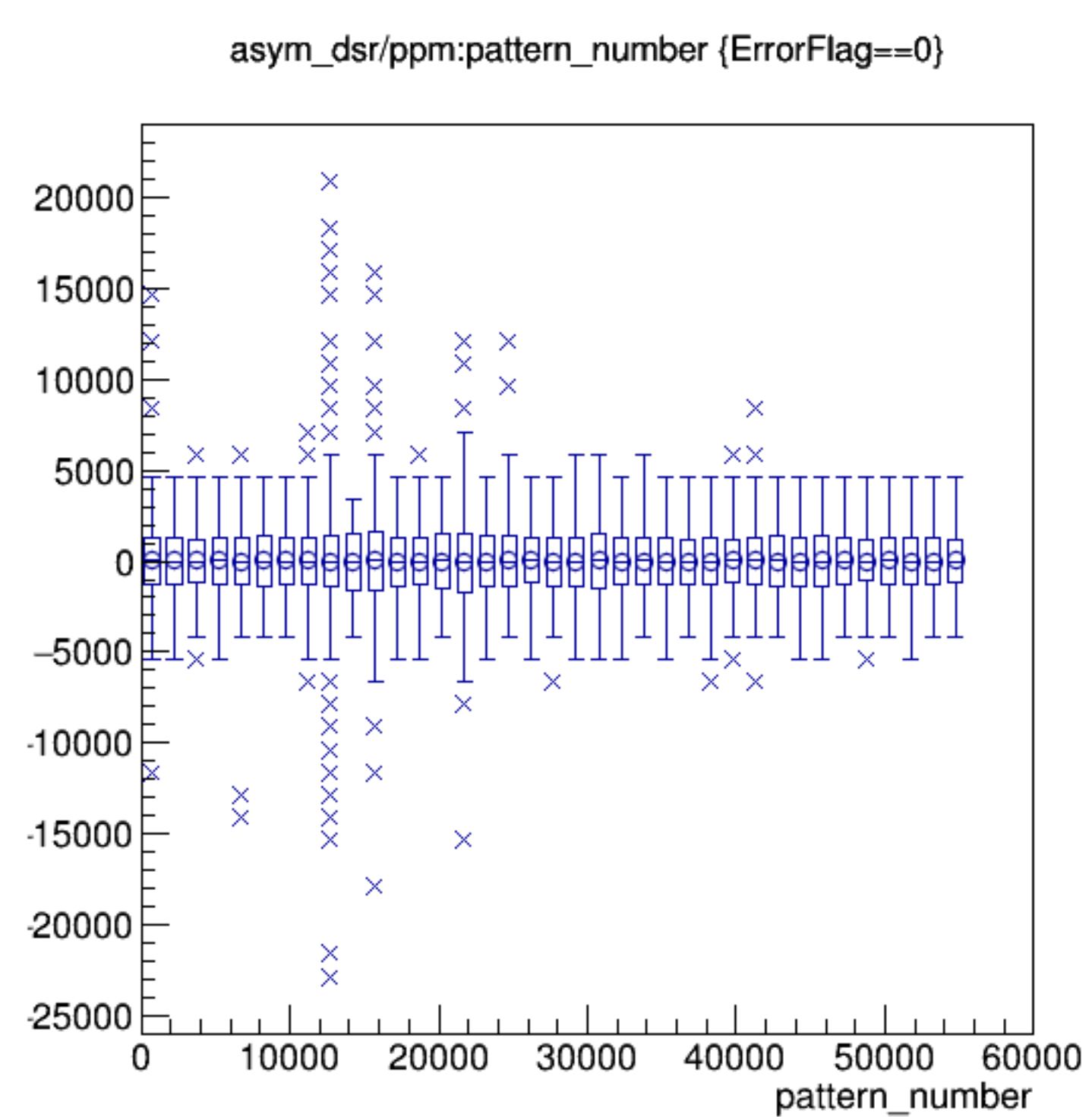
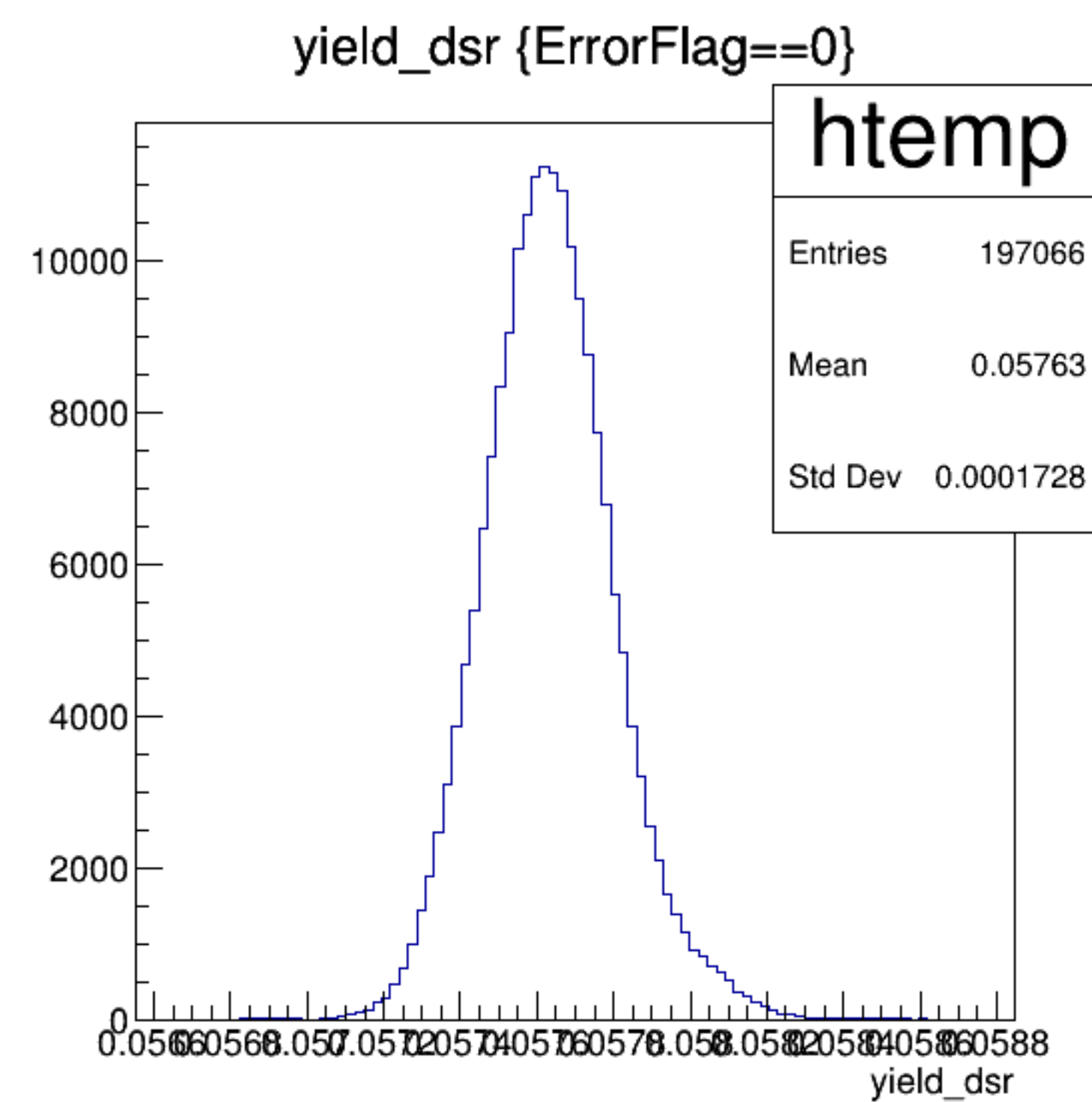


run4485_000_pairTree_usr.png

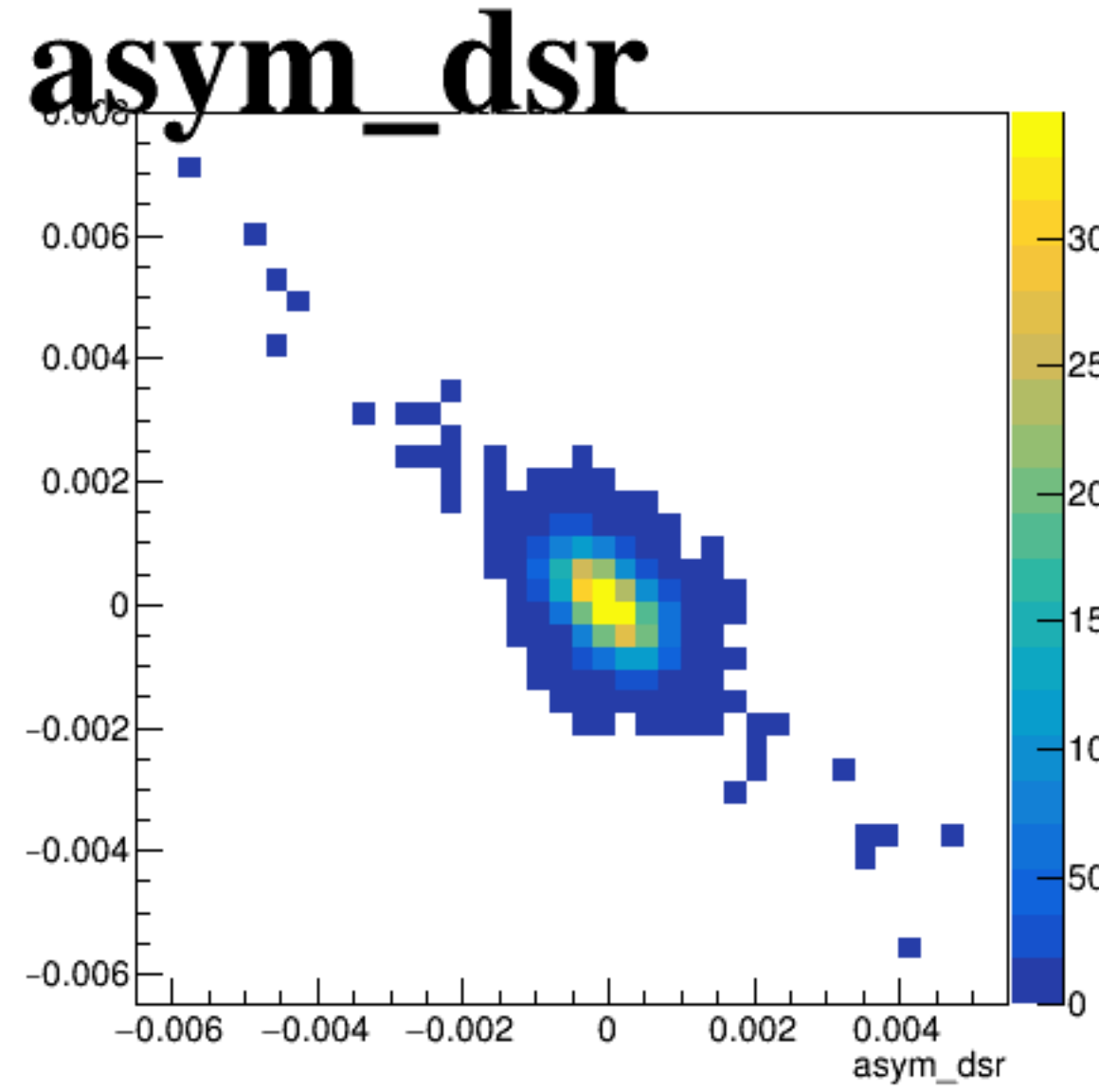
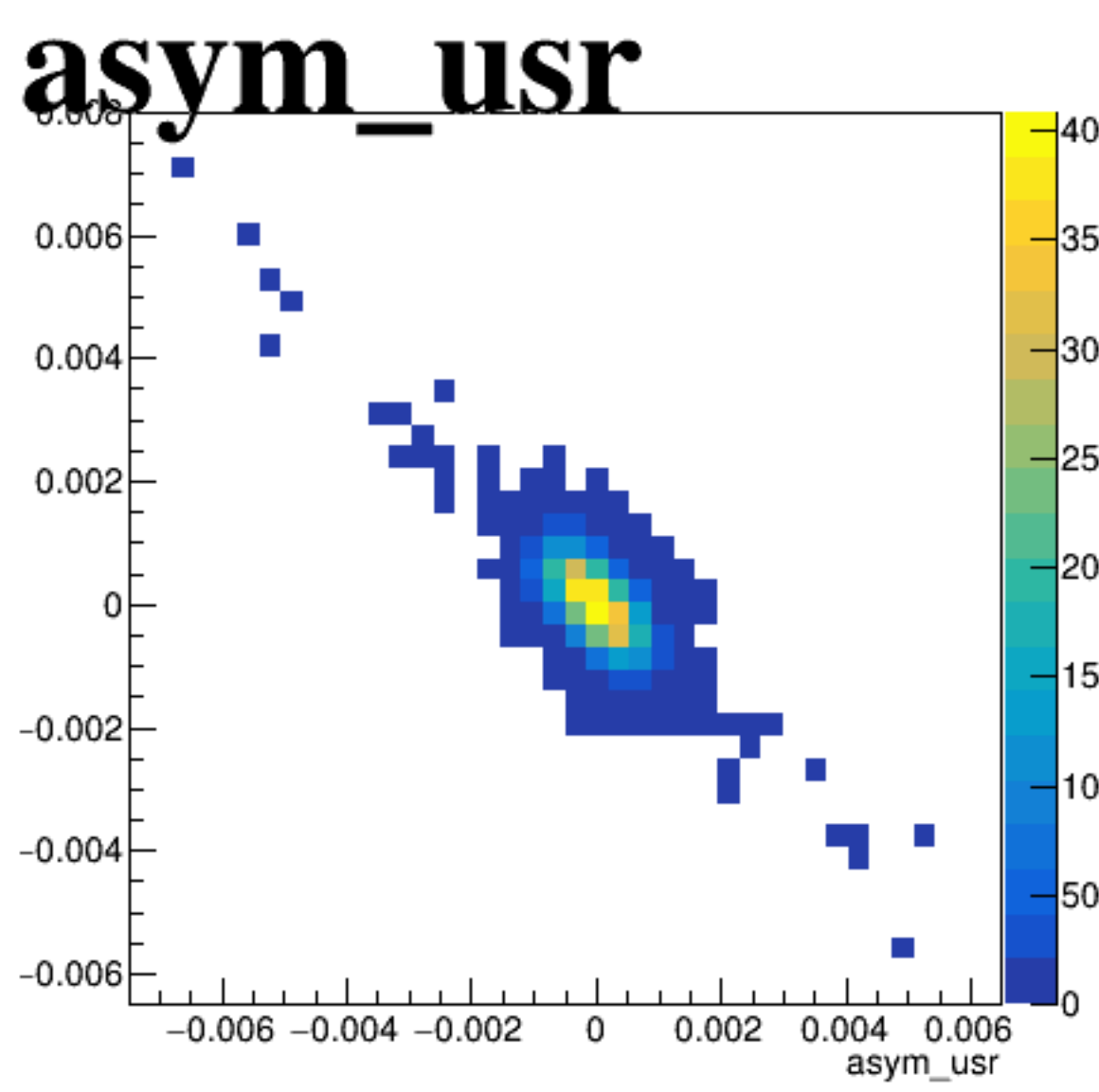
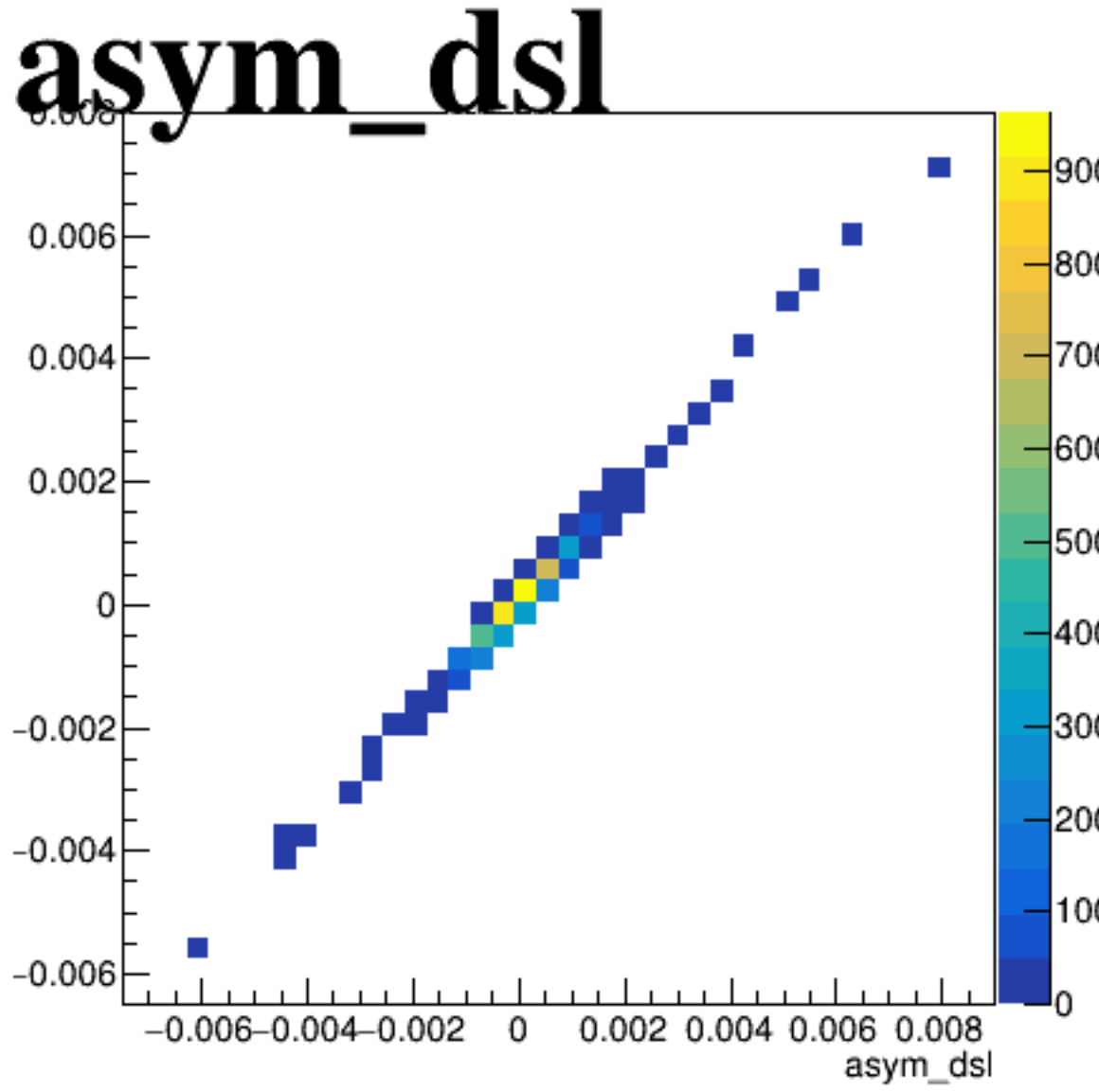
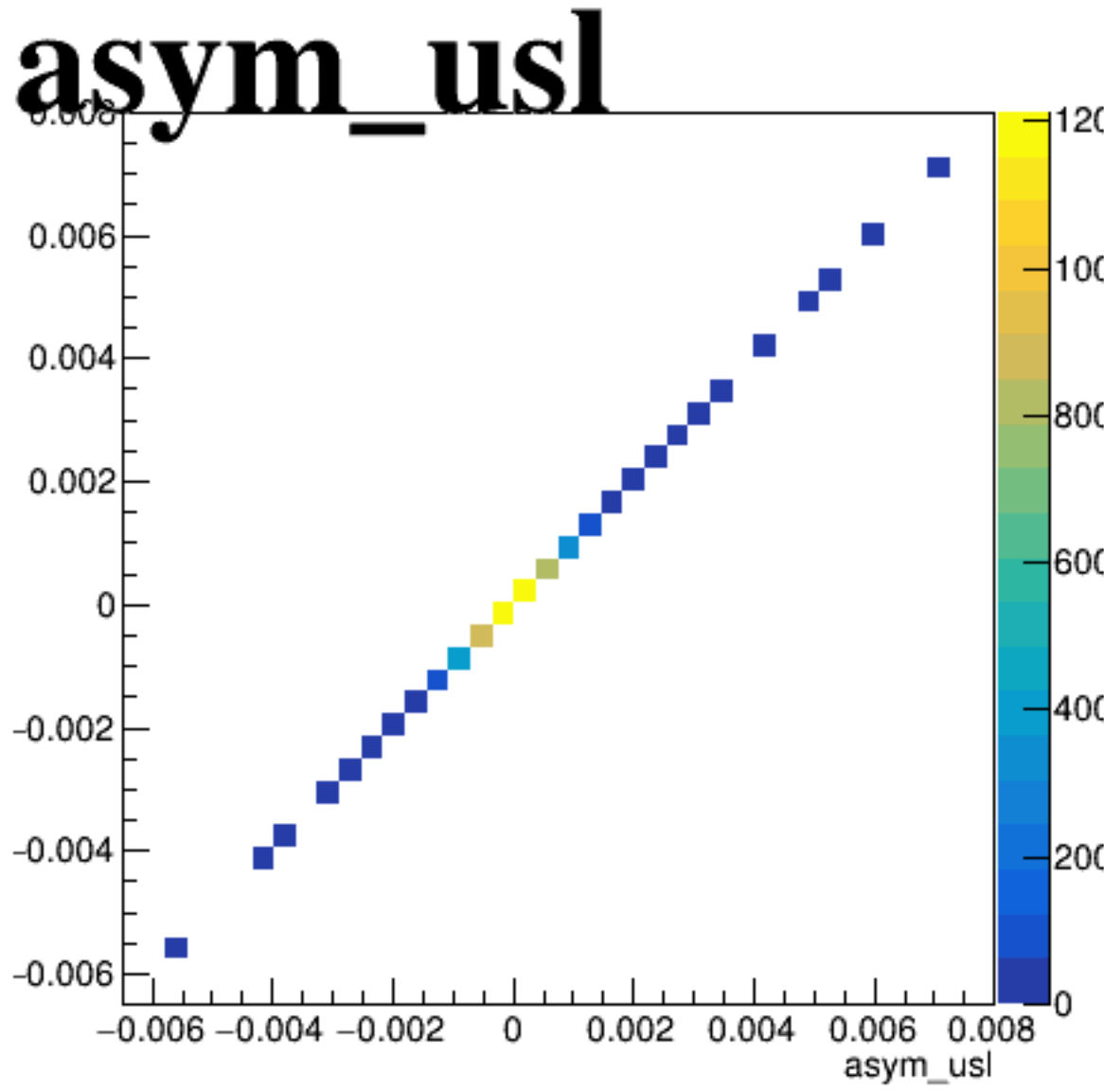




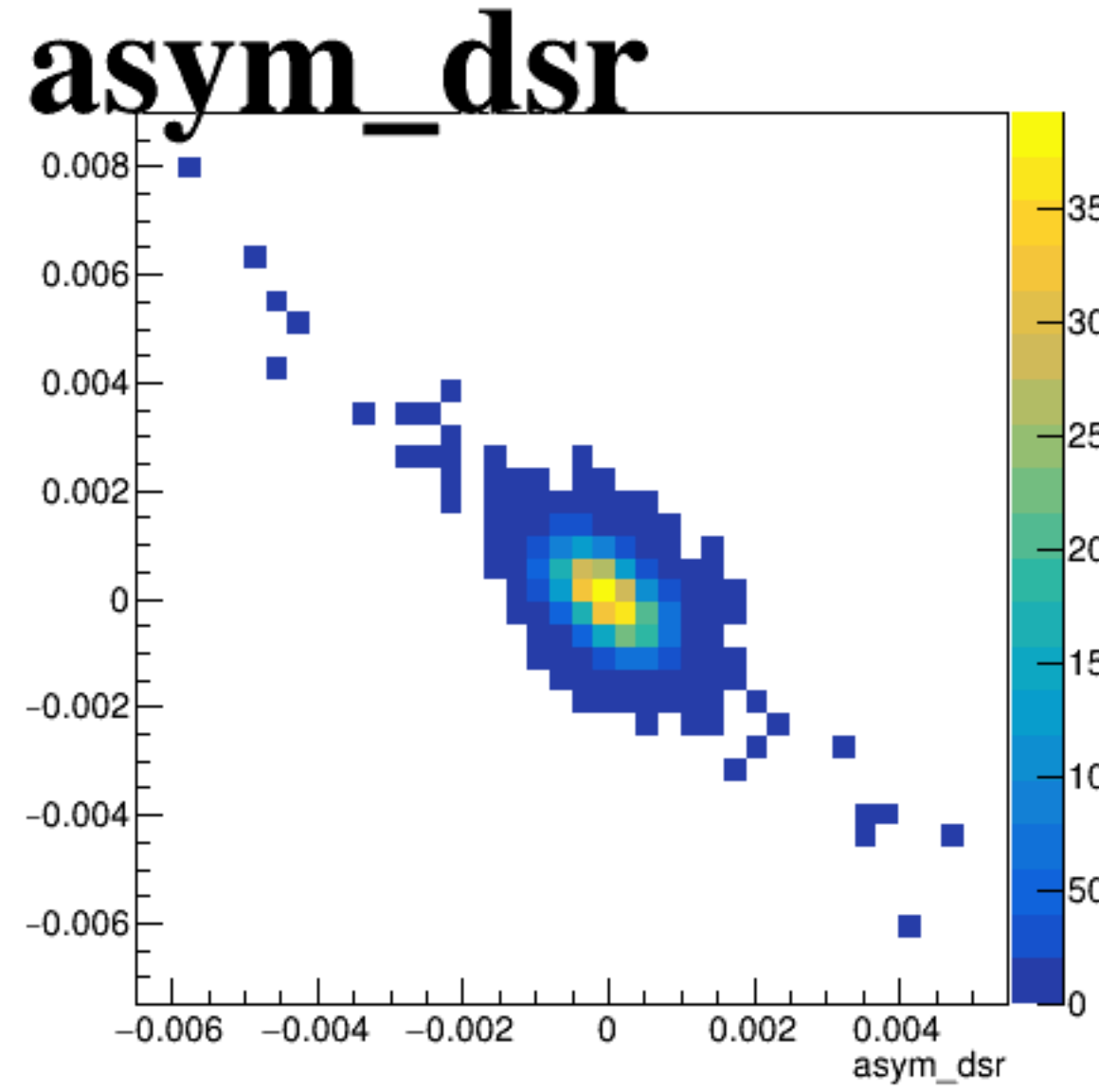
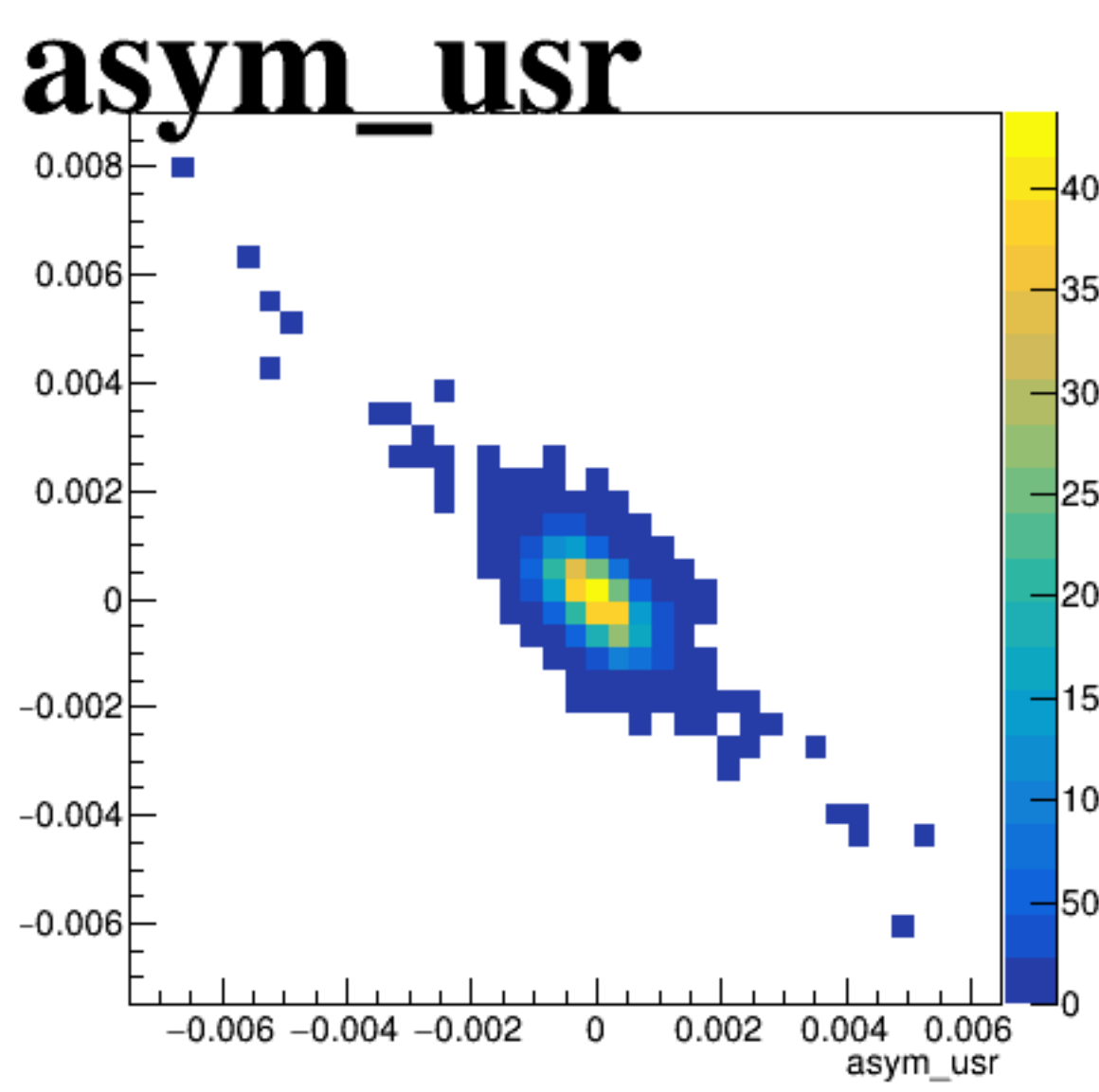
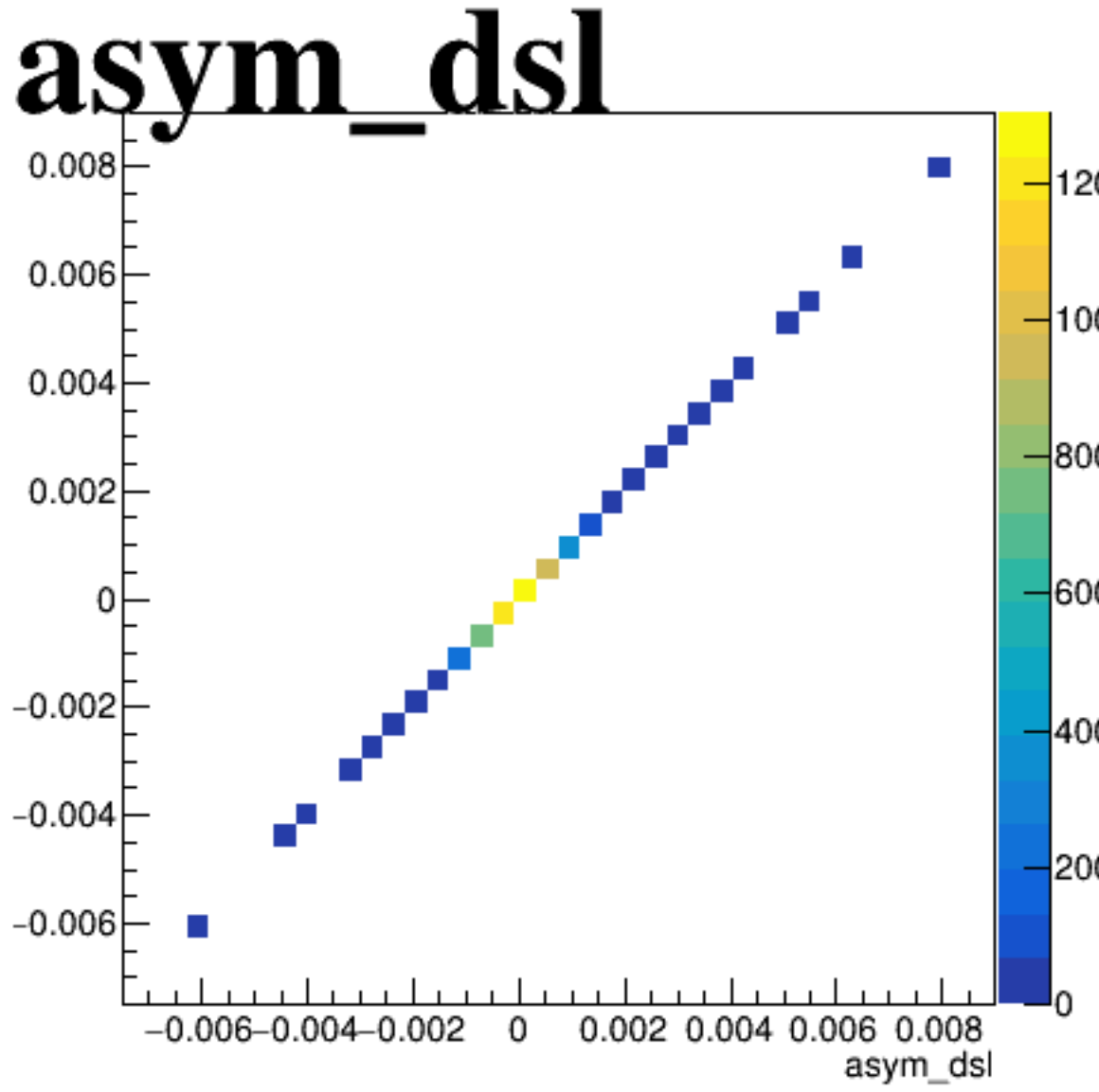
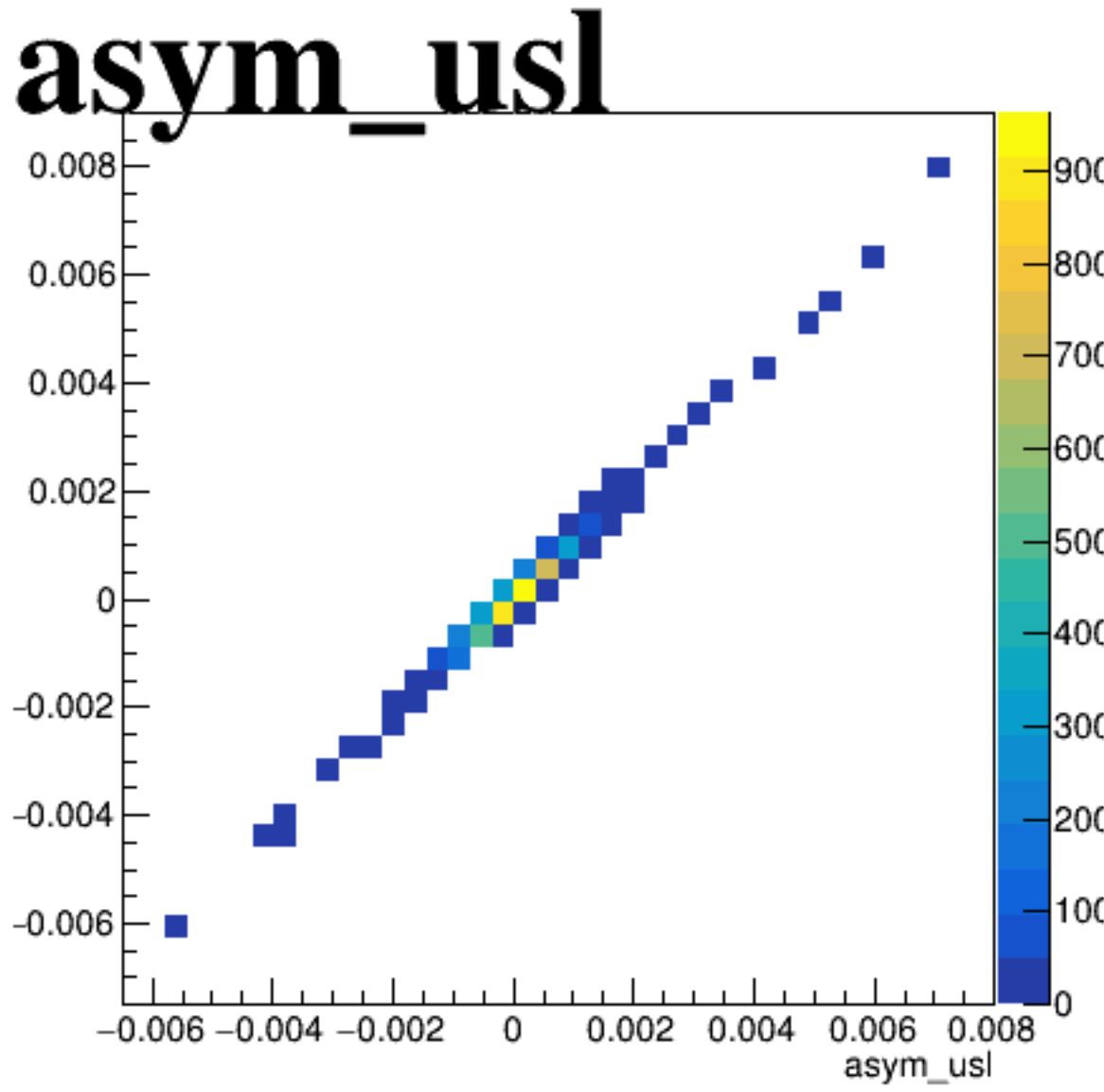
run4485_000_pairTree_dsr.png



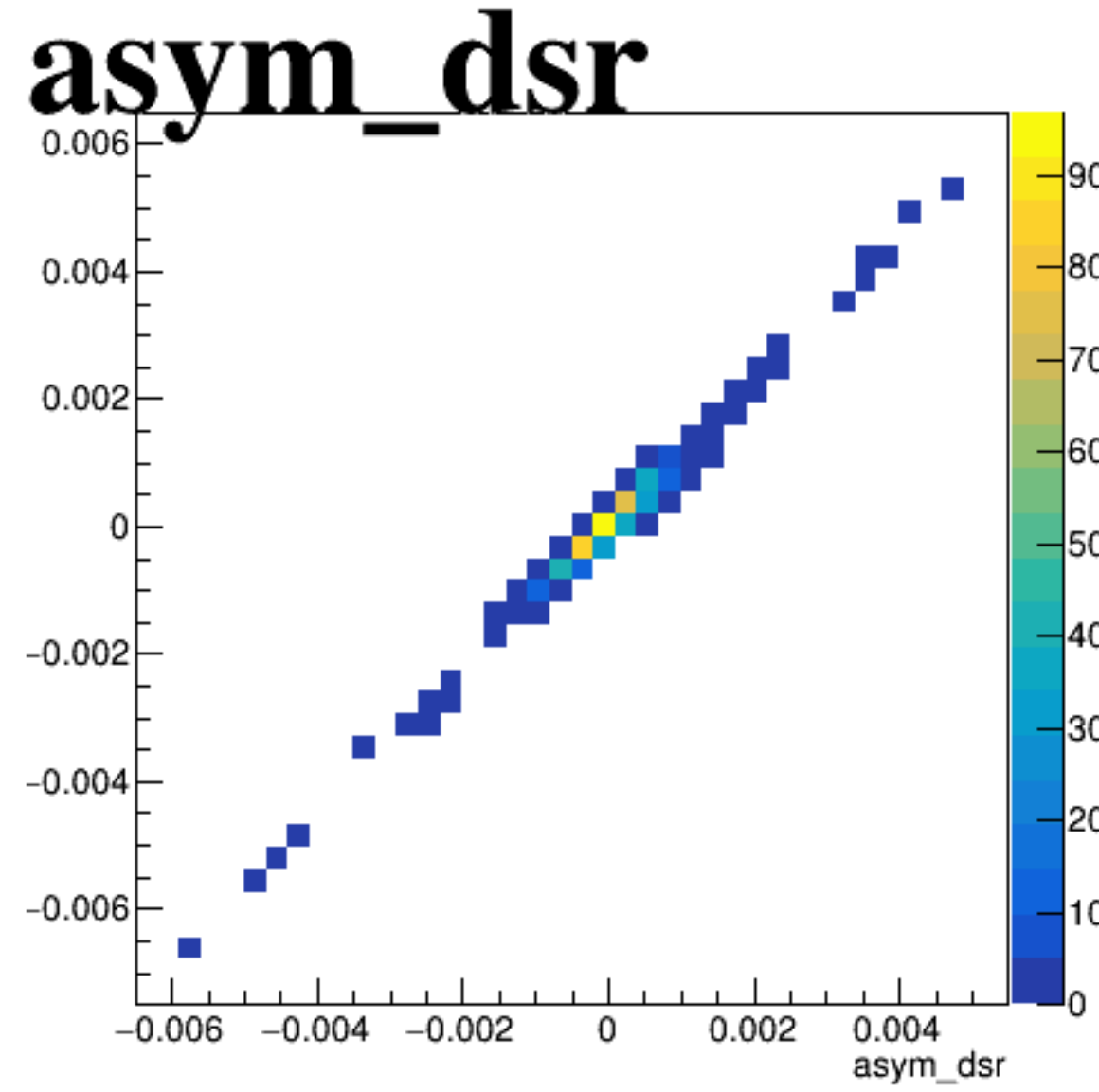
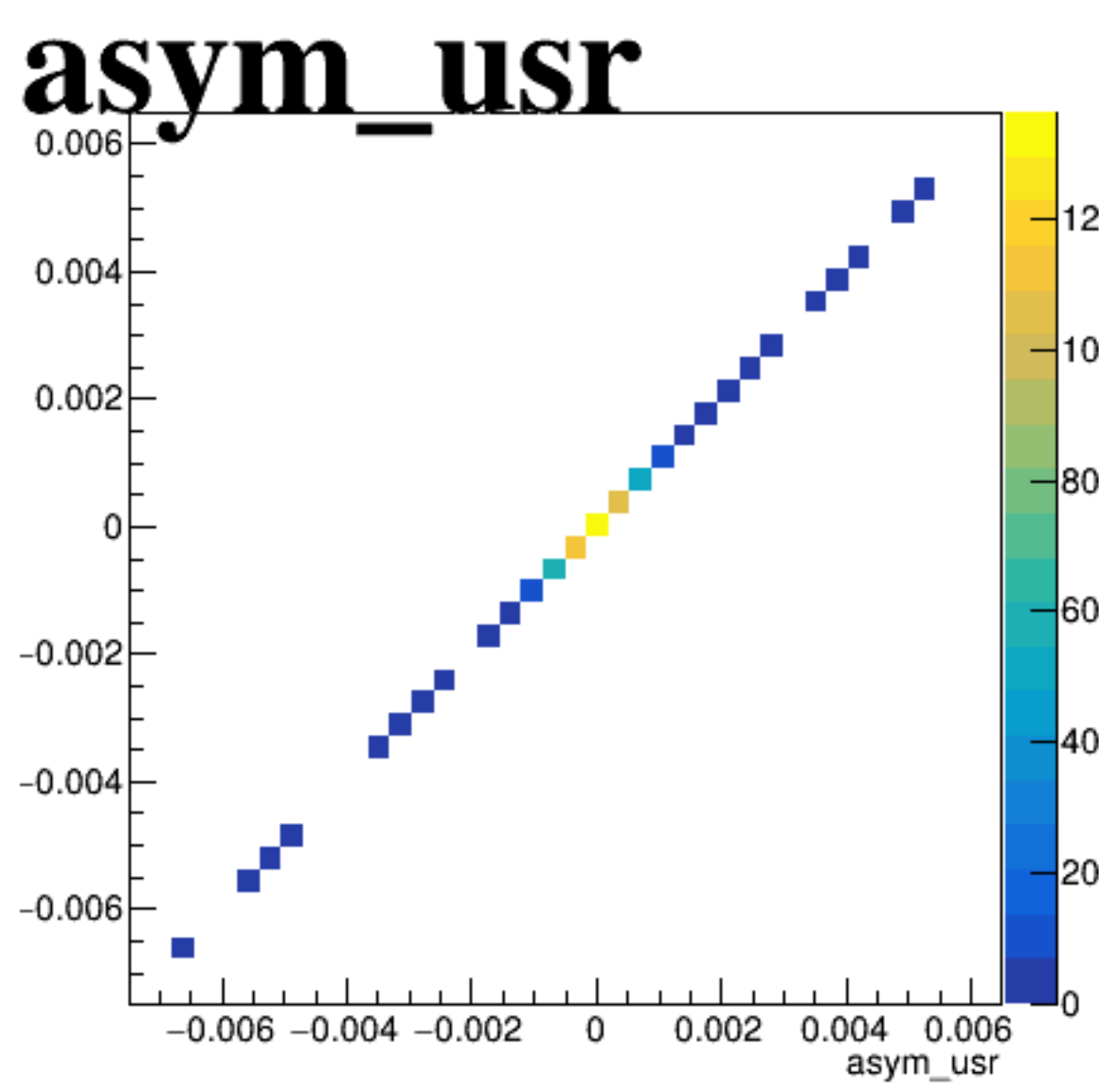
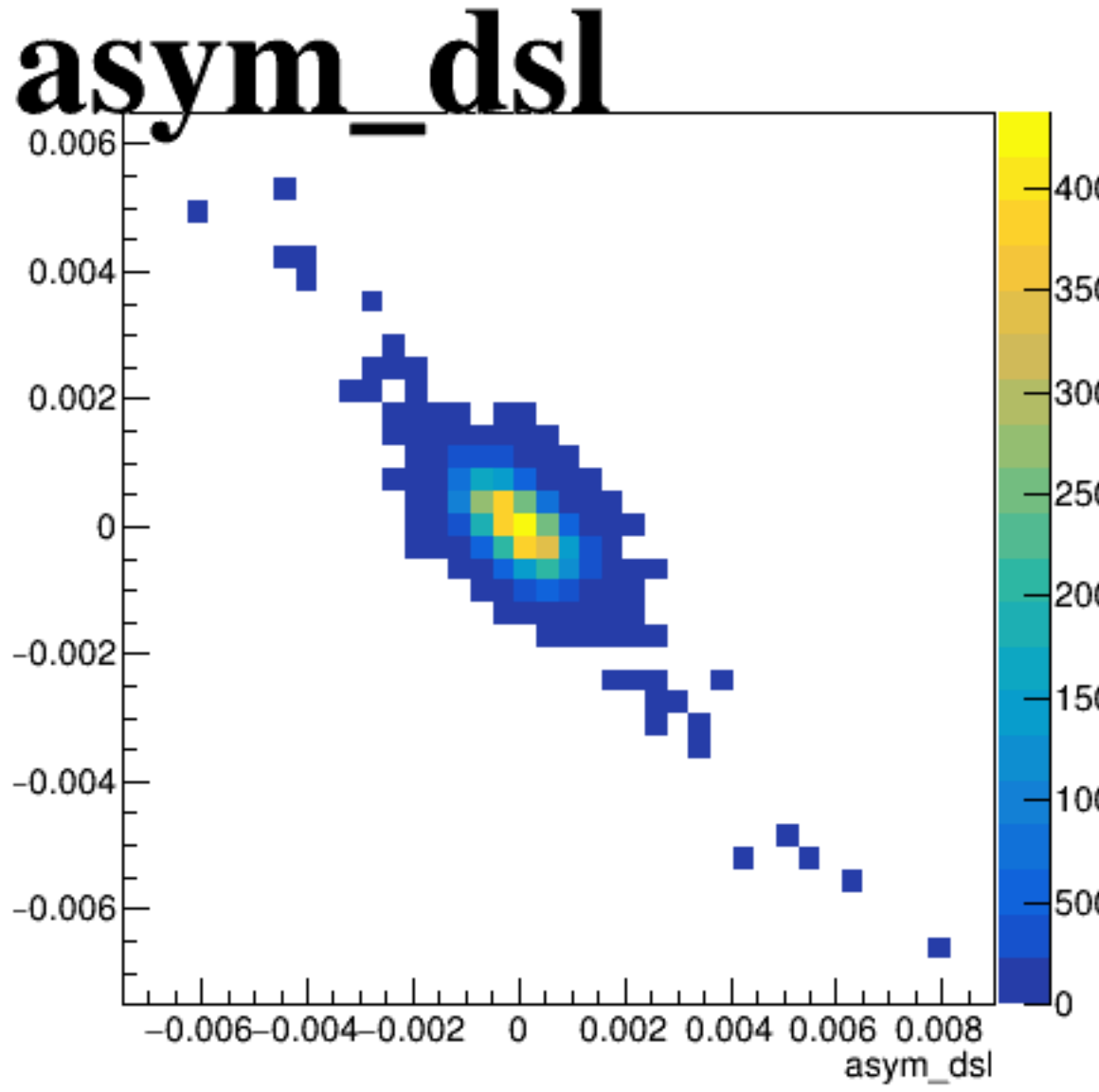
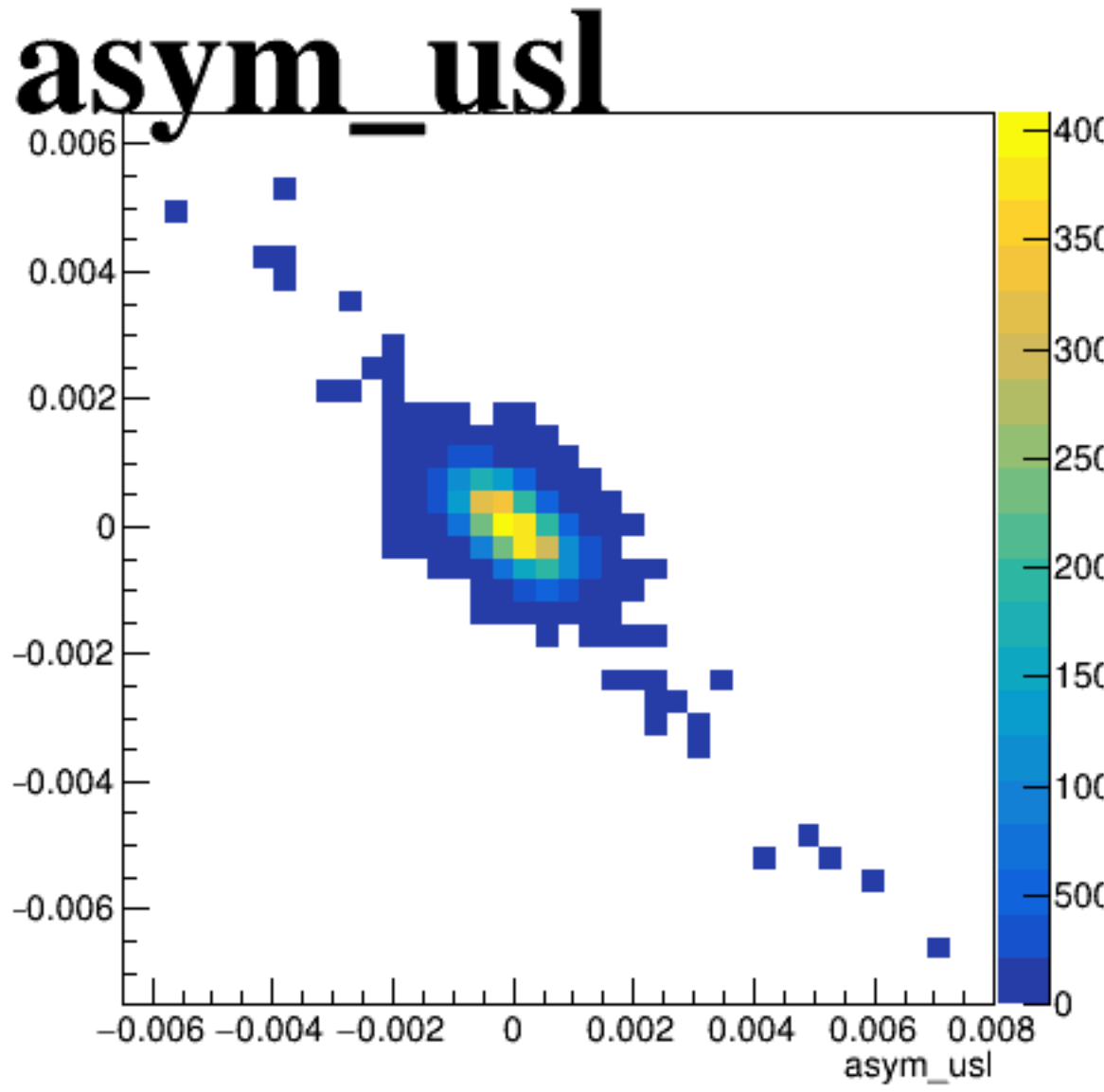
asym_usl



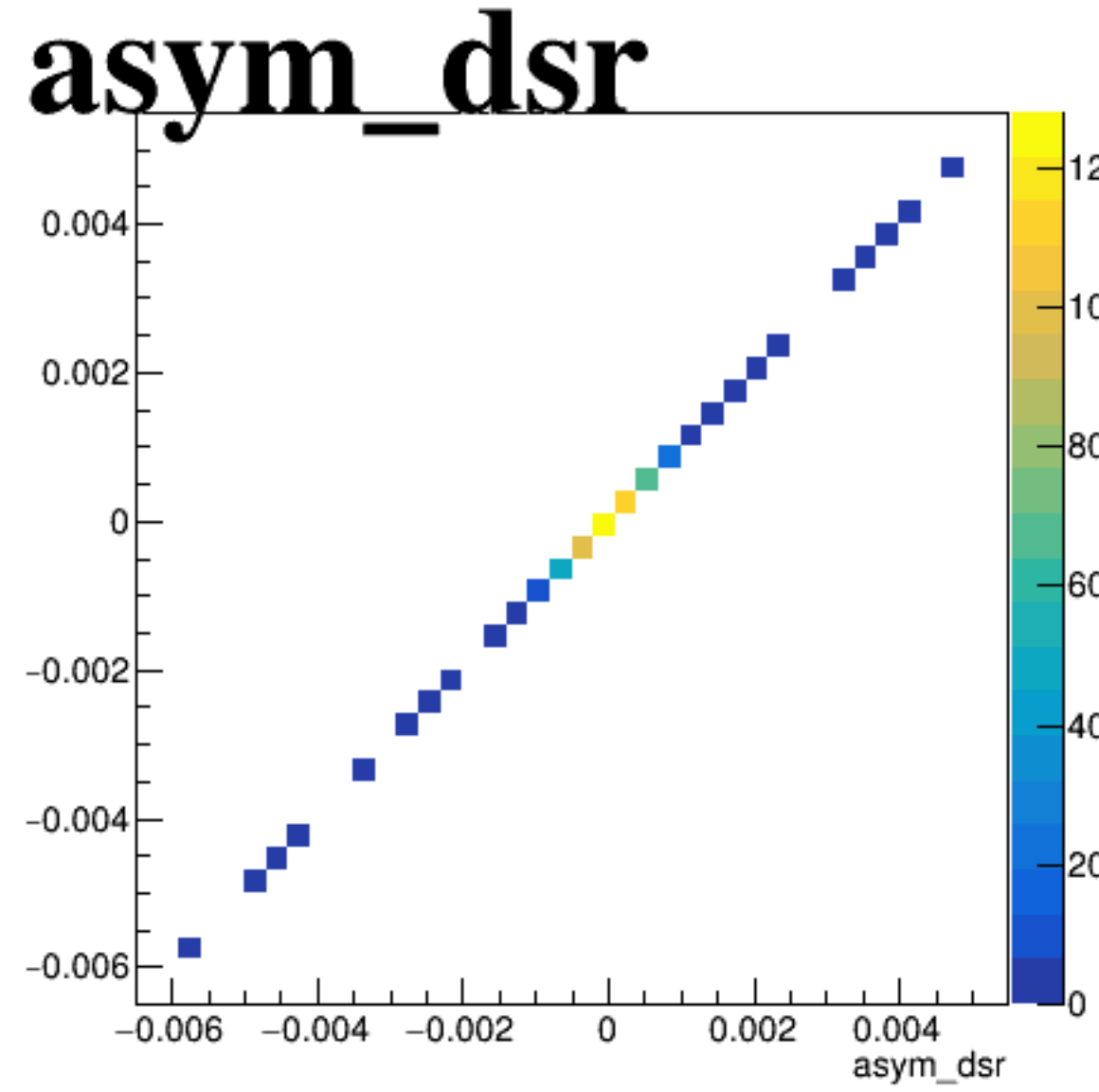
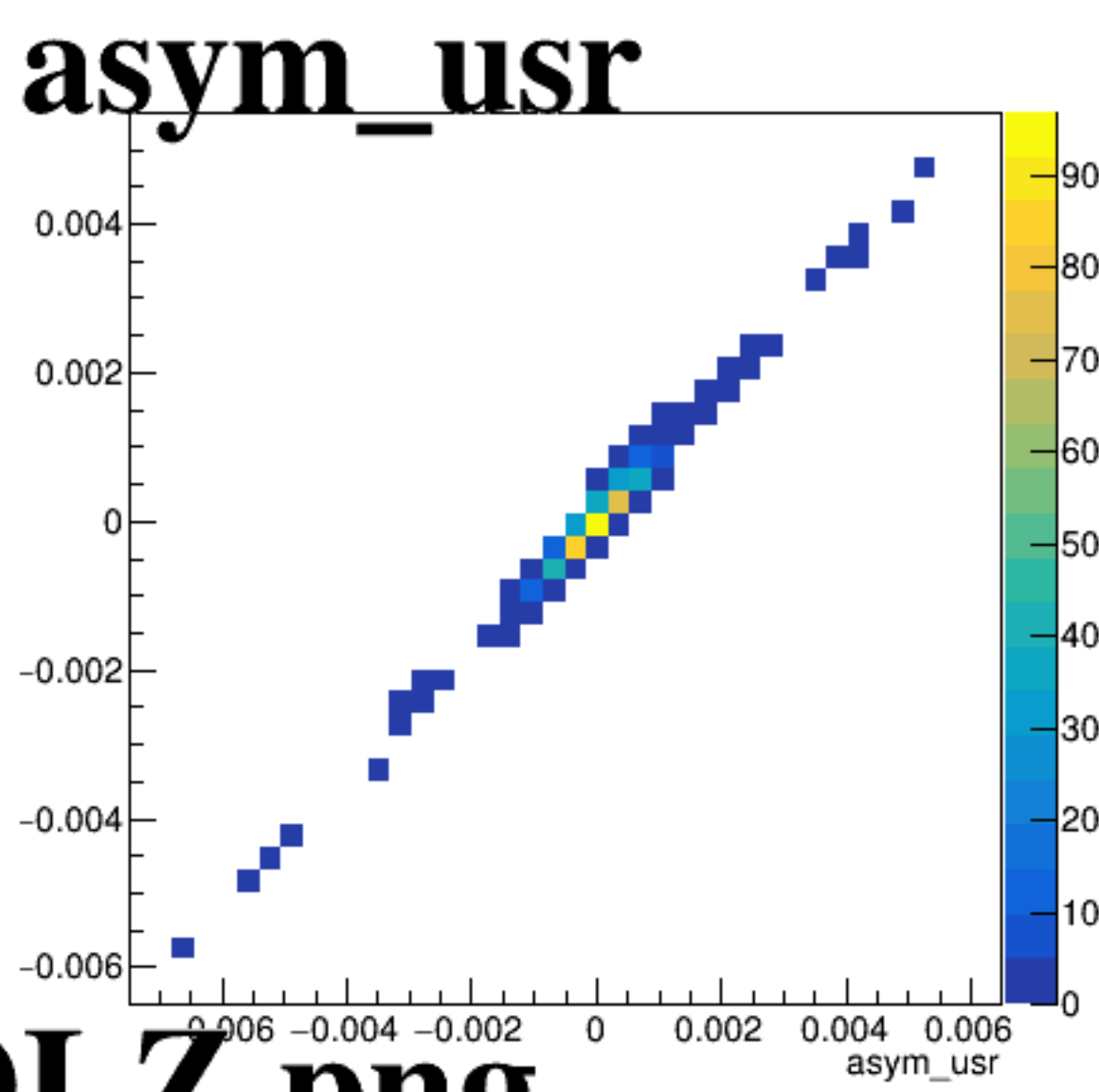
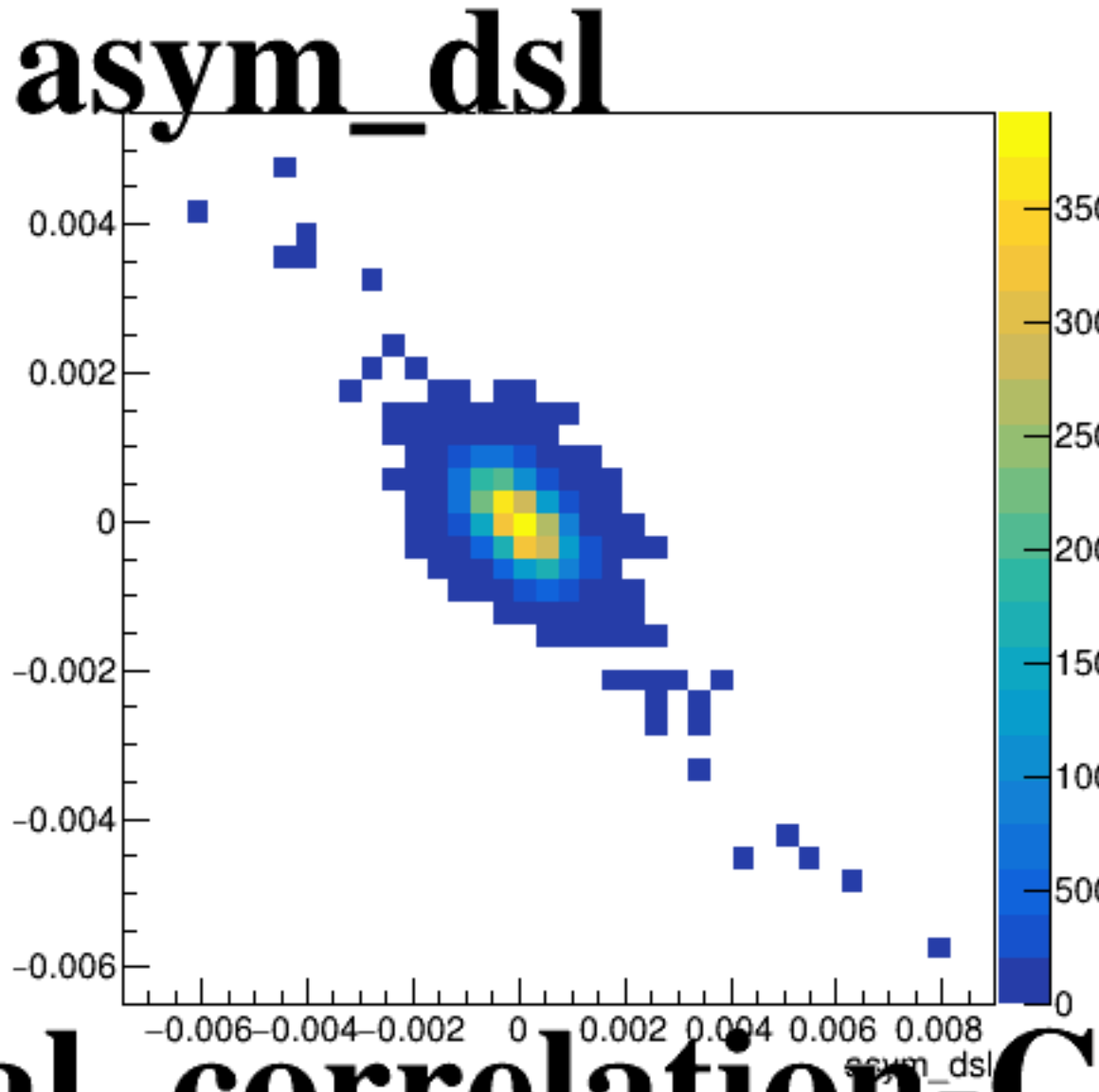
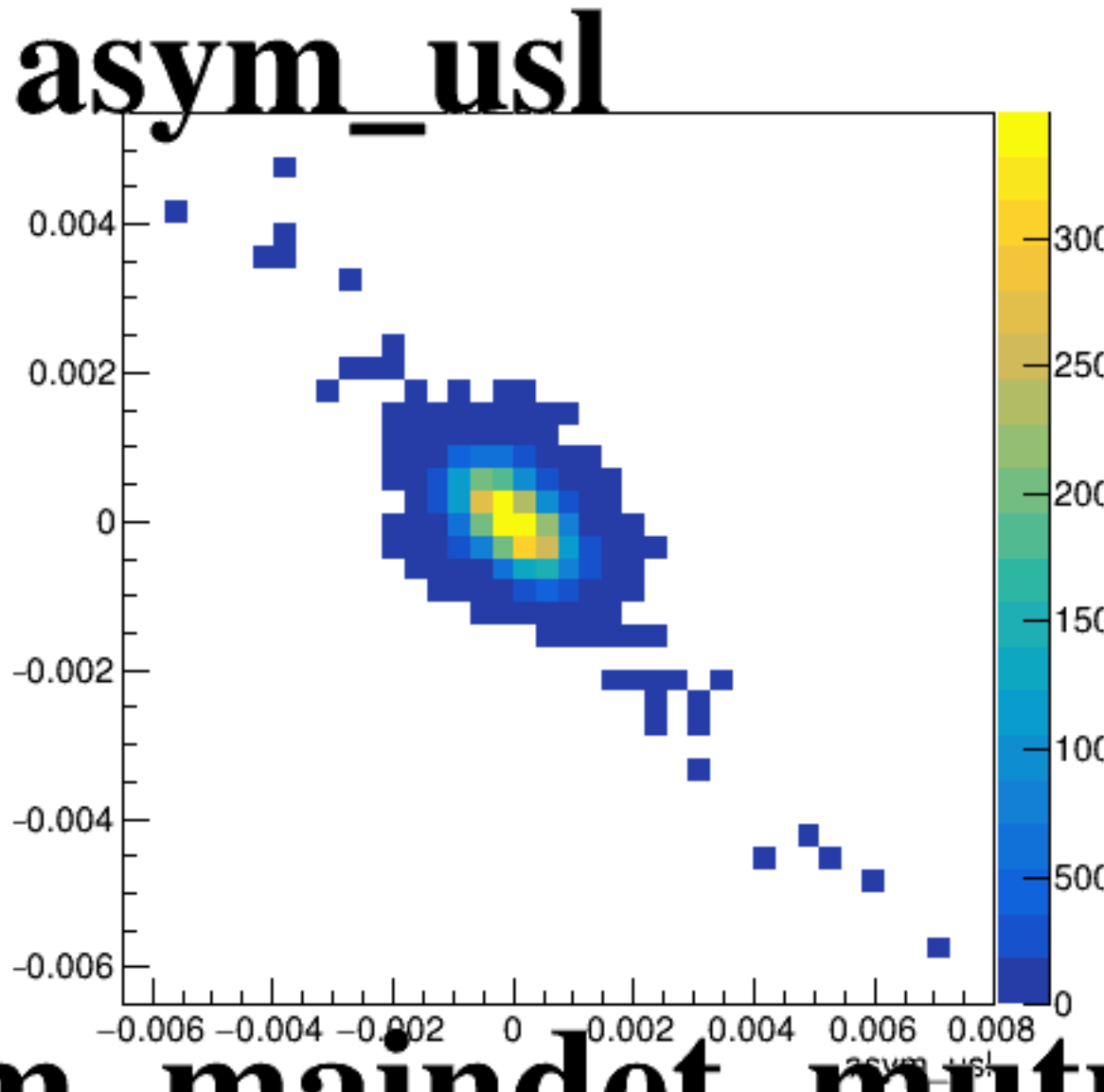
asym_dsl



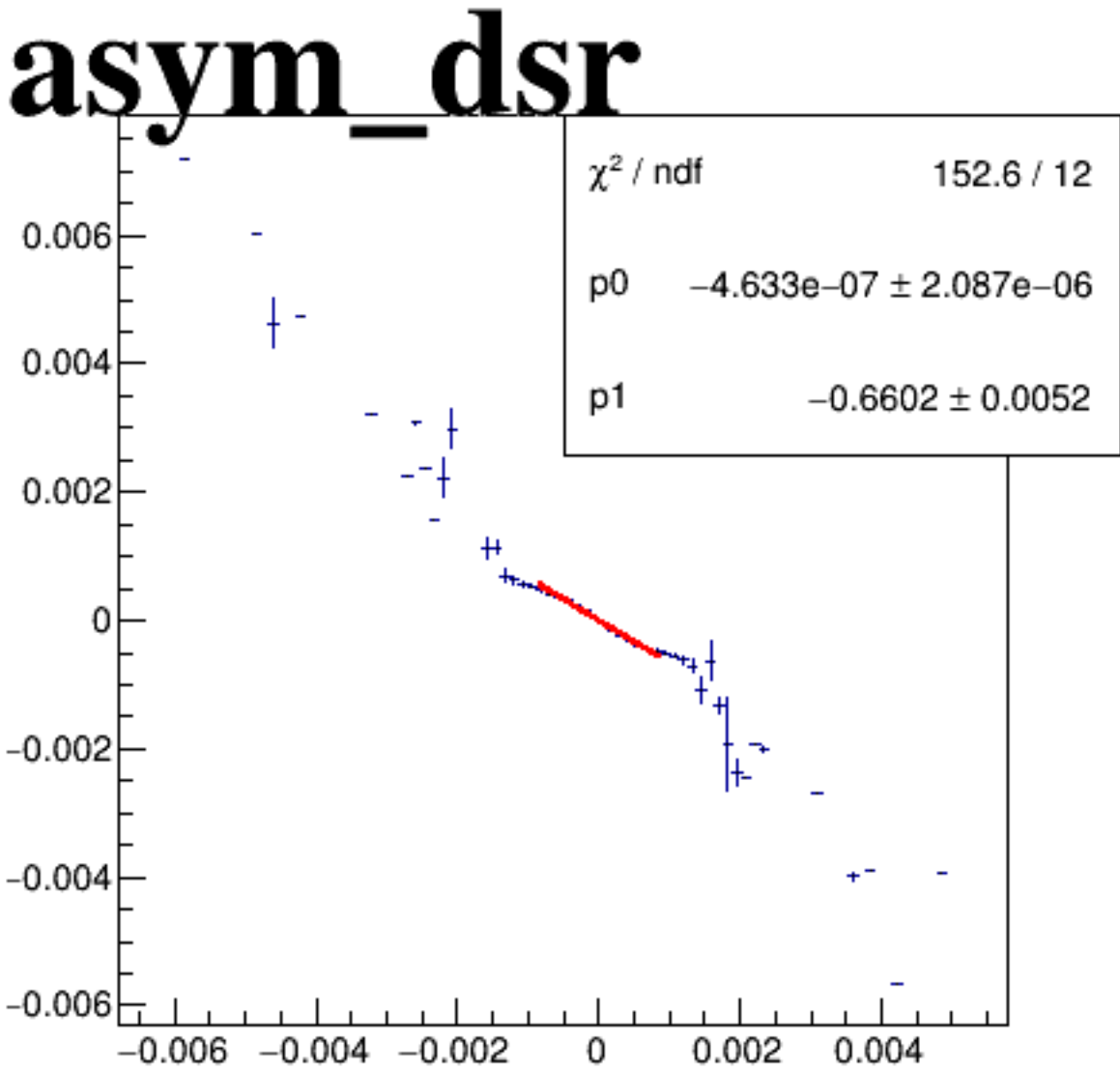
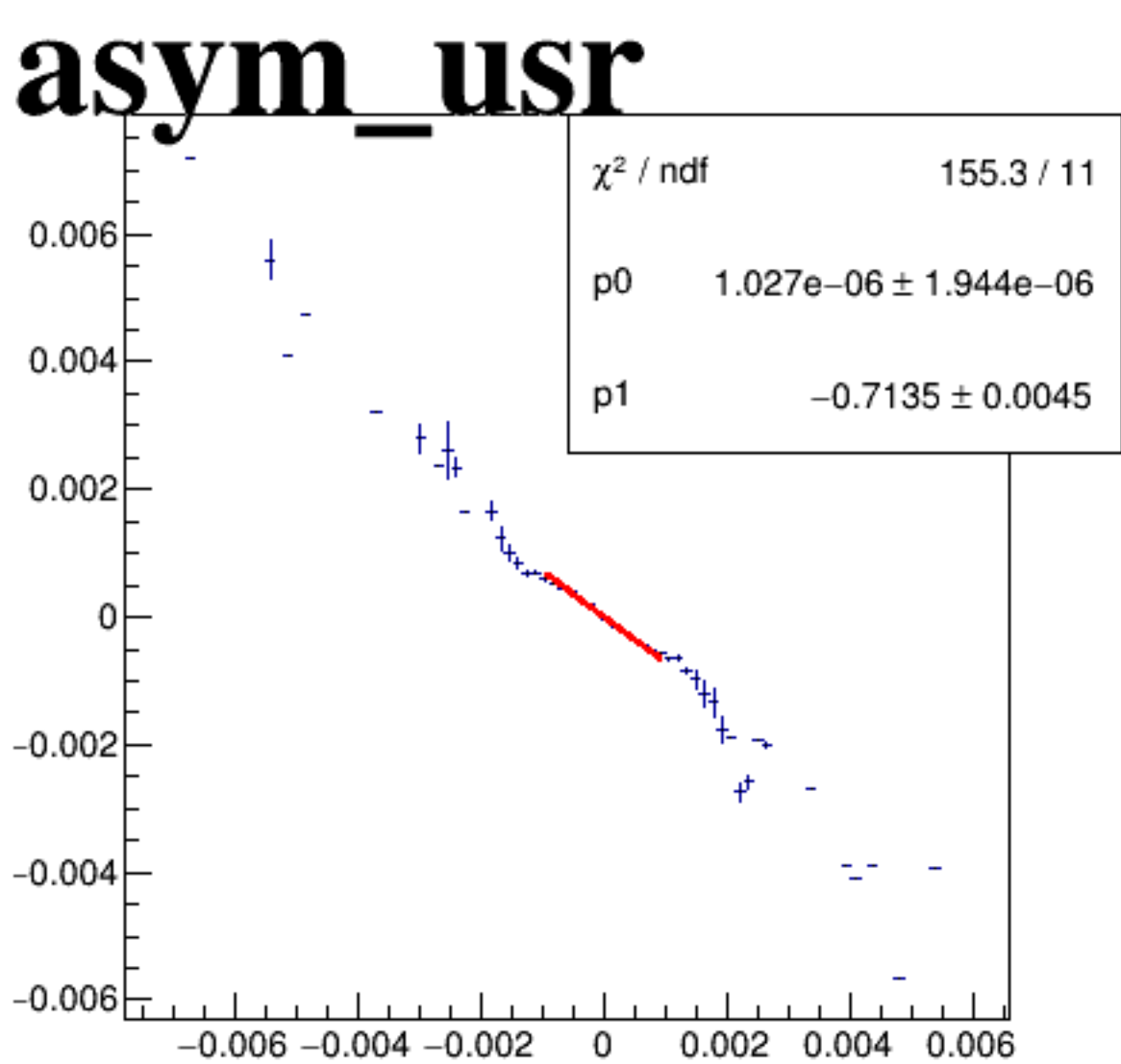
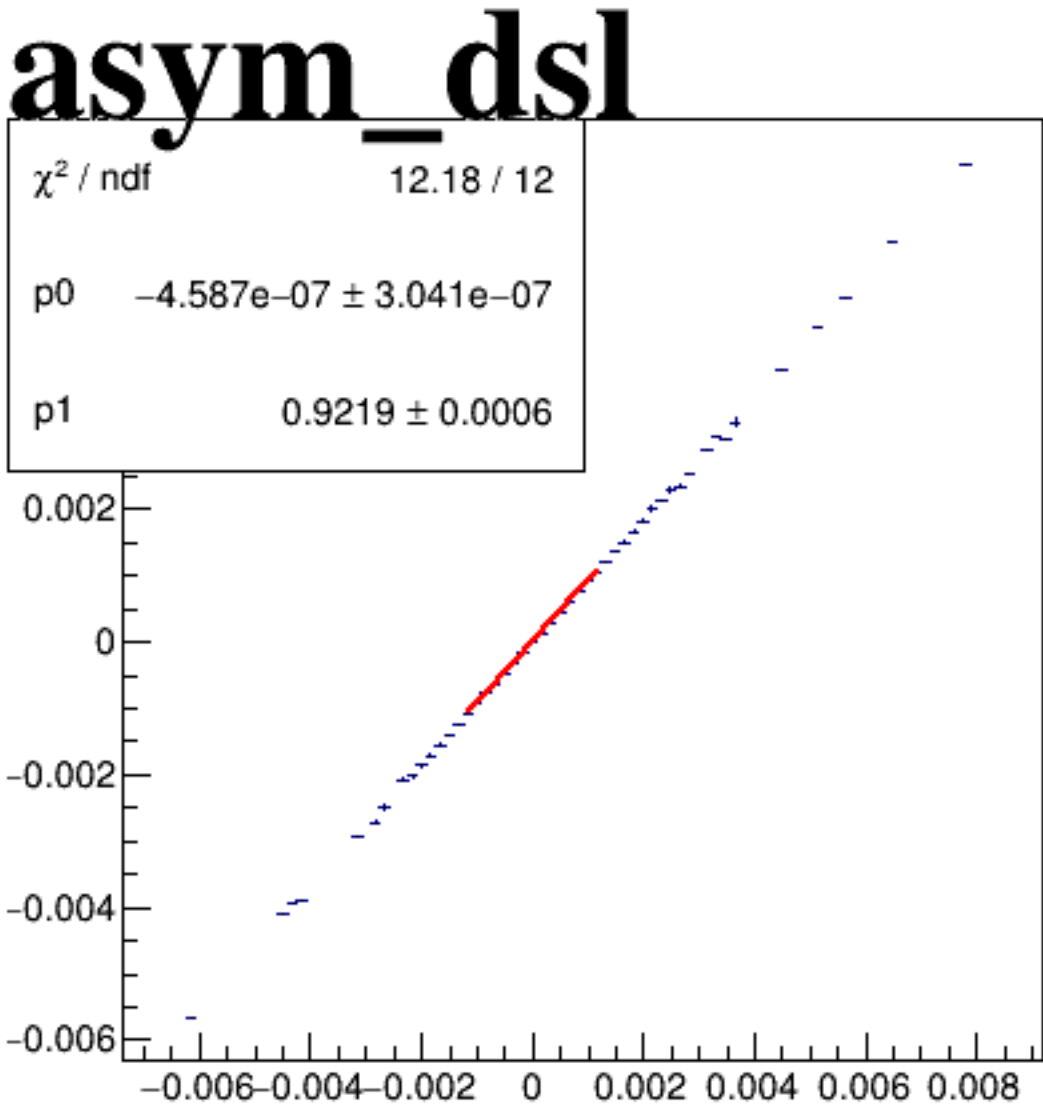
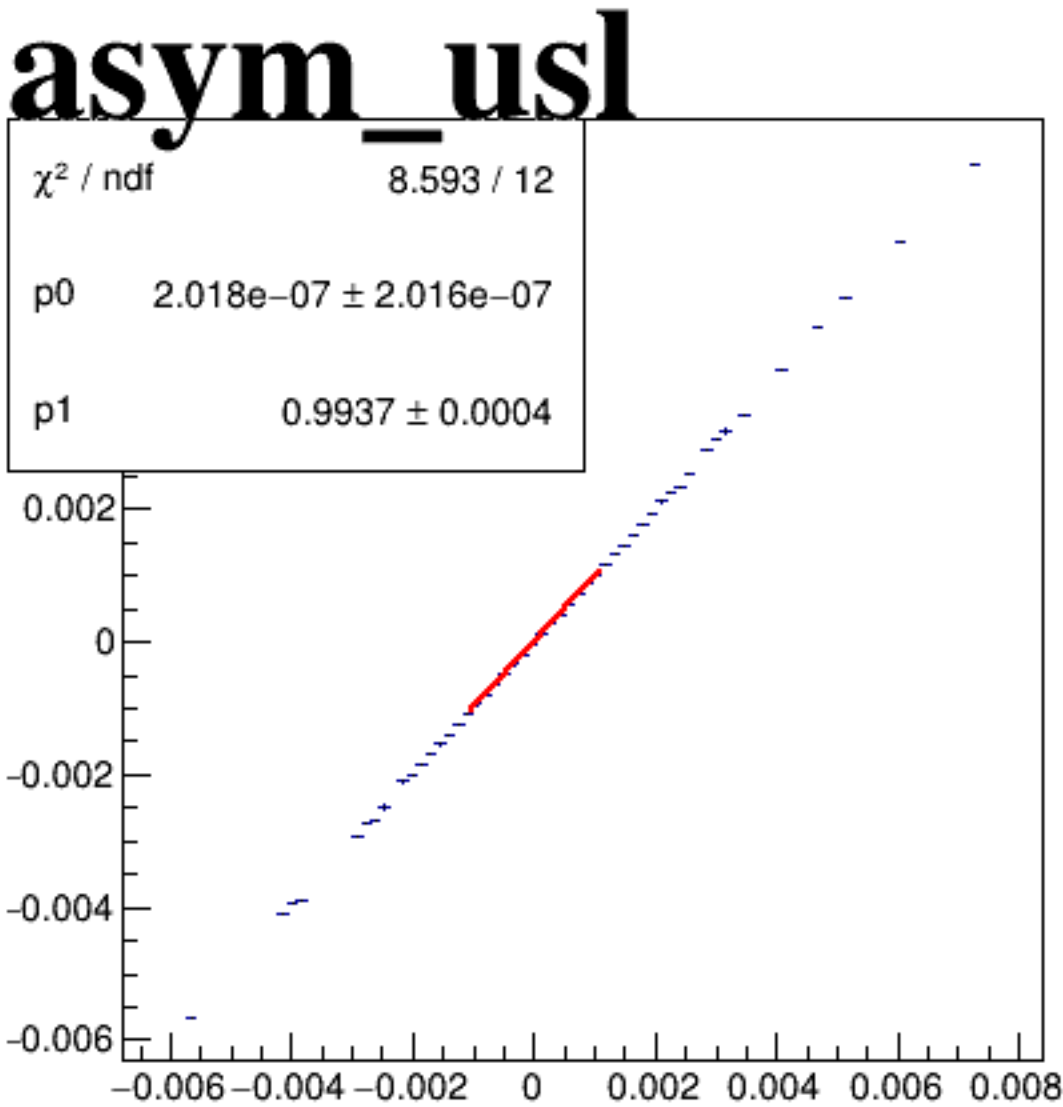
asym_usr



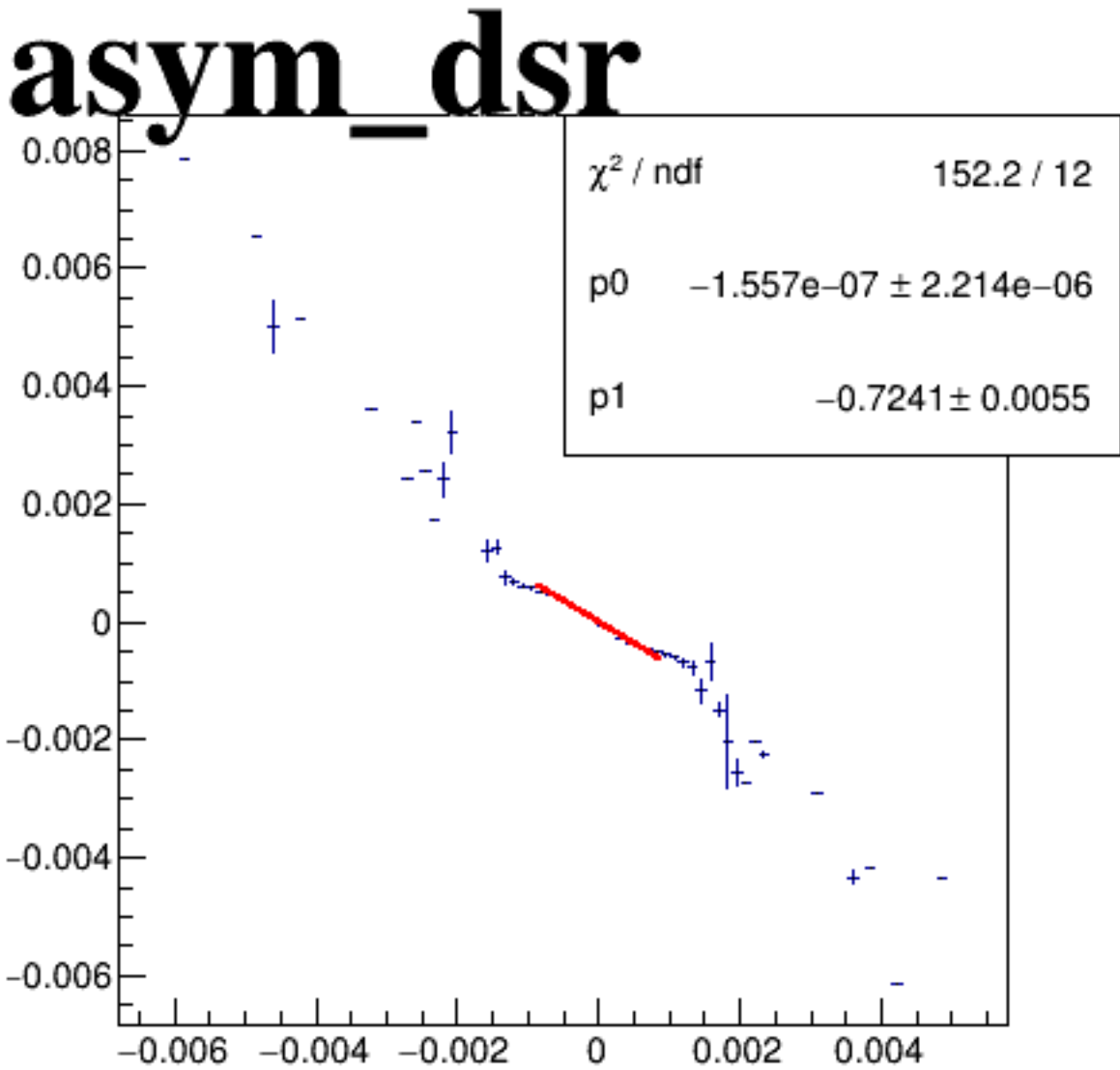
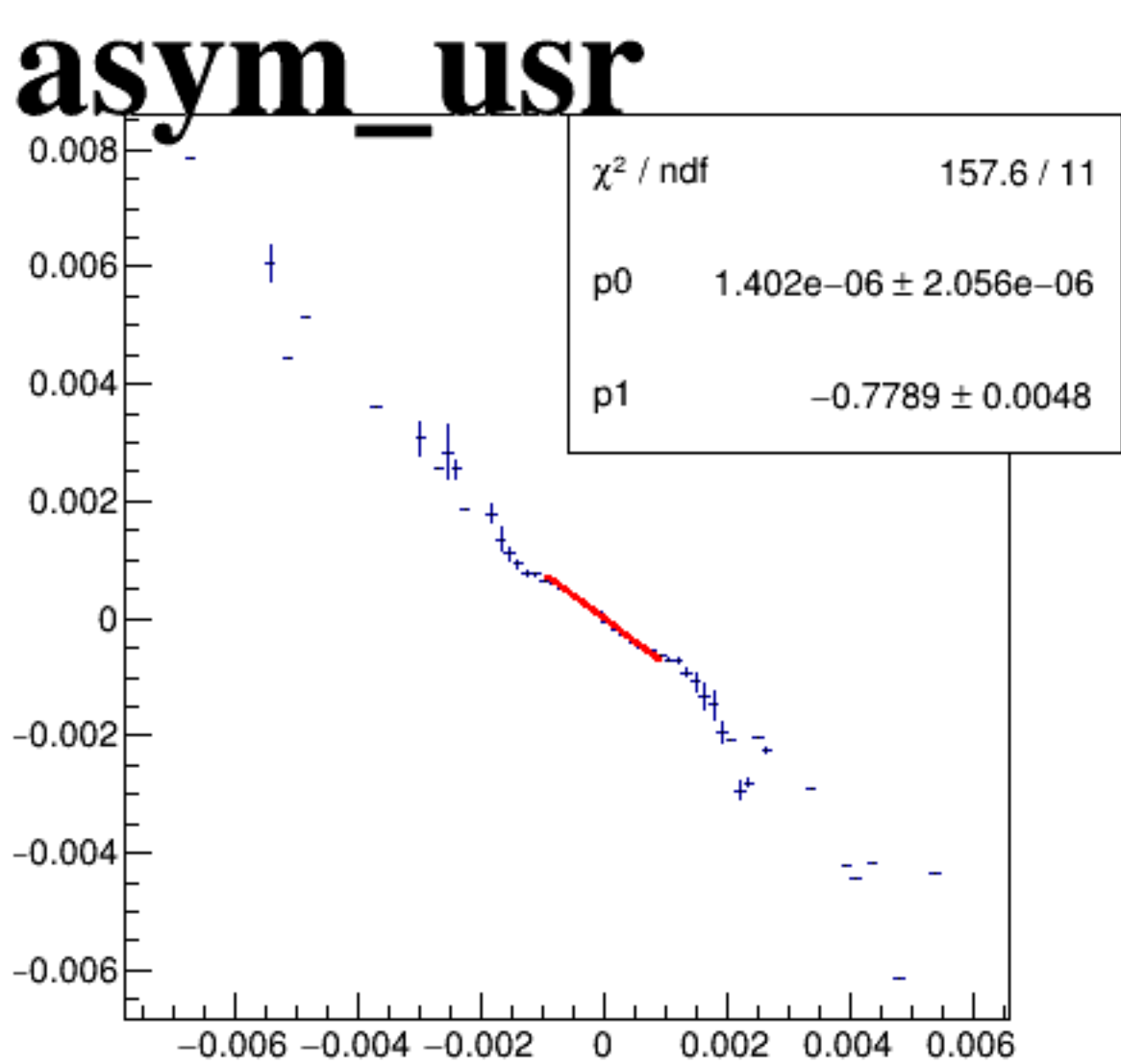
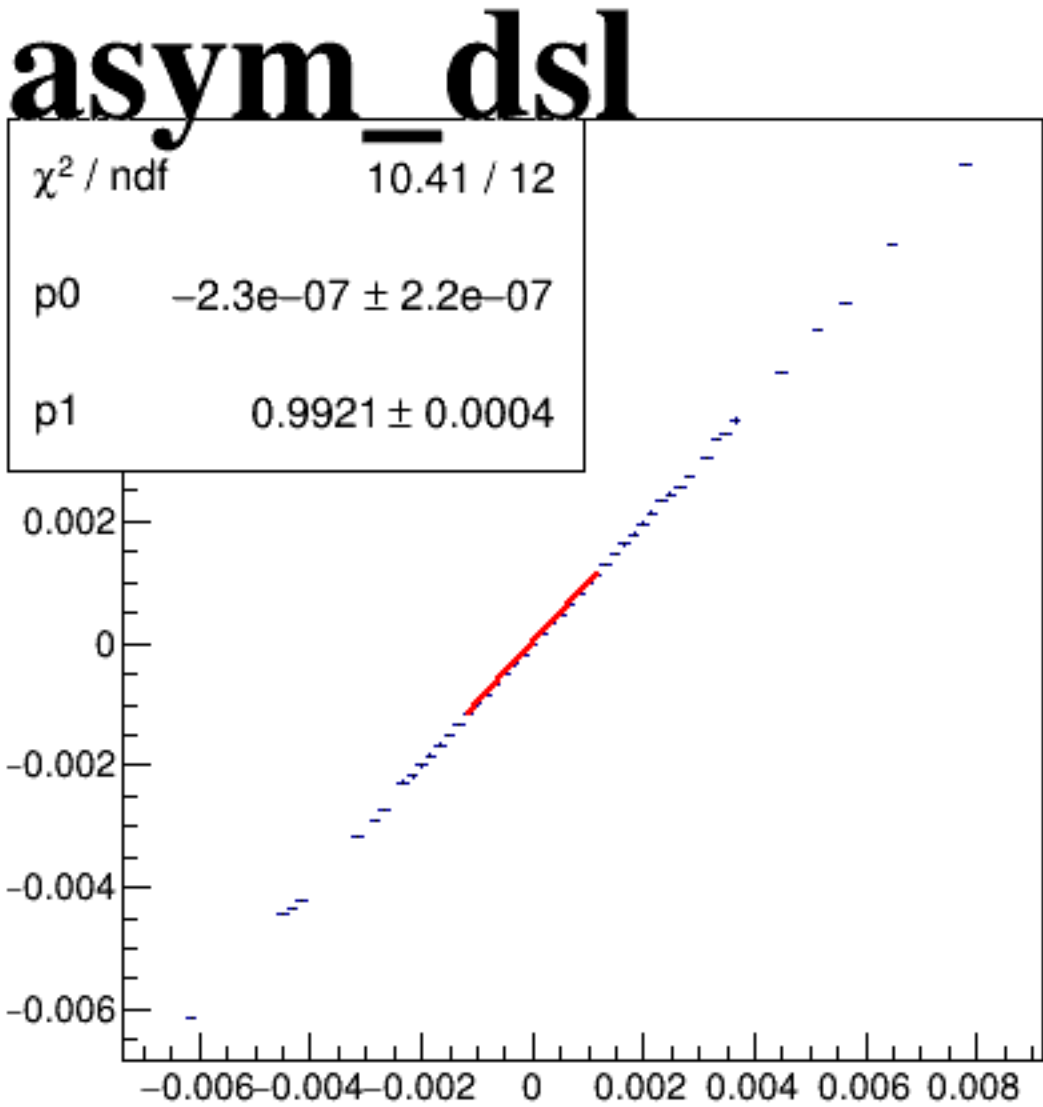
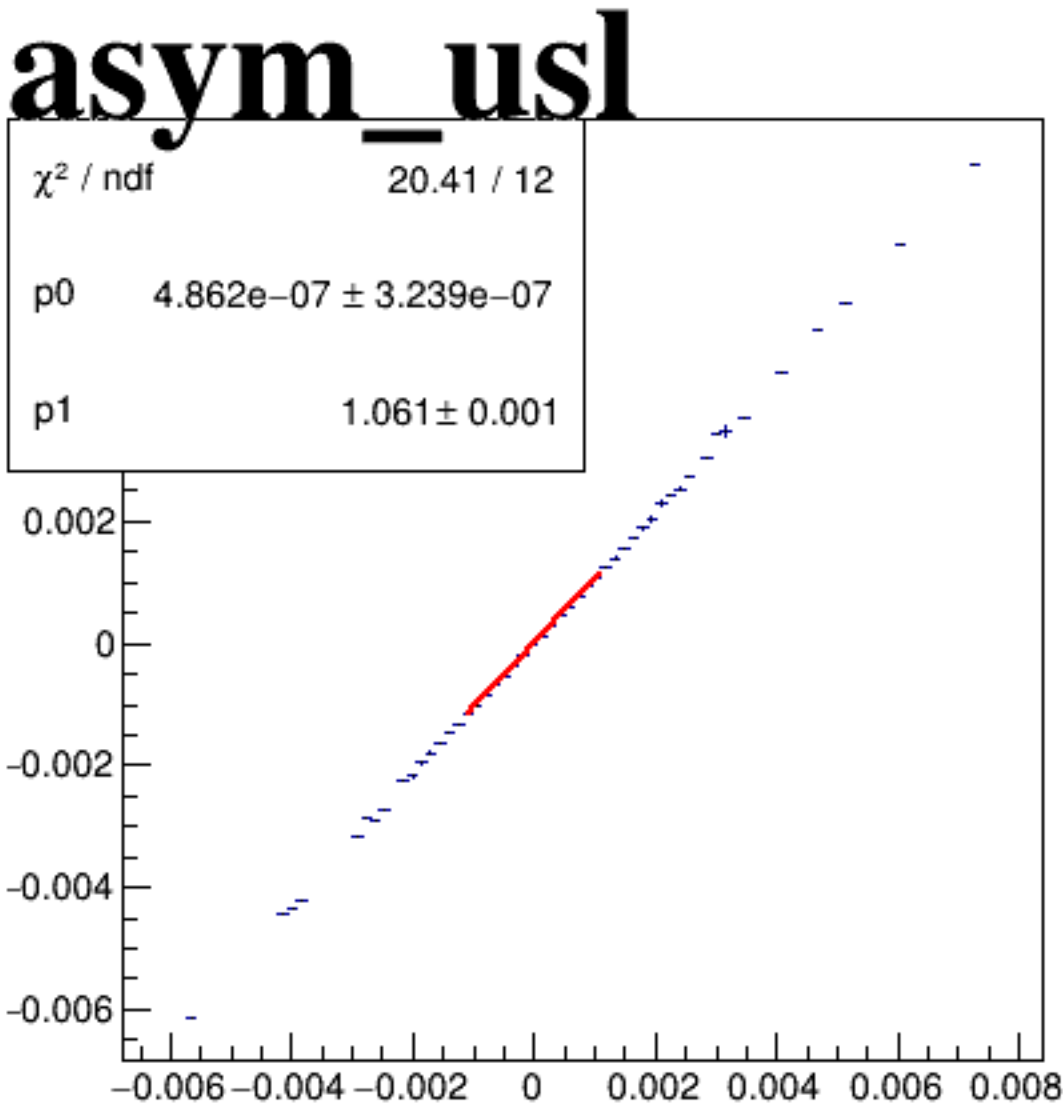
asym_dsr



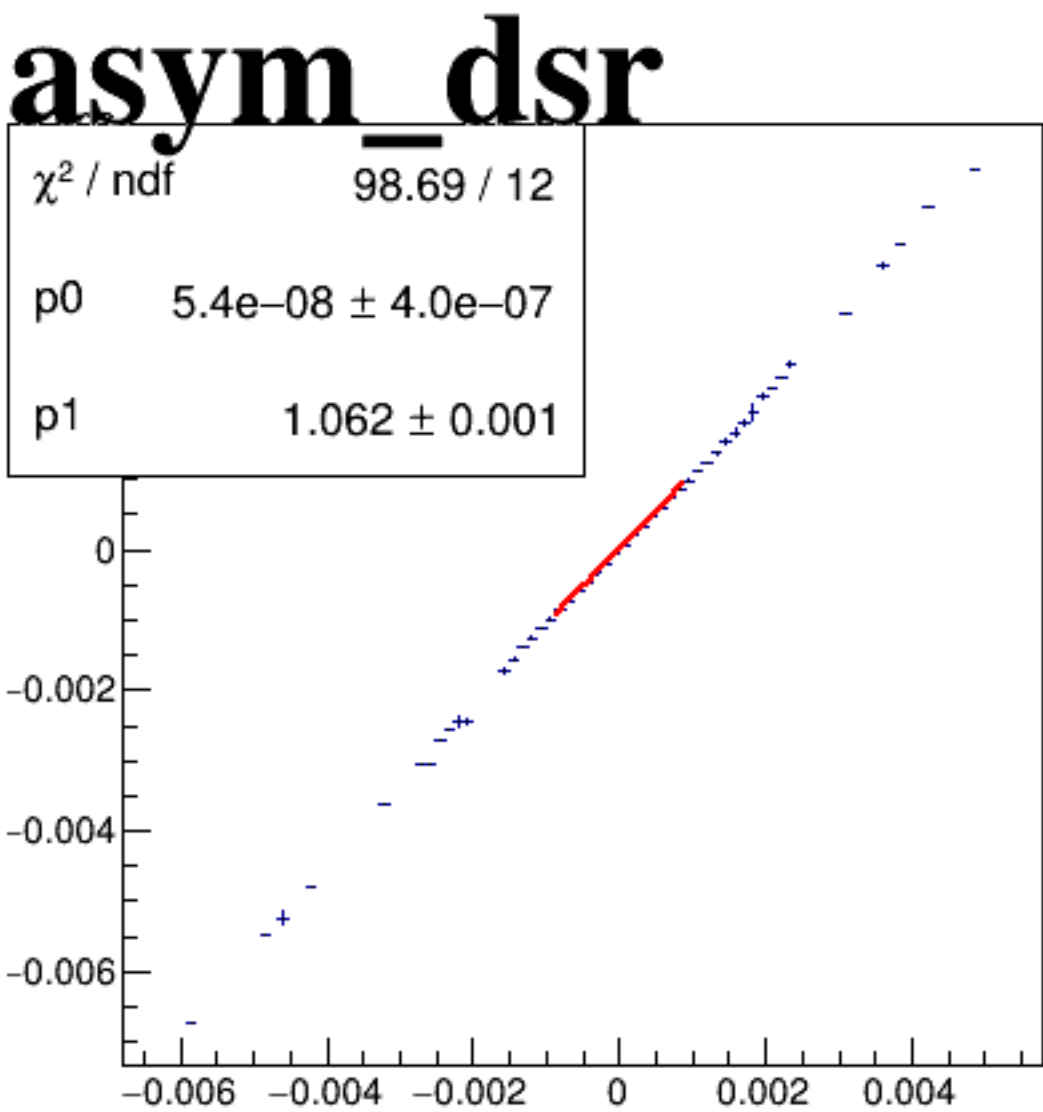
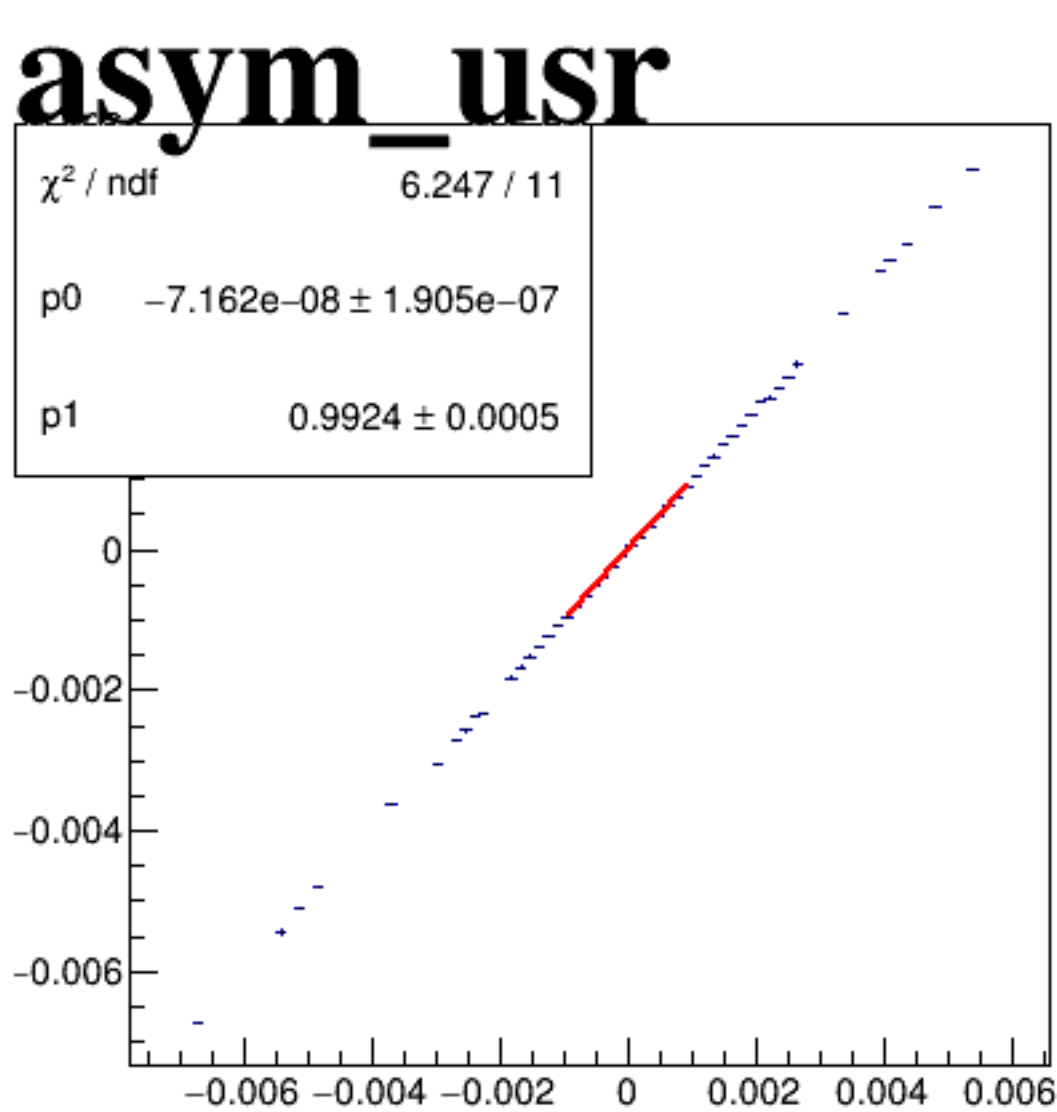
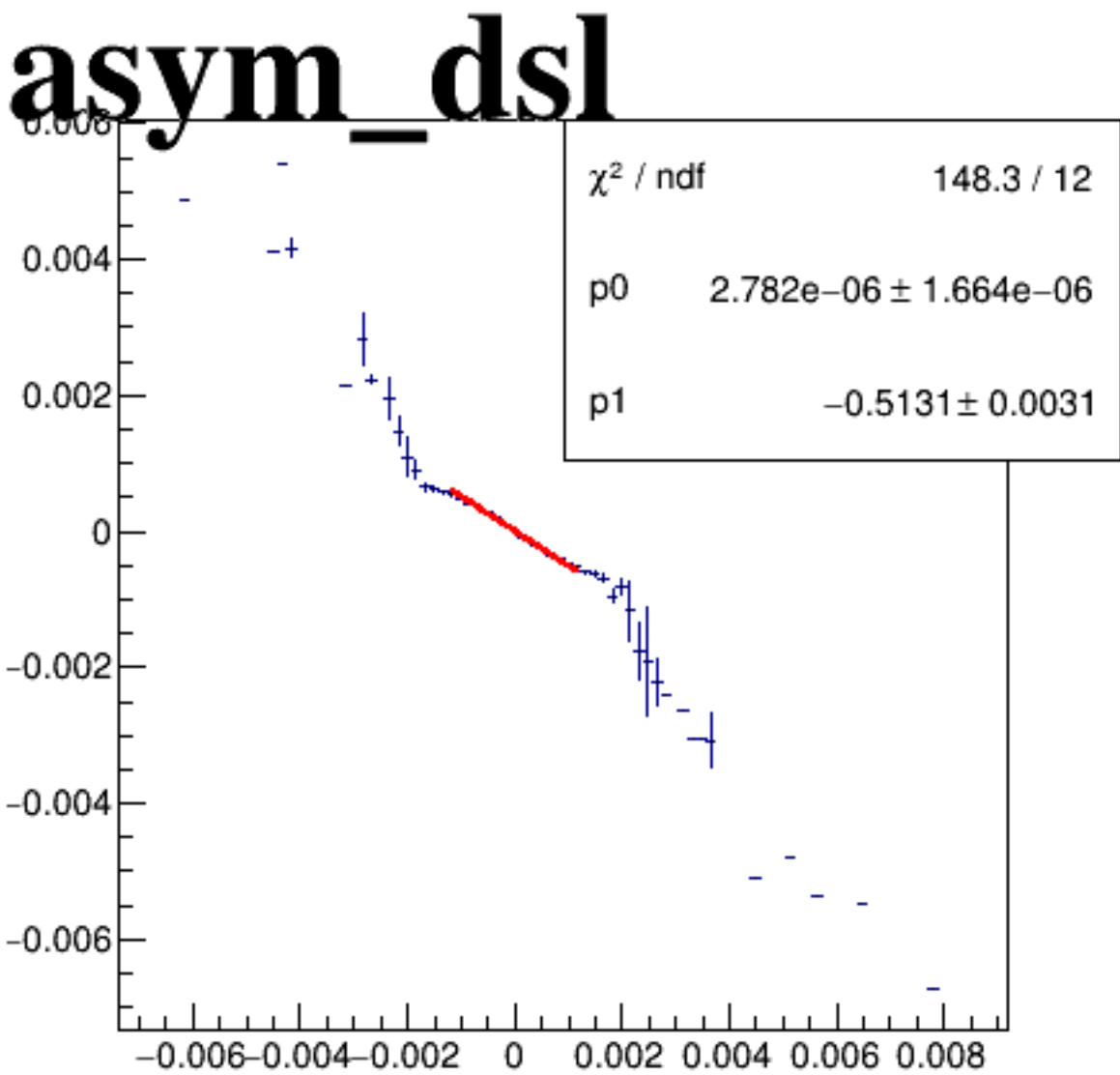
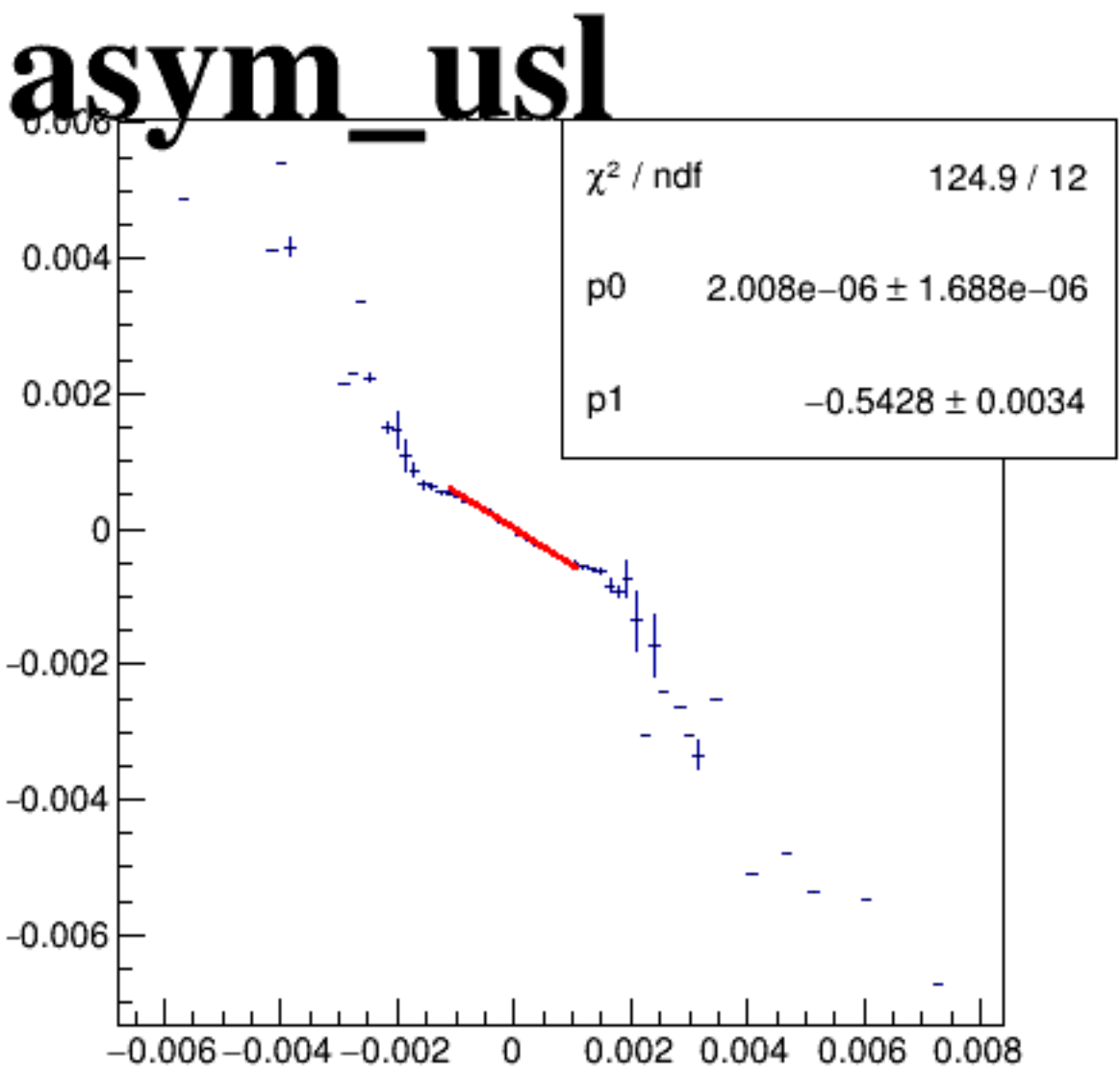
asym_usl



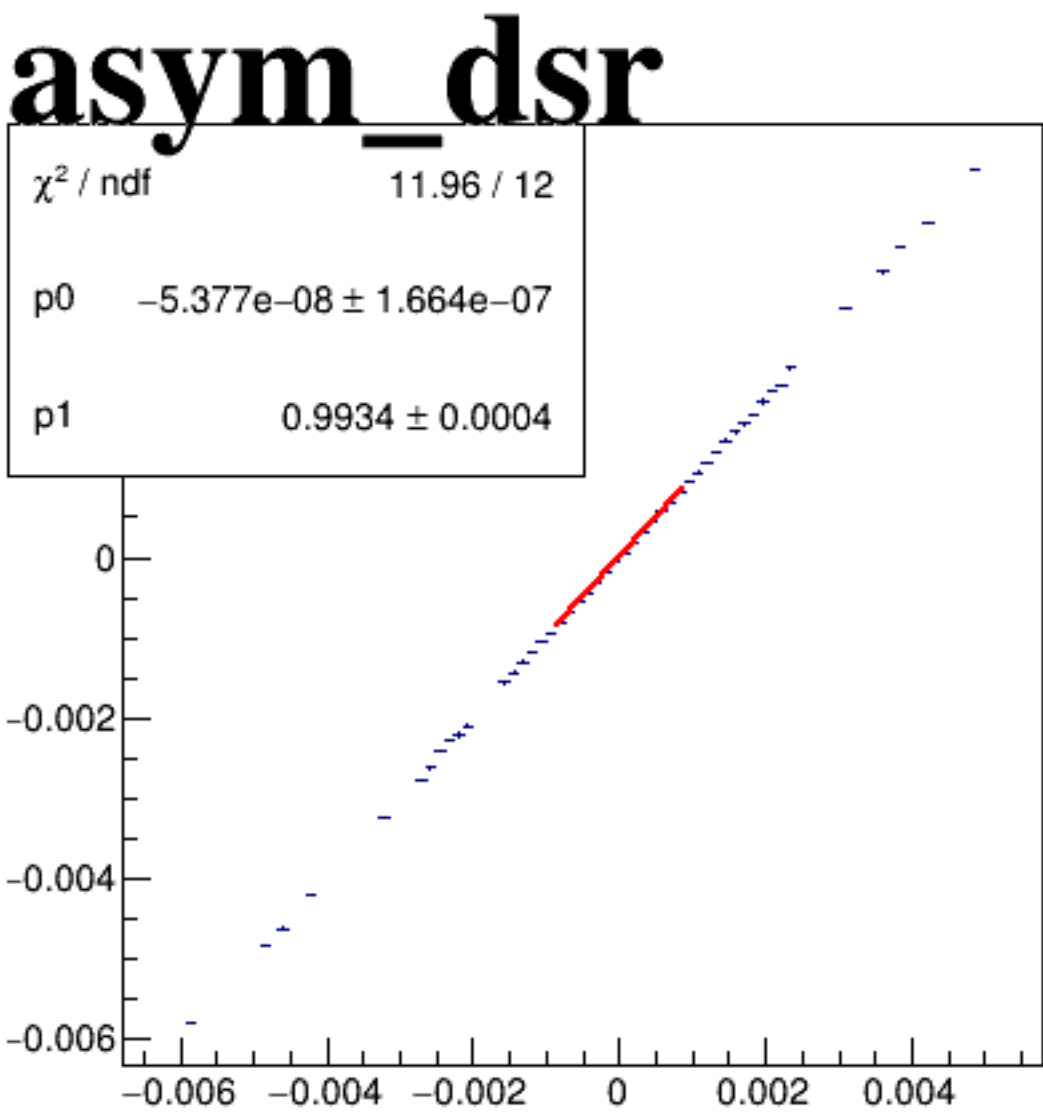
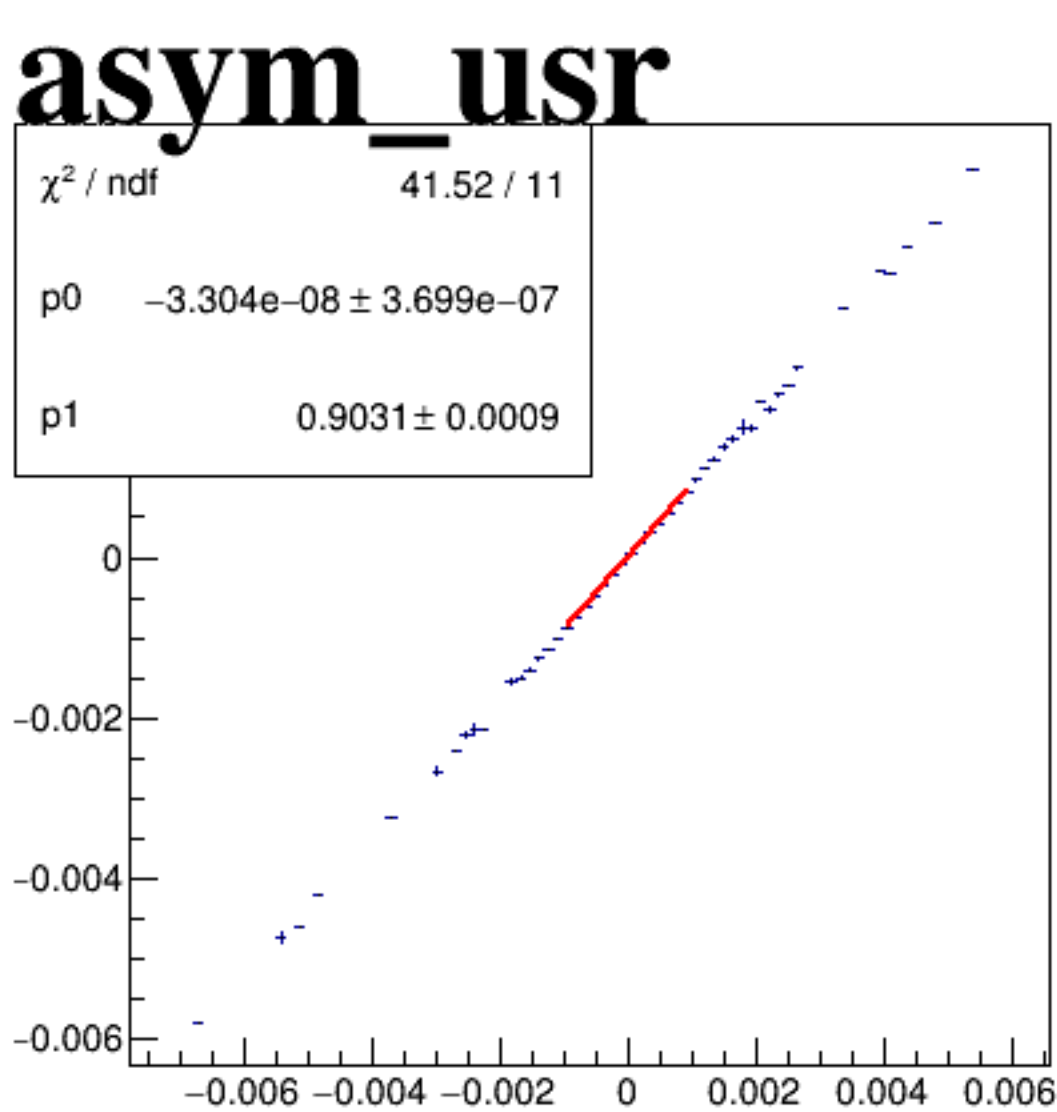
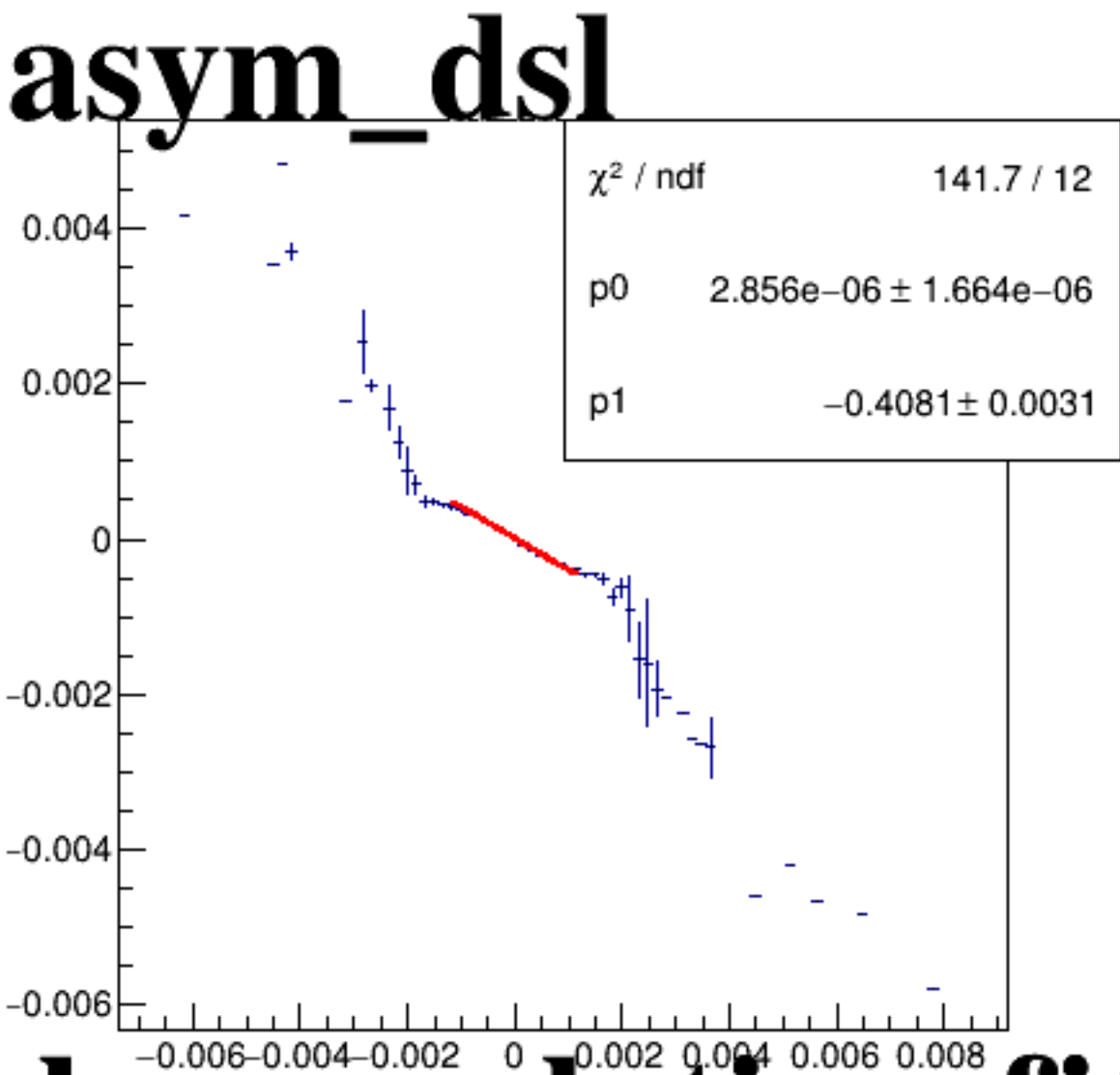
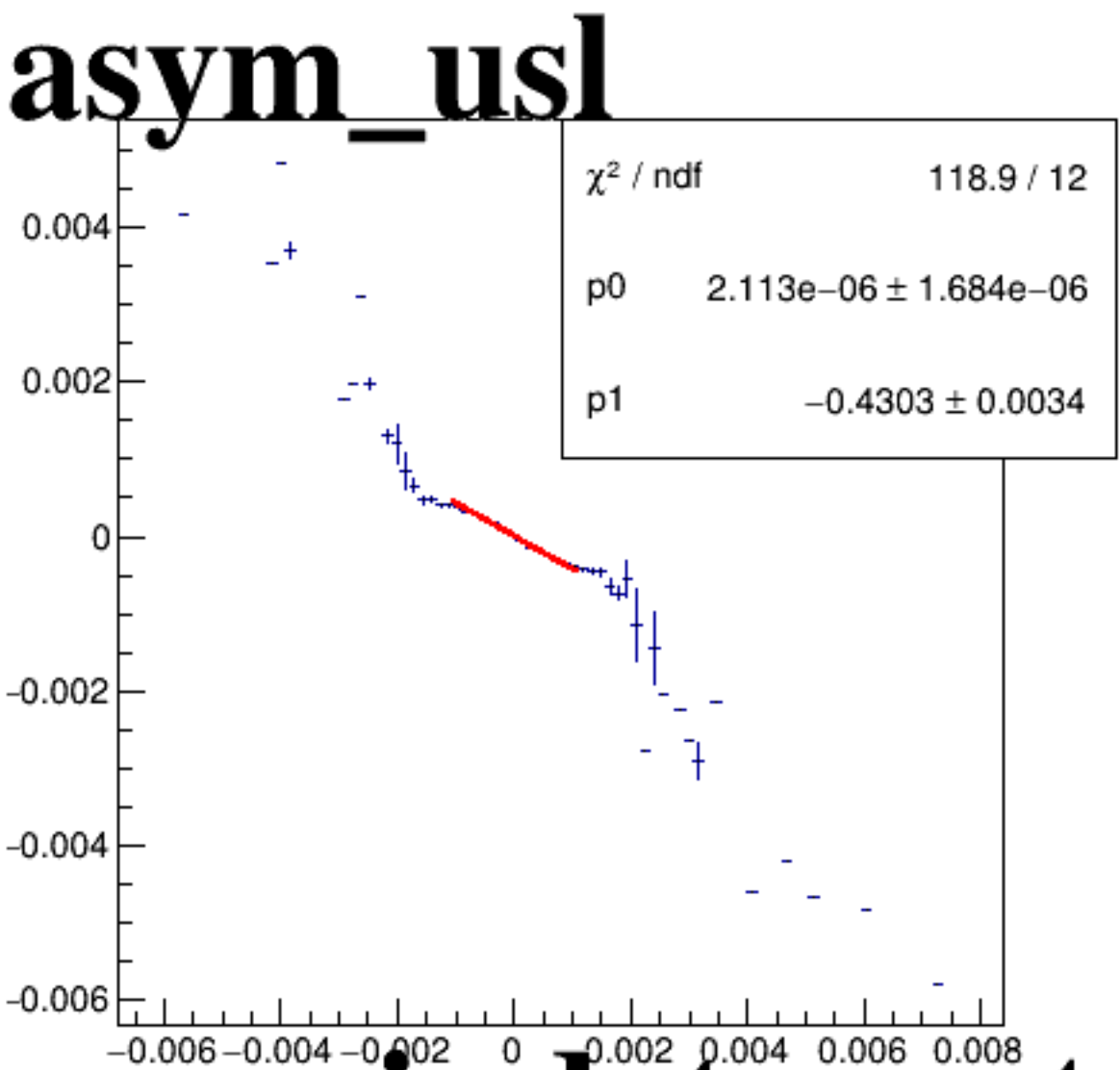
asym_dsl



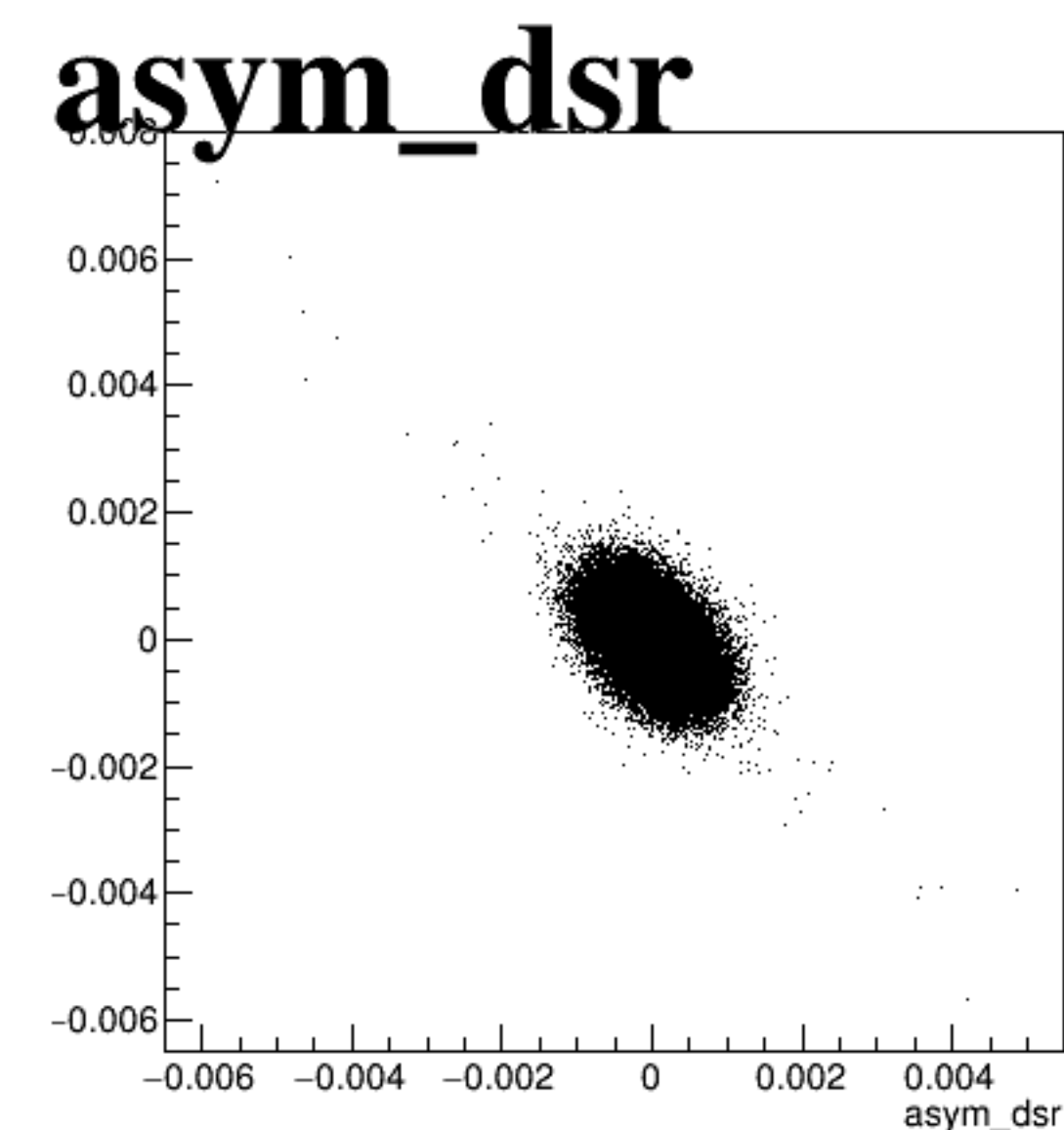
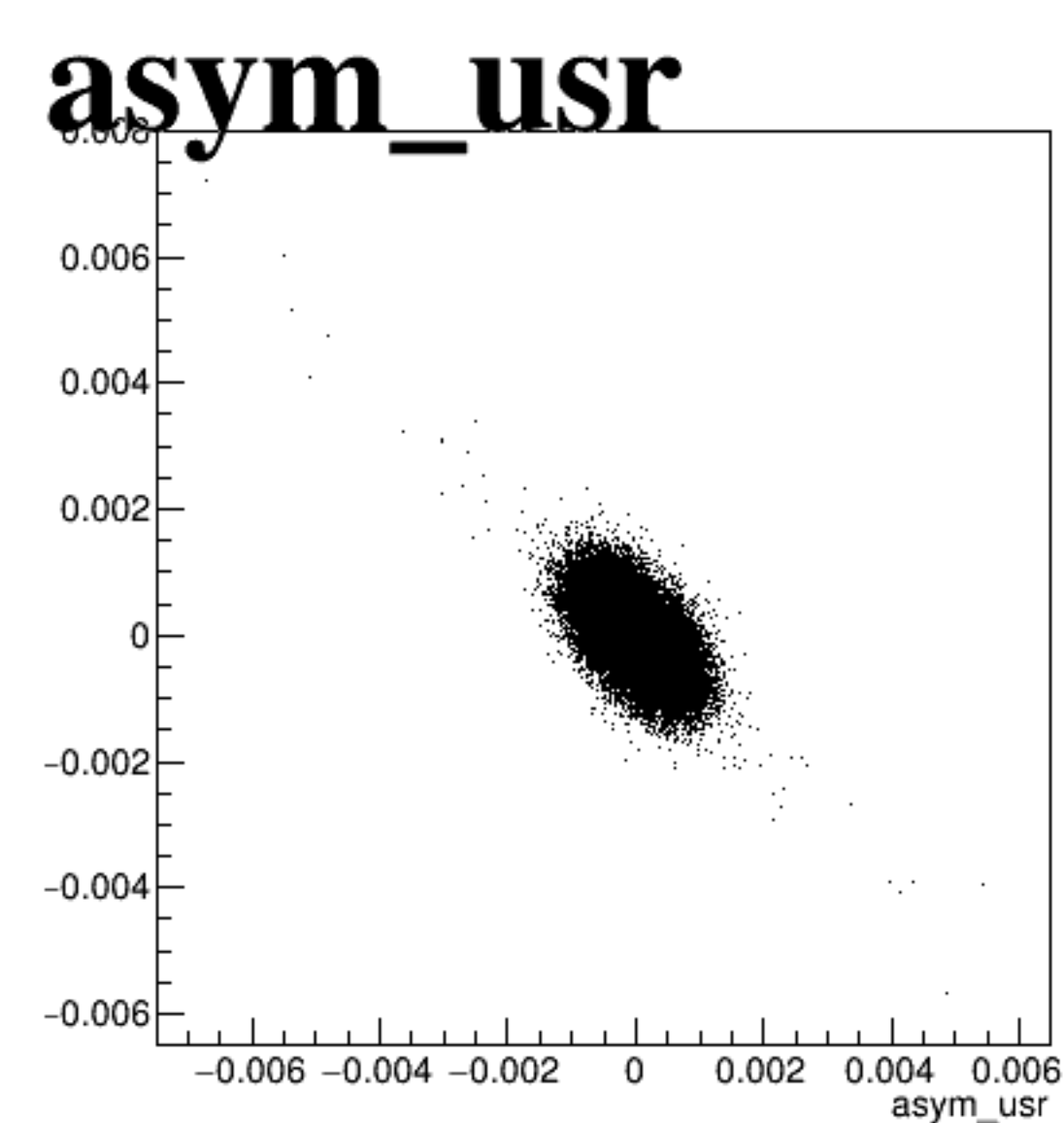
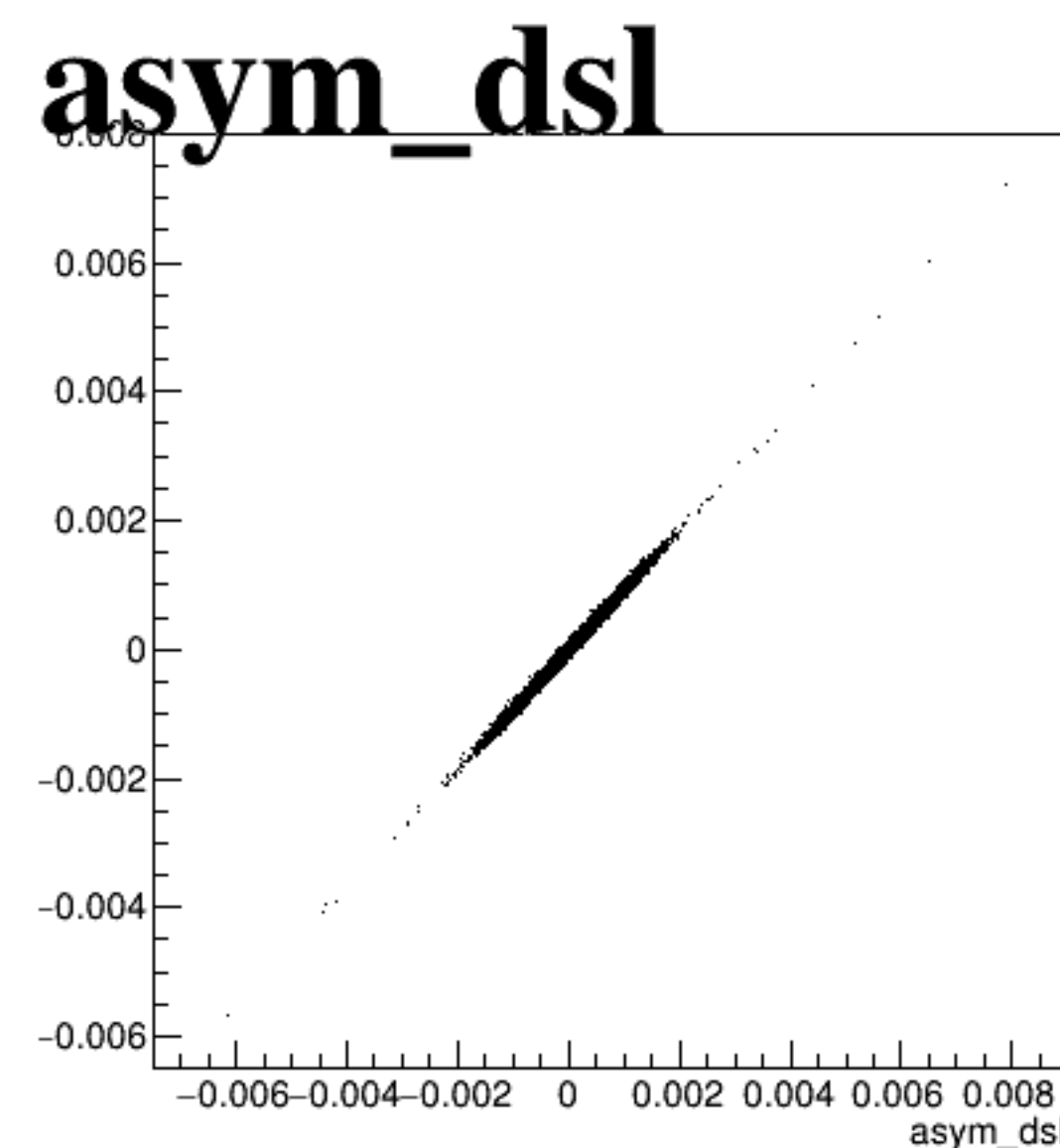
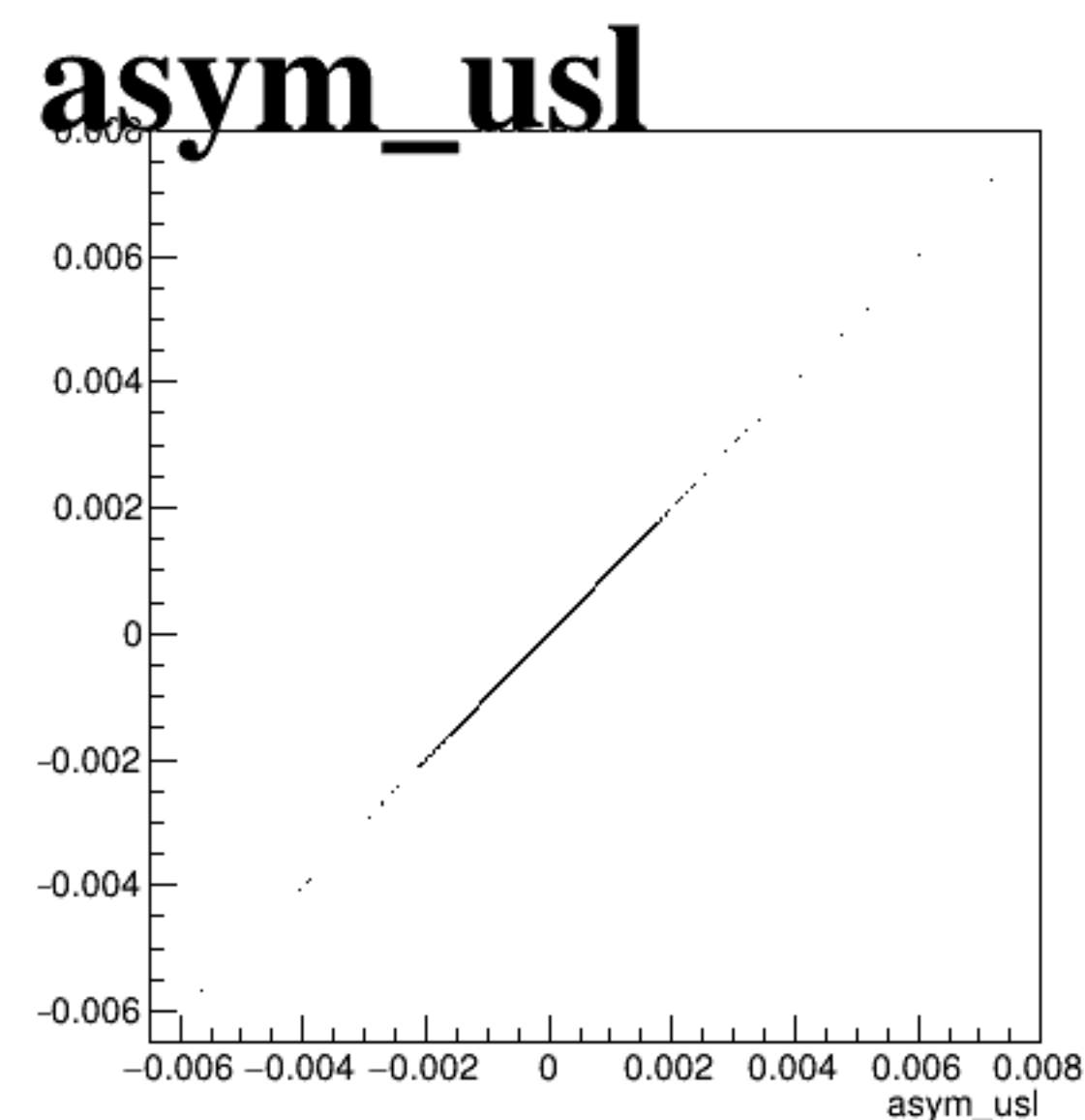
asym_usr



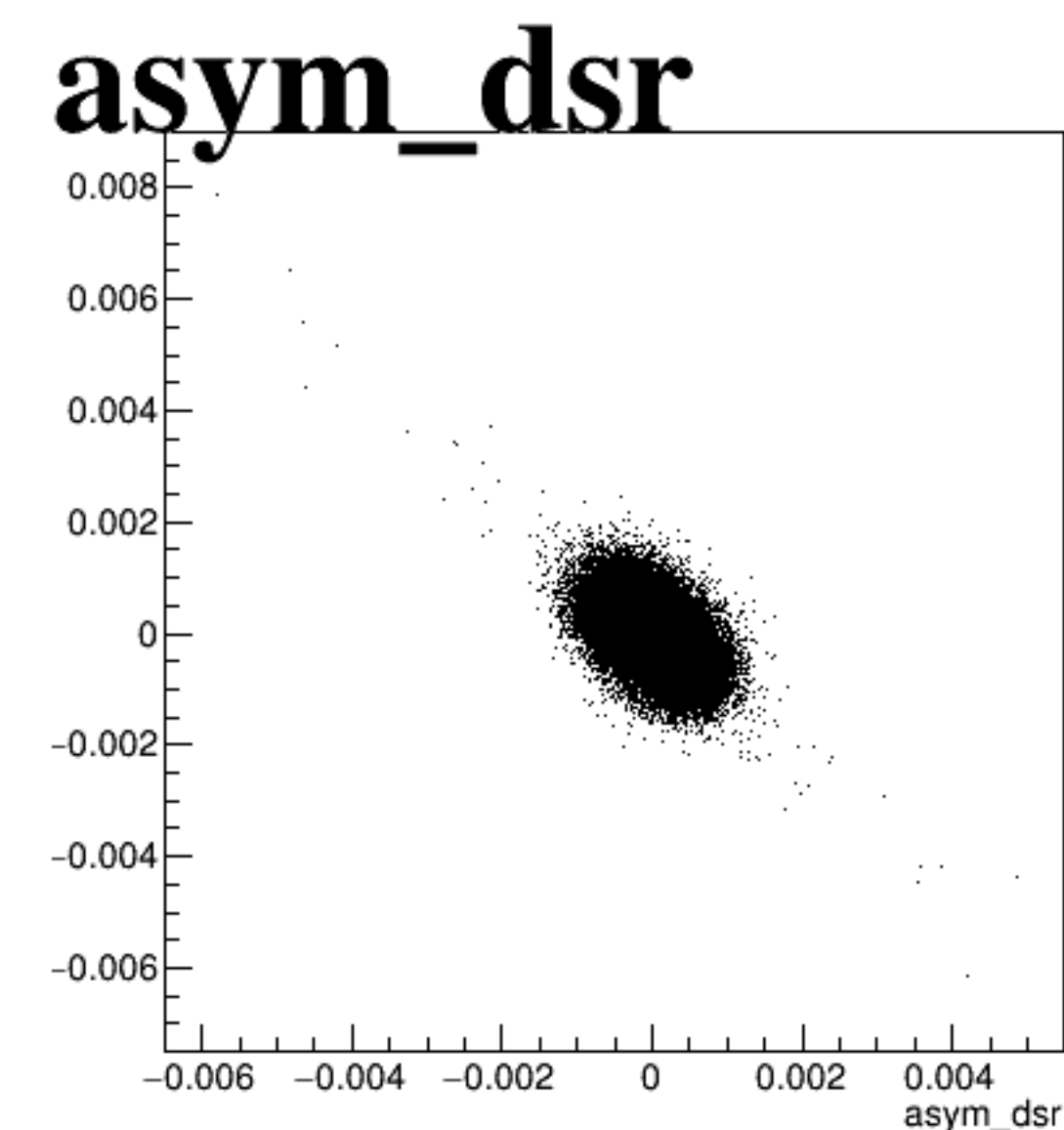
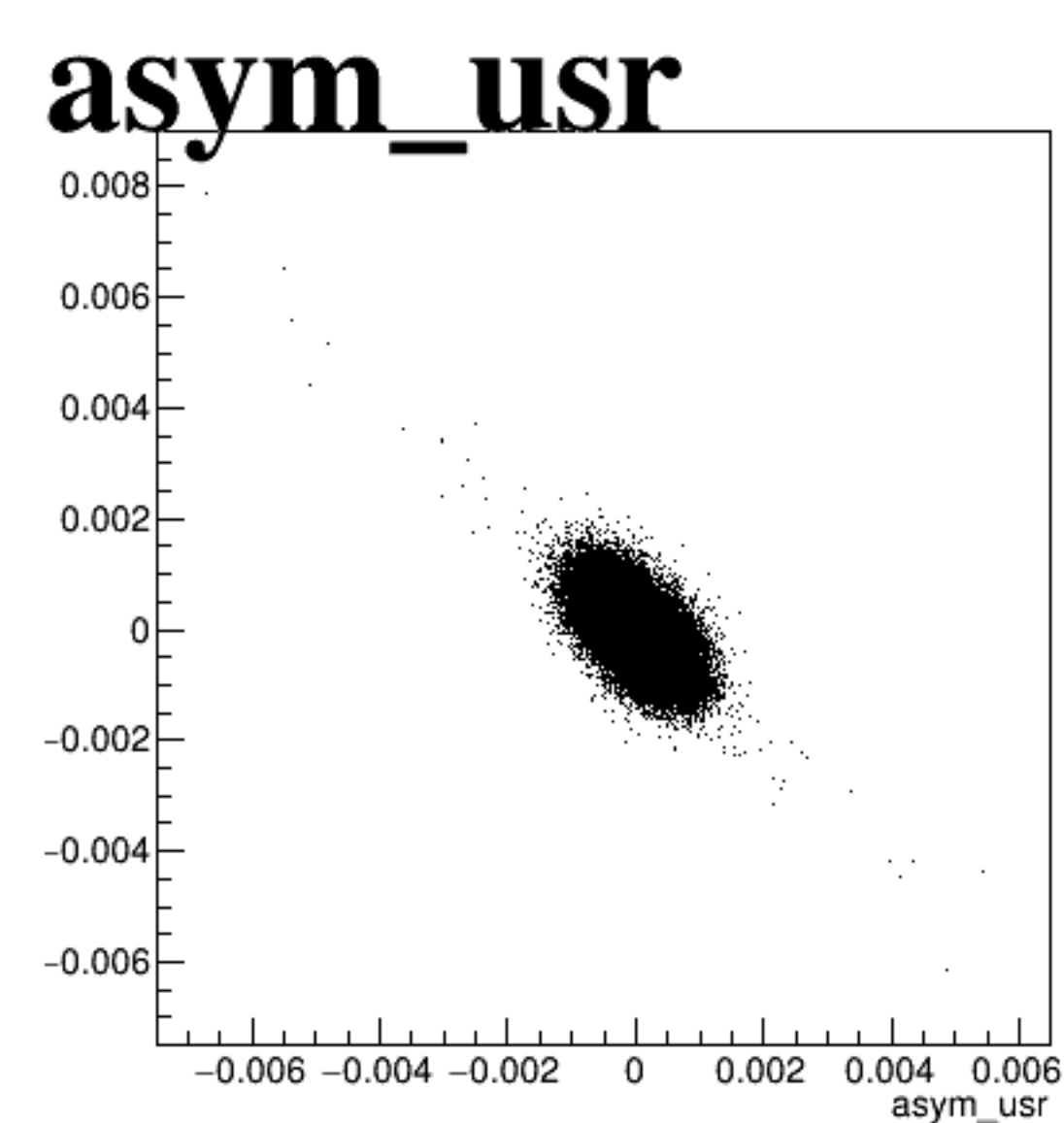
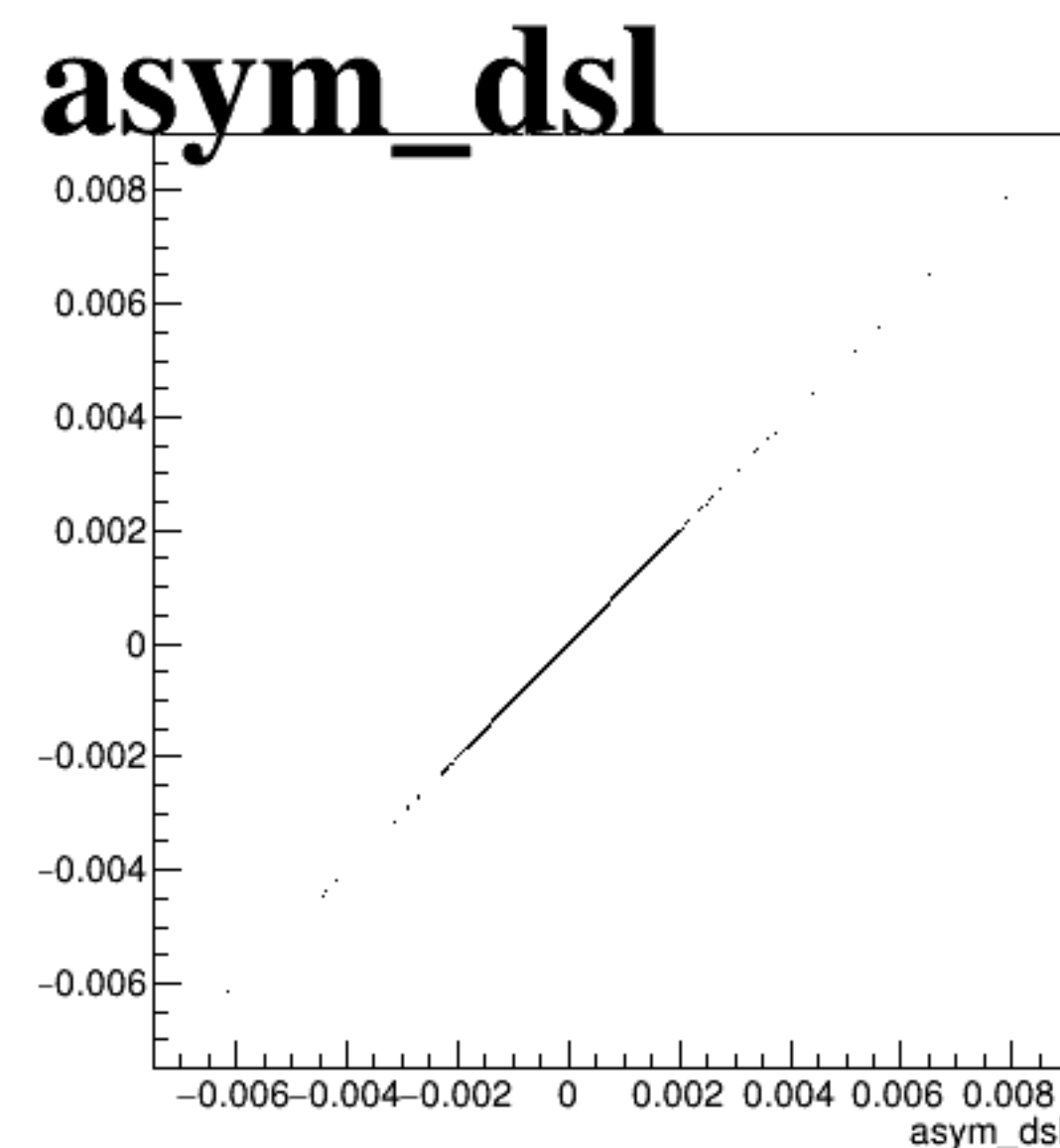
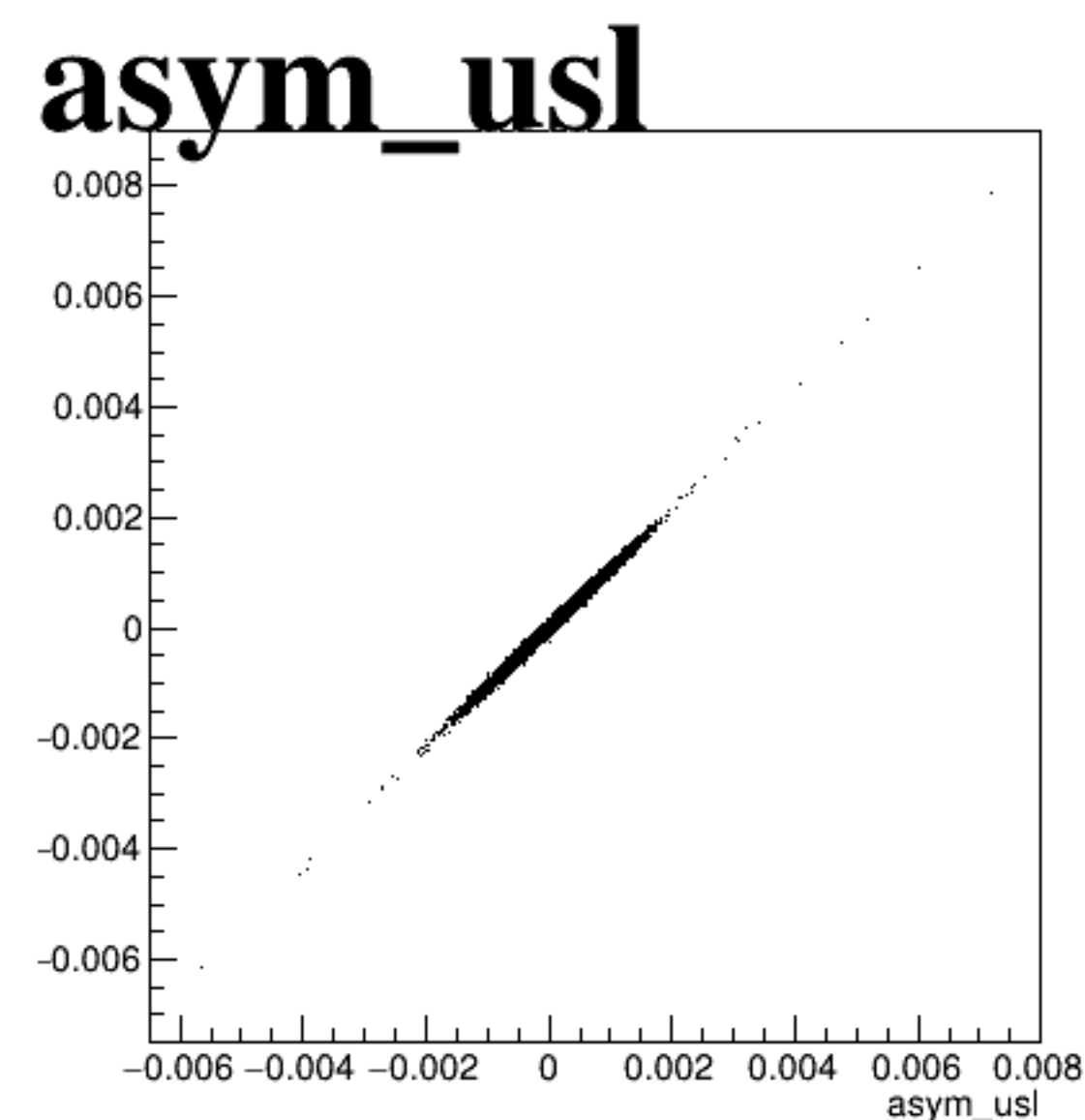
asym_dsr



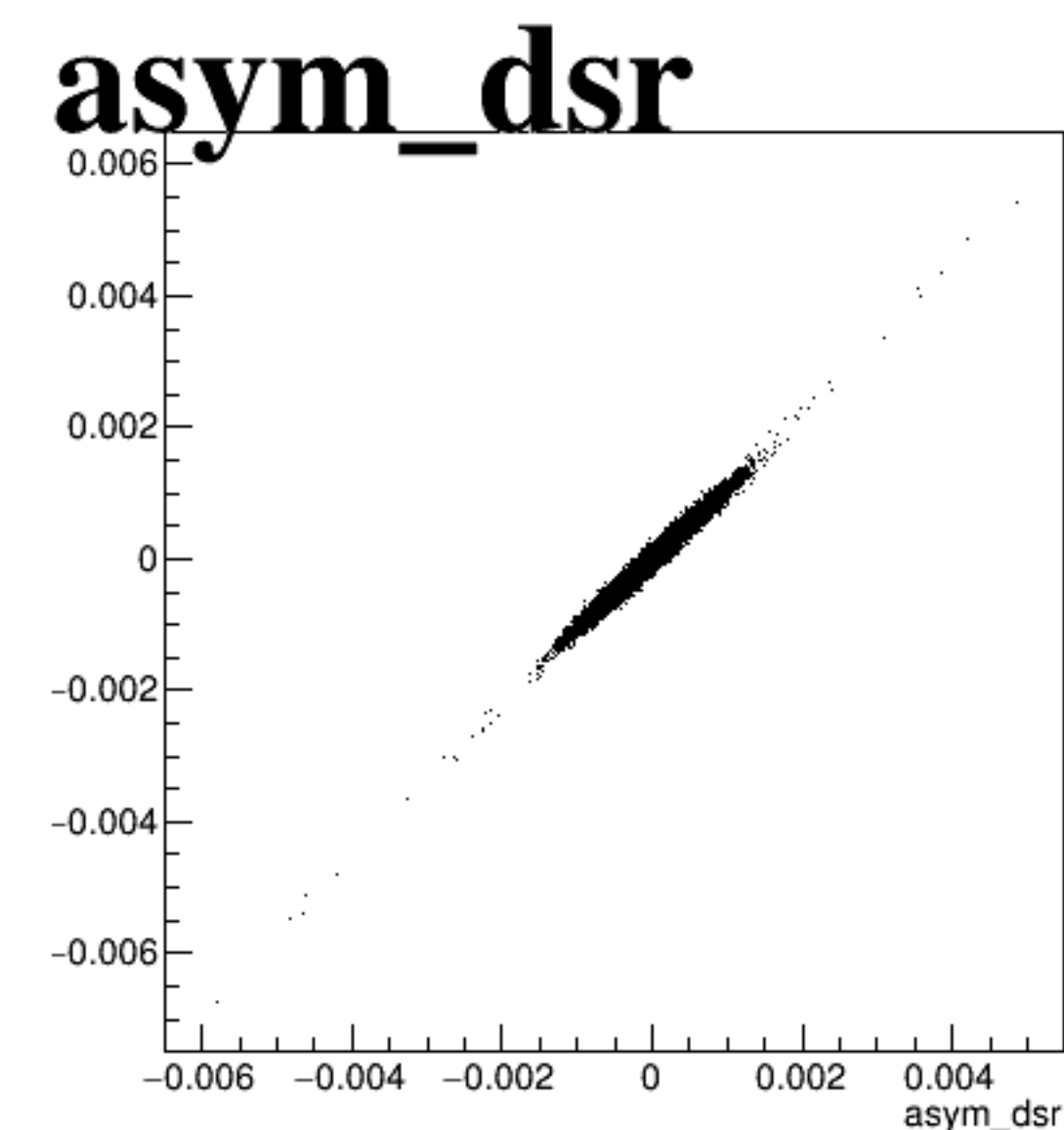
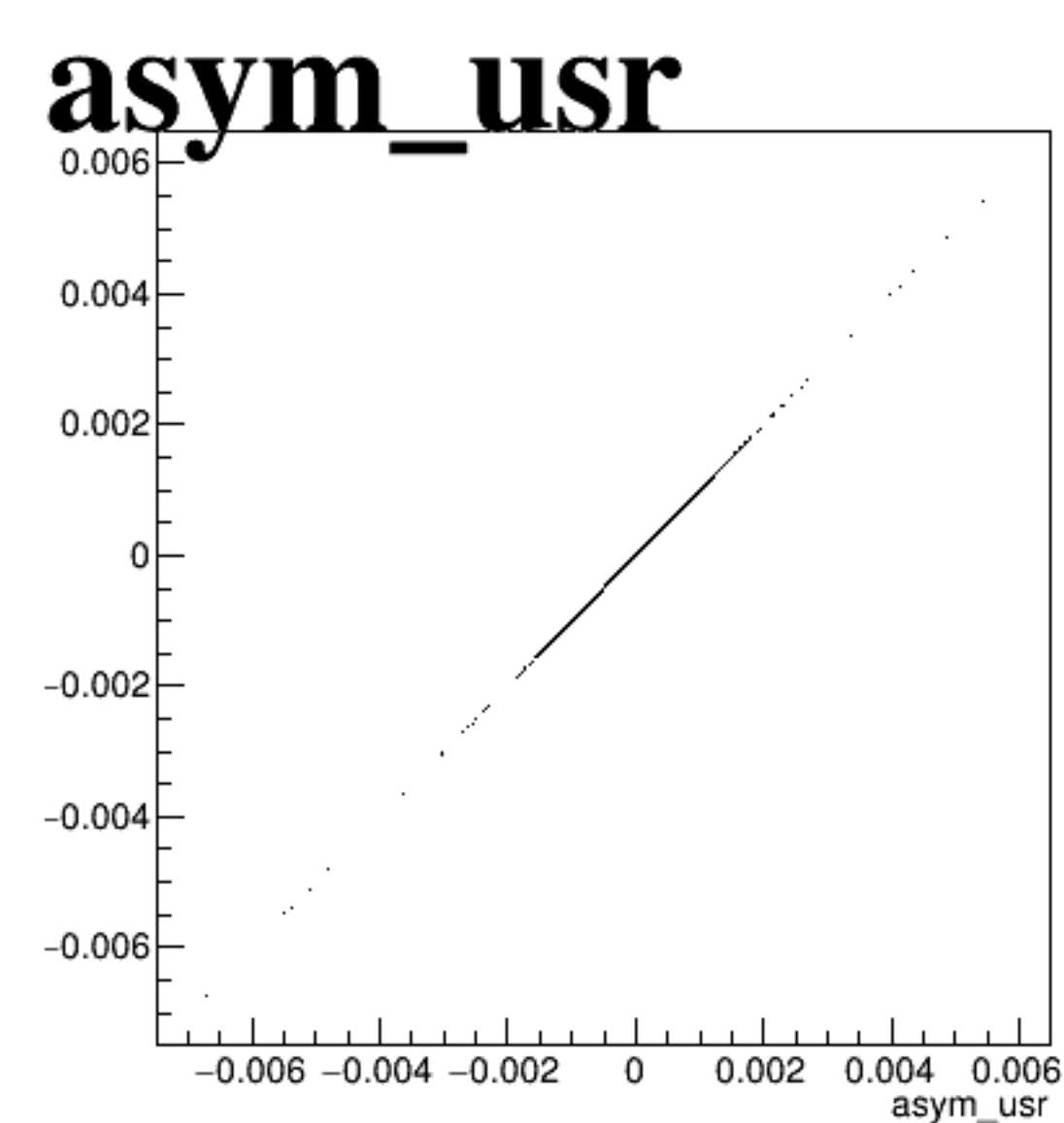
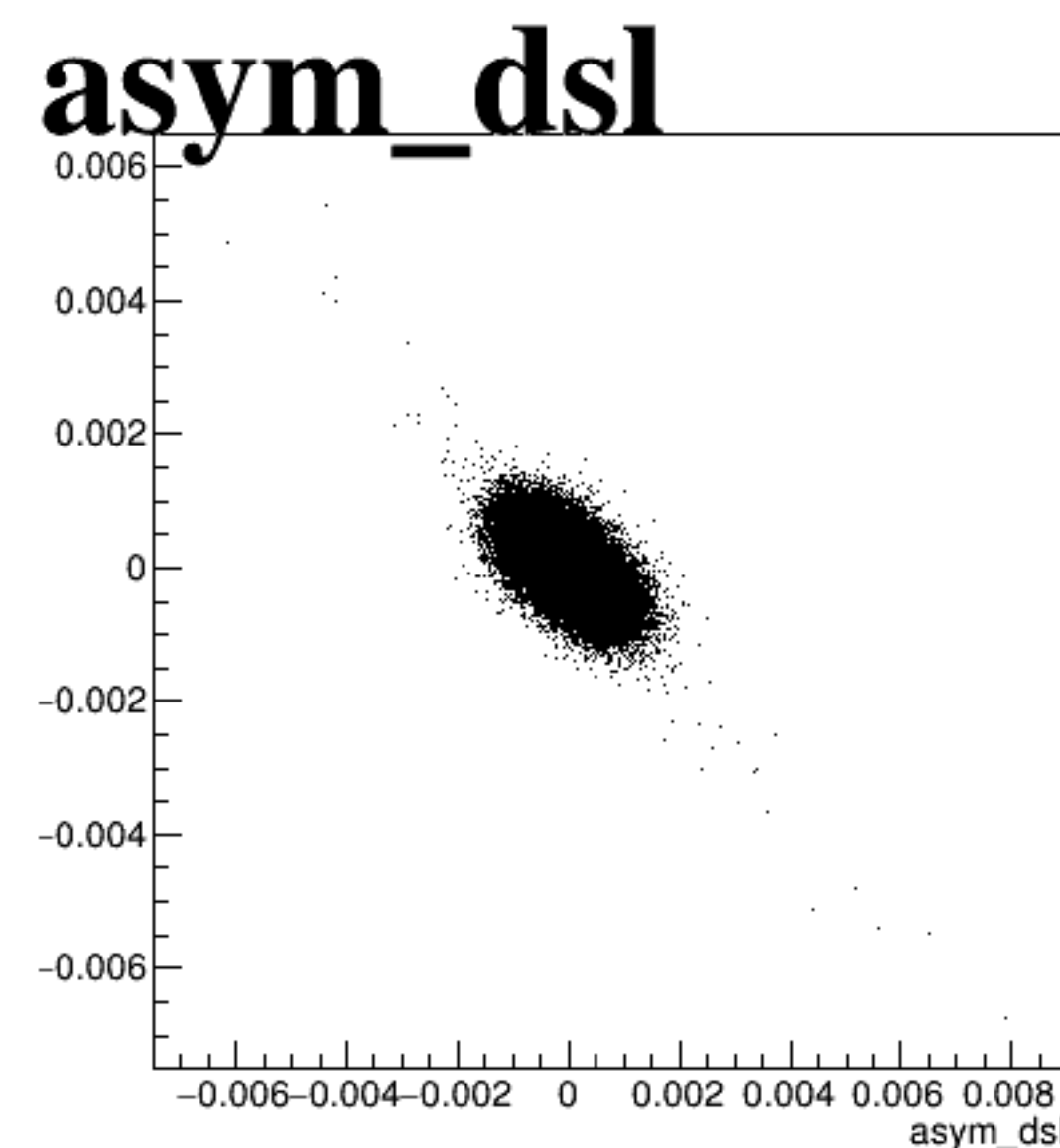
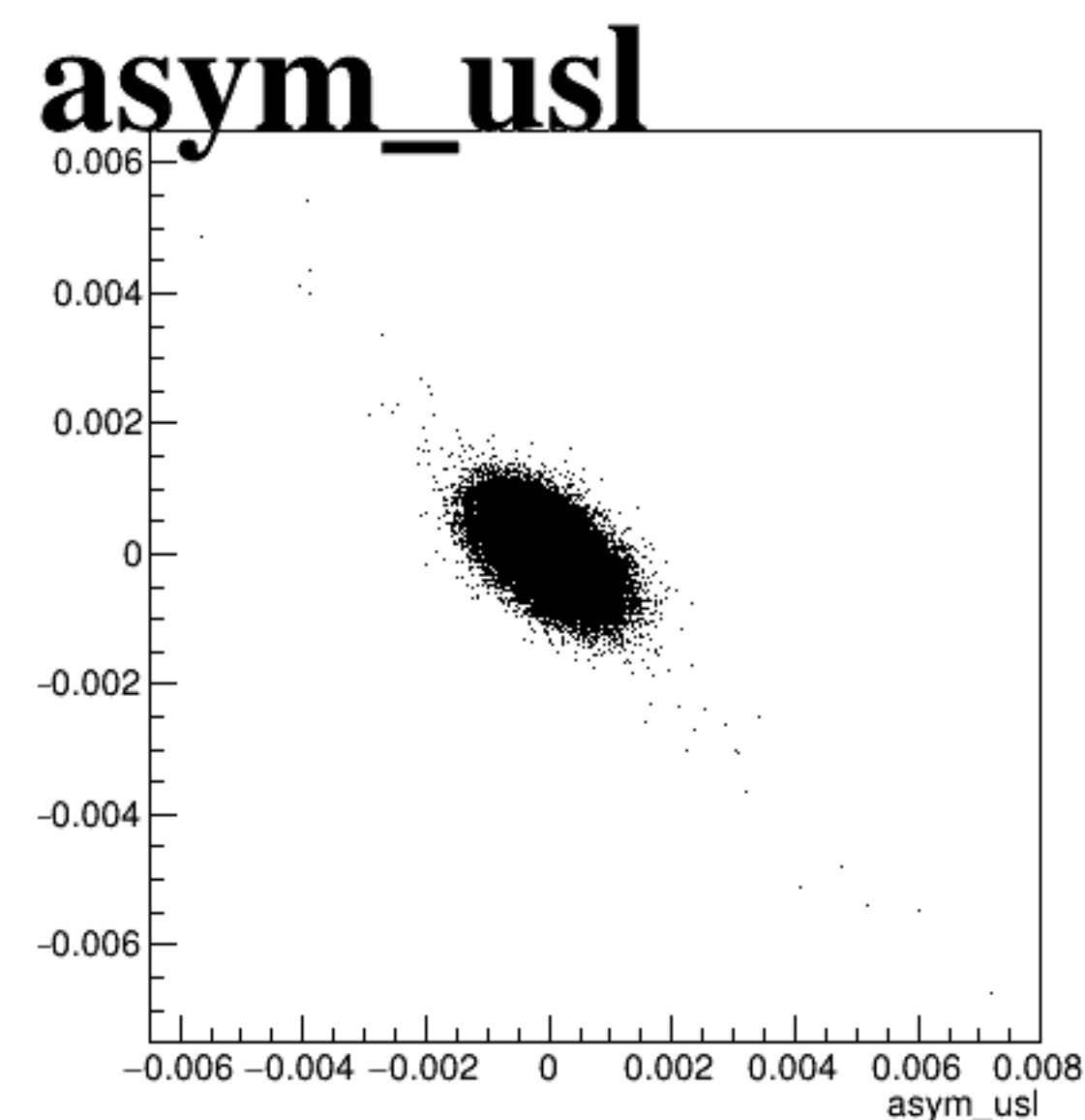
asym_usl



asym_dsl



asym_usr



asym_dsr

