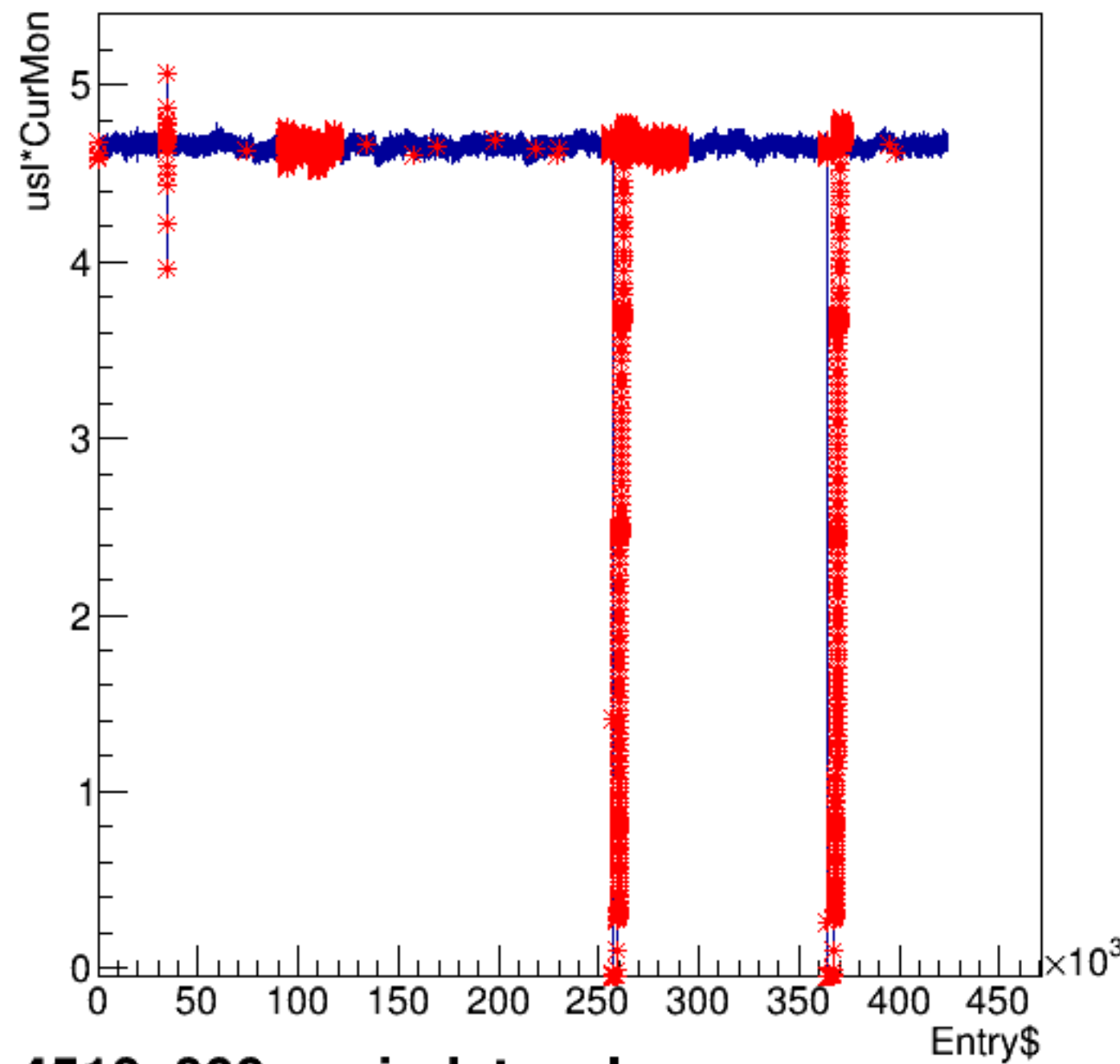
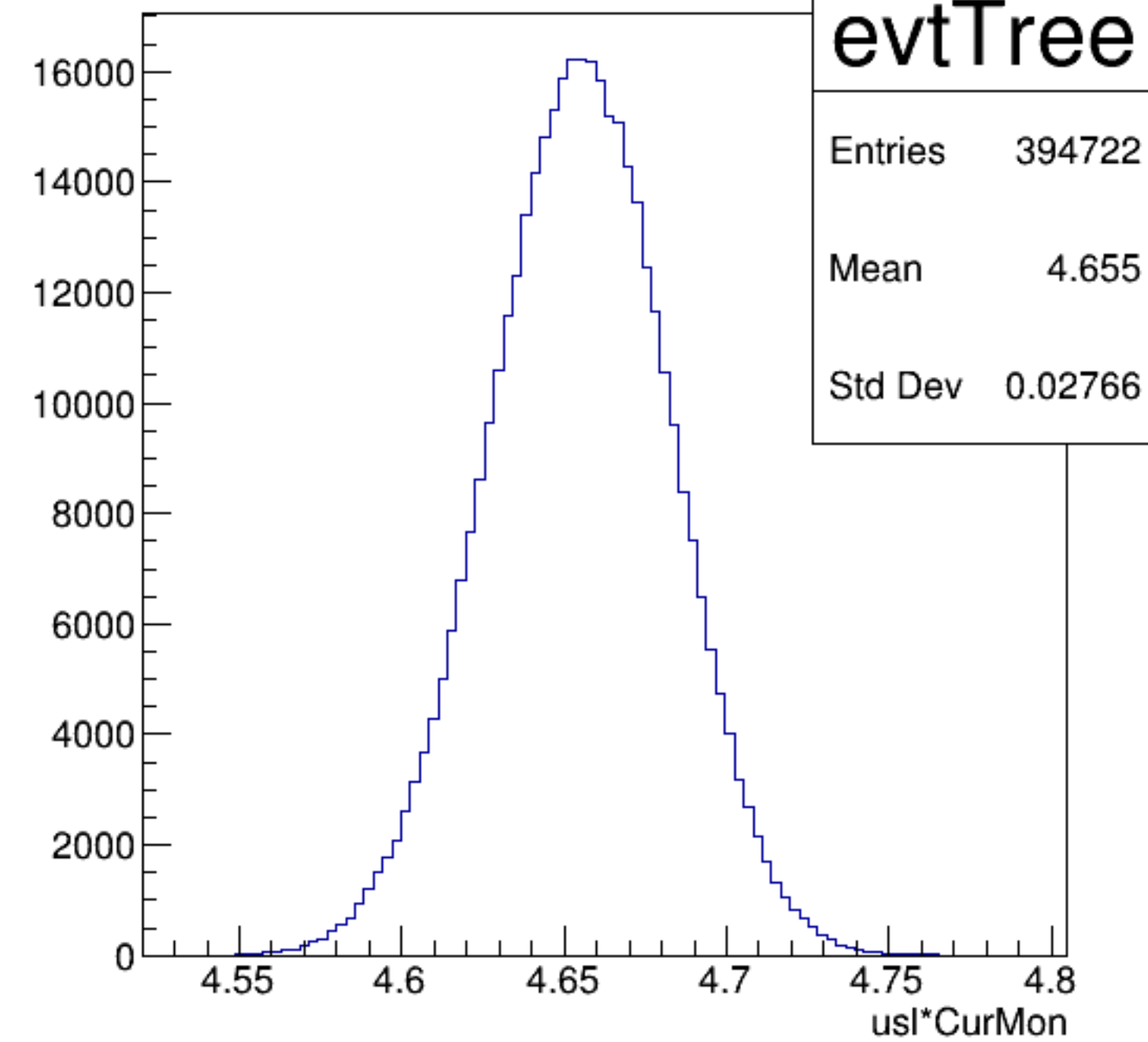


usl*CurMon:Entry\$



run4510_000_maindet_usl.png

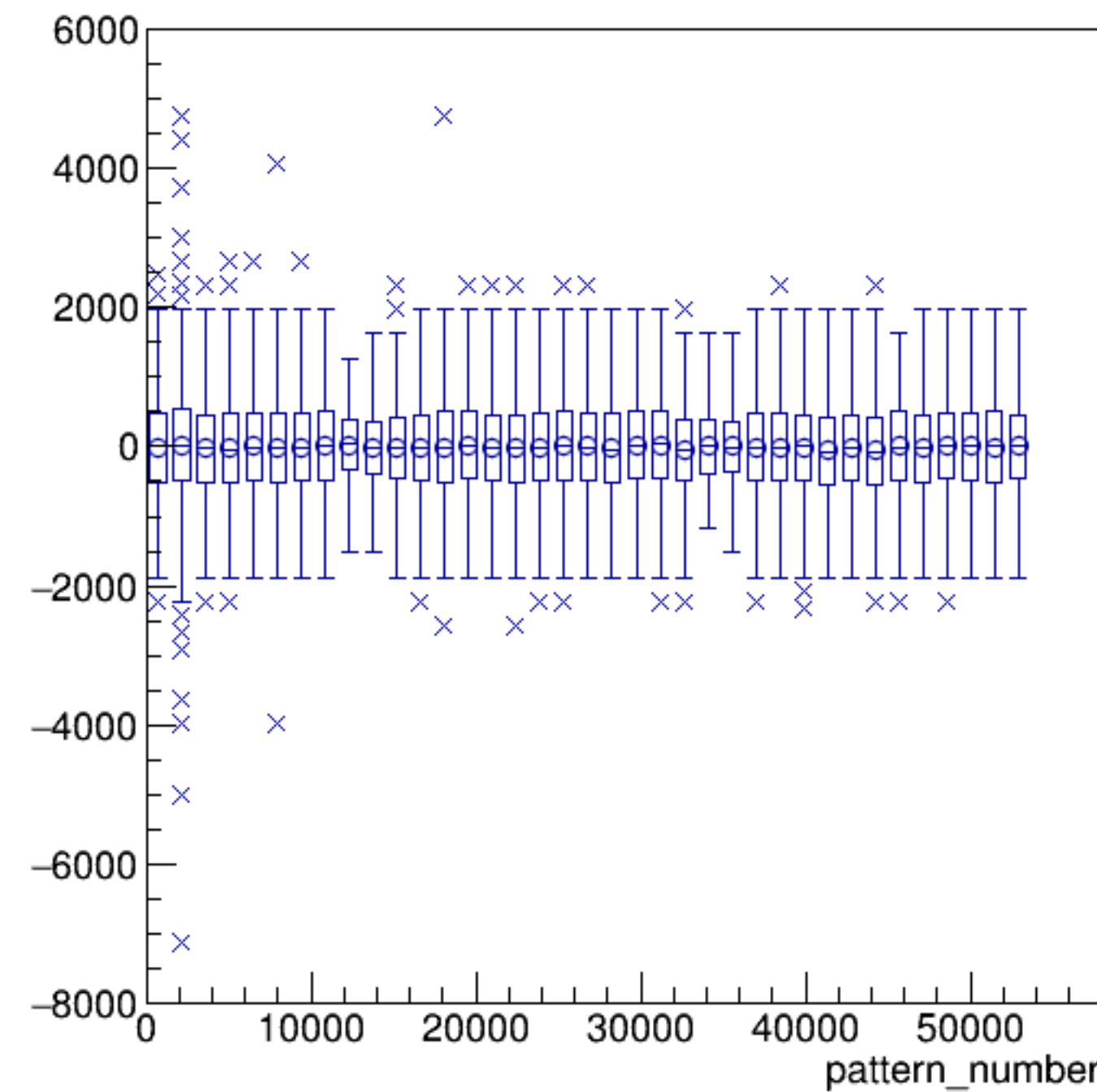
usl*CurMon {ErrorFlag==0}



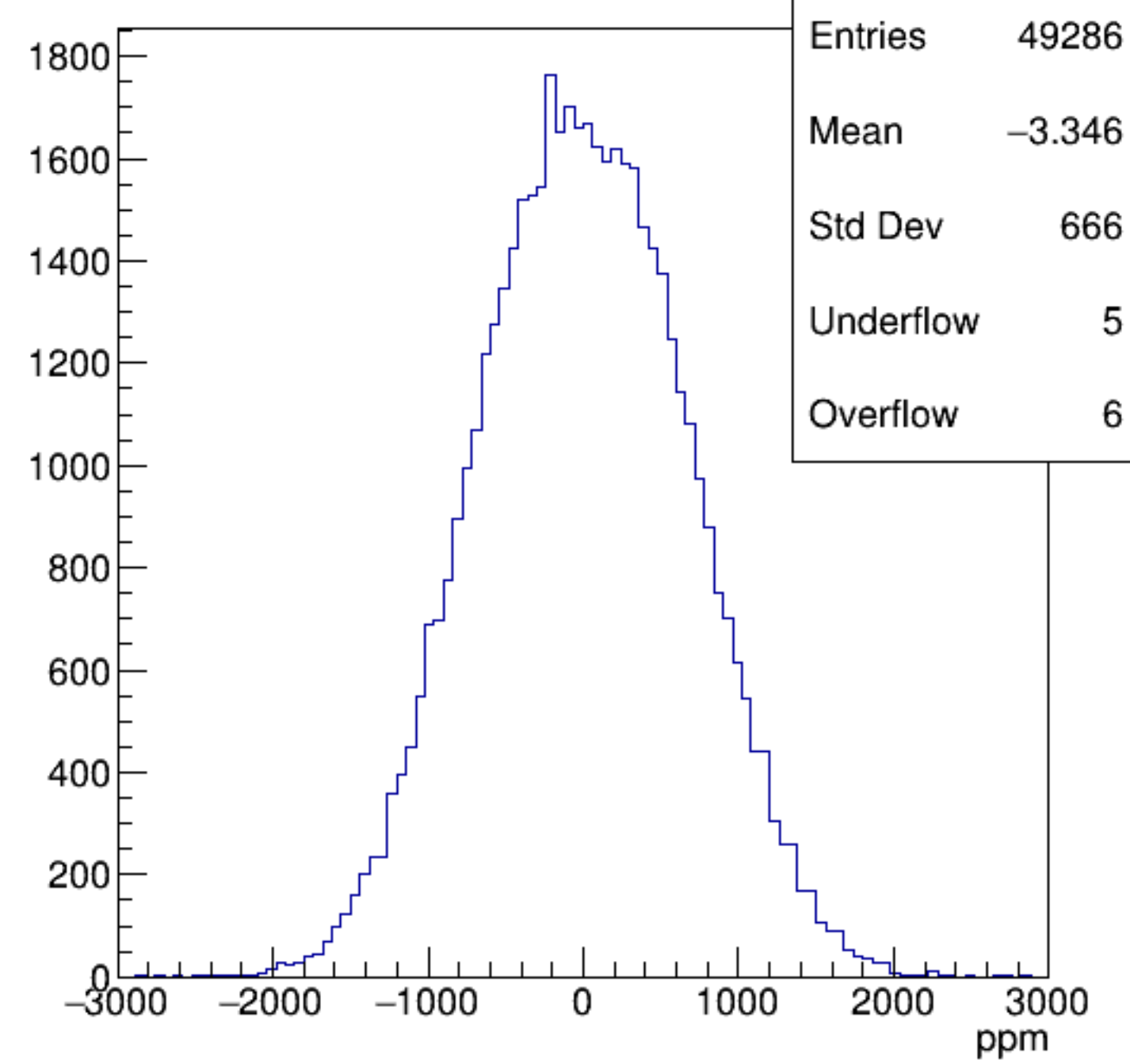
evtTree

Entries	394722
Mean	4.655
Std Dev	0.02766

asym_usl/ppm:pattern_number {ErrorFlag==0}

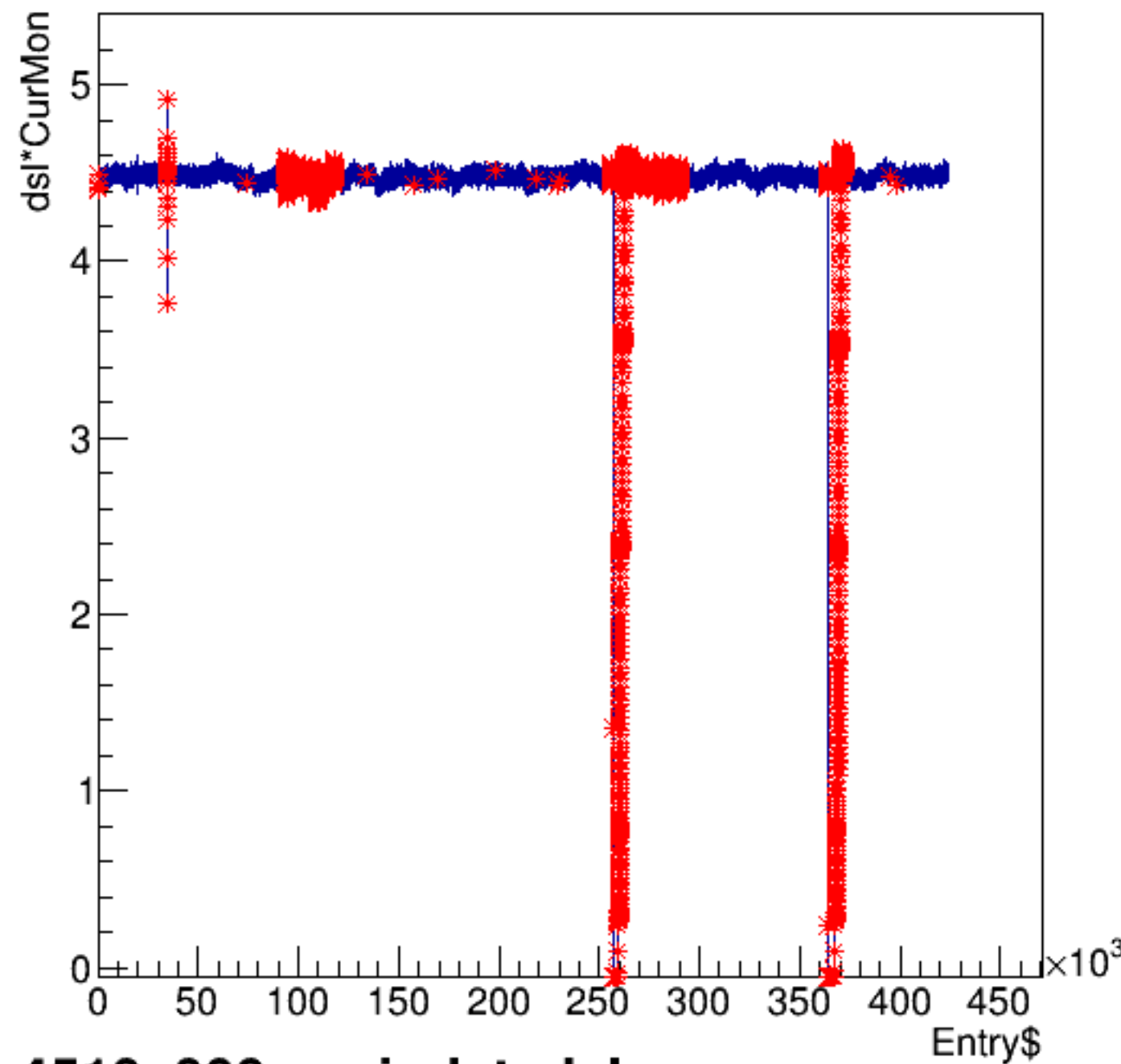


asym_usl

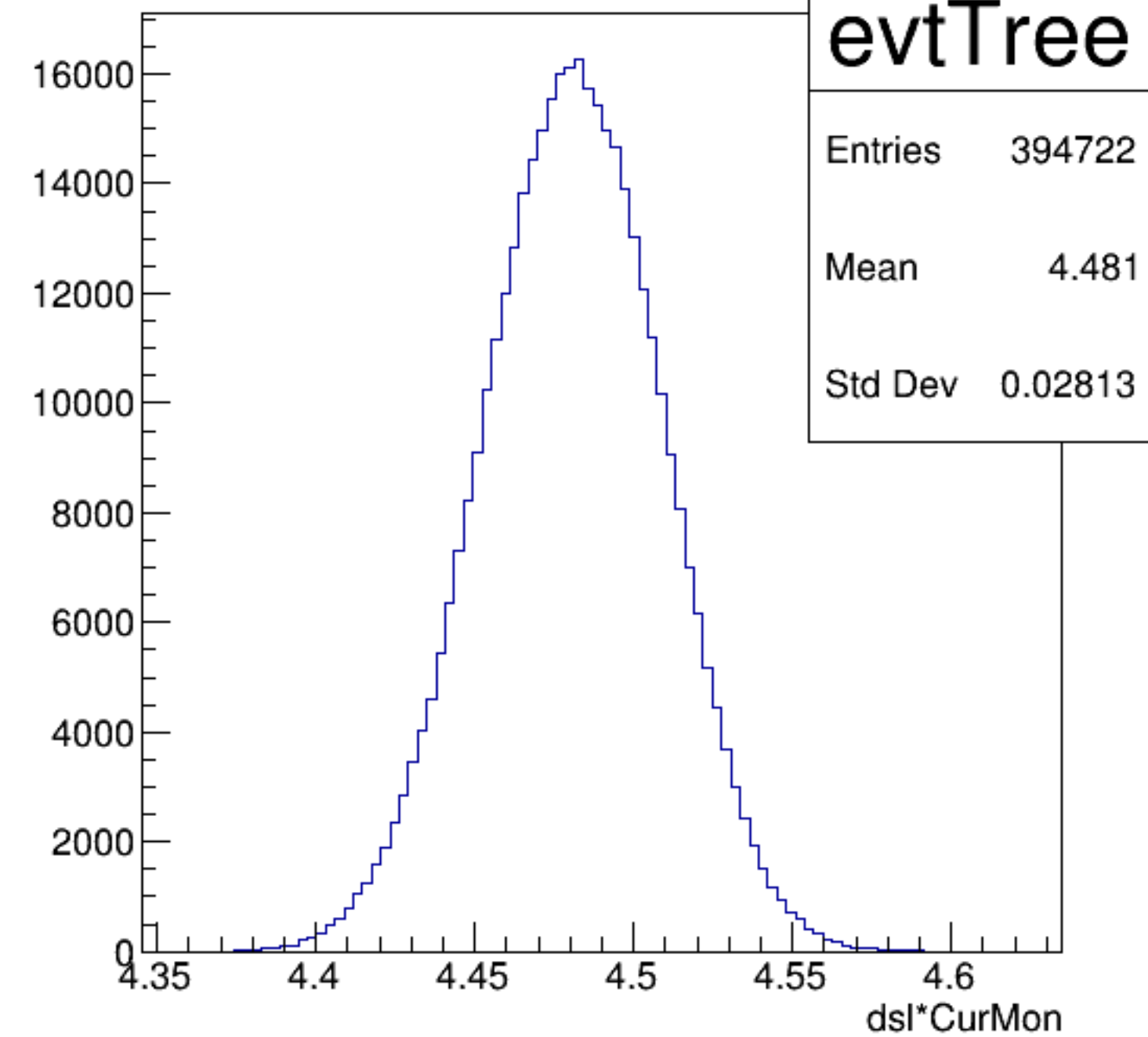


Entries	49286
Mean	-3.346
Std Dev	666
Underflow	5
Overflow	6

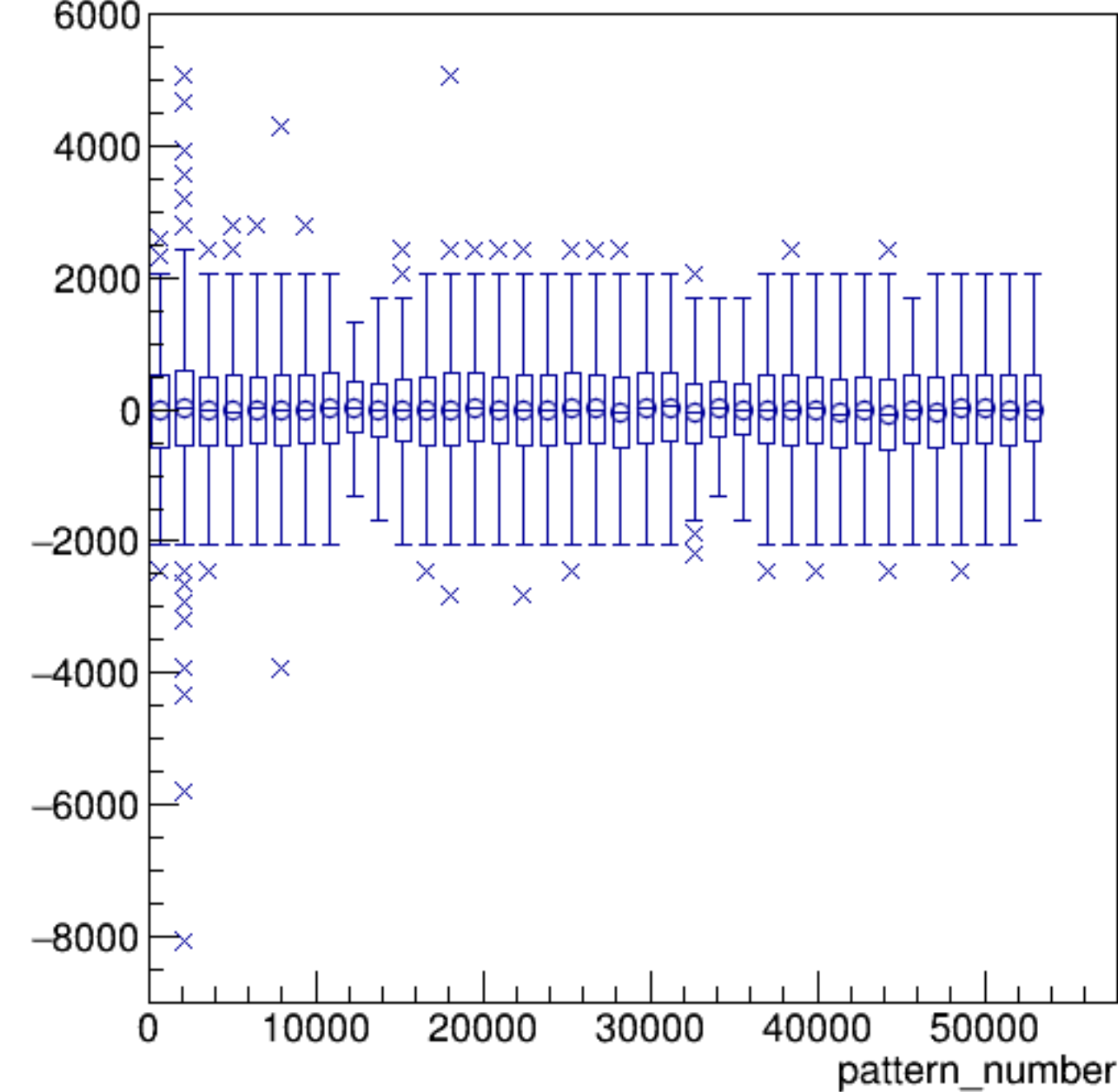
dsl*CurMon:Entry\$



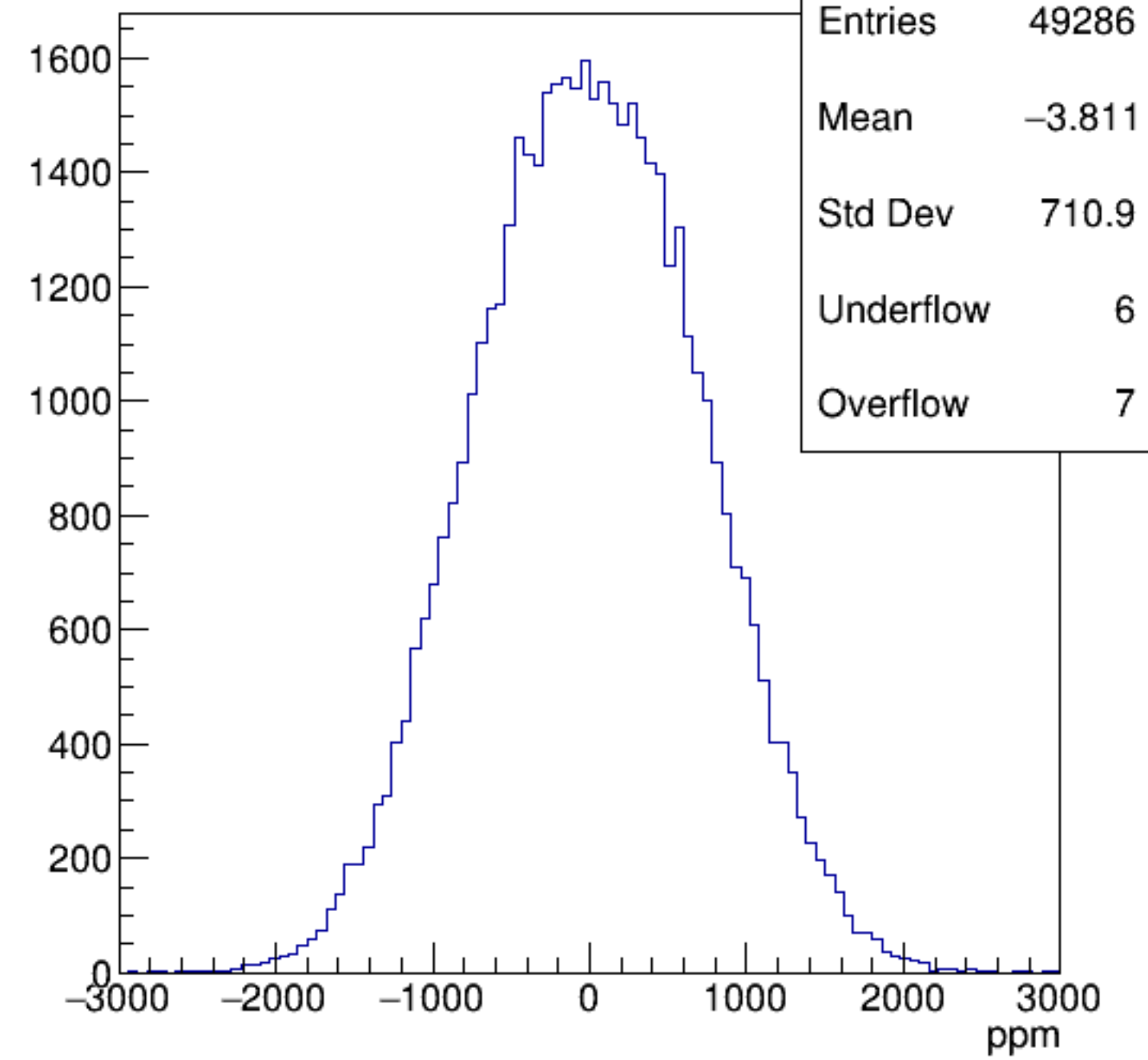
dsl*CurMon {ErrorFlag==0}



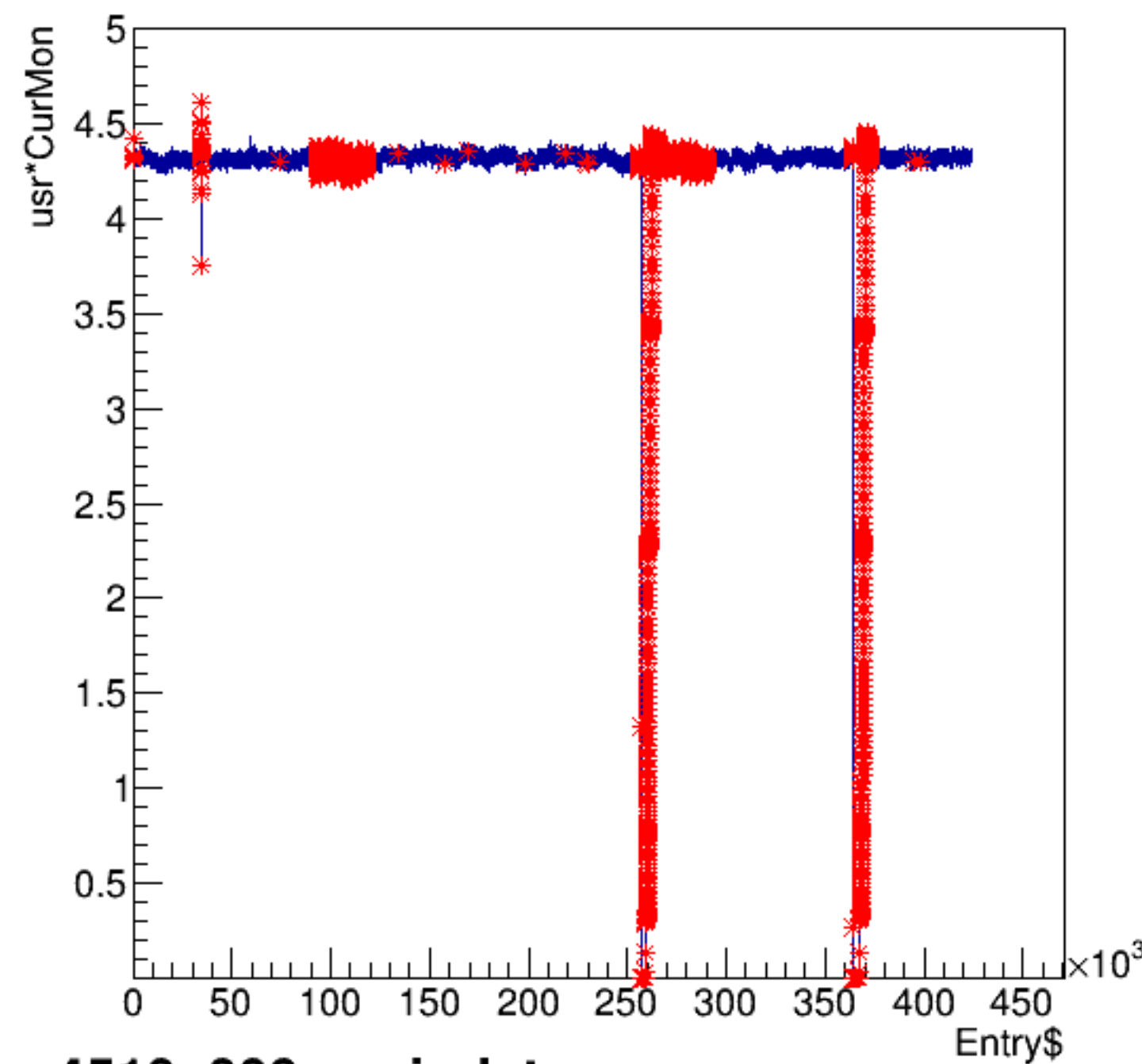
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl

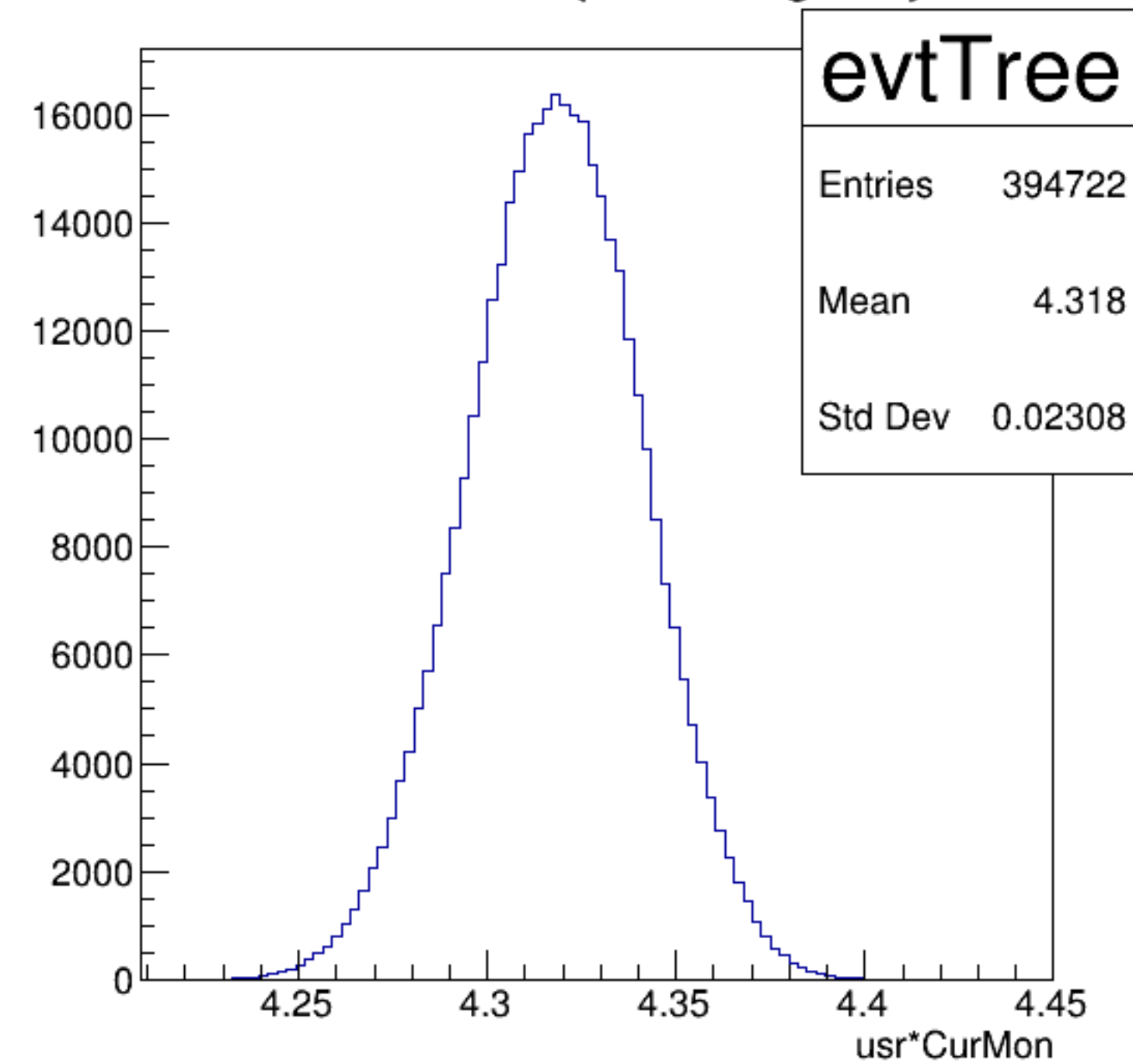


usr*CurMon:Entry\$

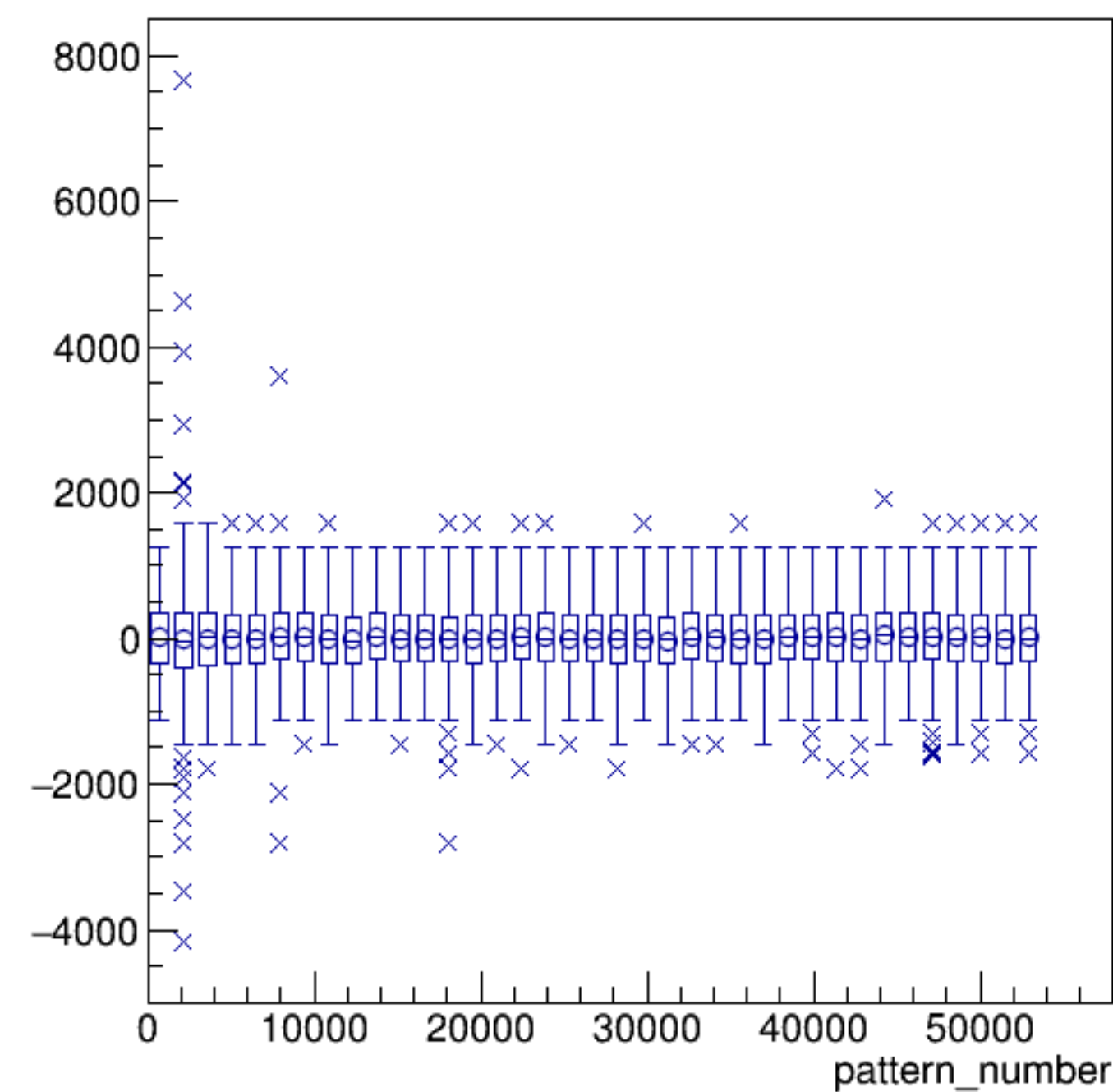


run4510_000_maindet_usr.png

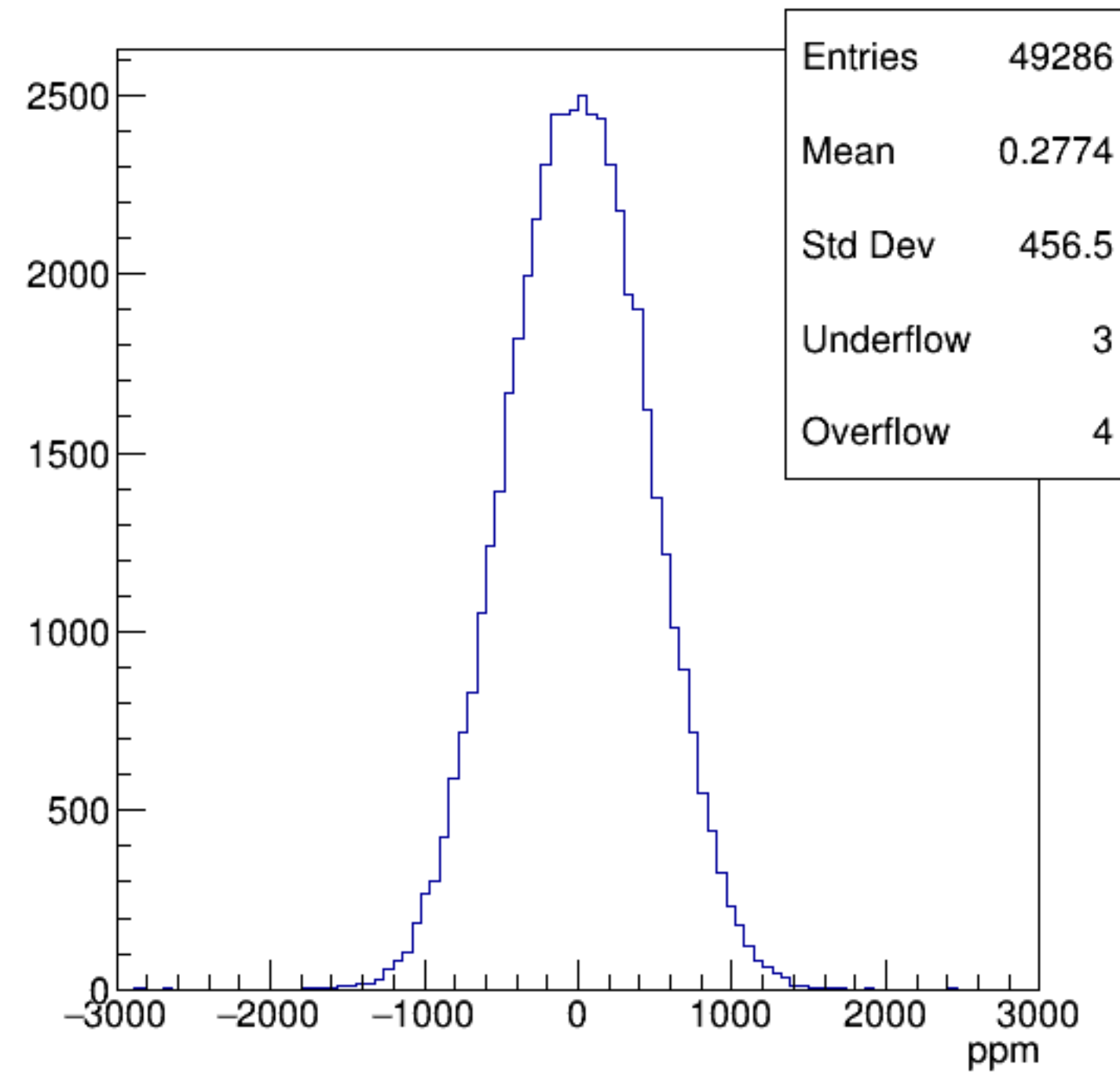
usr*CurMon {ErrorFlag==0}



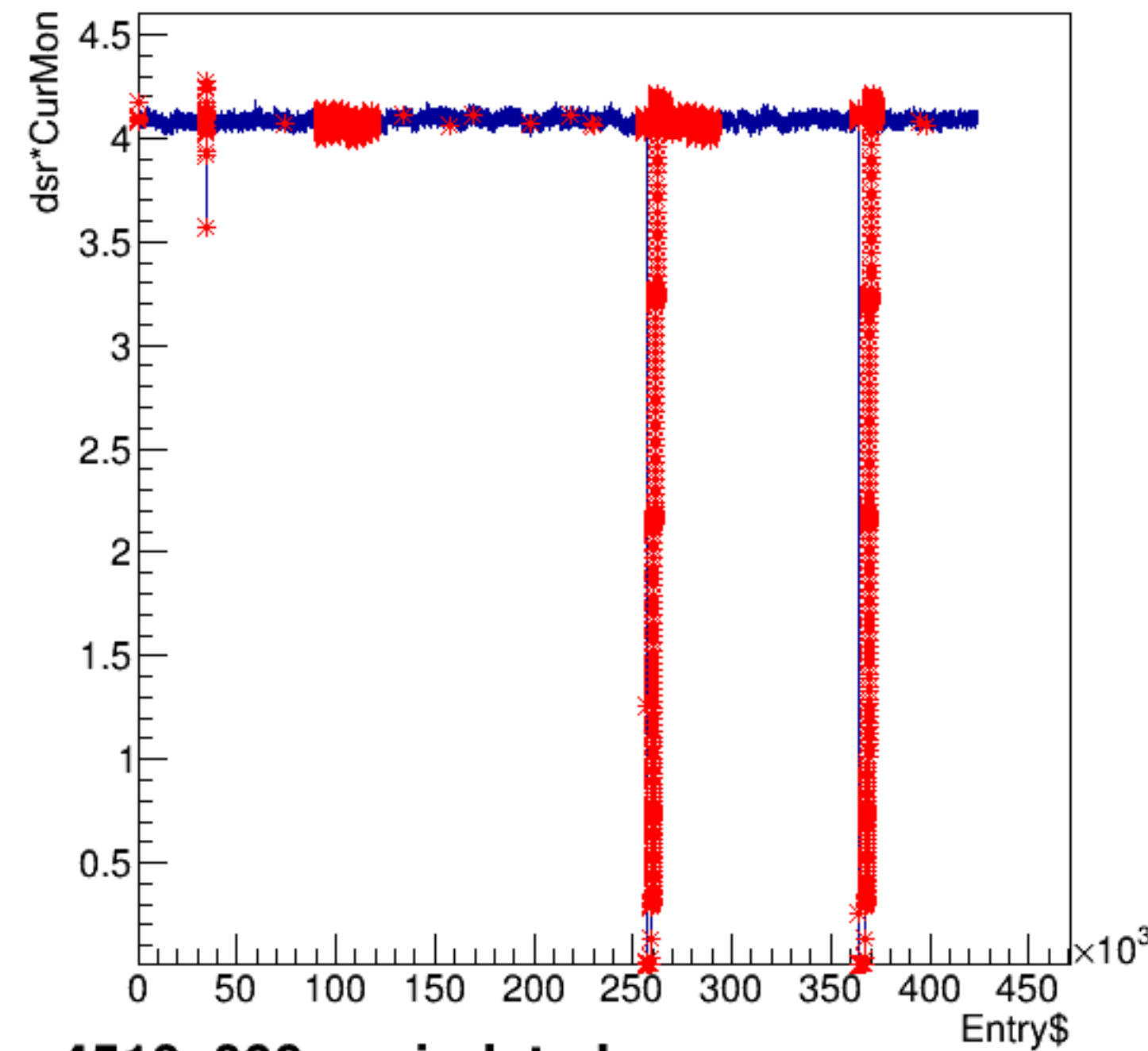
asym_usr/ppm:pattern_number {ErrorFlag==0}



asym_usr

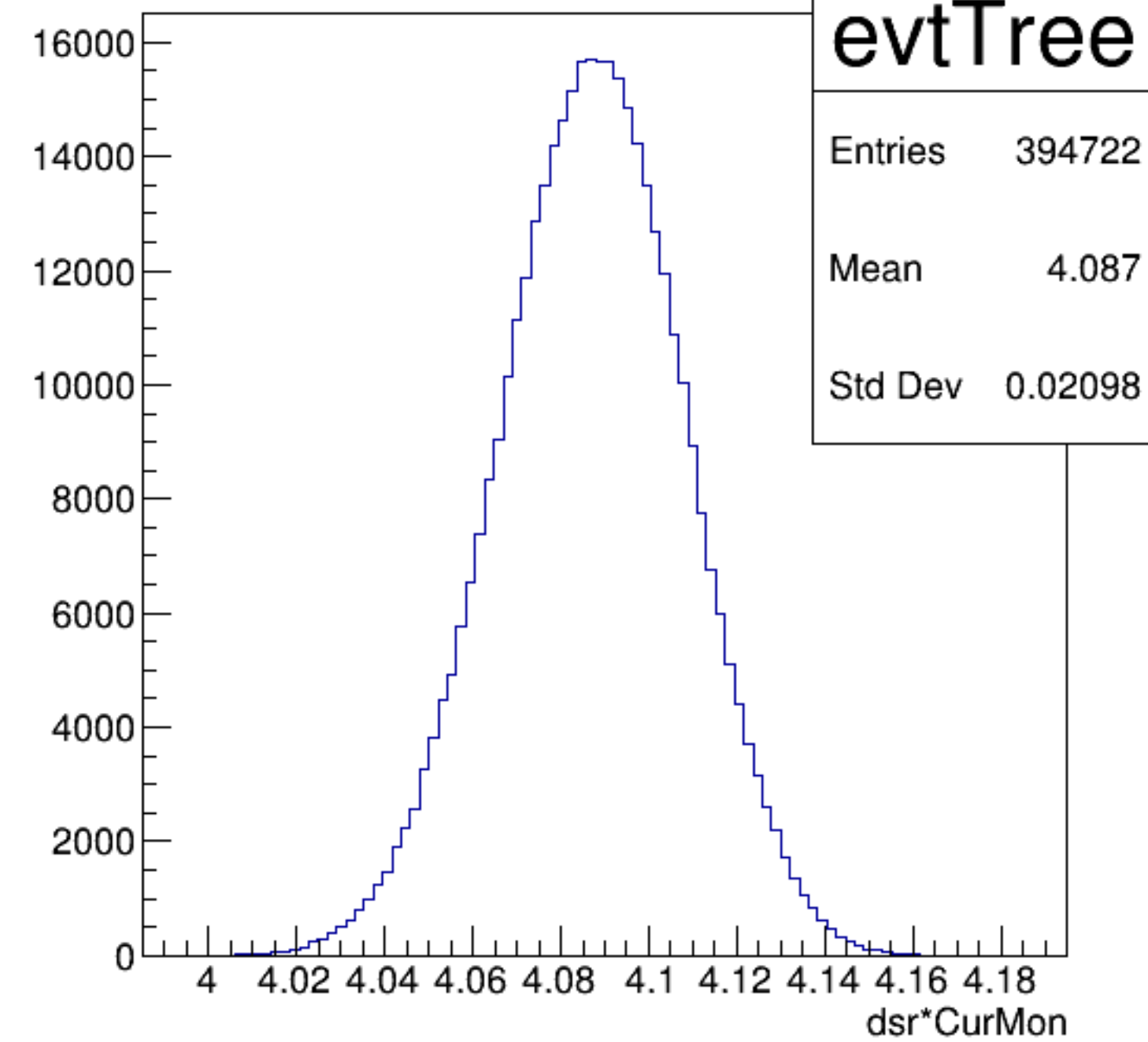


dsr*CurMon:Entry\$

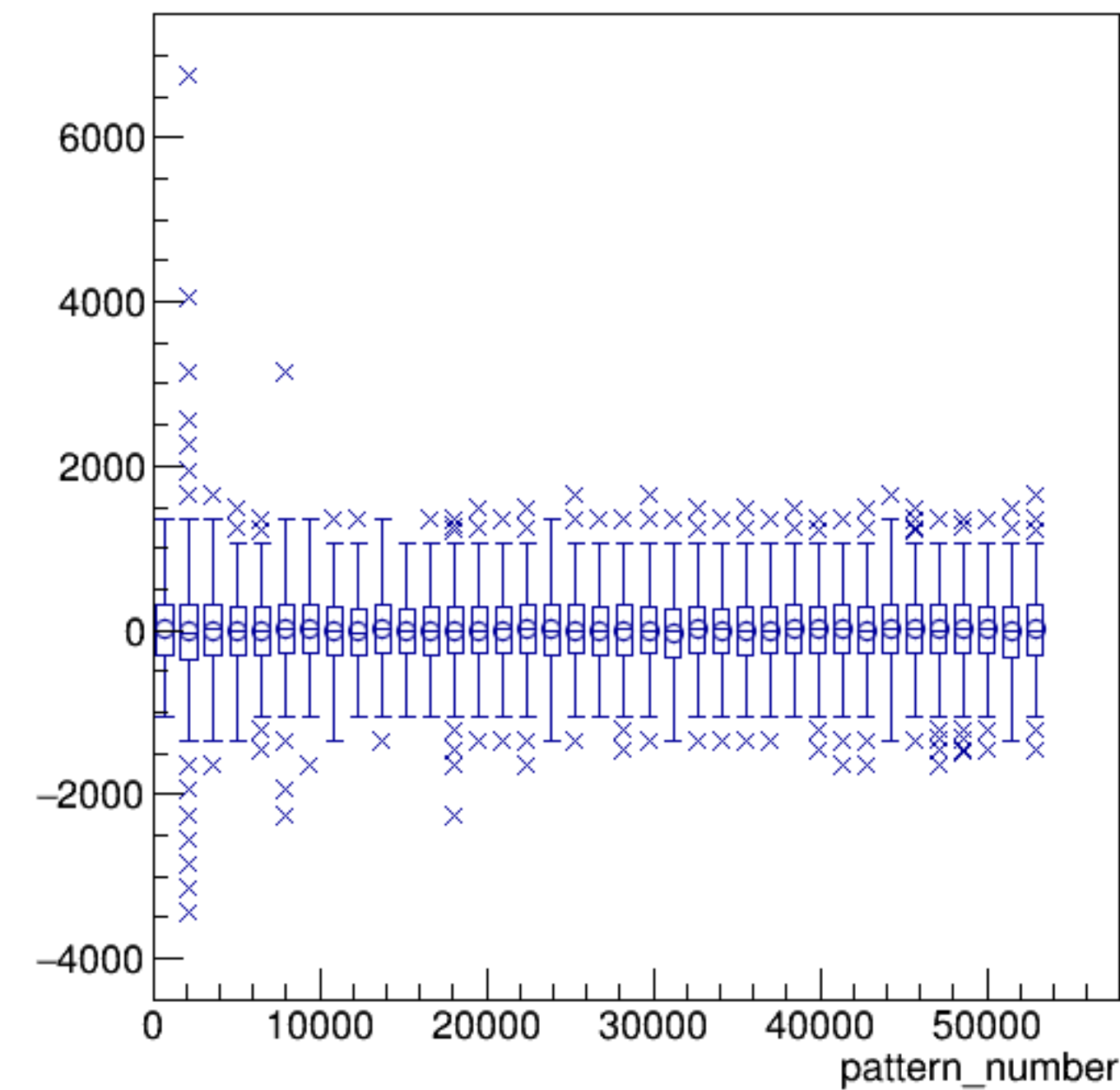


run4510_000_maindet_dsr.png

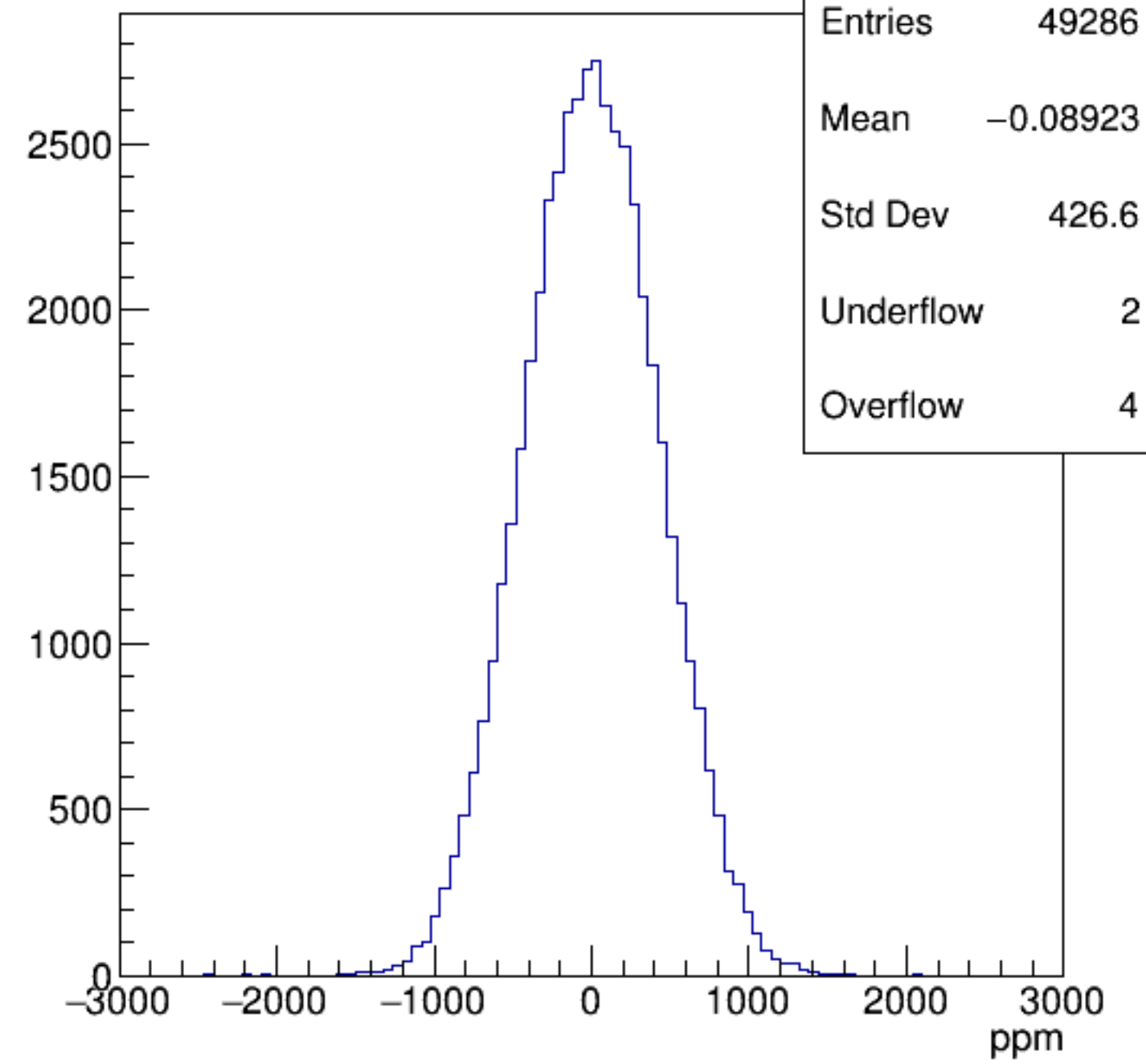
dsr*CurMon {ErrorFlag==0}



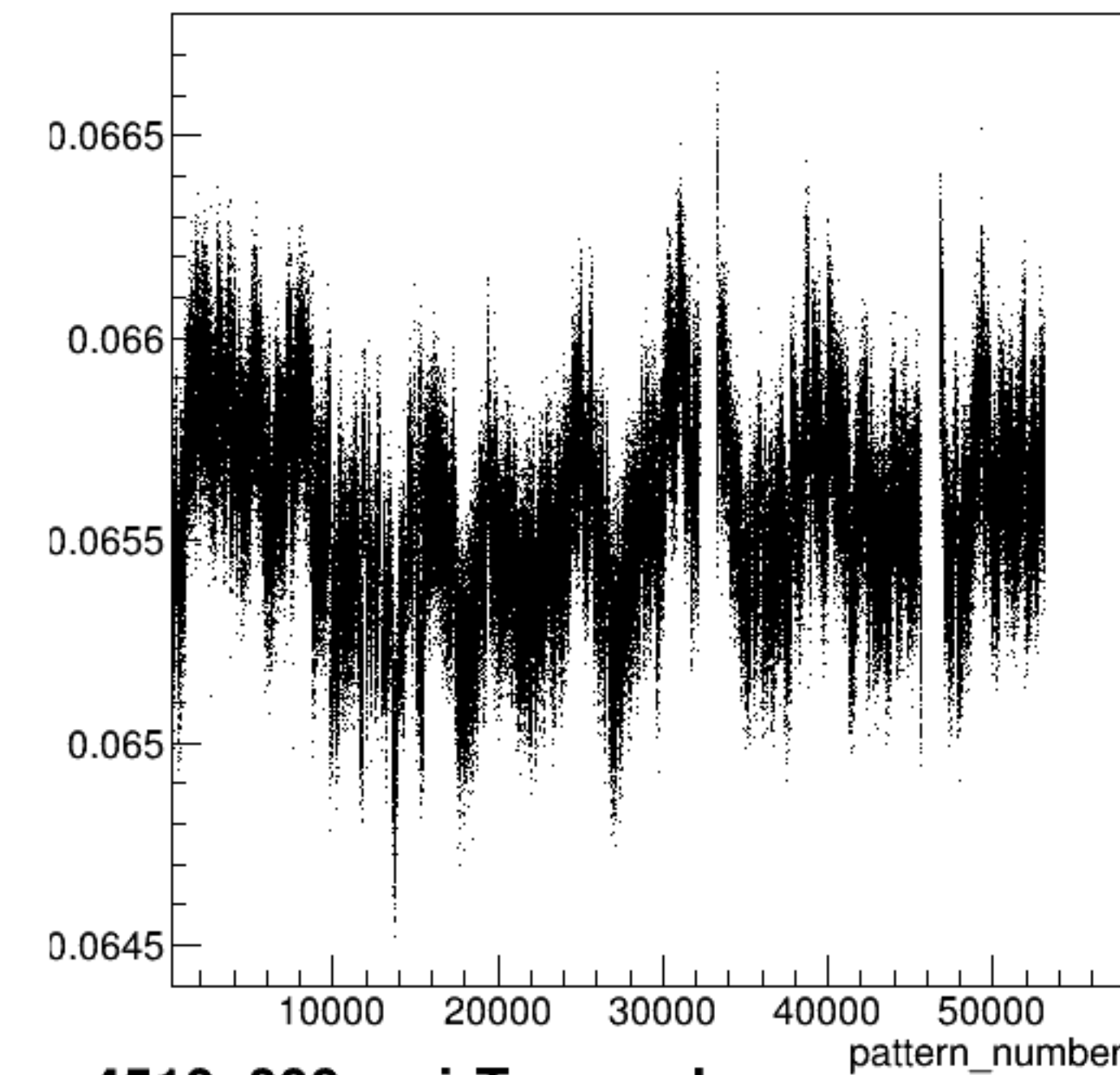
asym_dsr/ppm:pattern_number {ErrorFlag==0}



asym_dsr

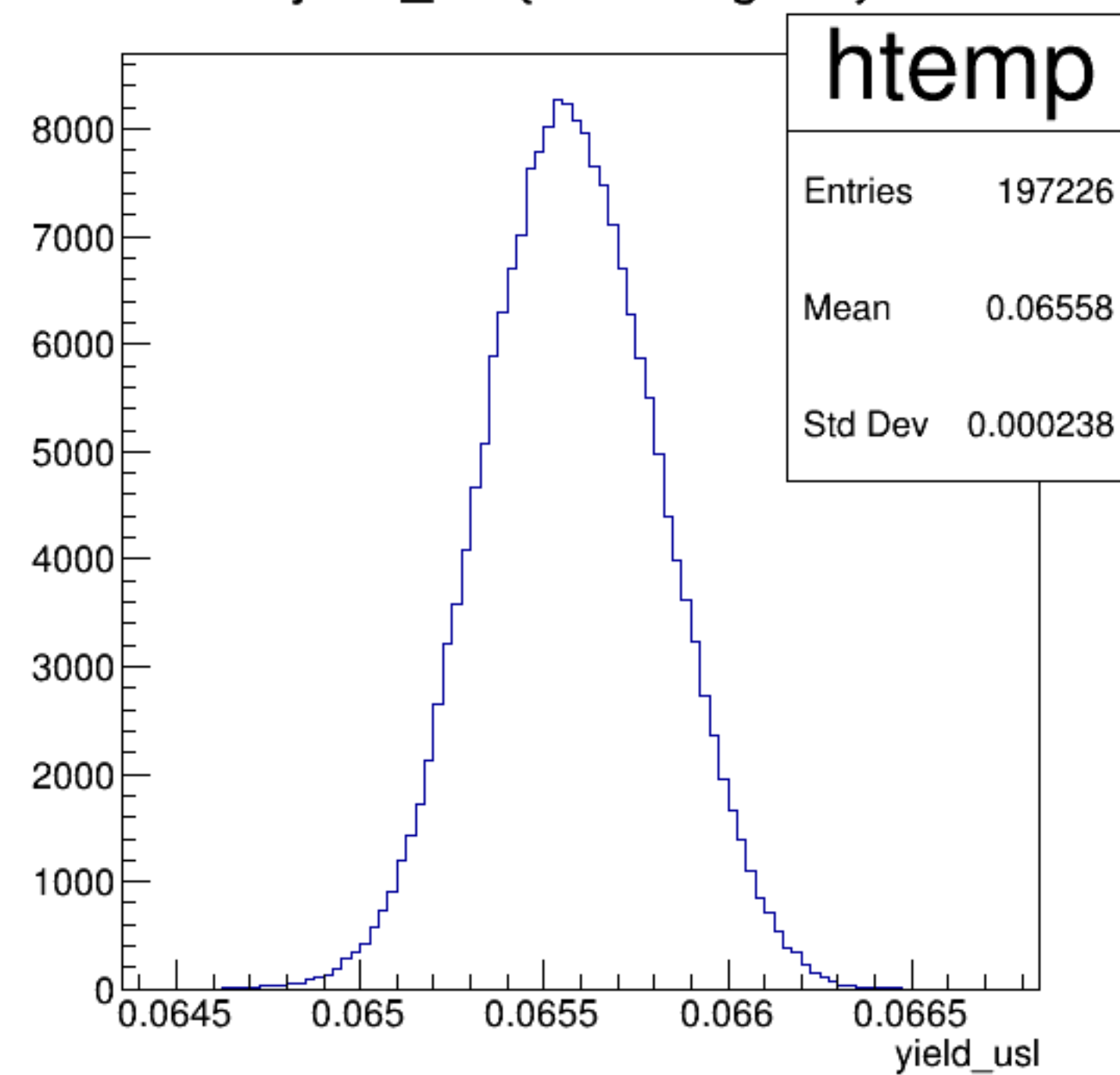


yield_usl:pattern_number {ErrorFlag==0}

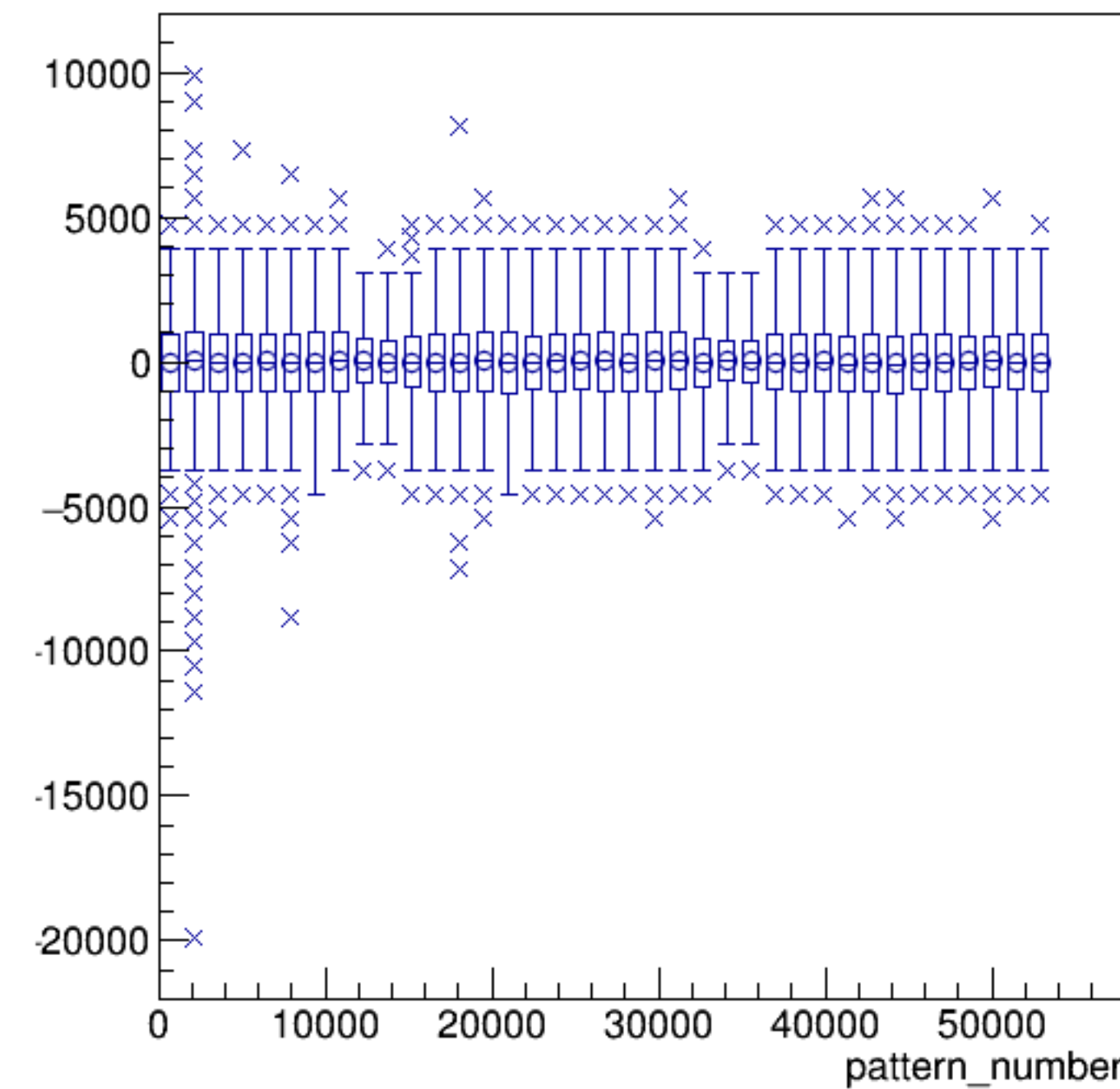


run4510_000_pairTree_usl.png

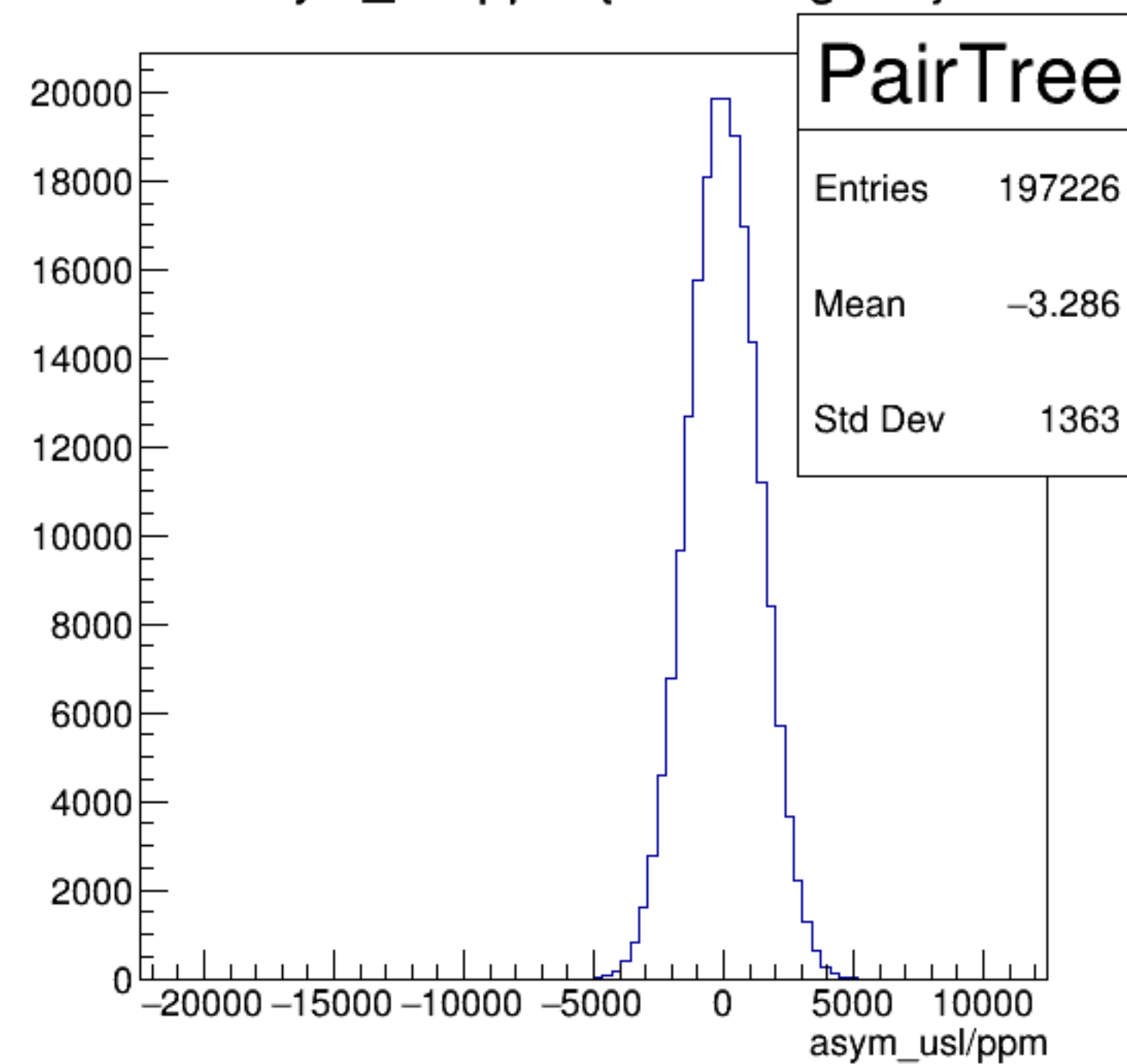
yield_usl {ErrorFlag==0}



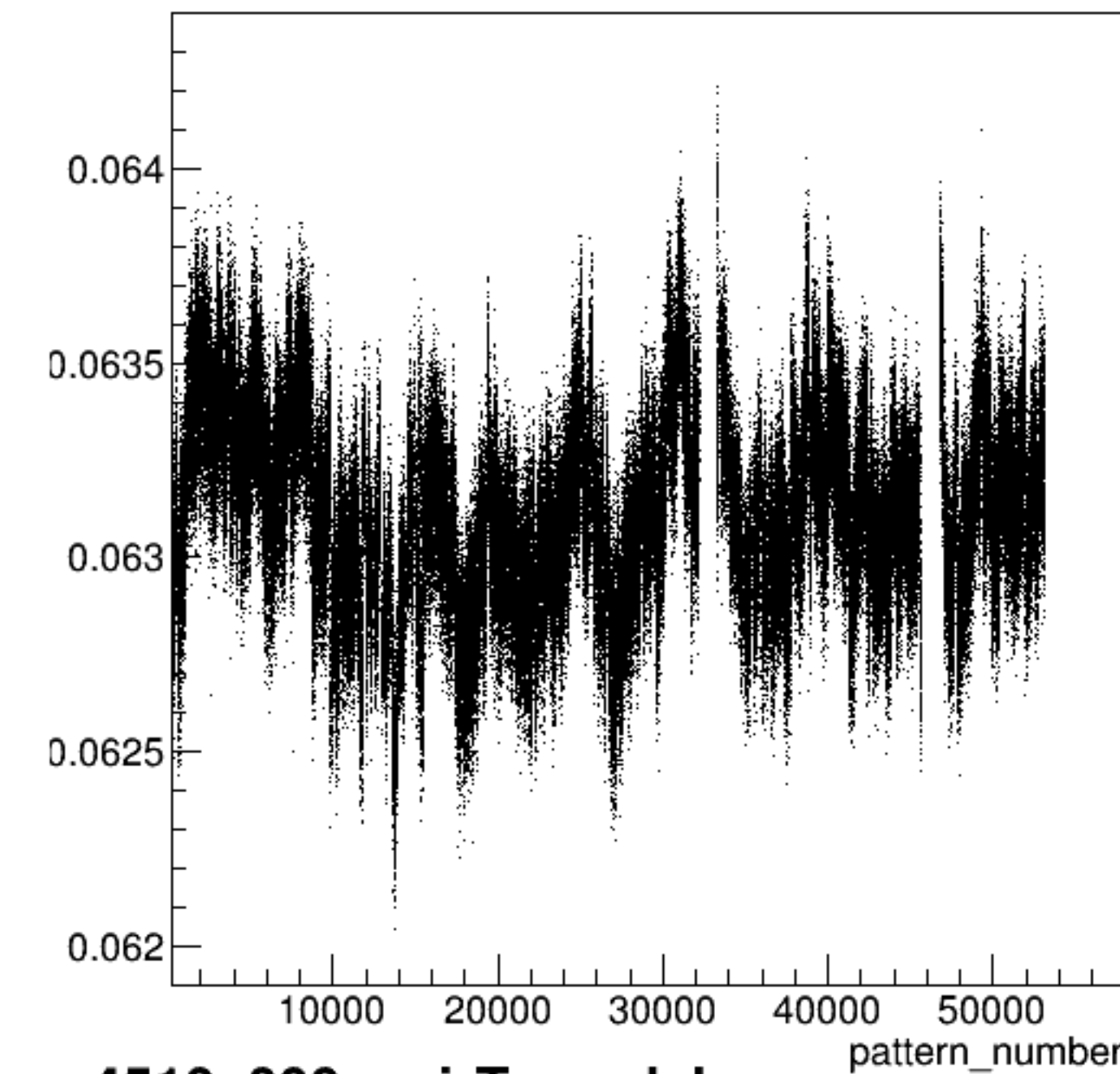
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl/ppm {ErrorFlag==0}

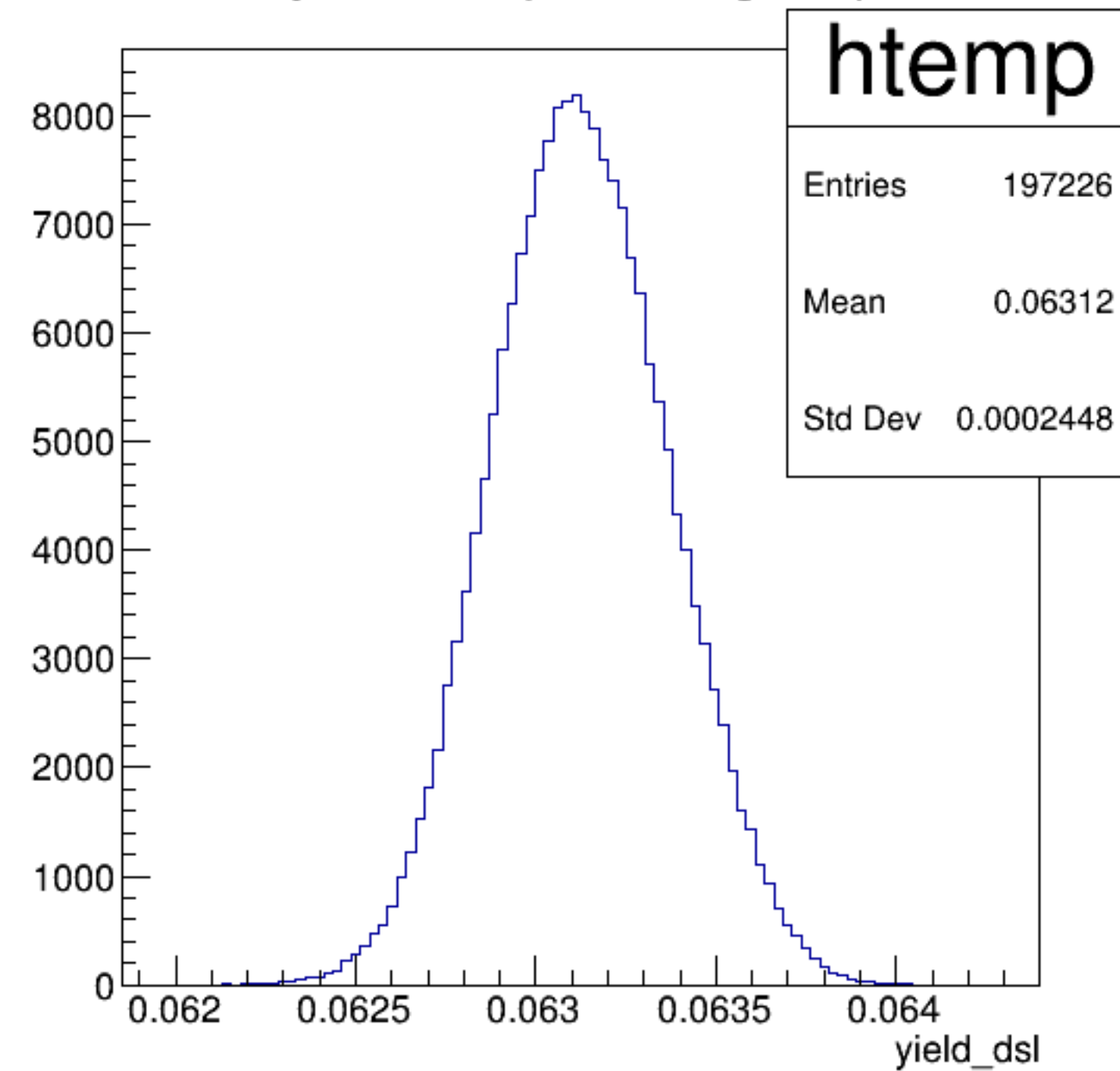


yield_dsl:pattern_number {ErrorFlag==0}

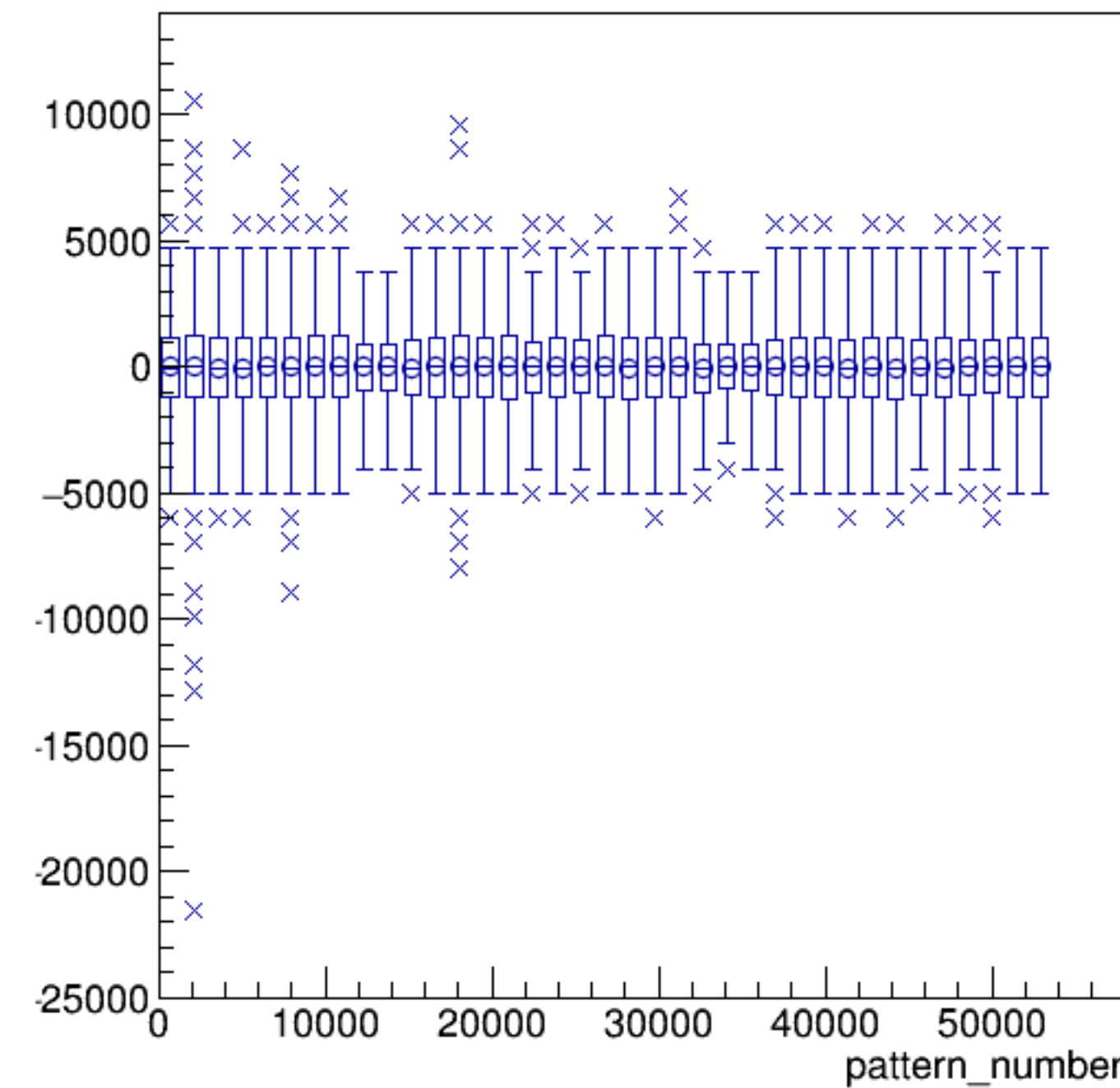


run4510_000_pairTree_dsl.png

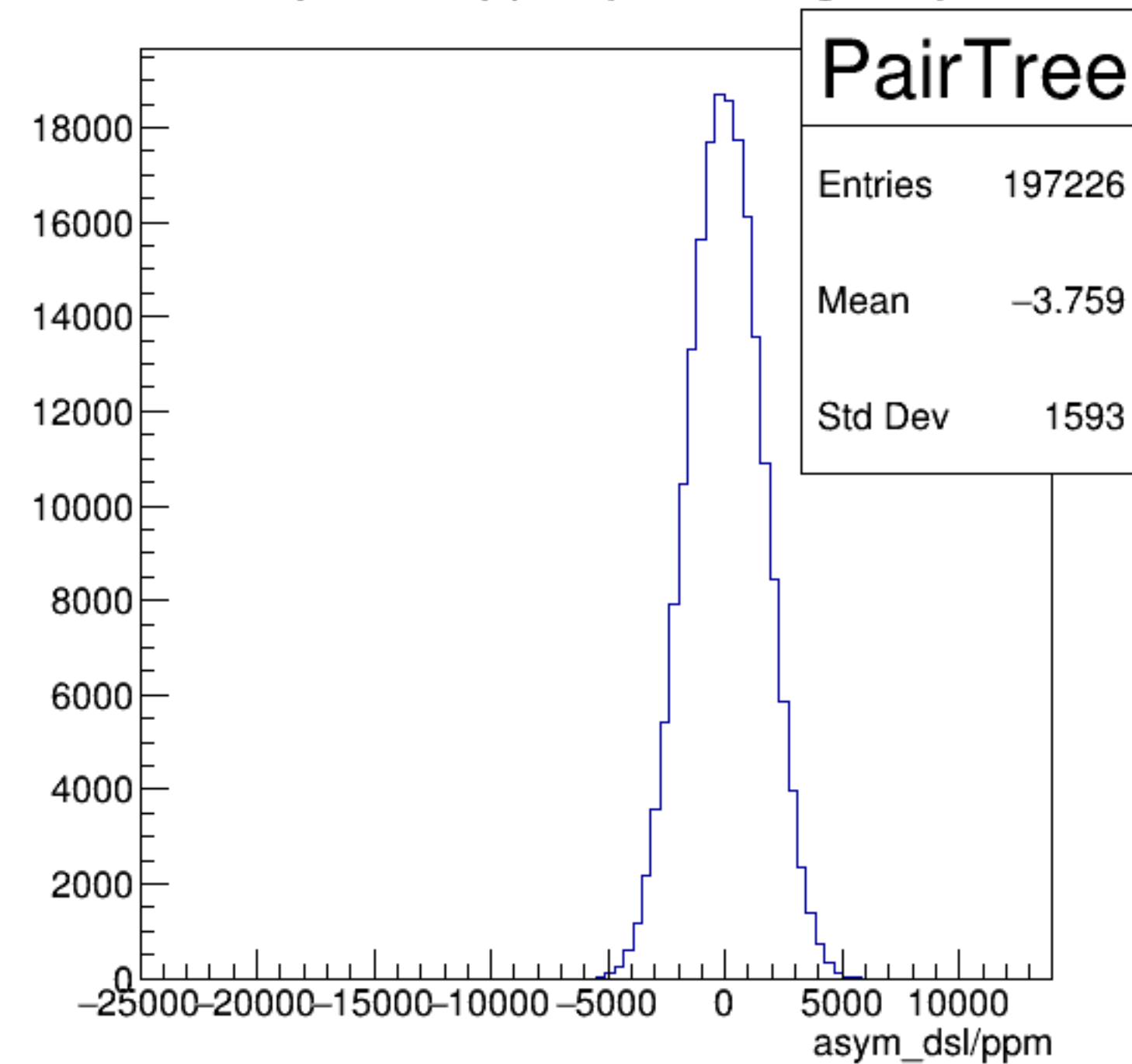
yield_dsl {ErrorFlag==0}



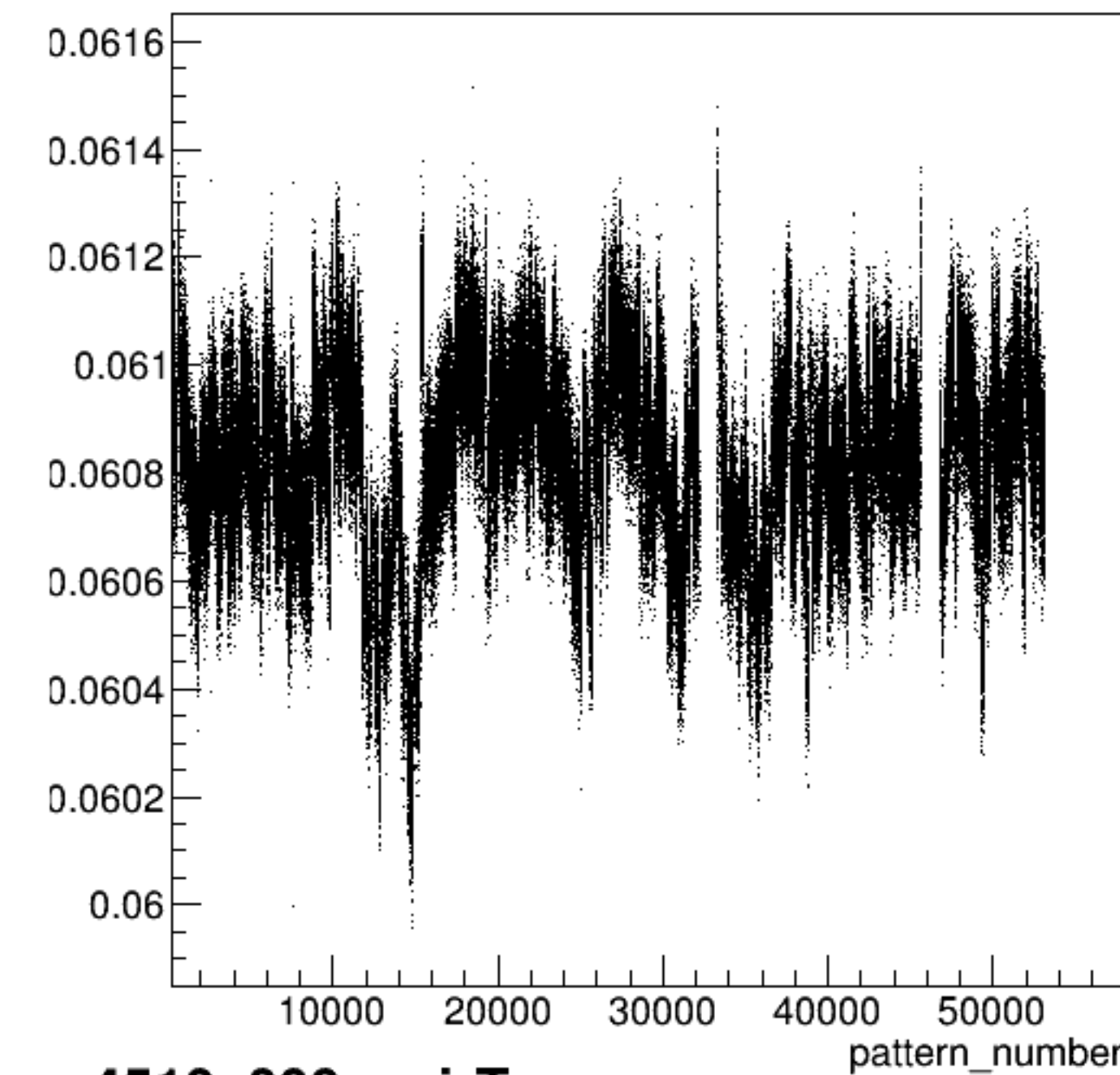
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl/ppm {ErrorFlag==0}

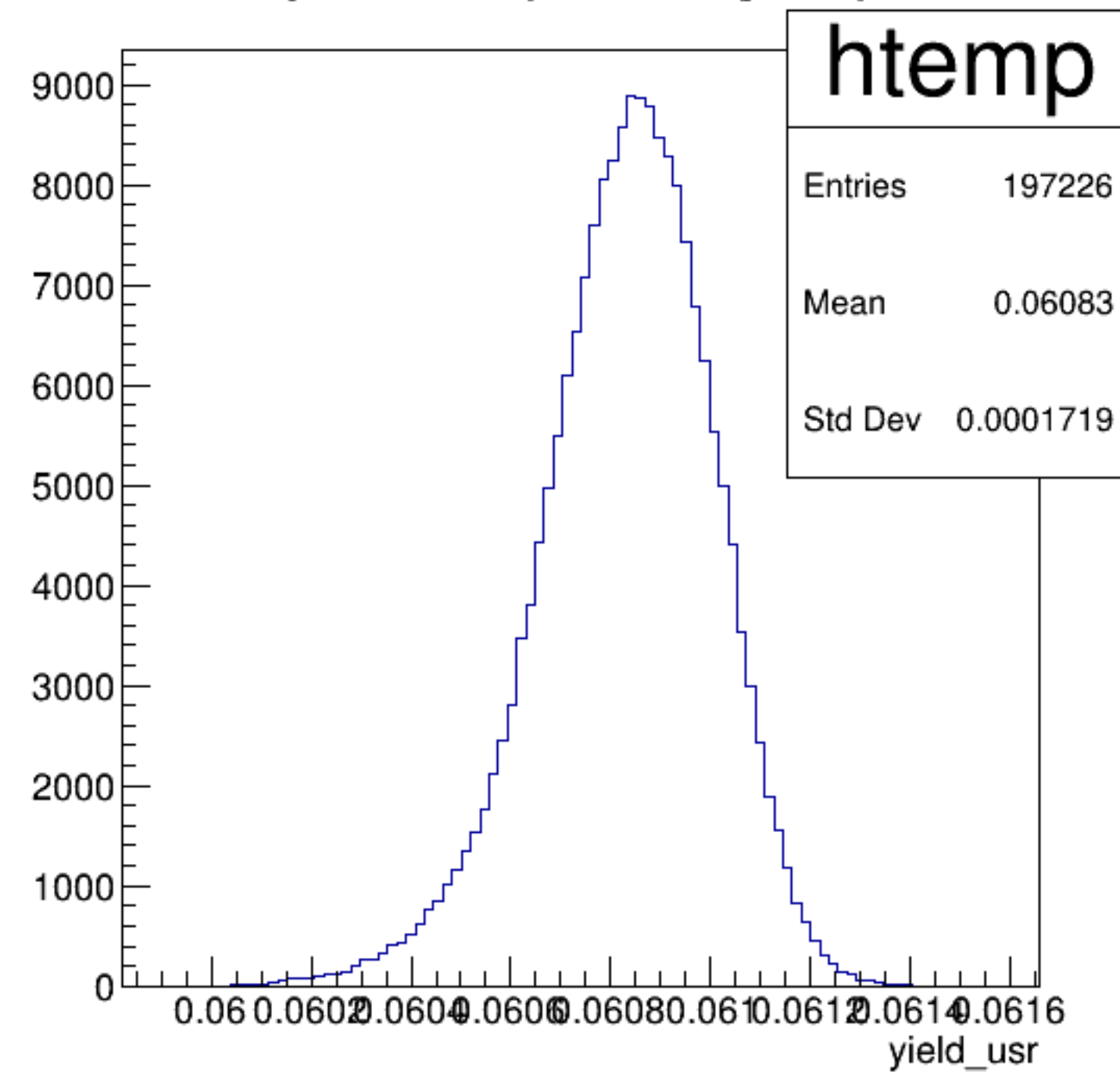


yield_usr:pattern_number {ErrorFlag==0}

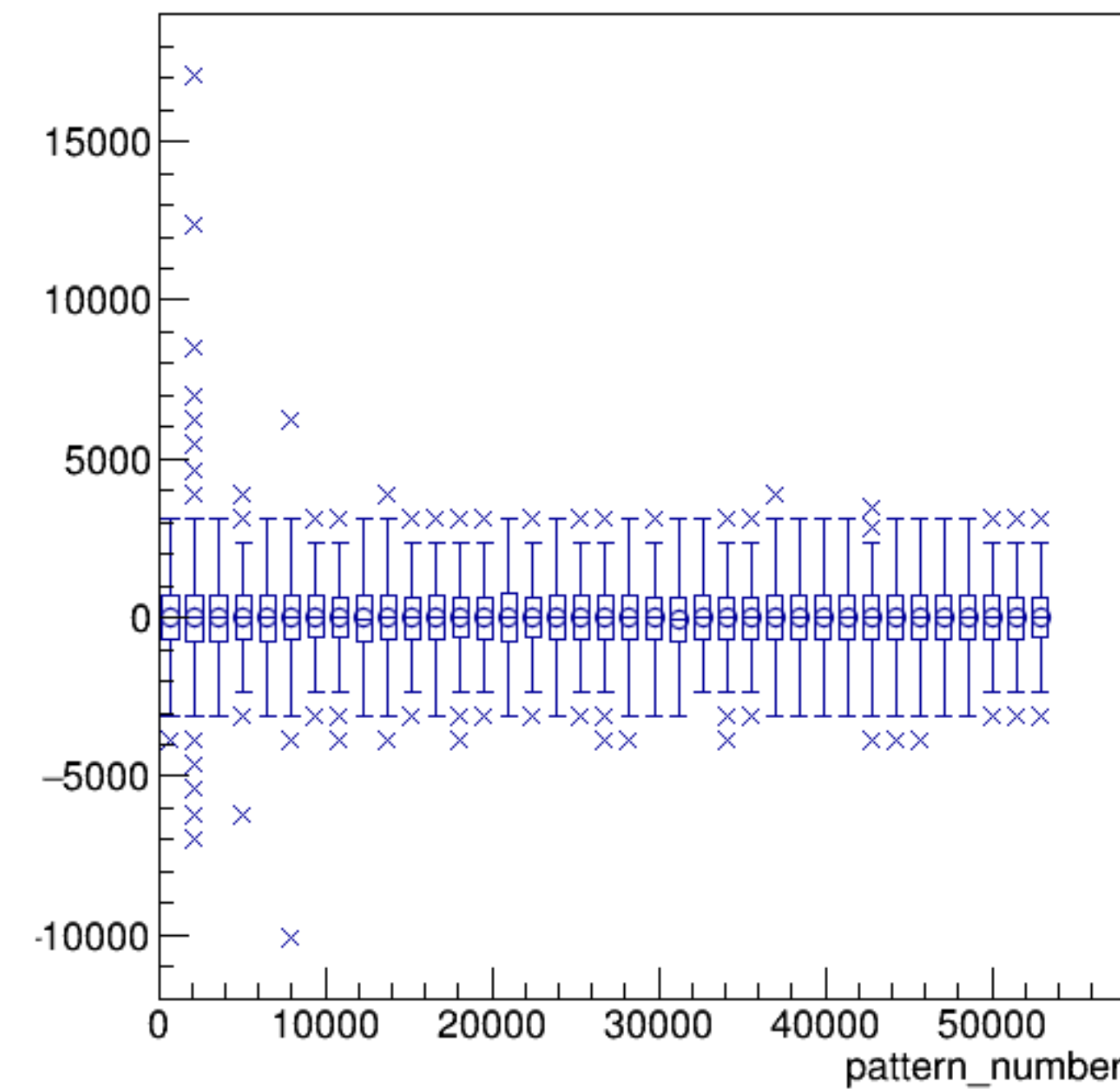


run4510_000_pairTree_usr.png

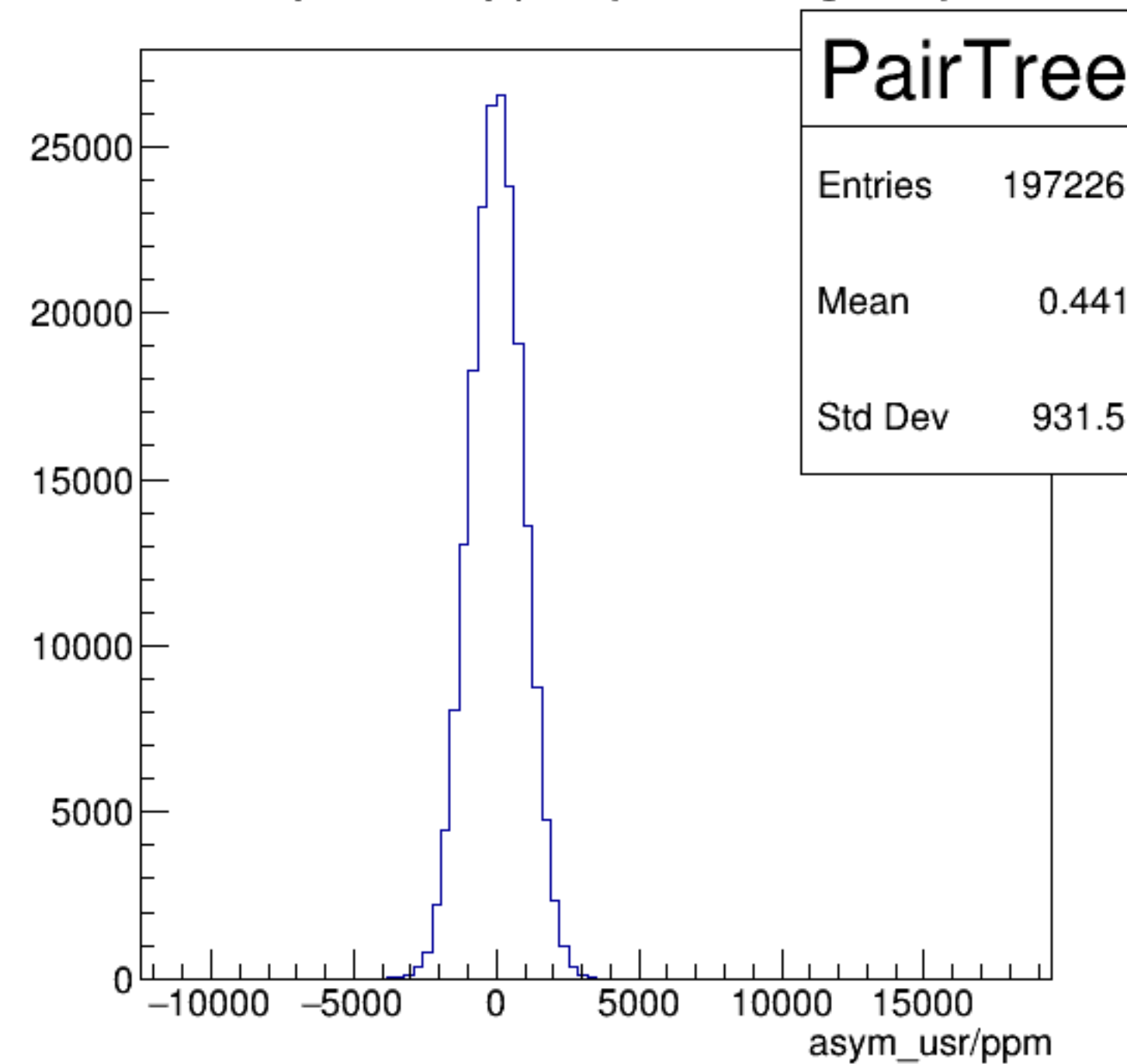
yield_usr {ErrorFlag==0}



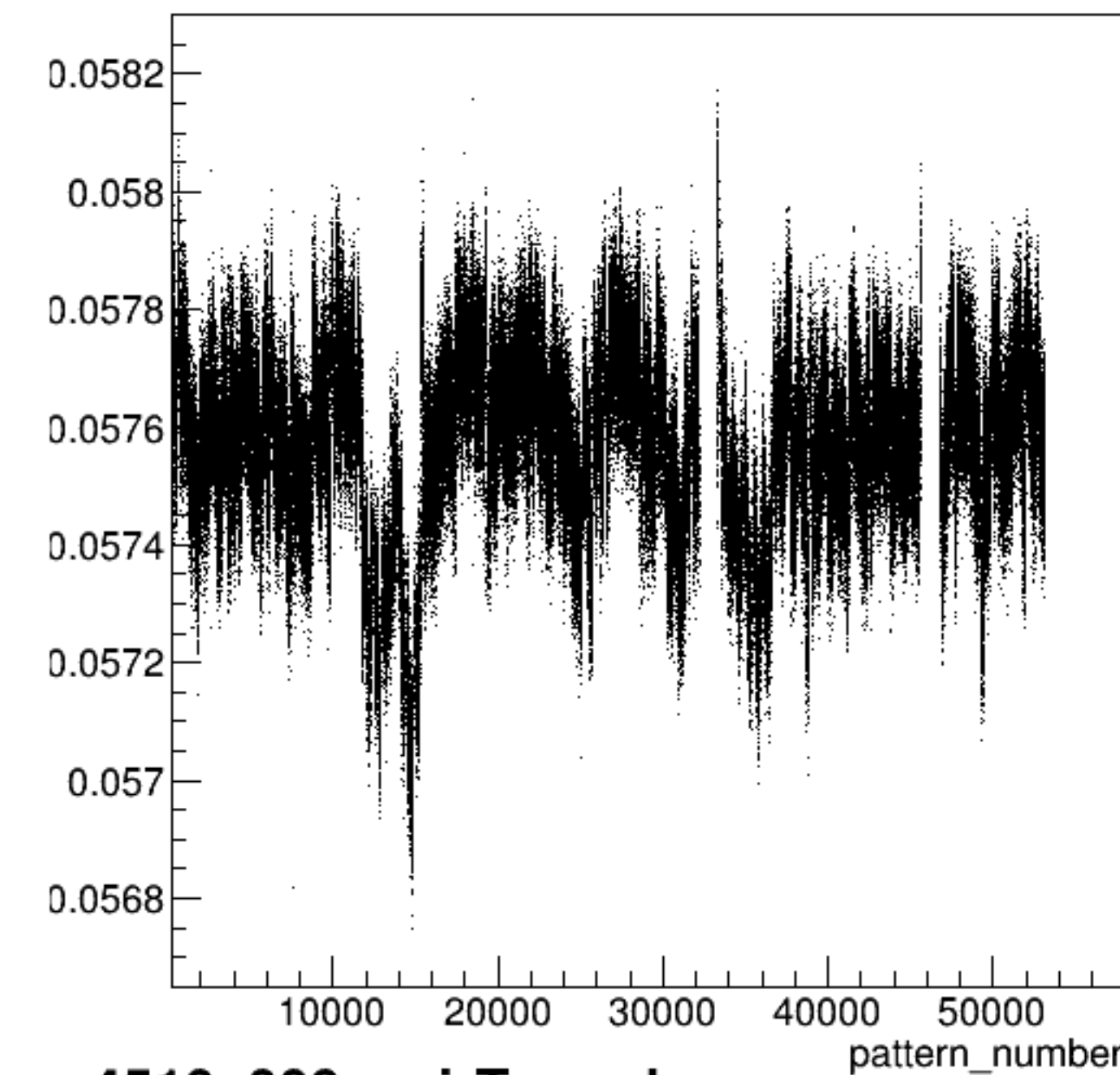
asym_usr/ppm:pattern_number {ErrorFlag==0}



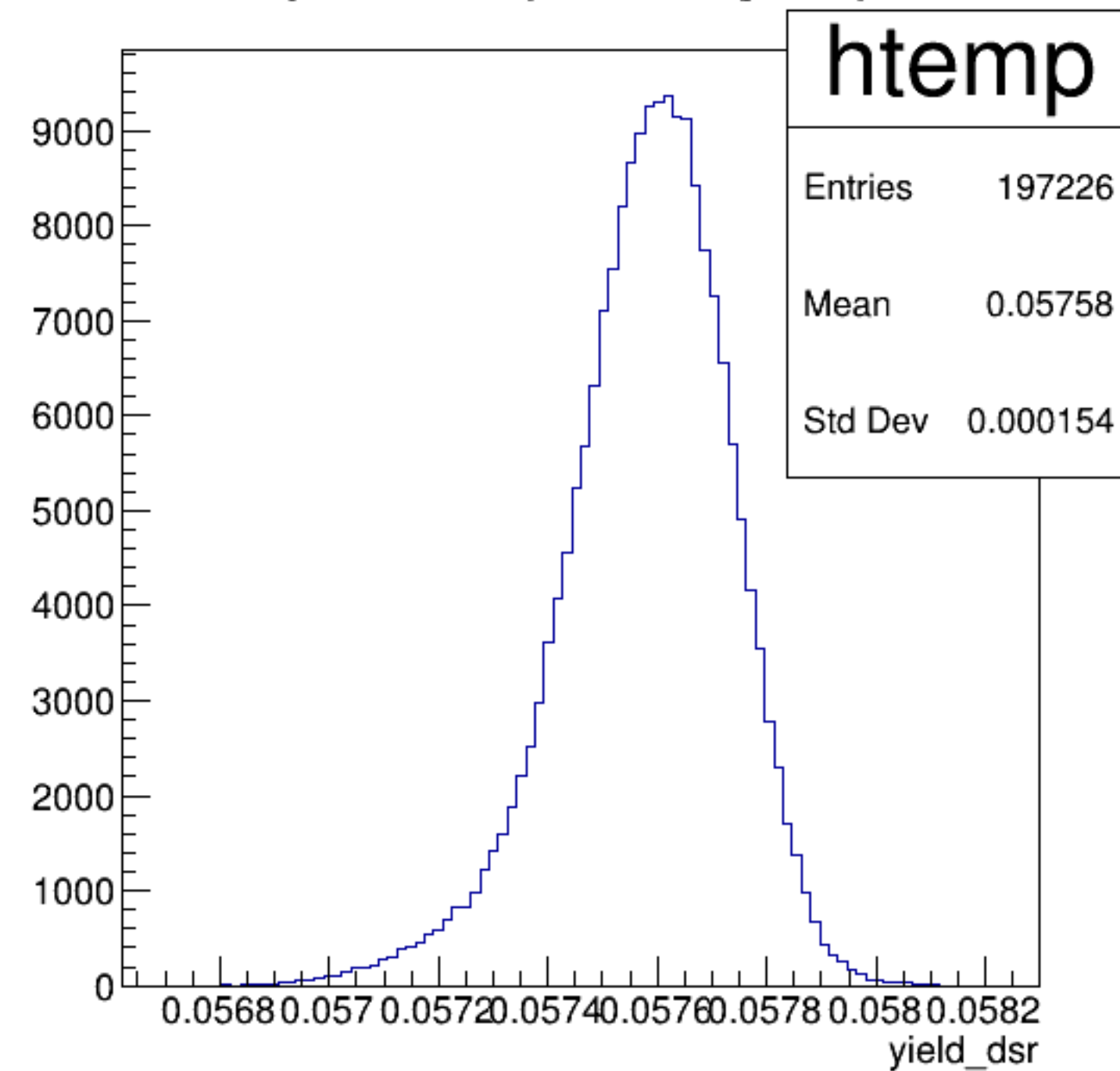
asym_usr/ppm {ErrorFlag==0}



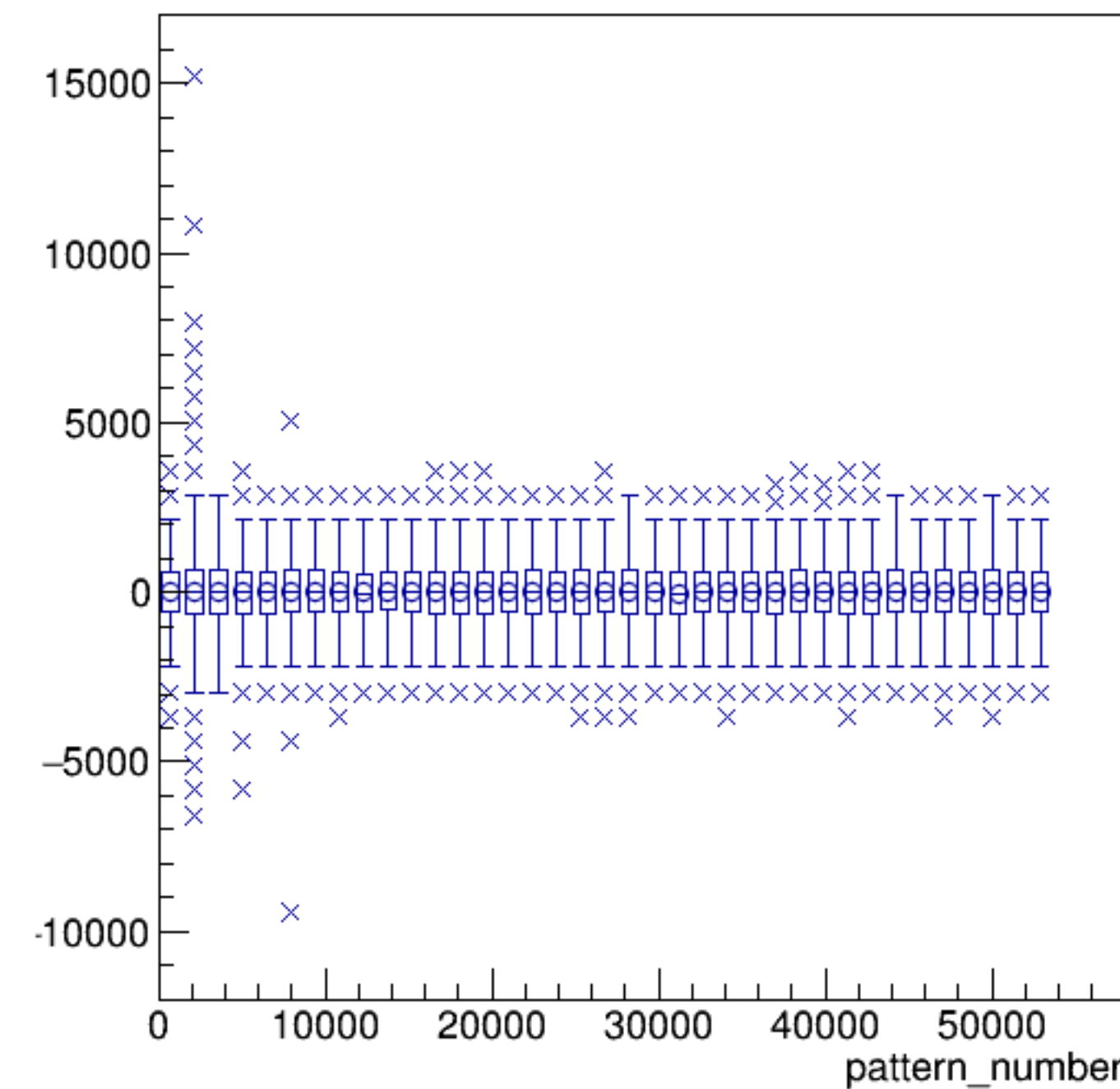
yield_dsr:pattern_number {ErrorFlag==0}



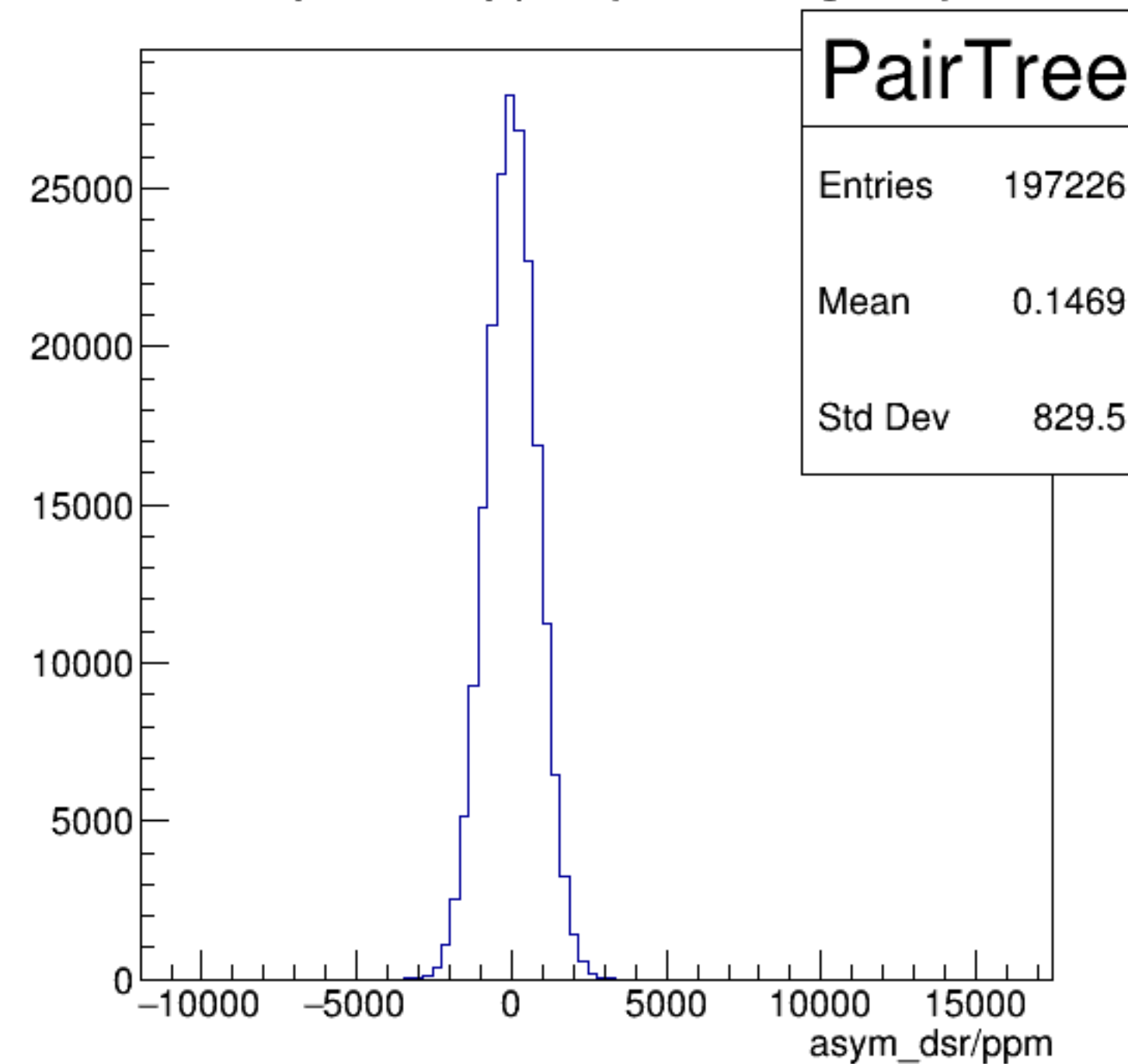
yield_dsr {ErrorFlag==0}



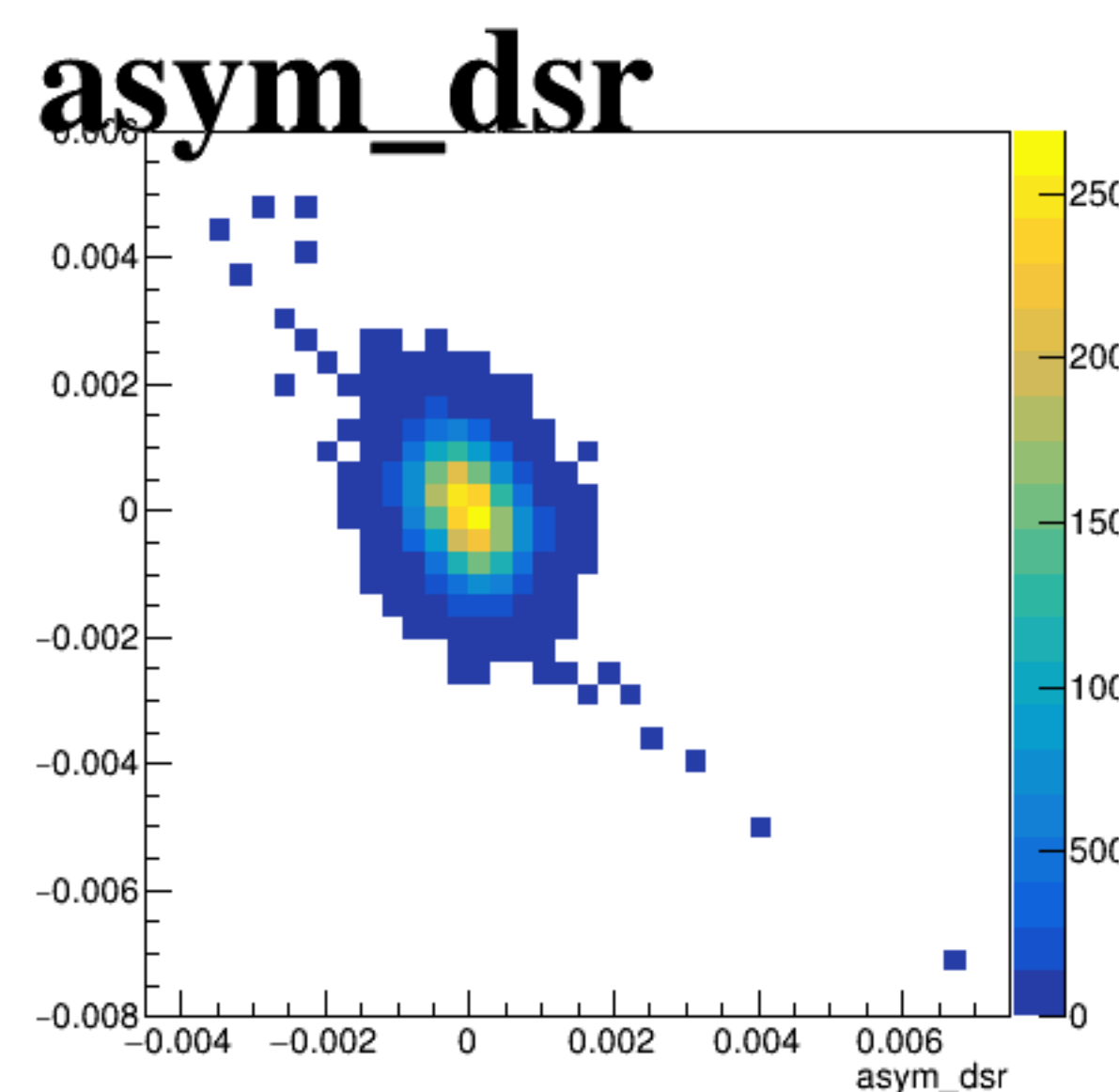
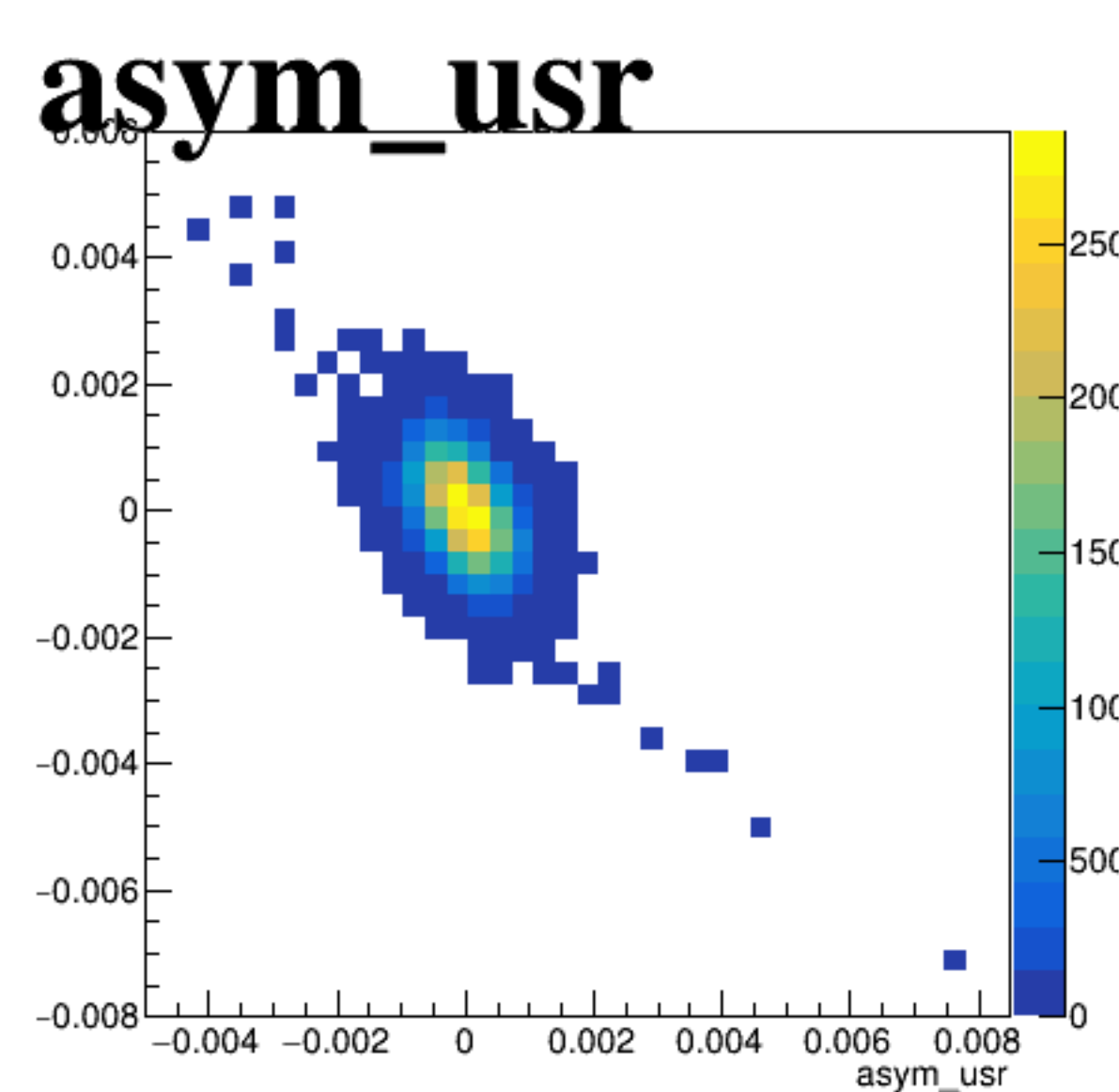
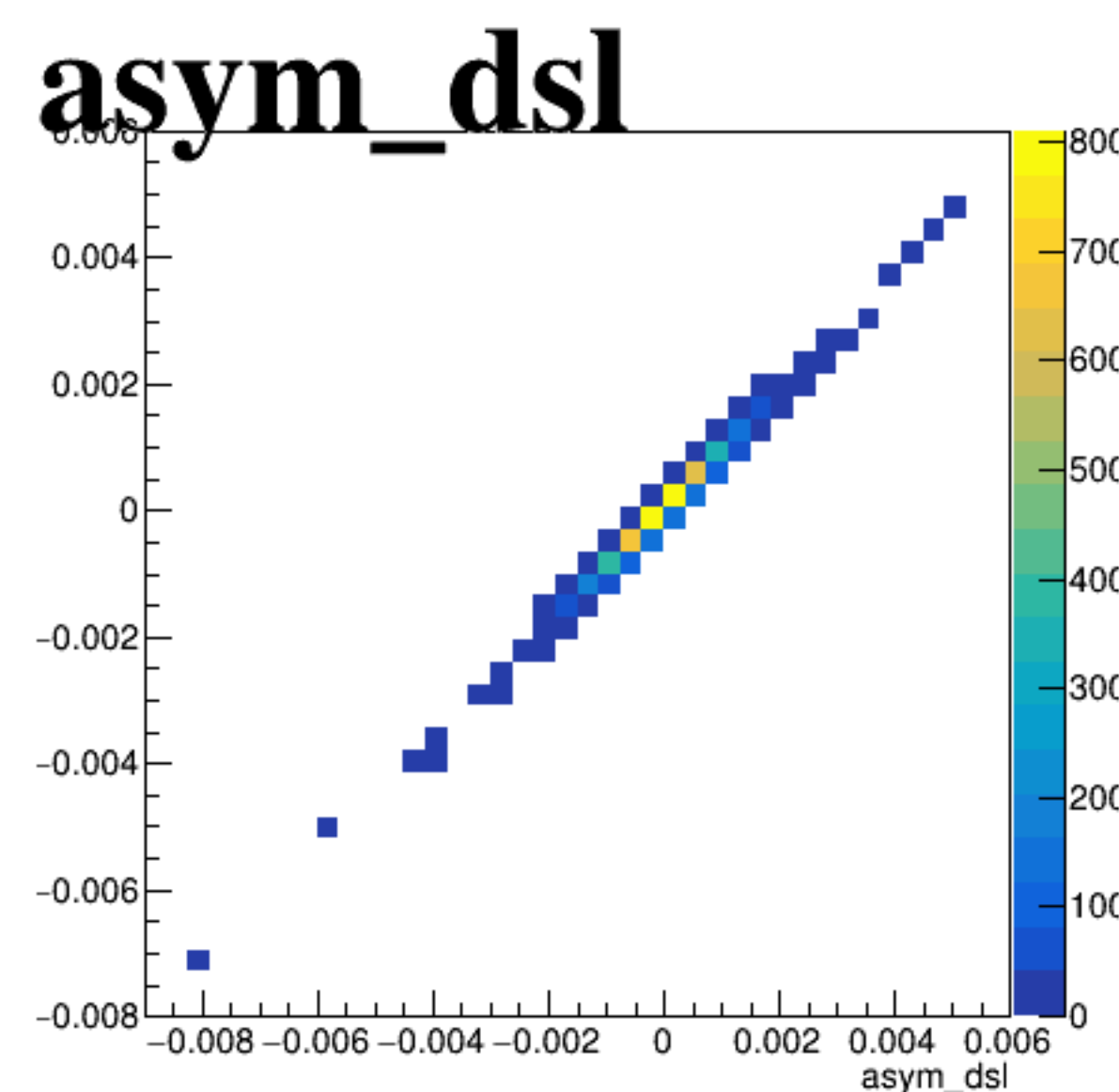
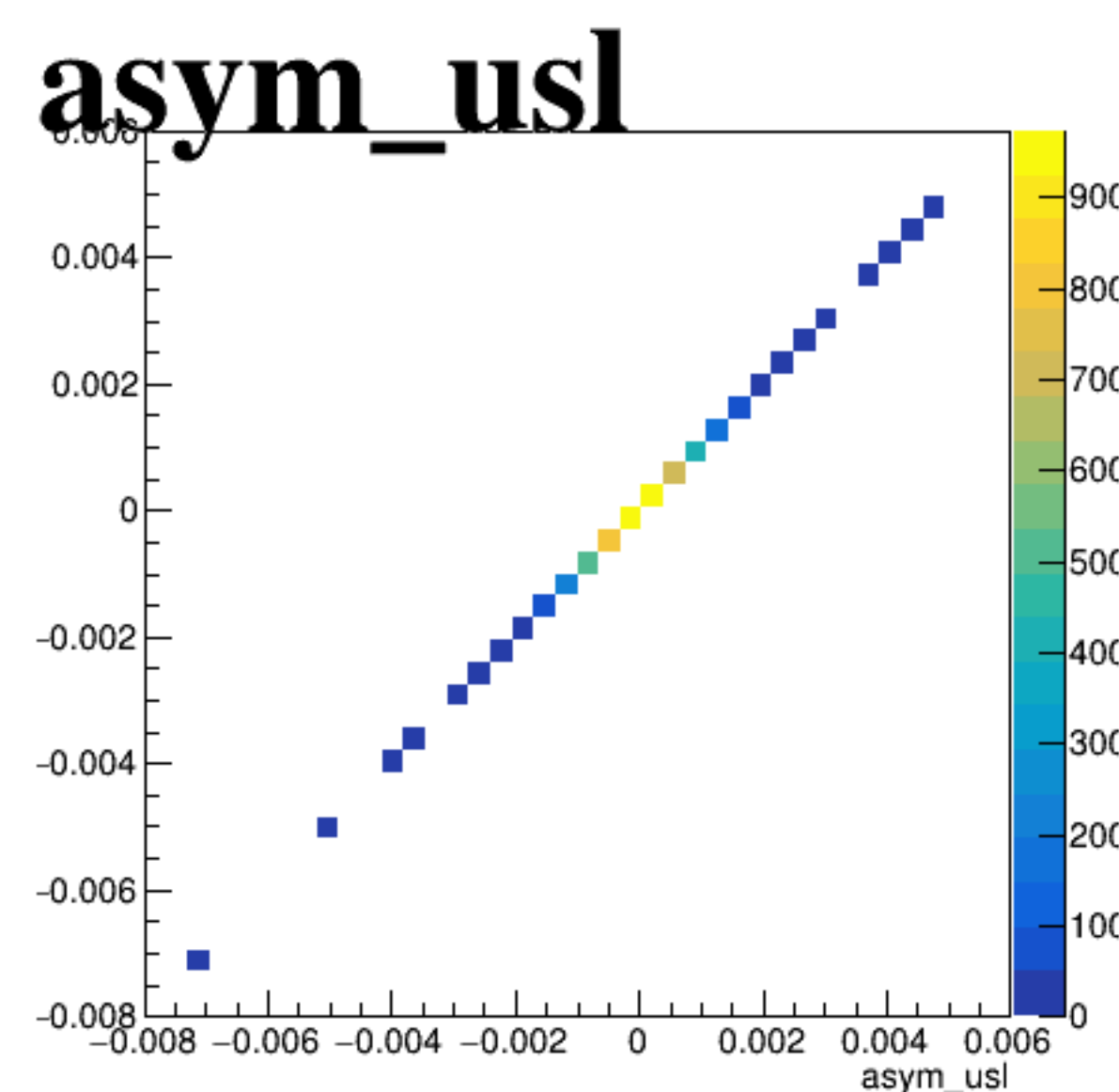
asym_dsr/ppm:pattern_number {ErrorFlag==0}



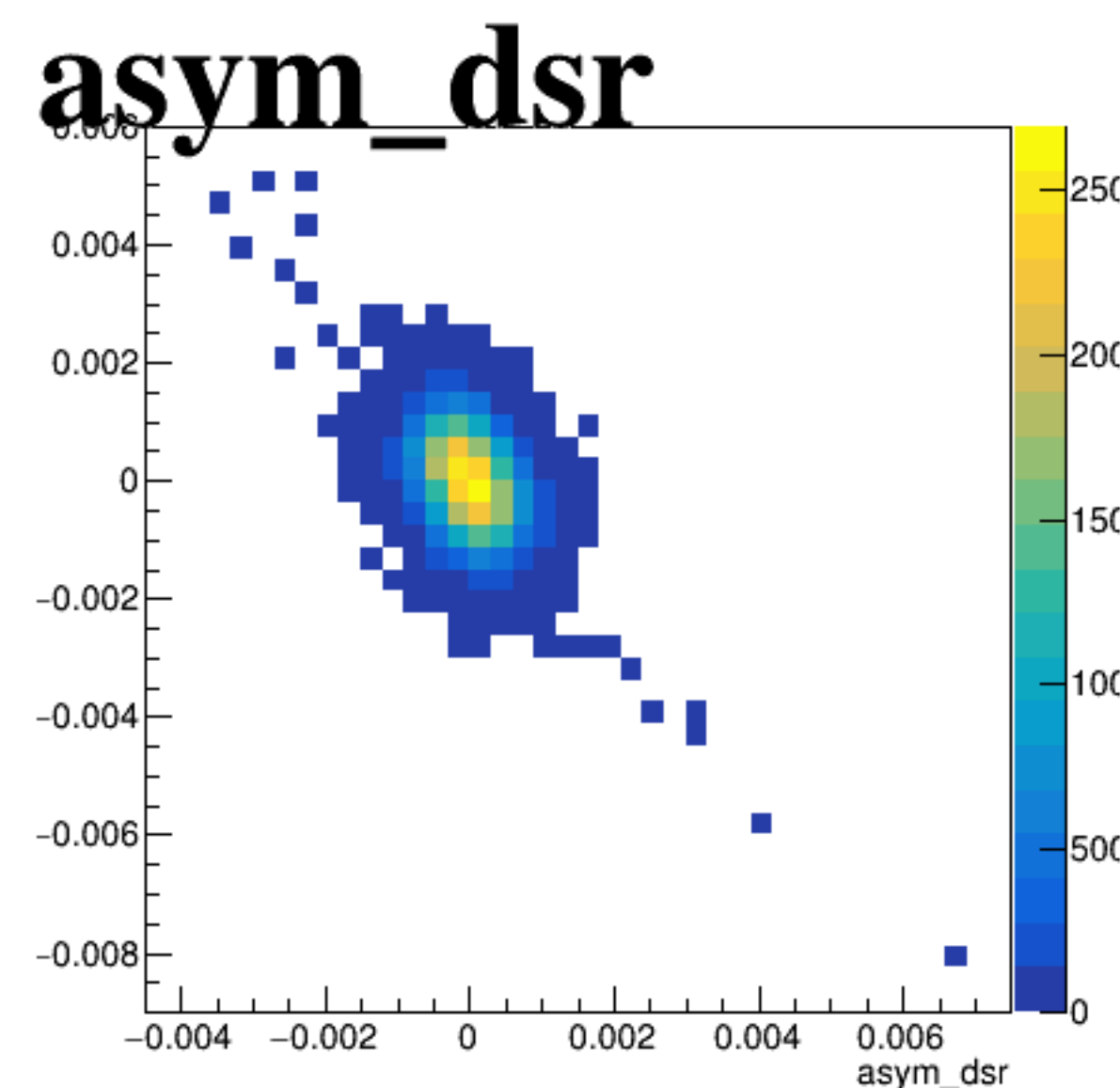
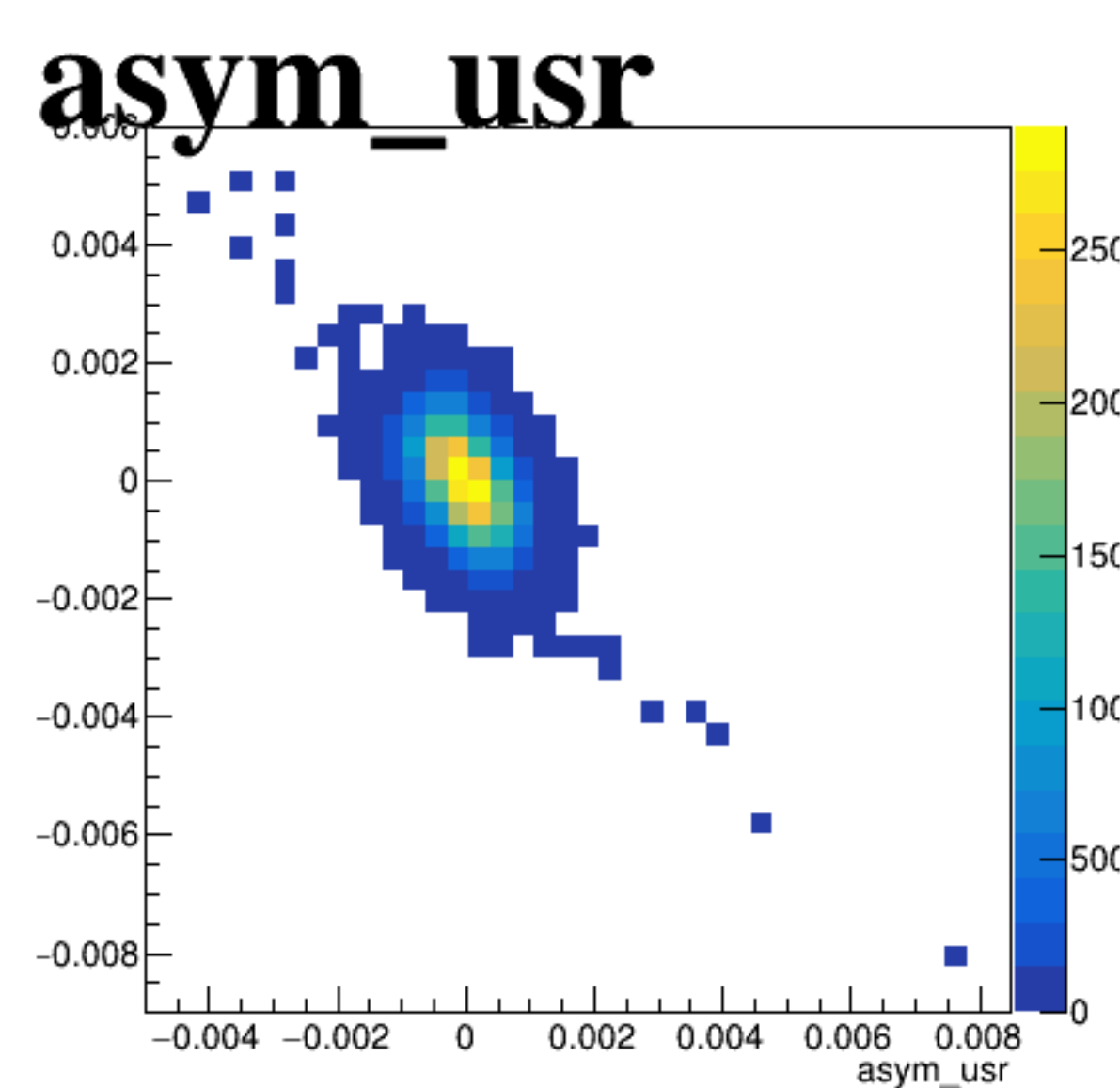
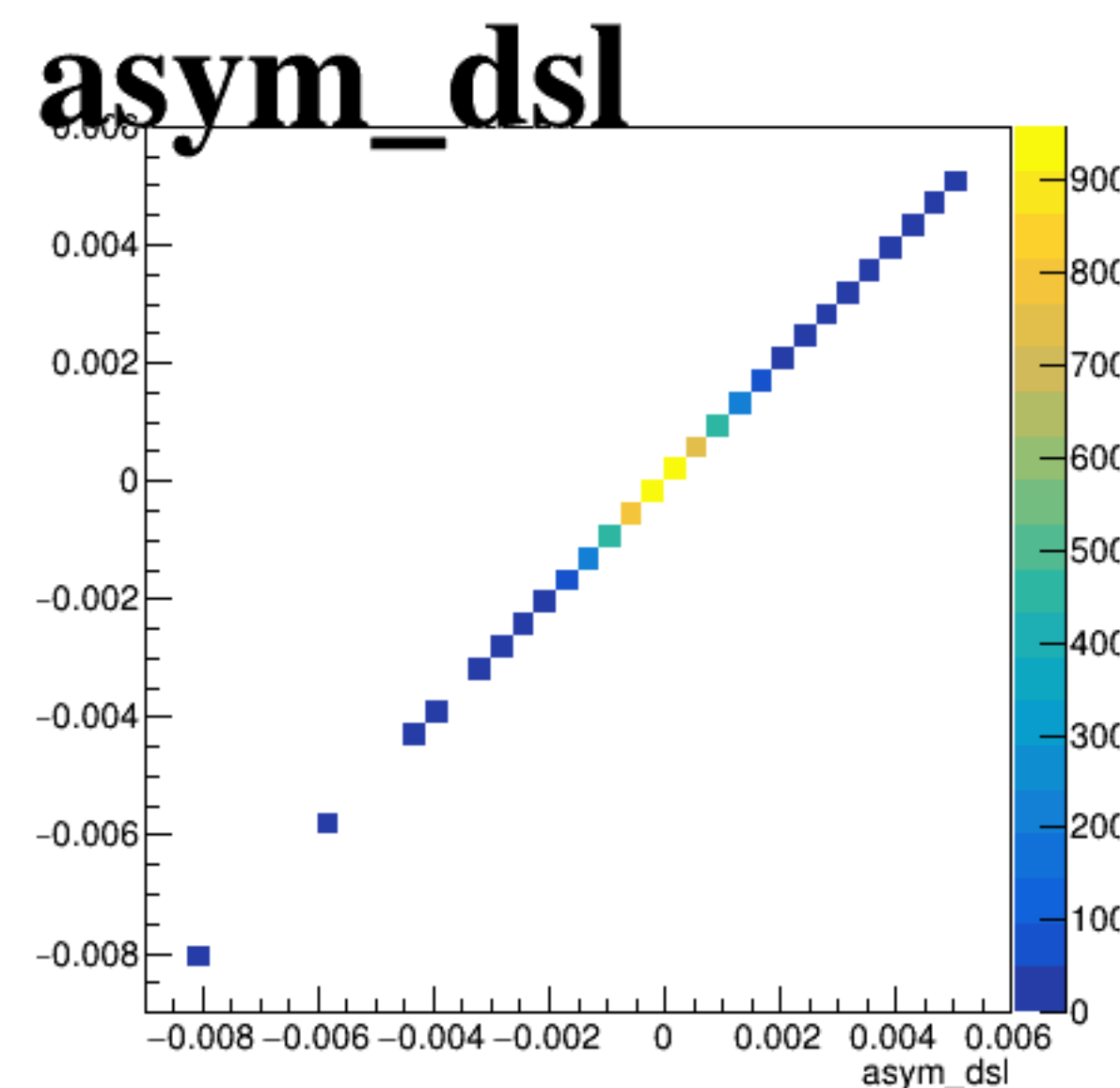
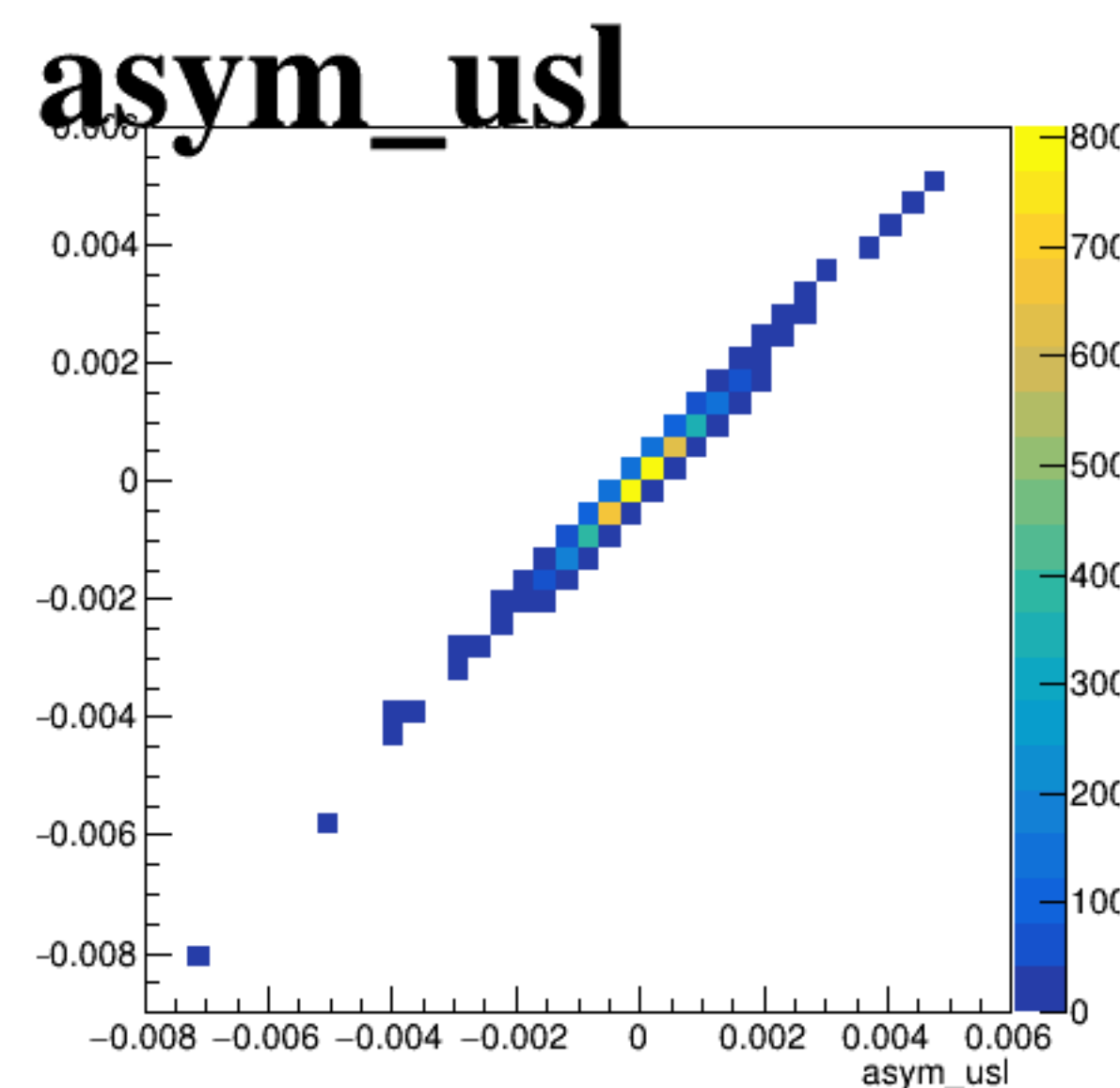
asym_dsr/ppm {ErrorFlag==0}



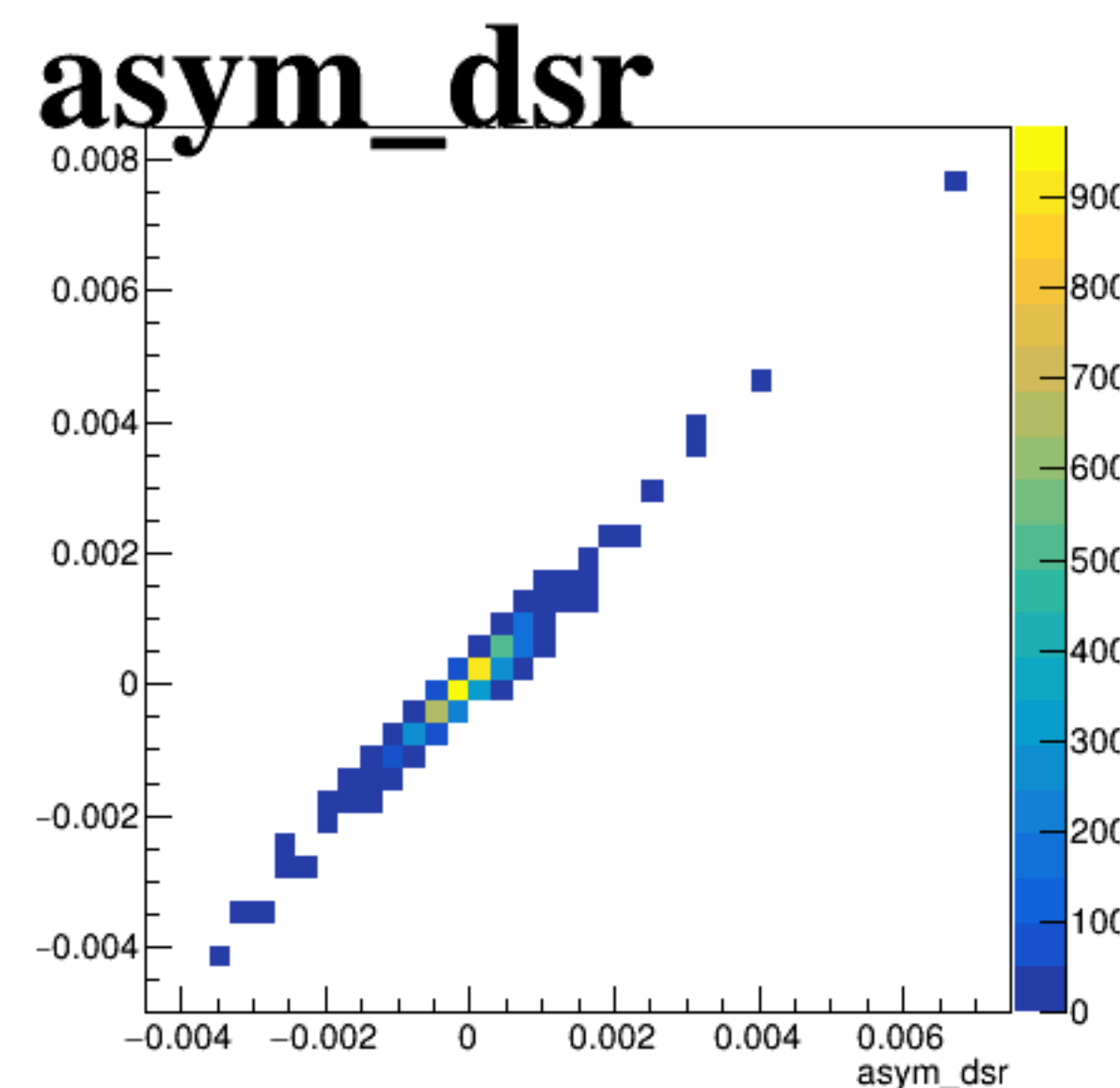
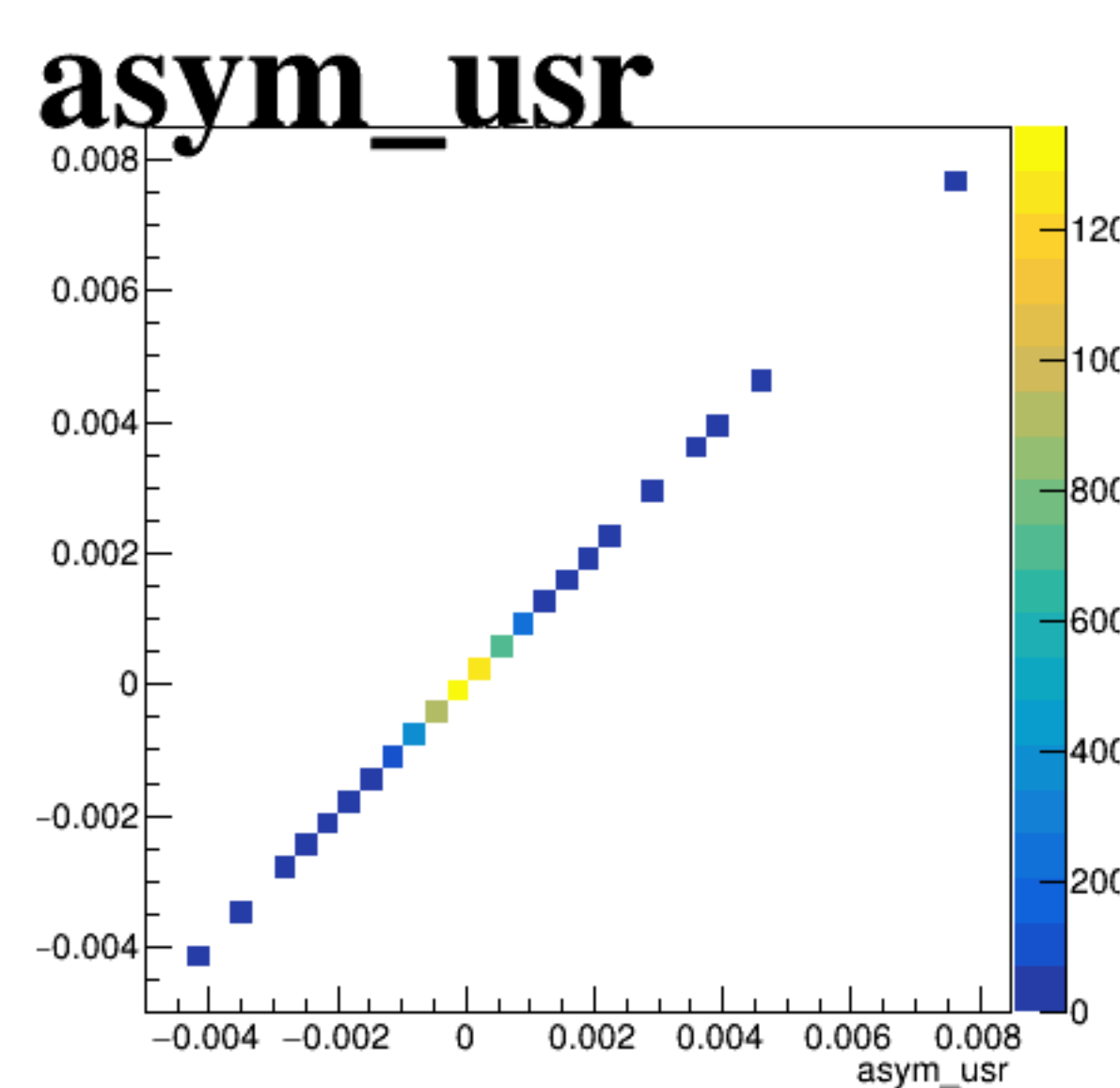
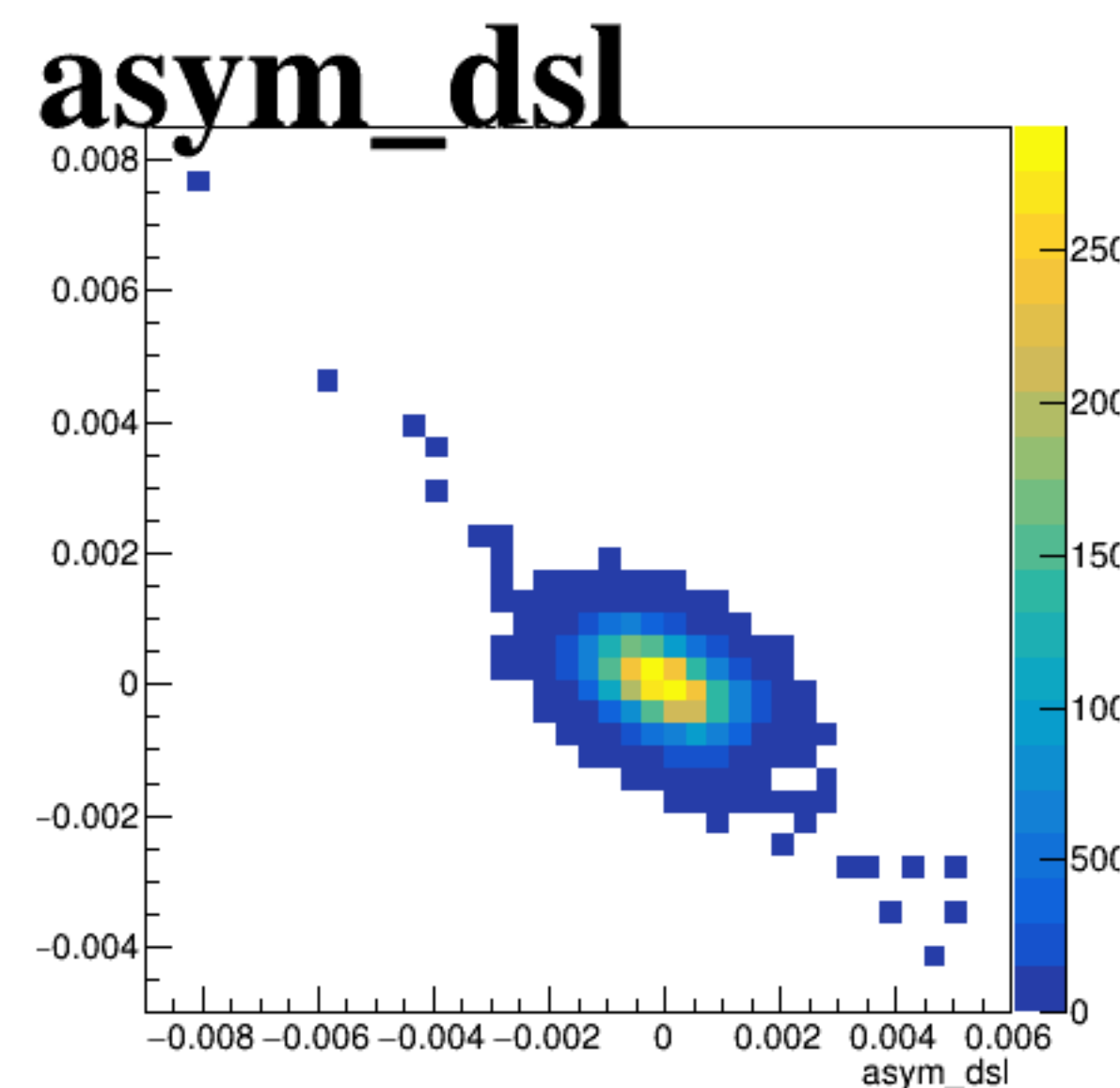
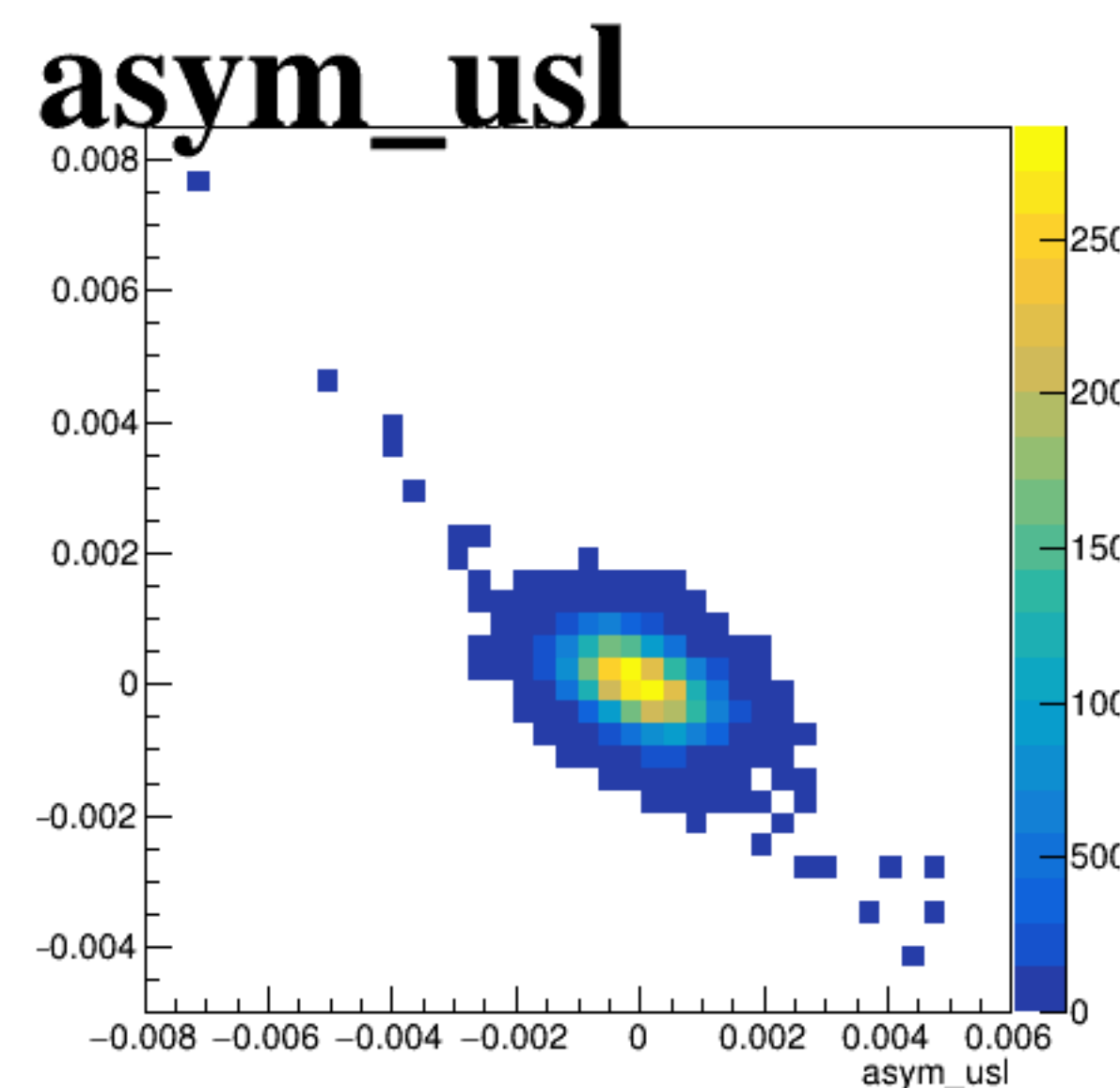
asym_usl



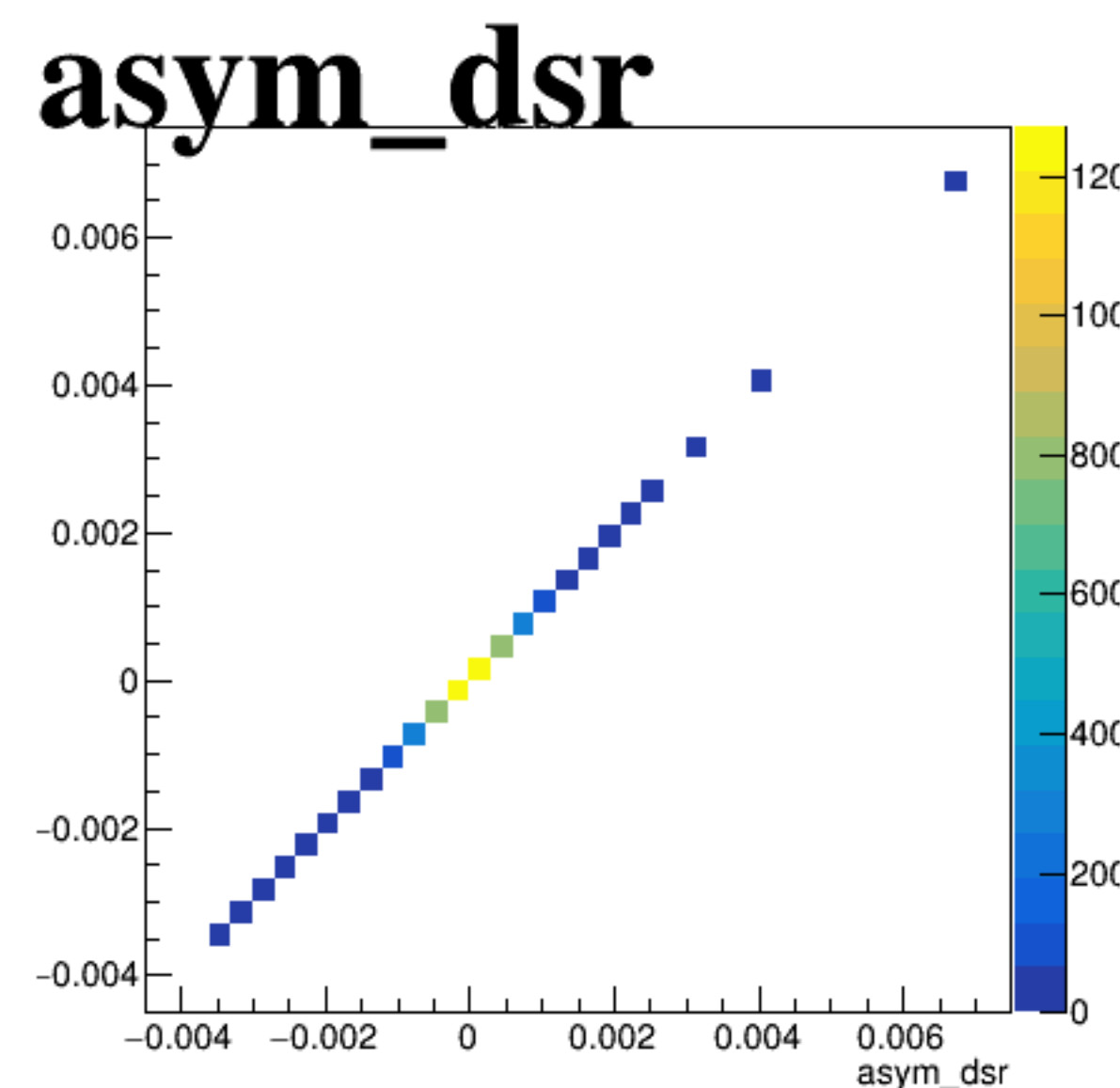
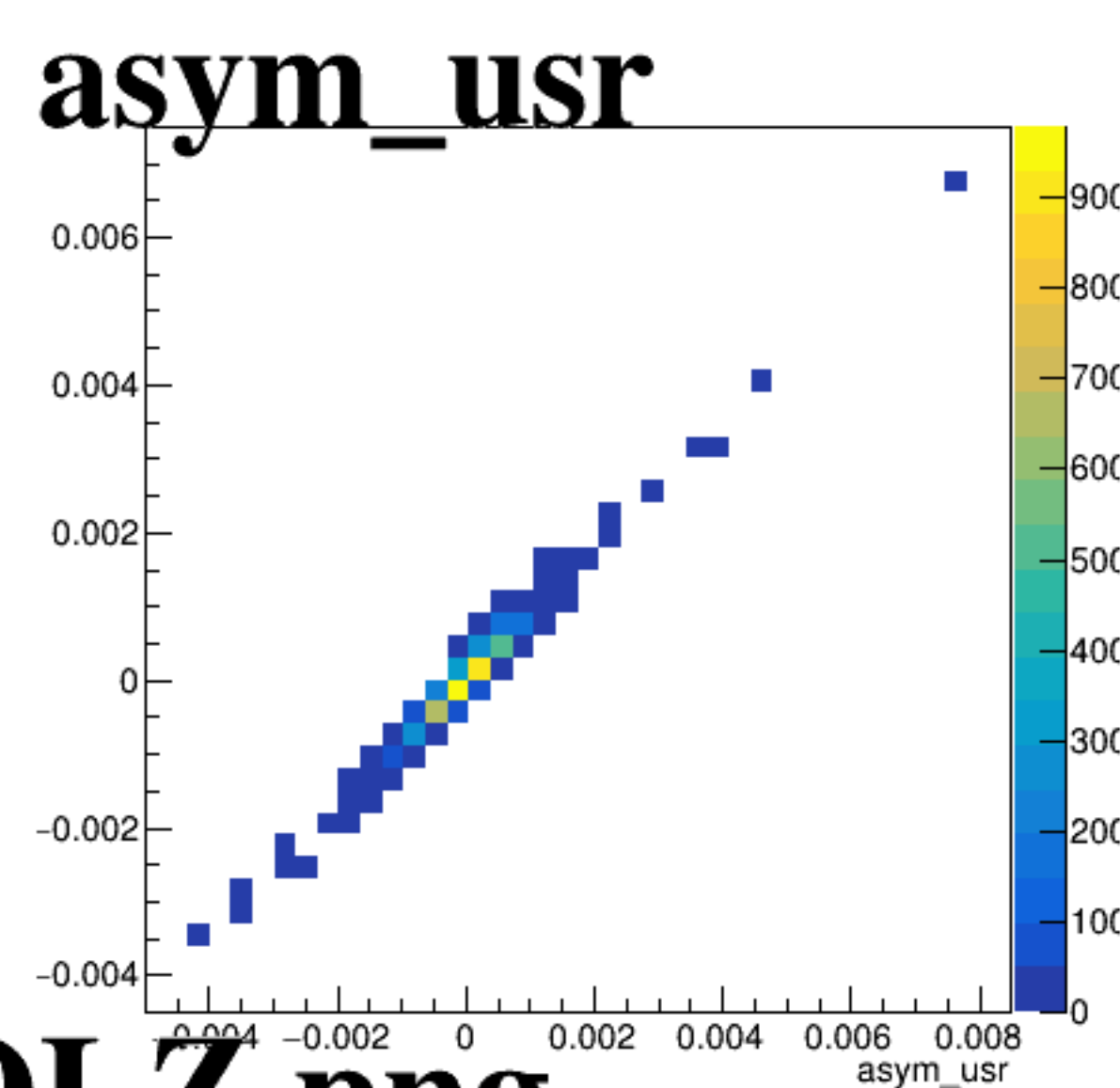
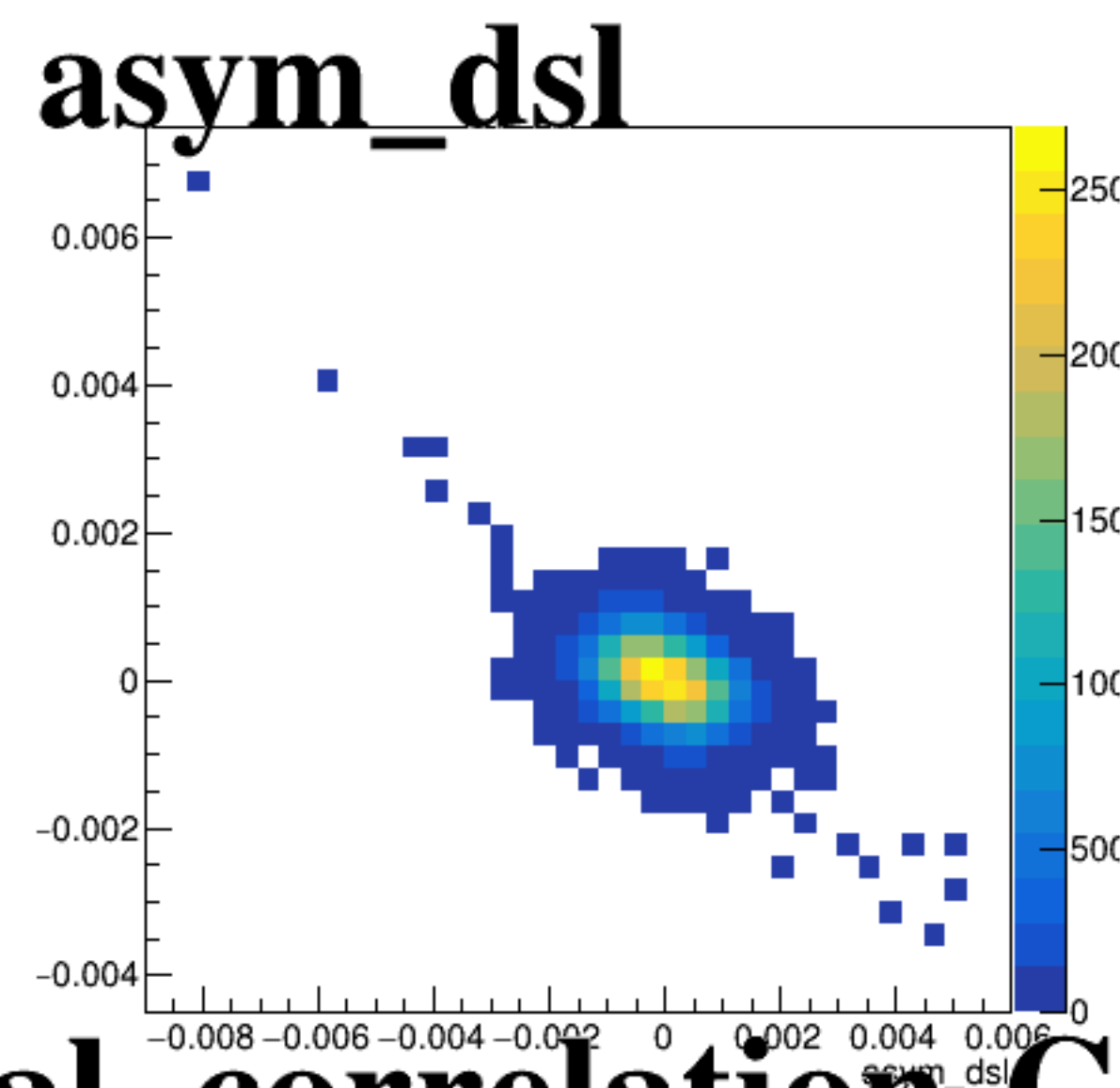
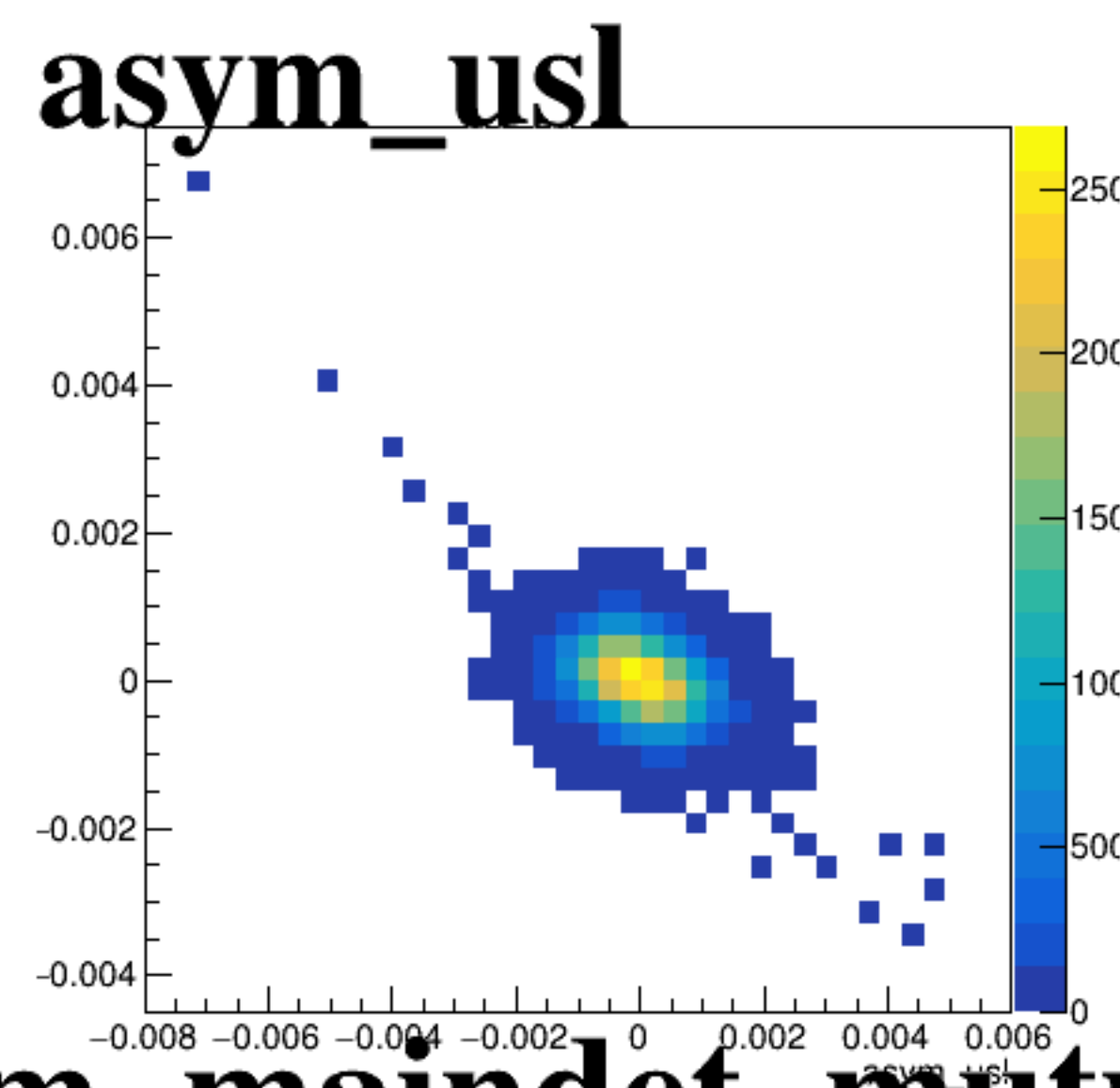
asym_dsl



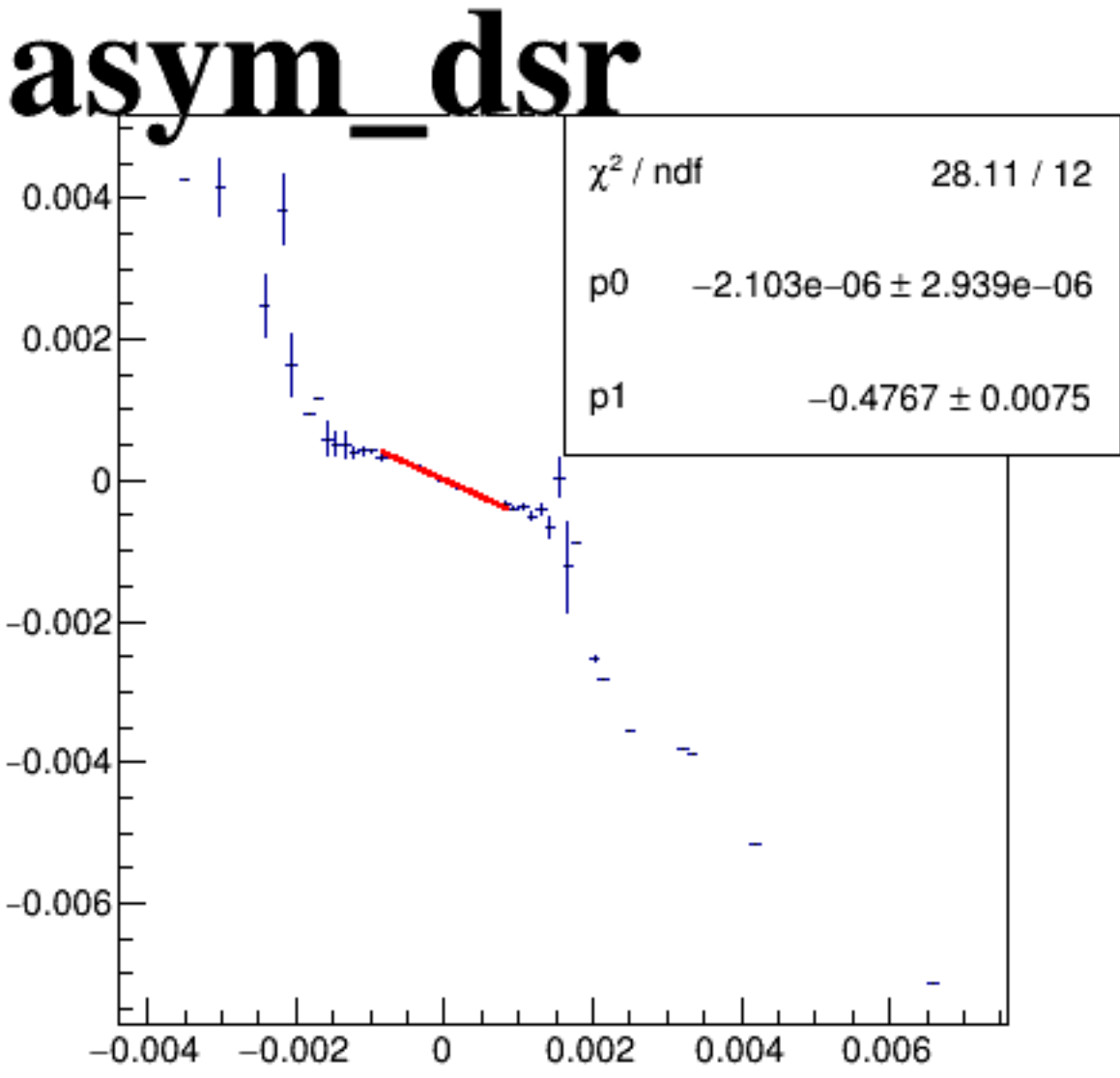
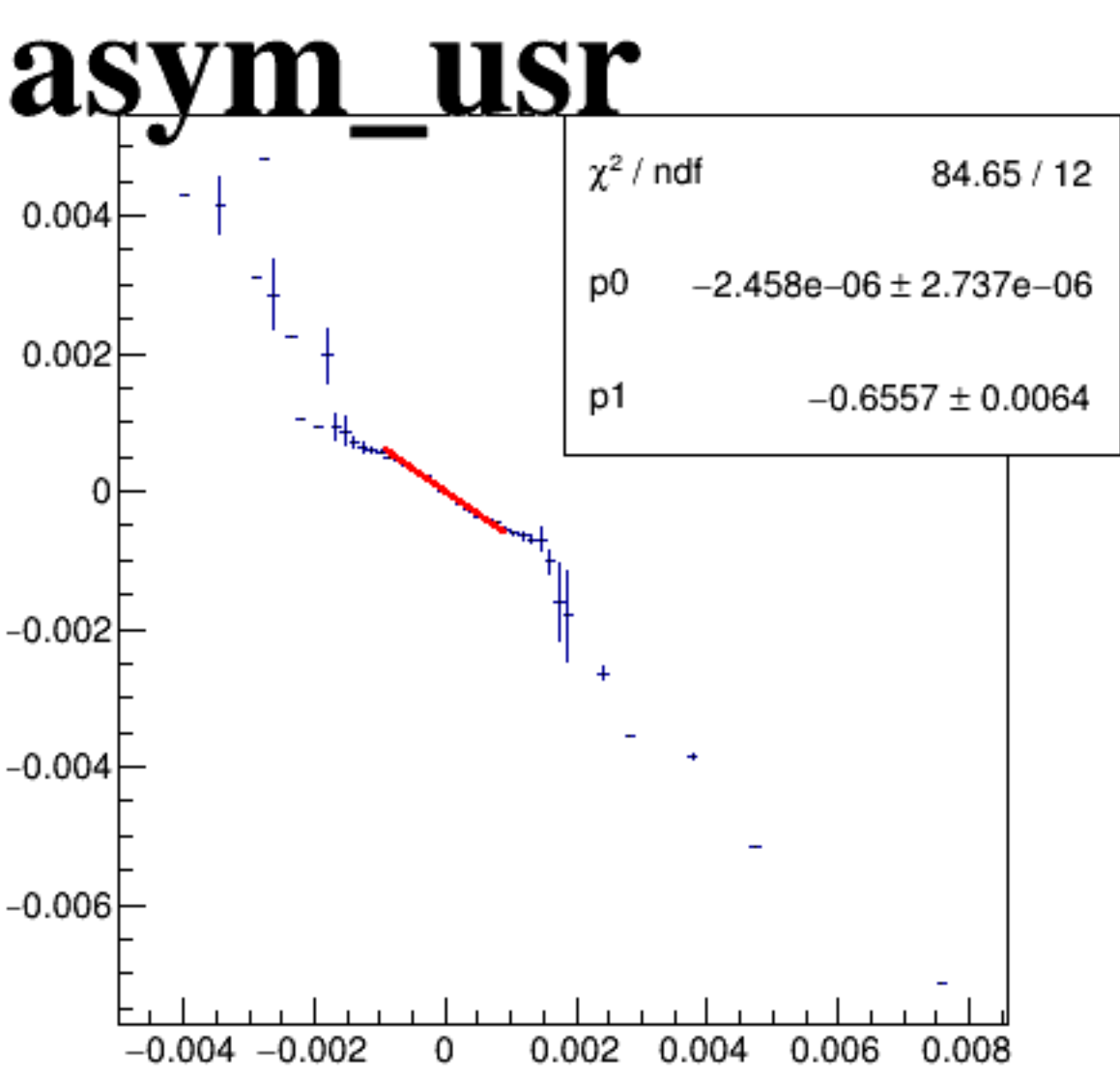
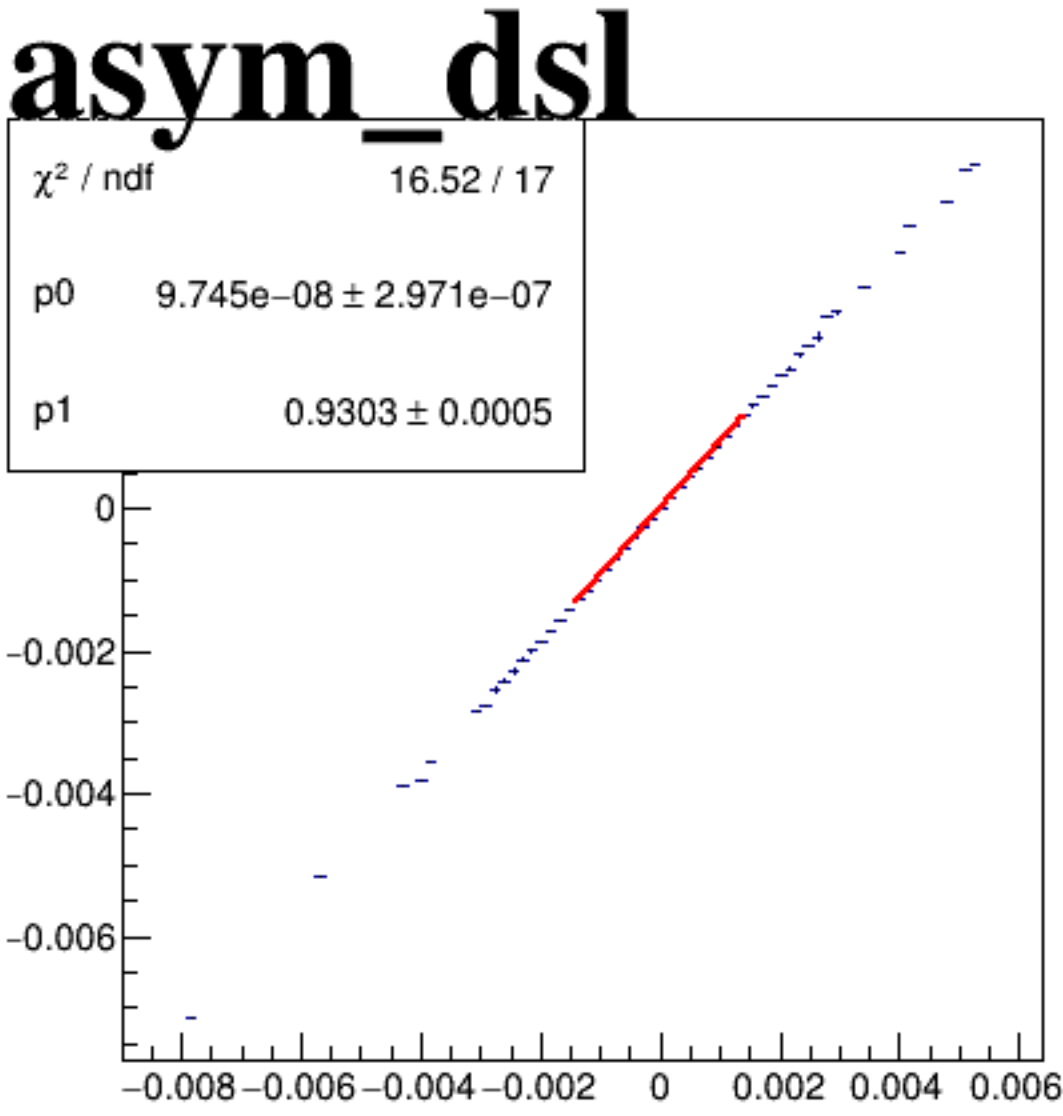
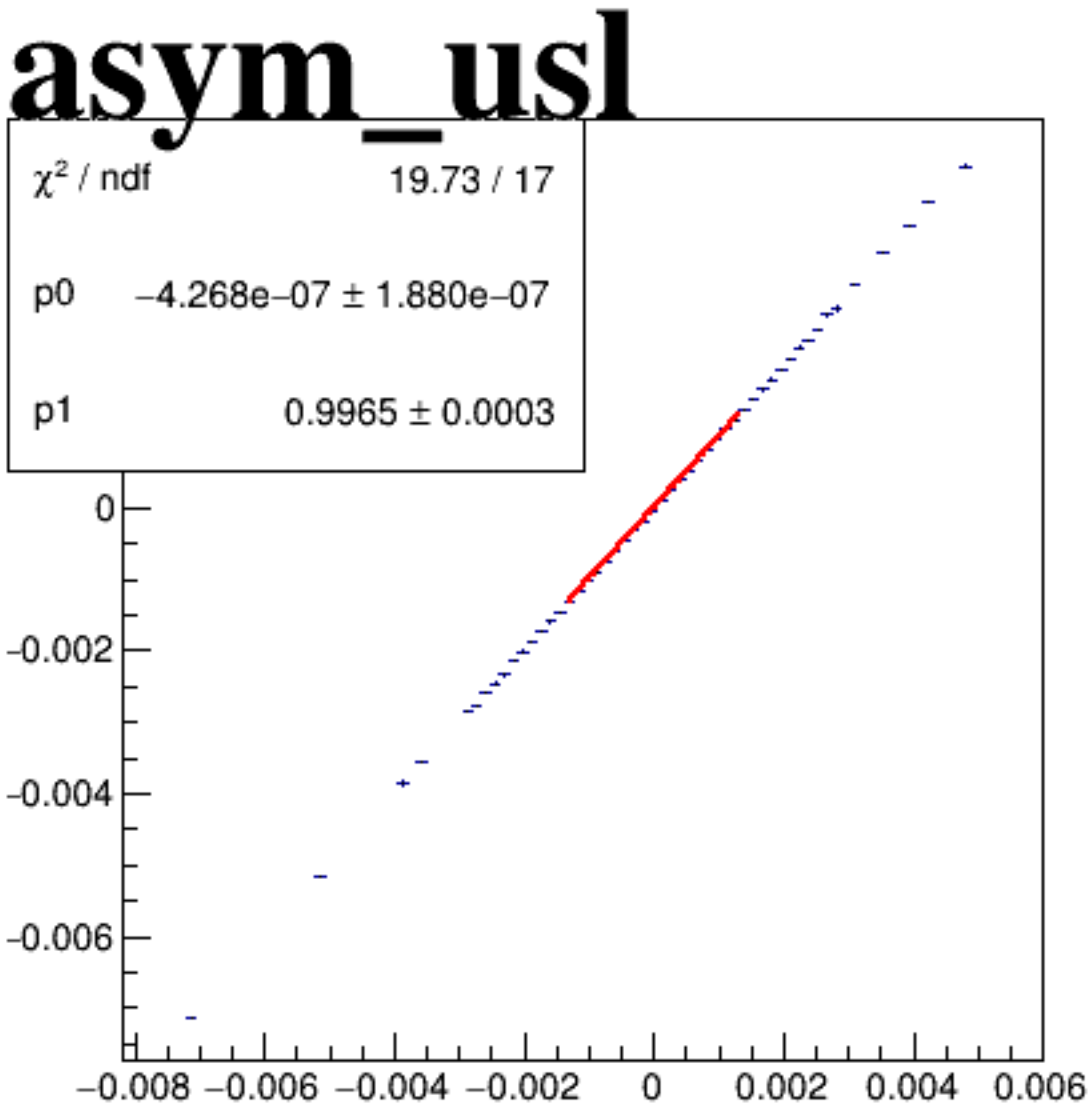
asym_usr



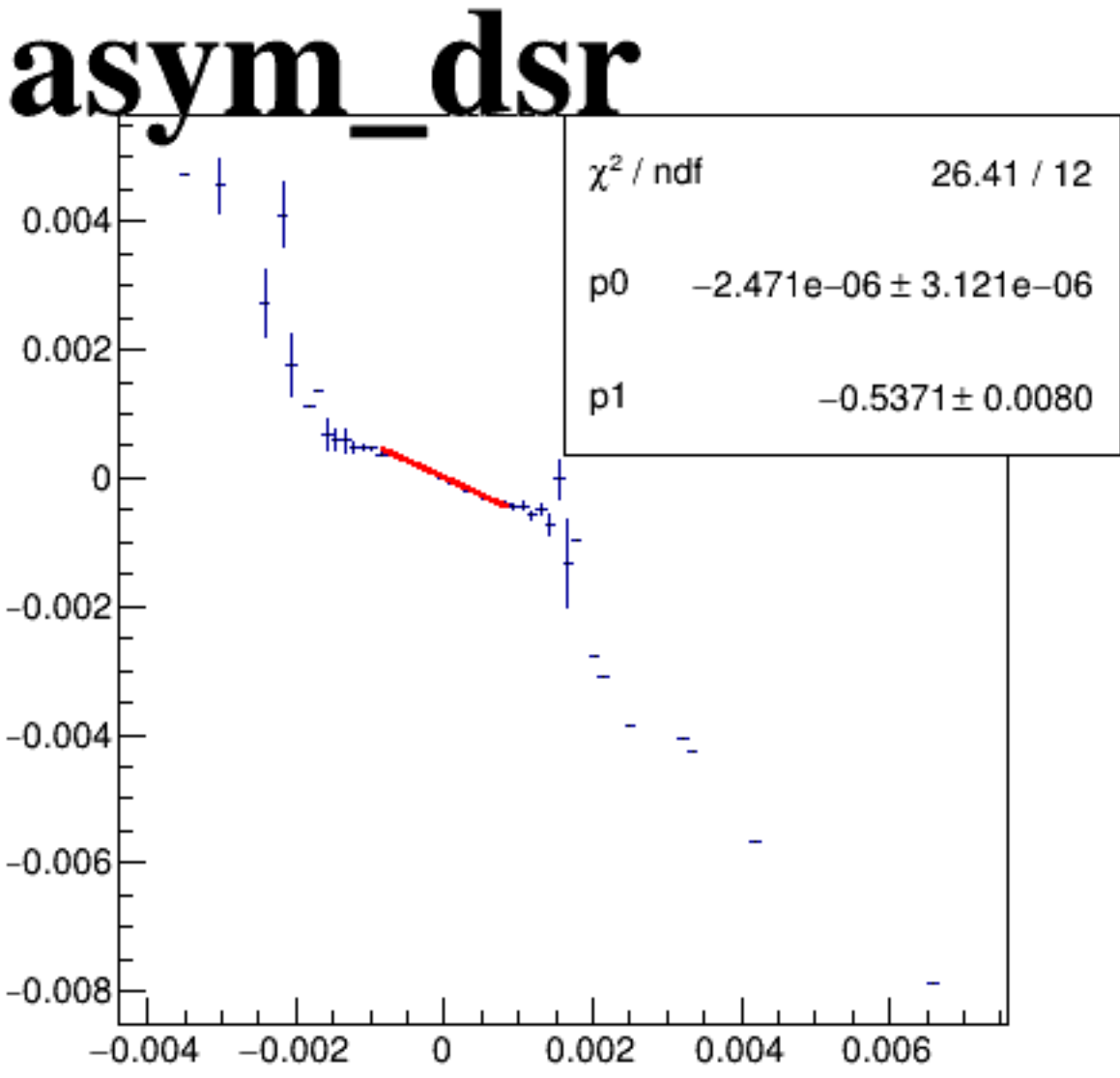
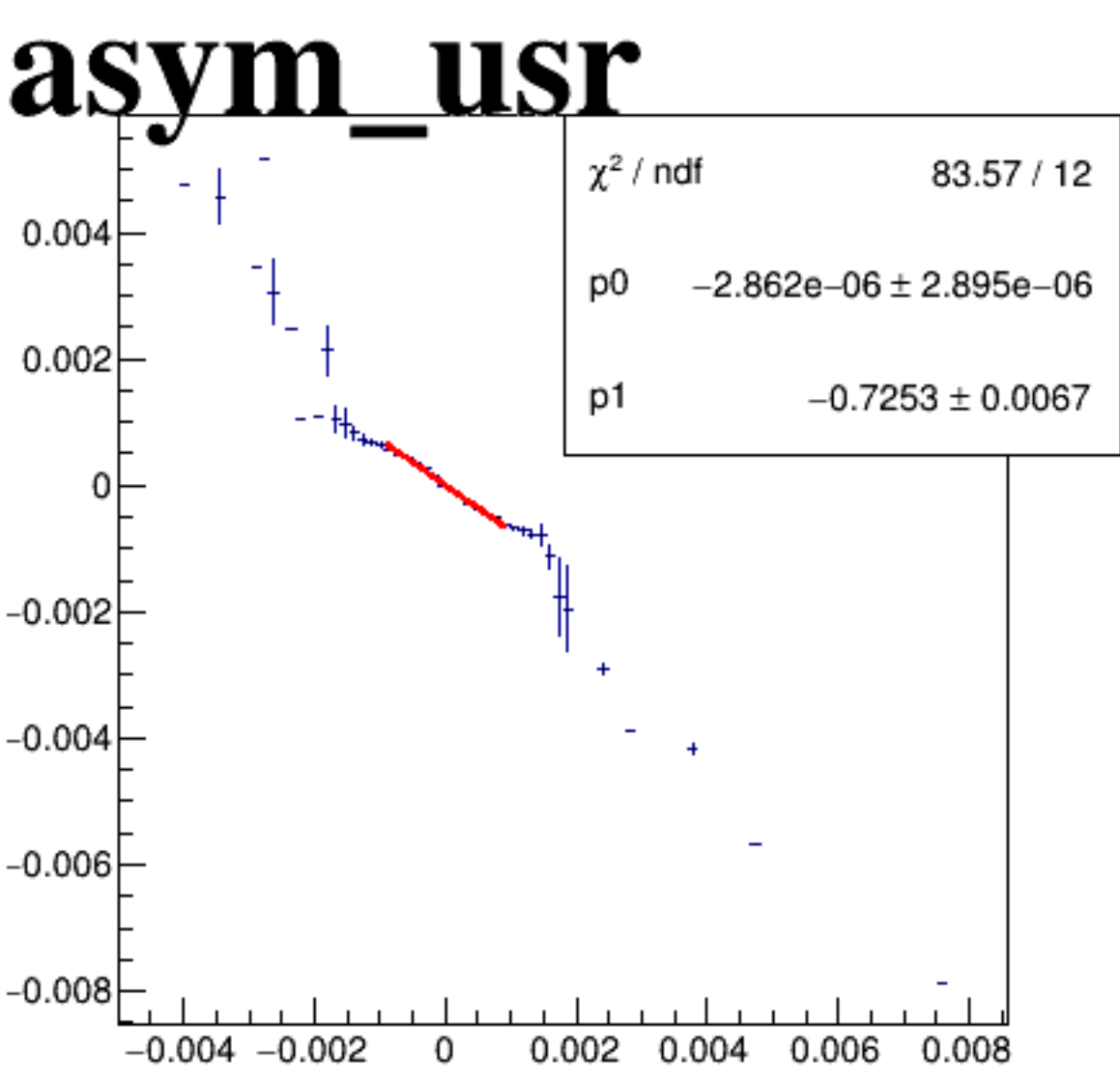
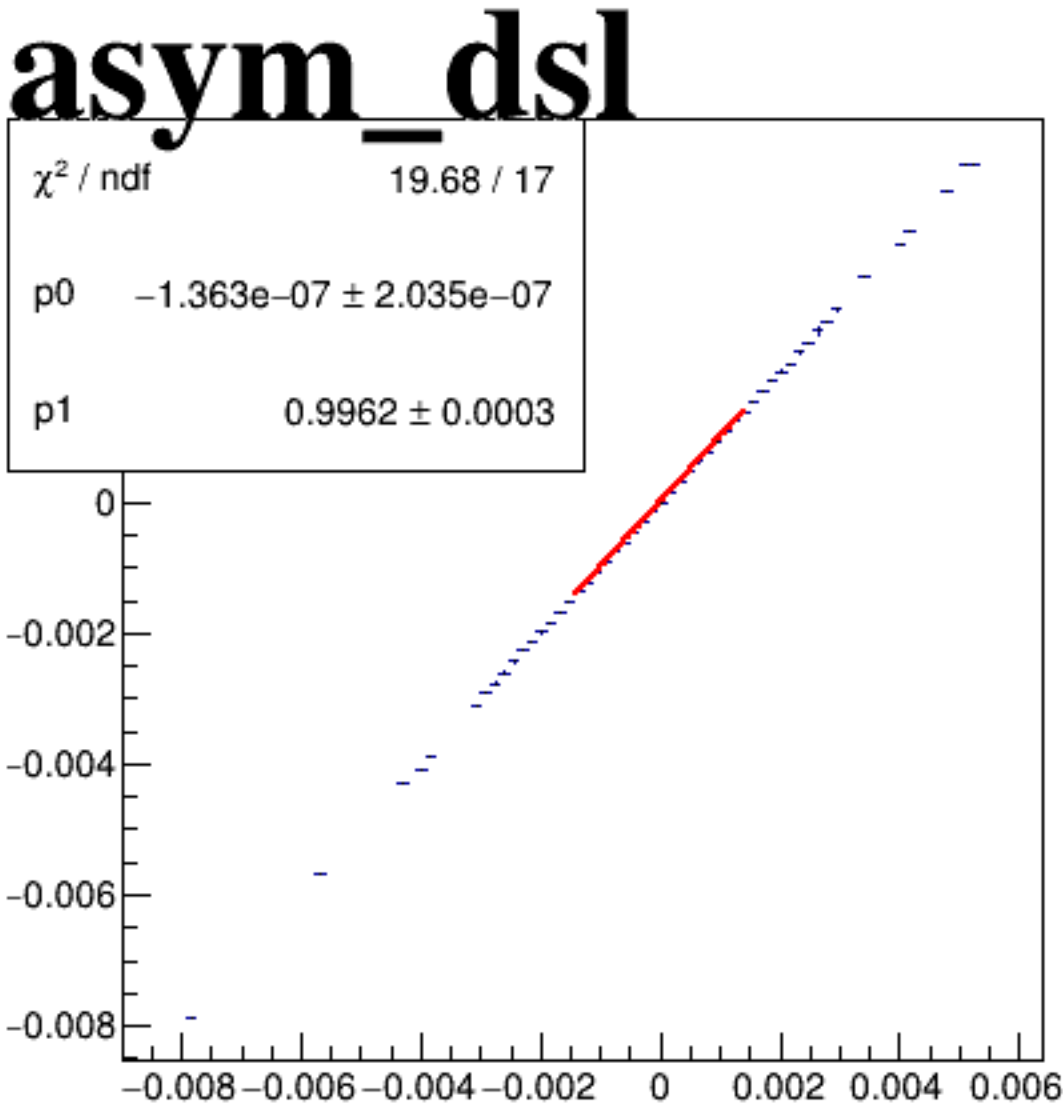
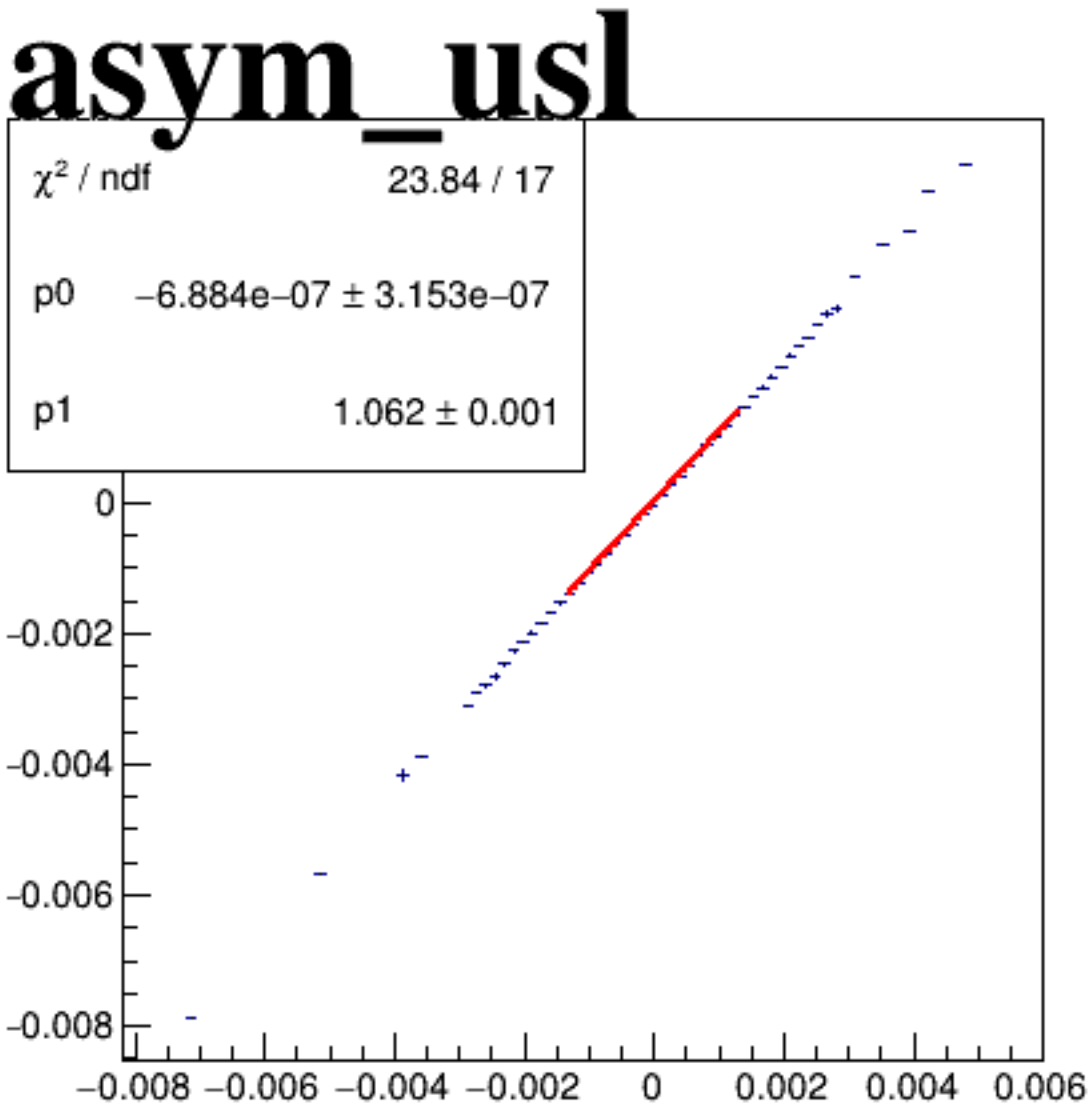
asym_dsr



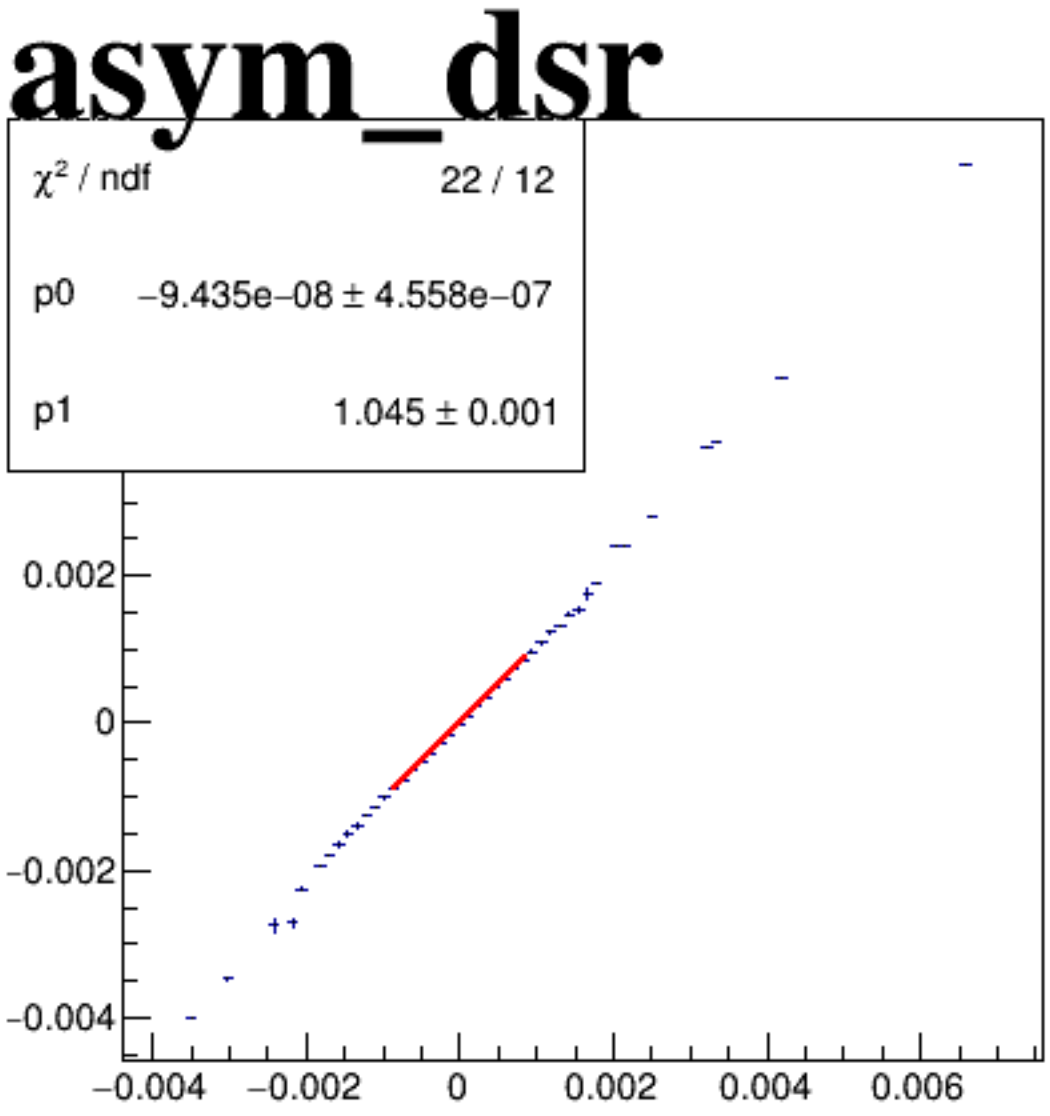
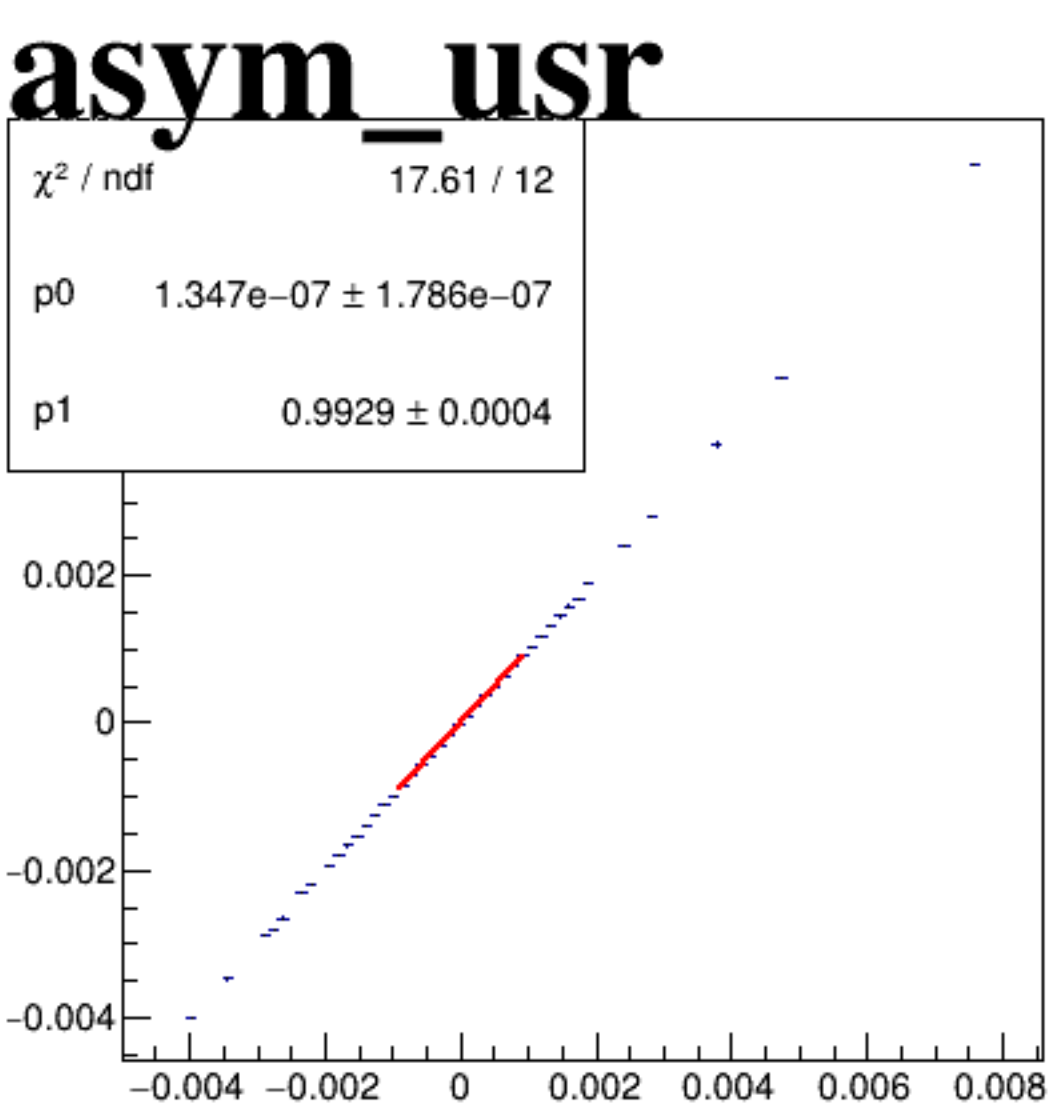
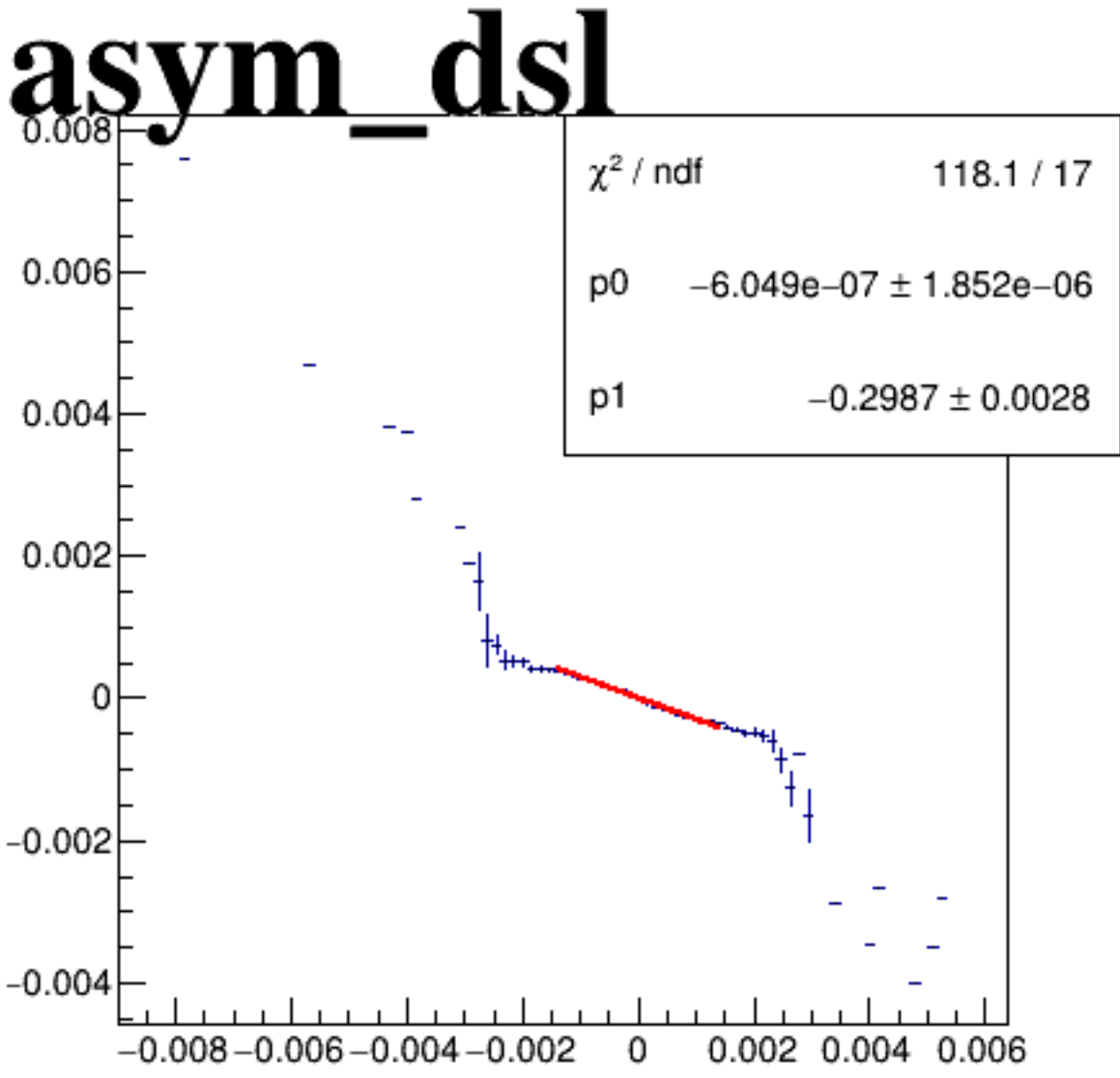
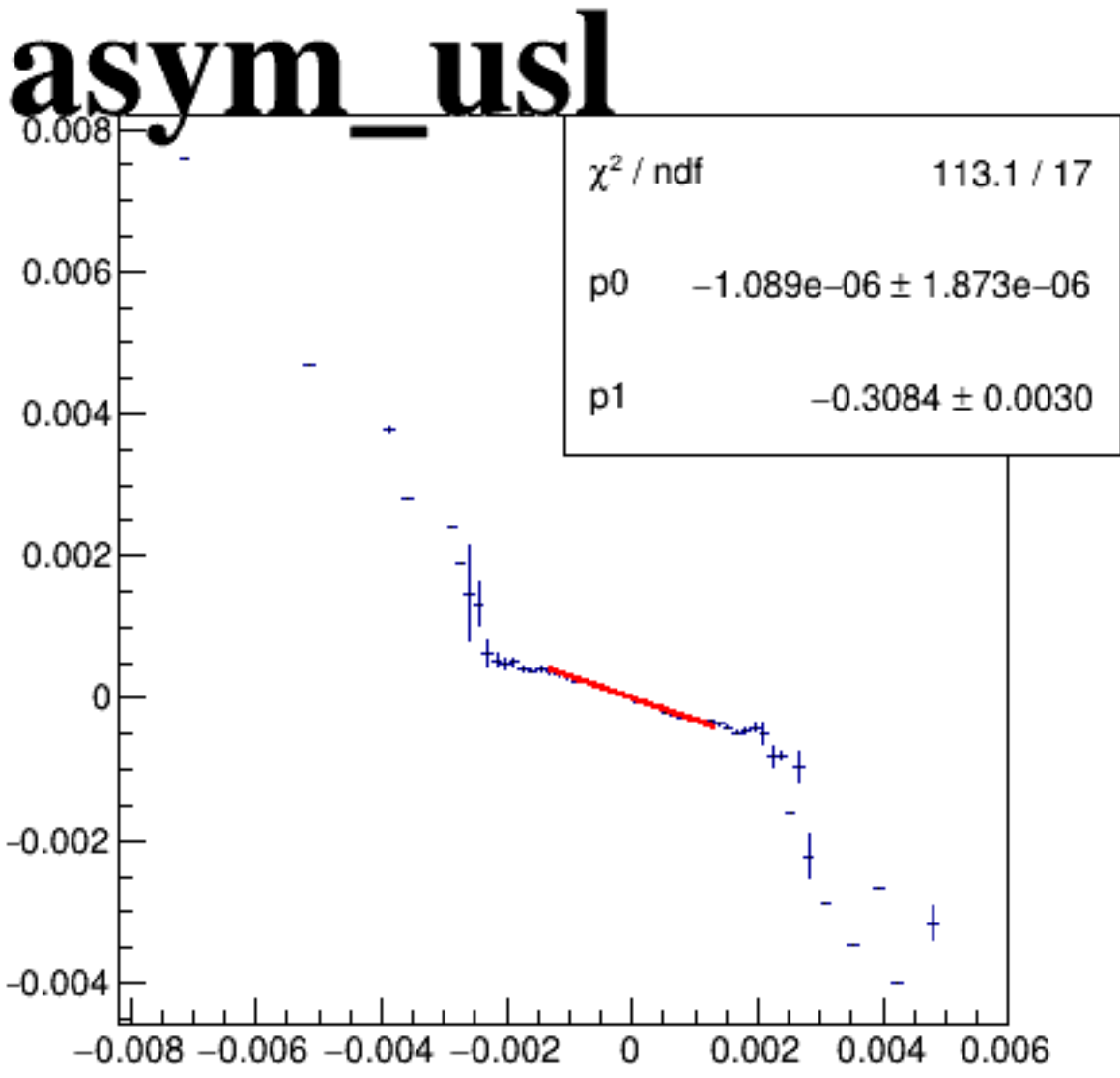
asym_usl



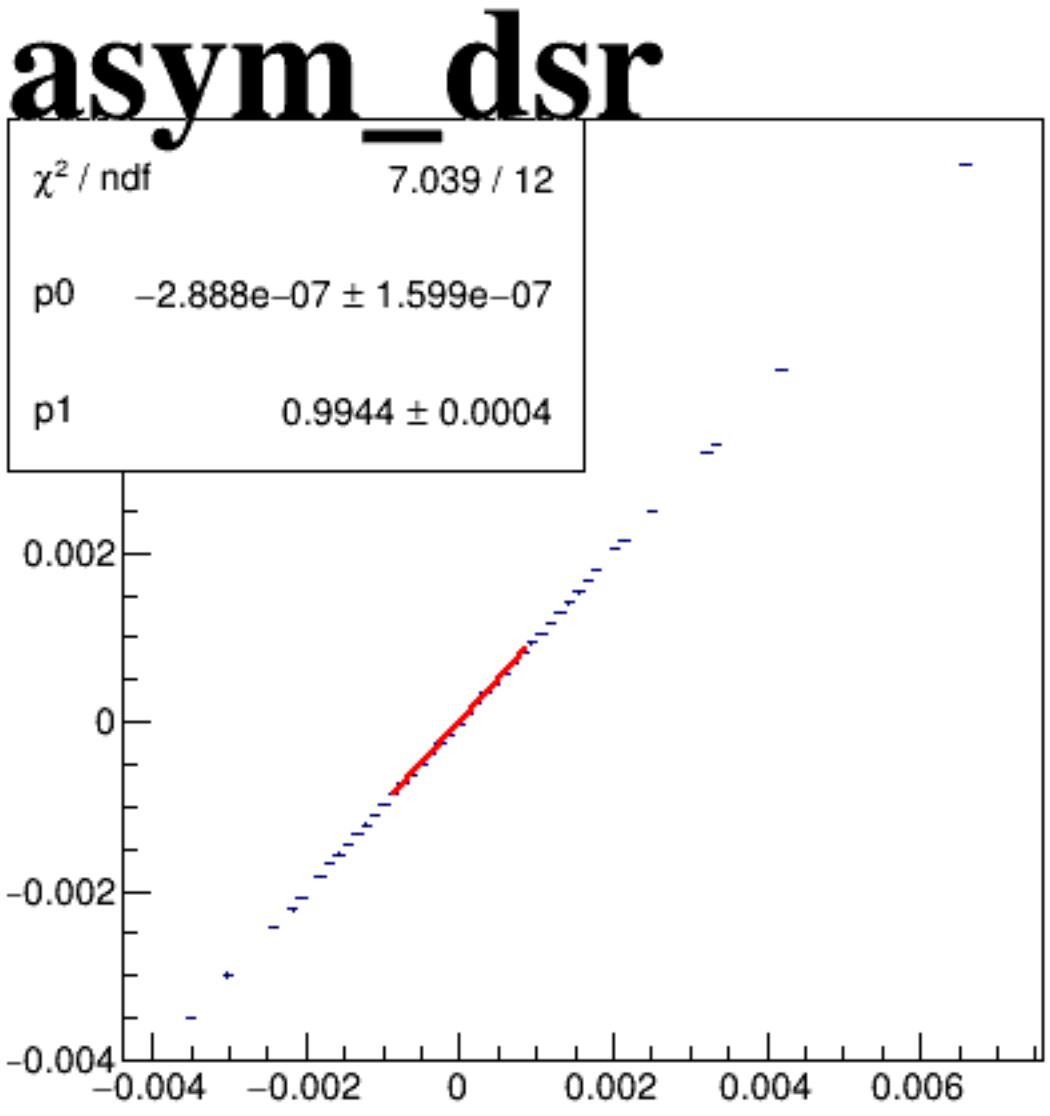
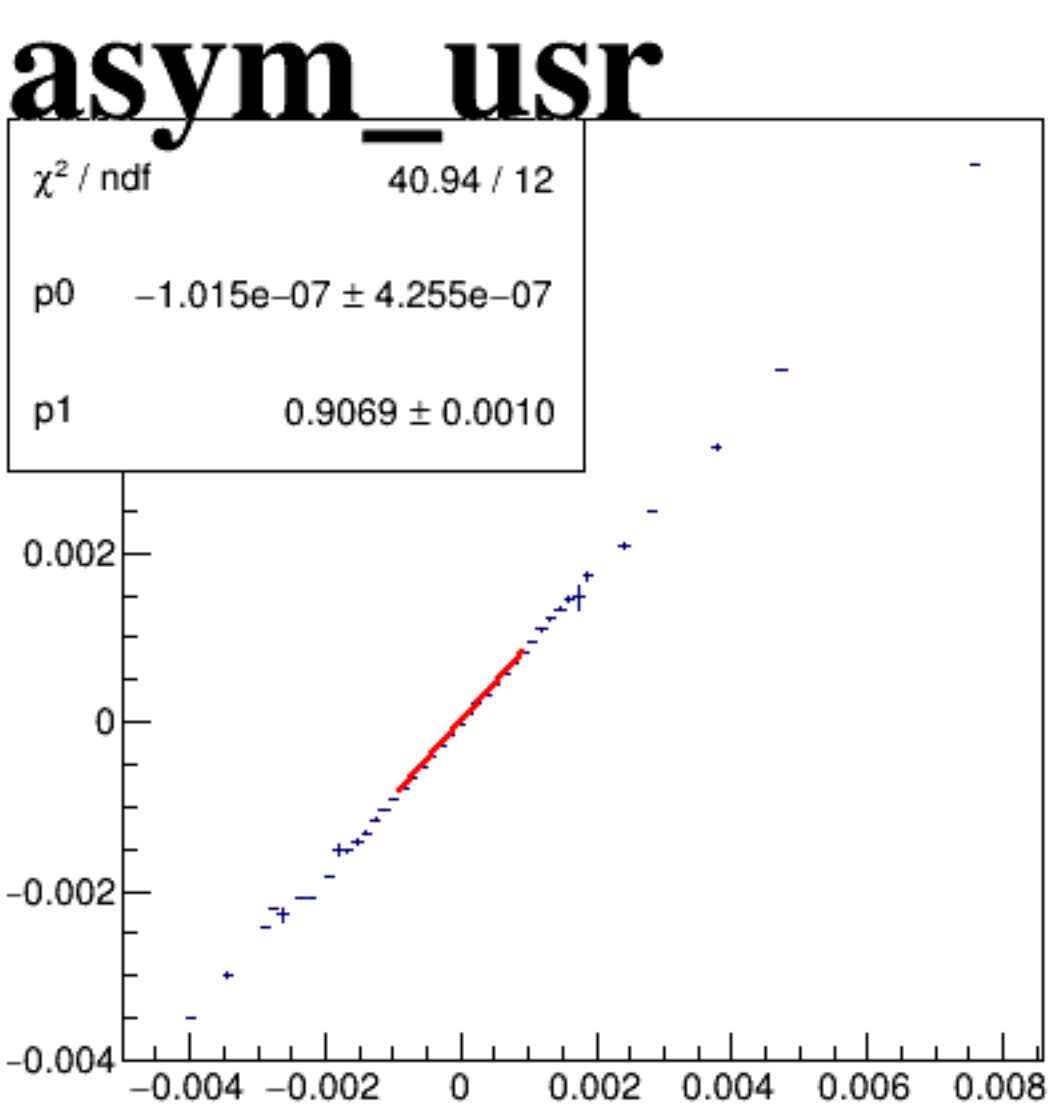
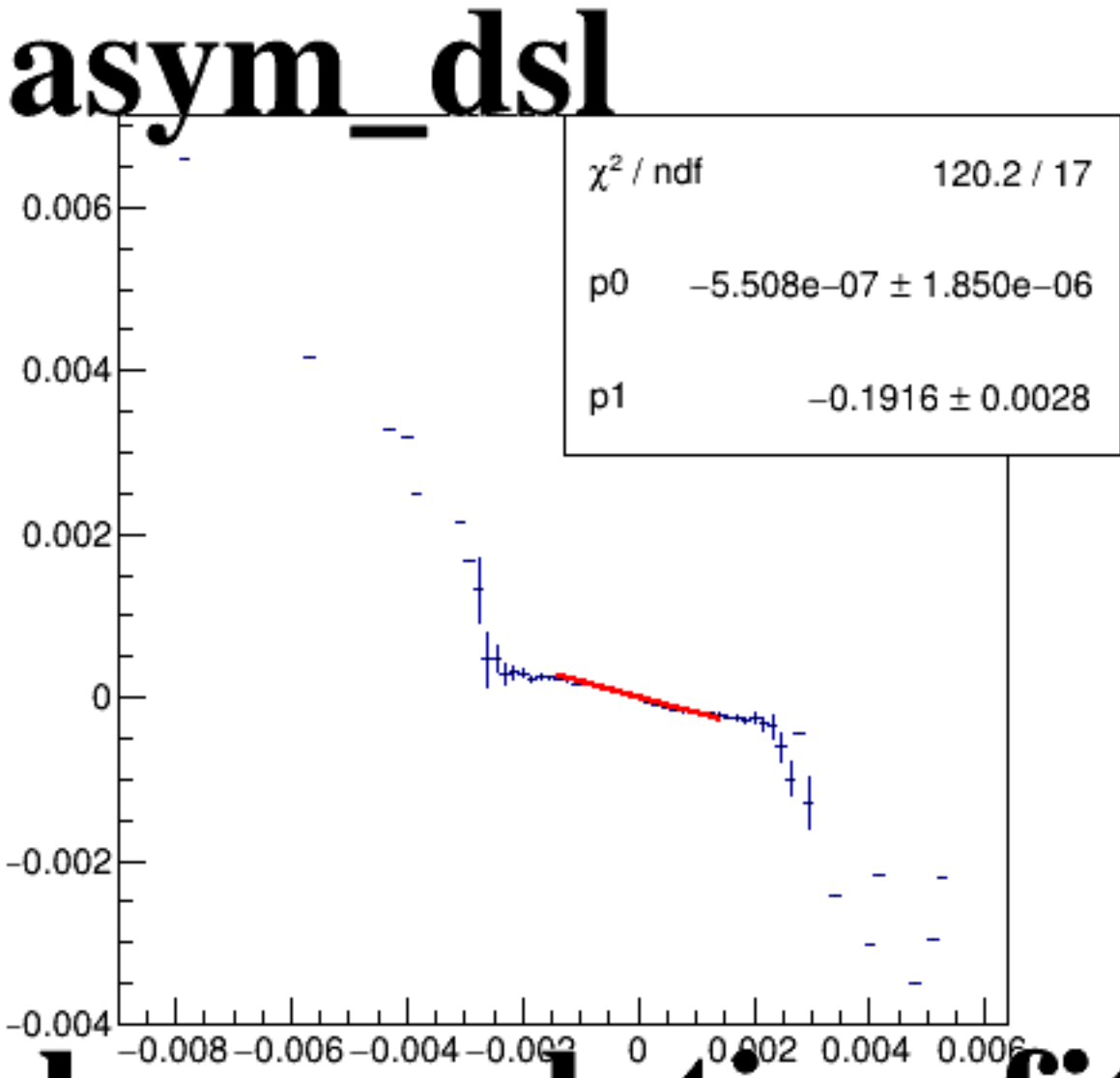
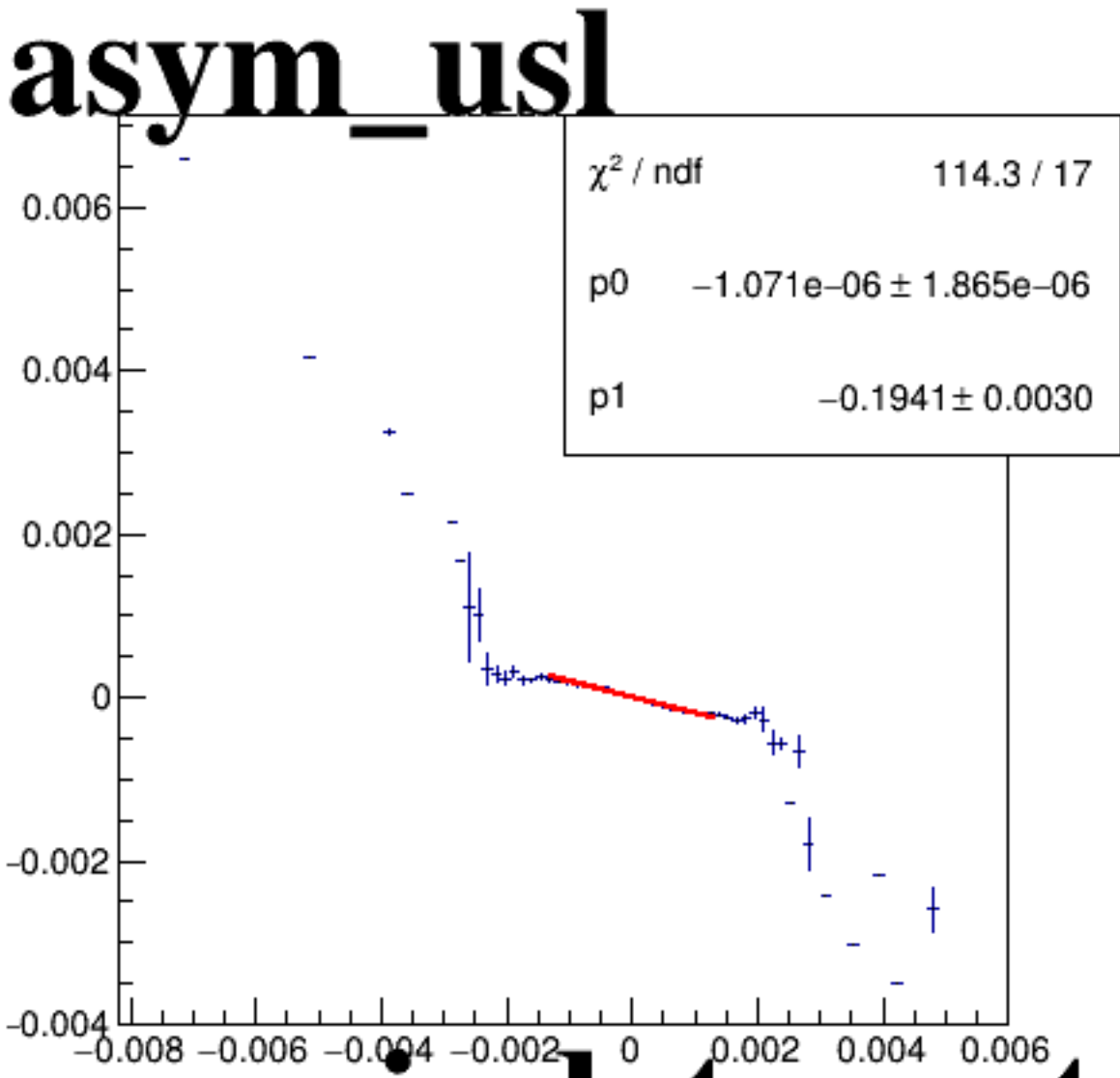
asym_dsl



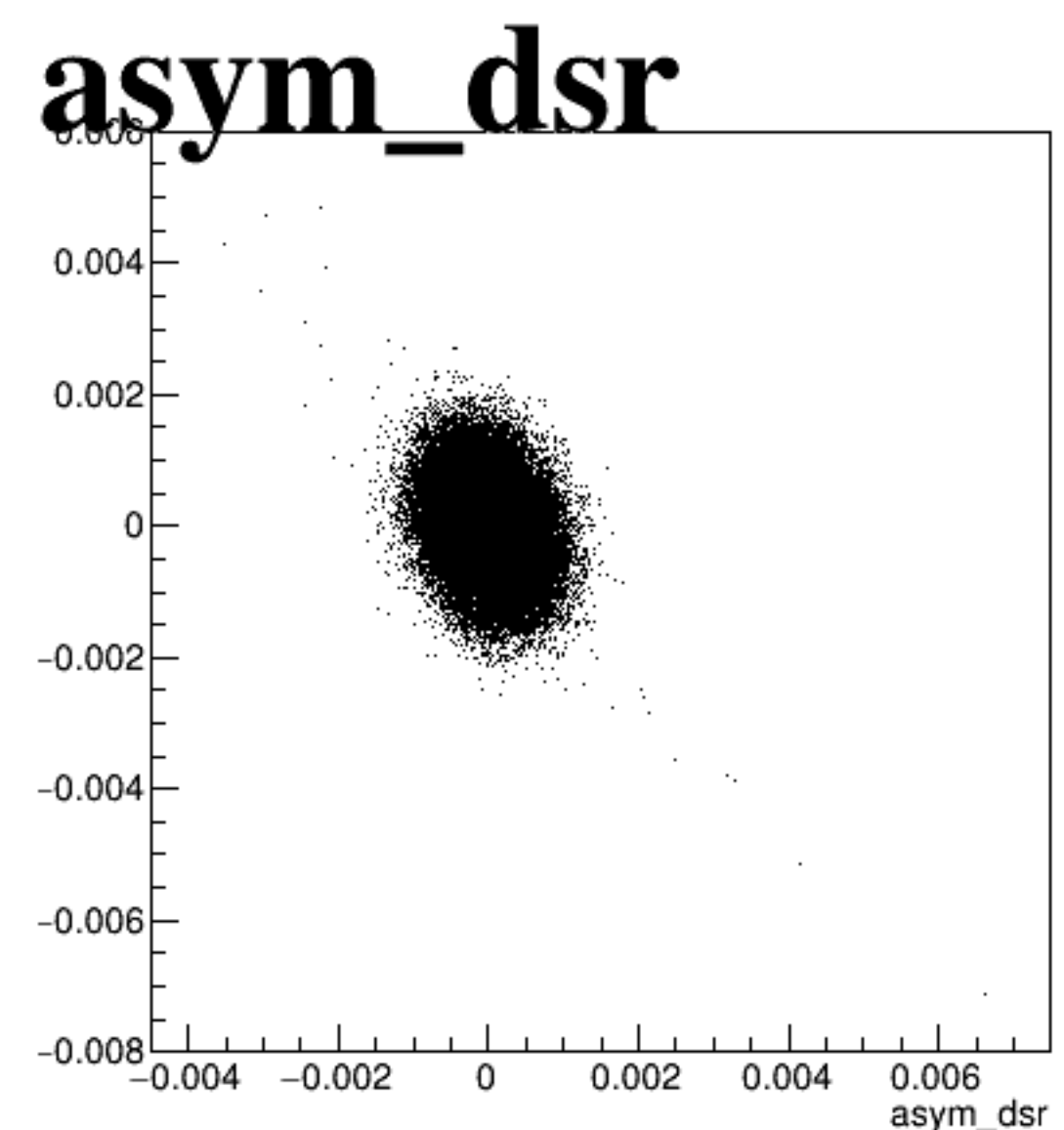
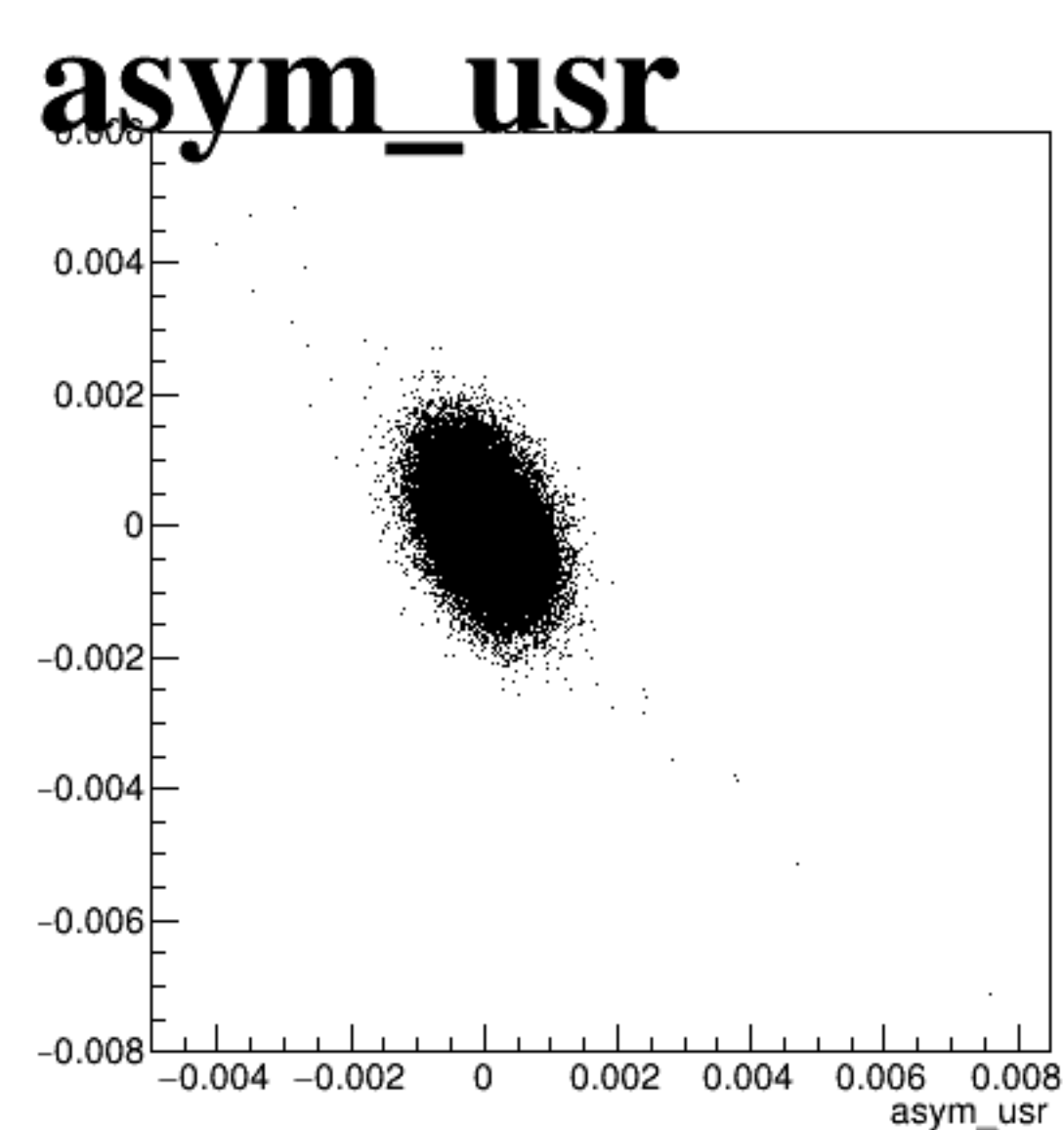
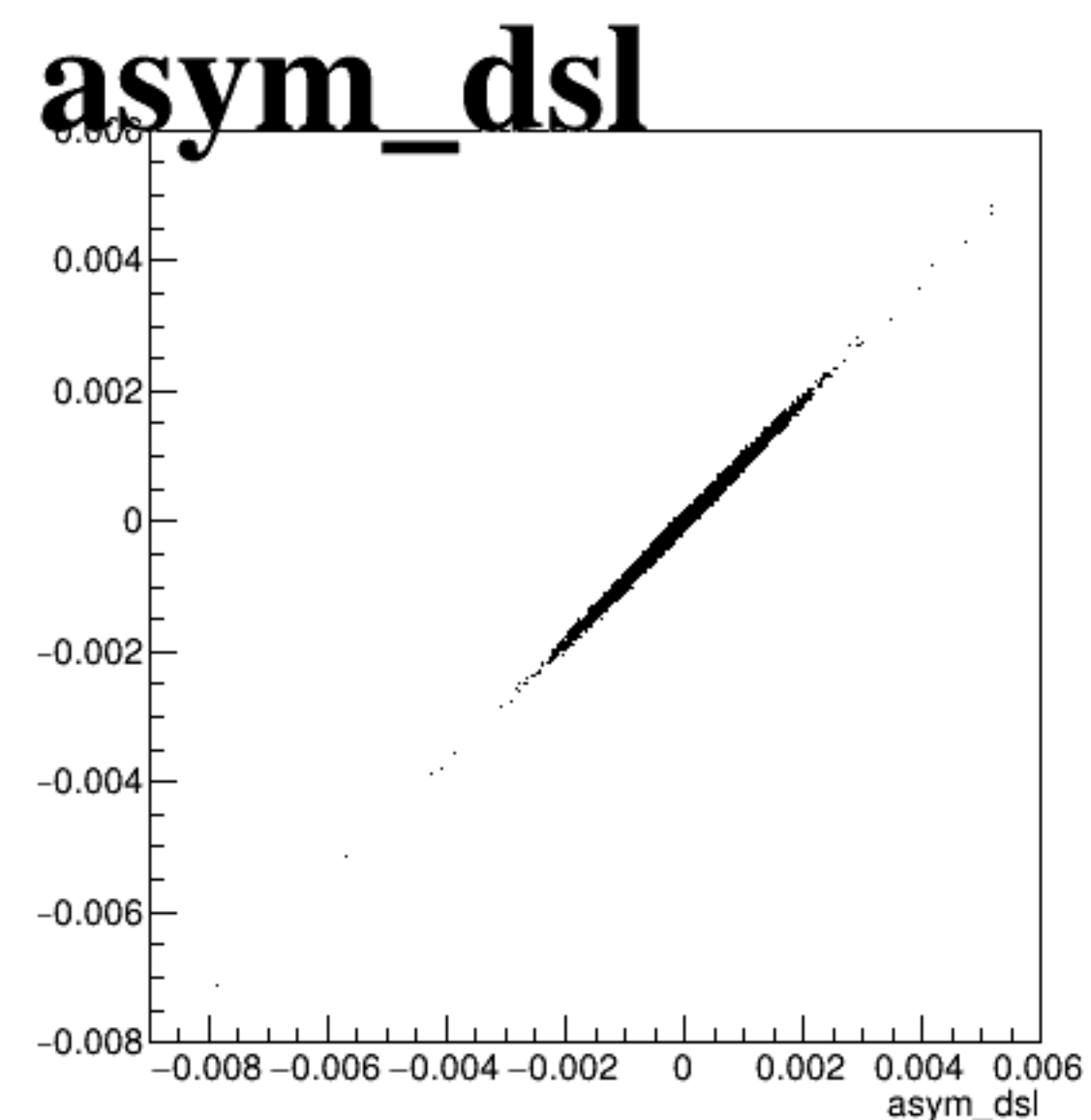
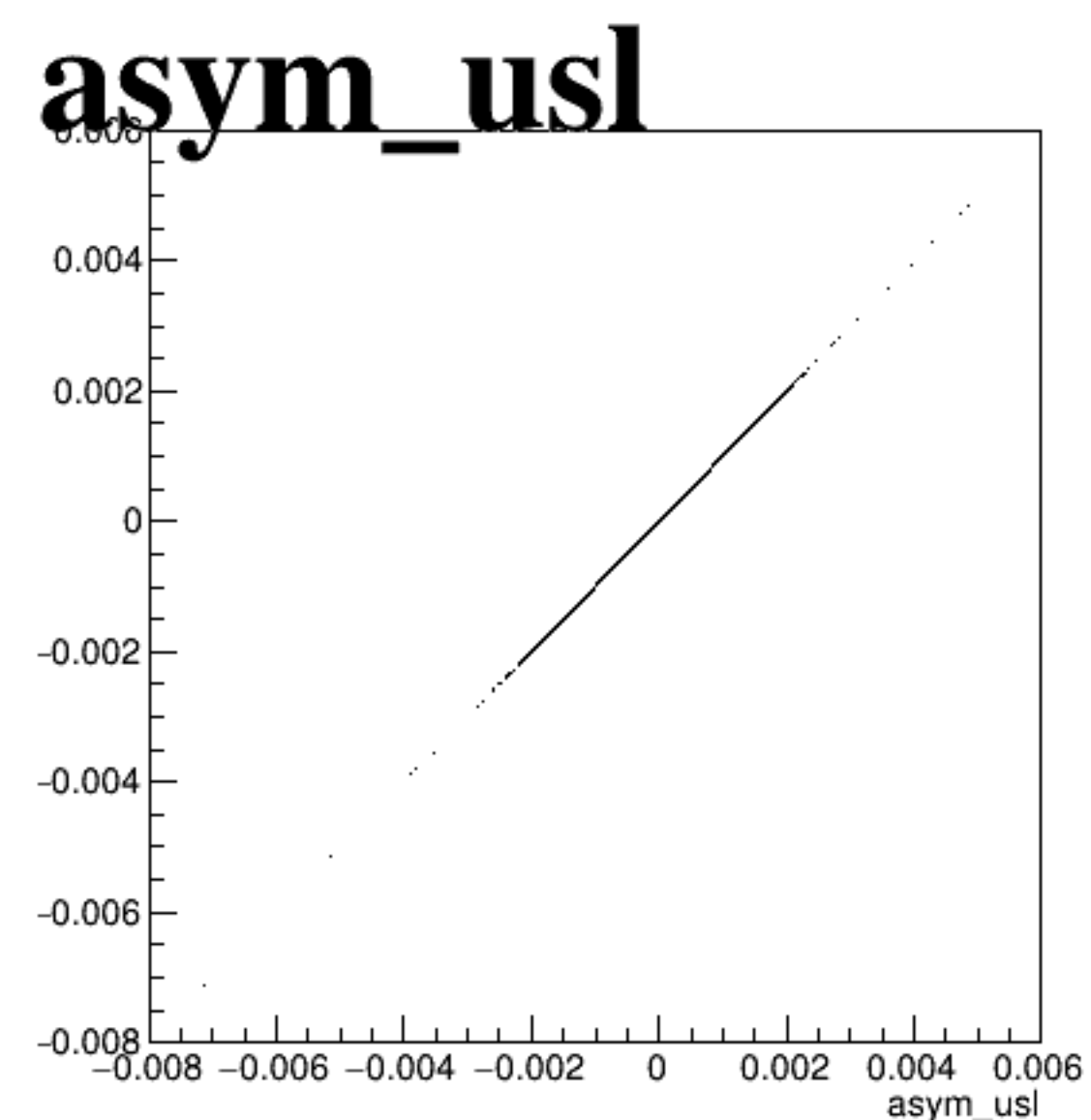
asym_usr



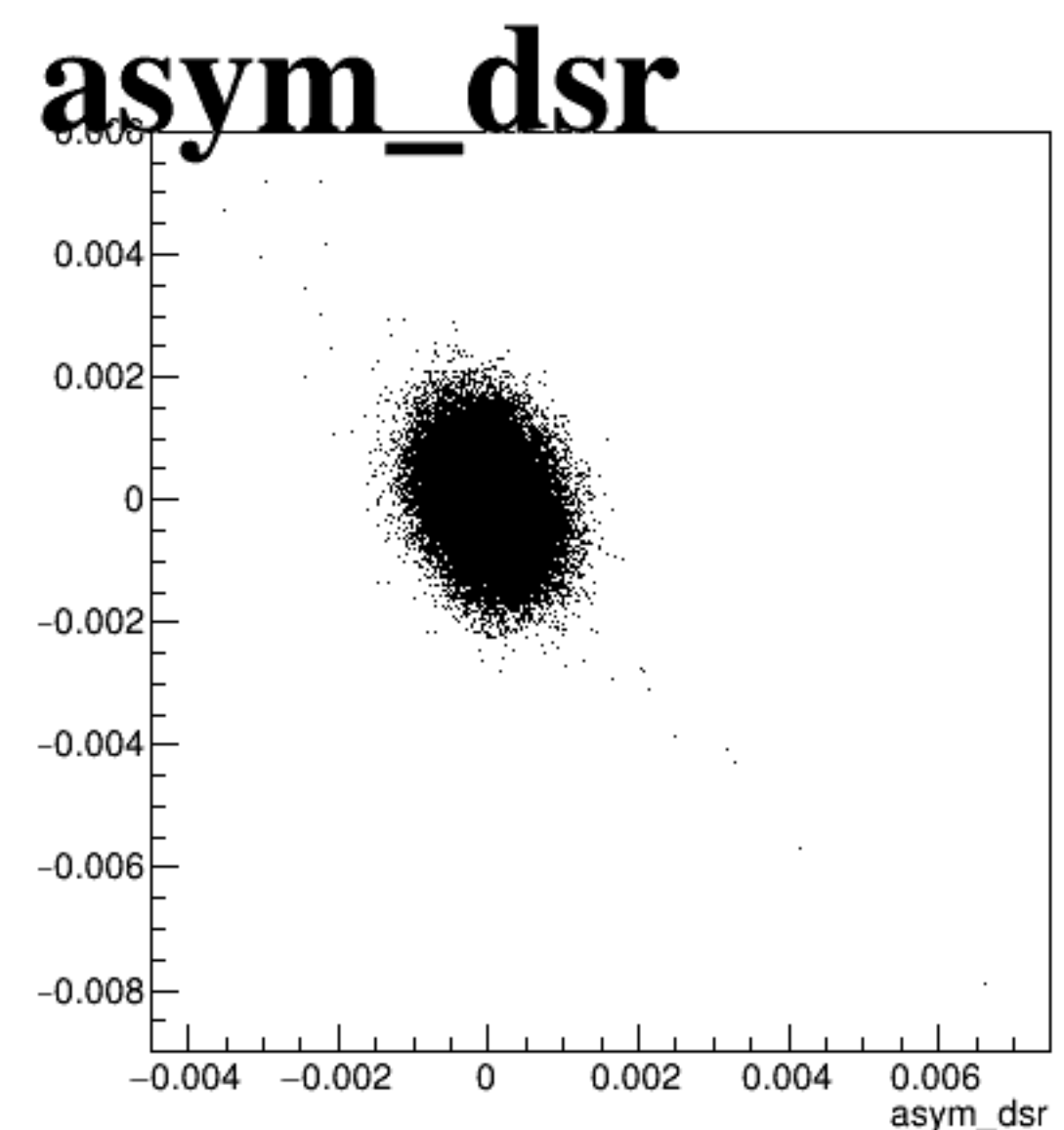
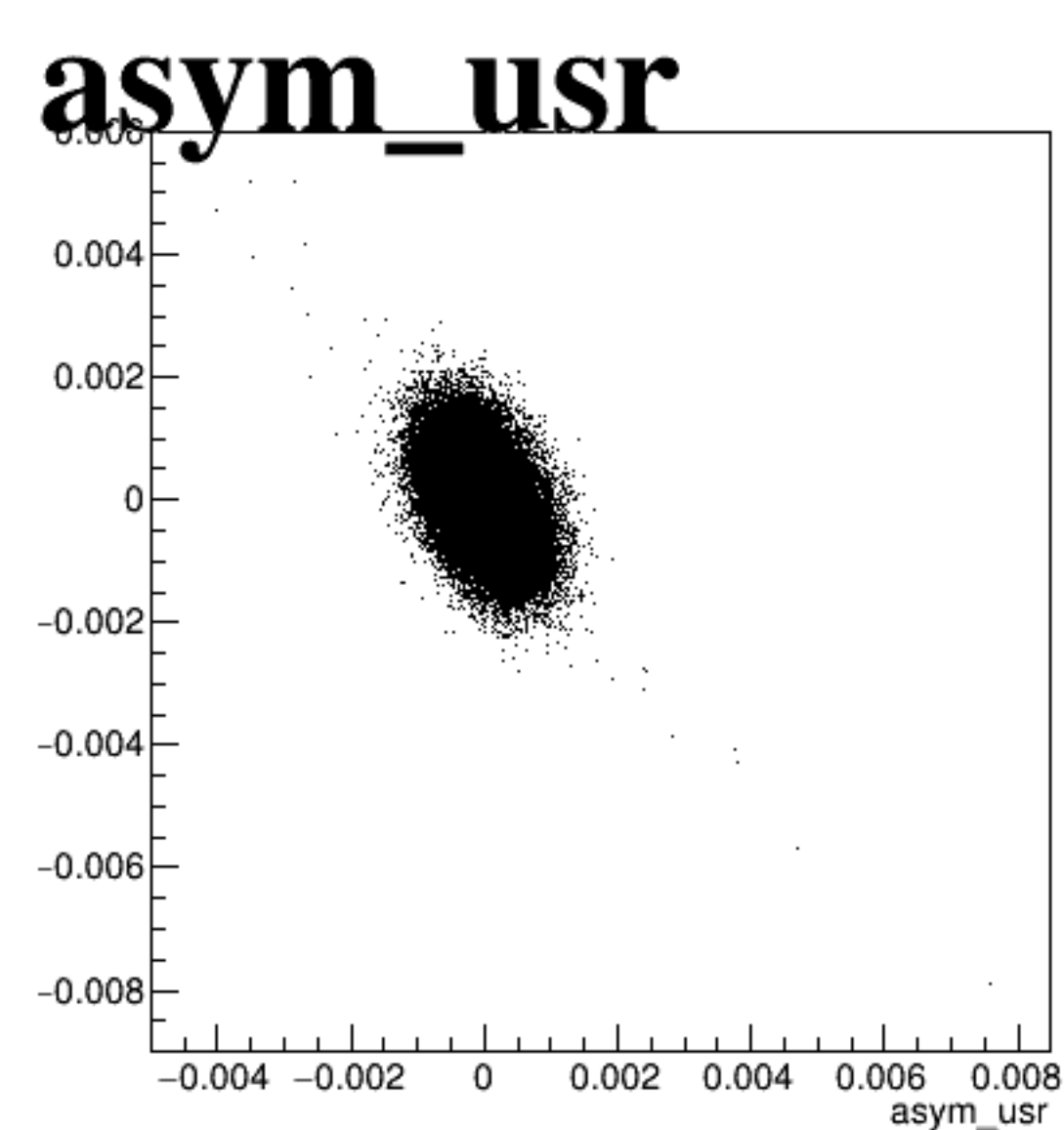
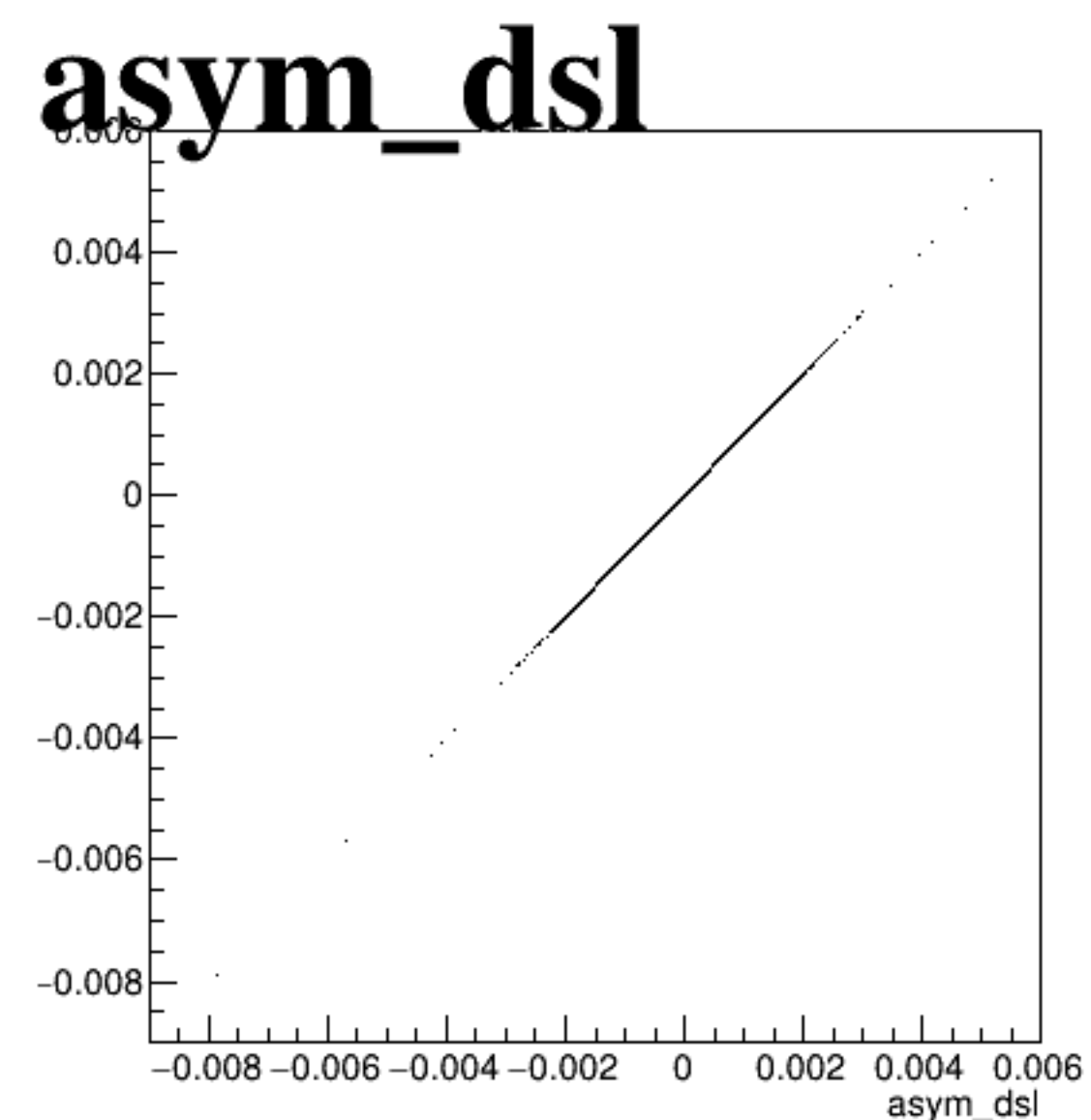
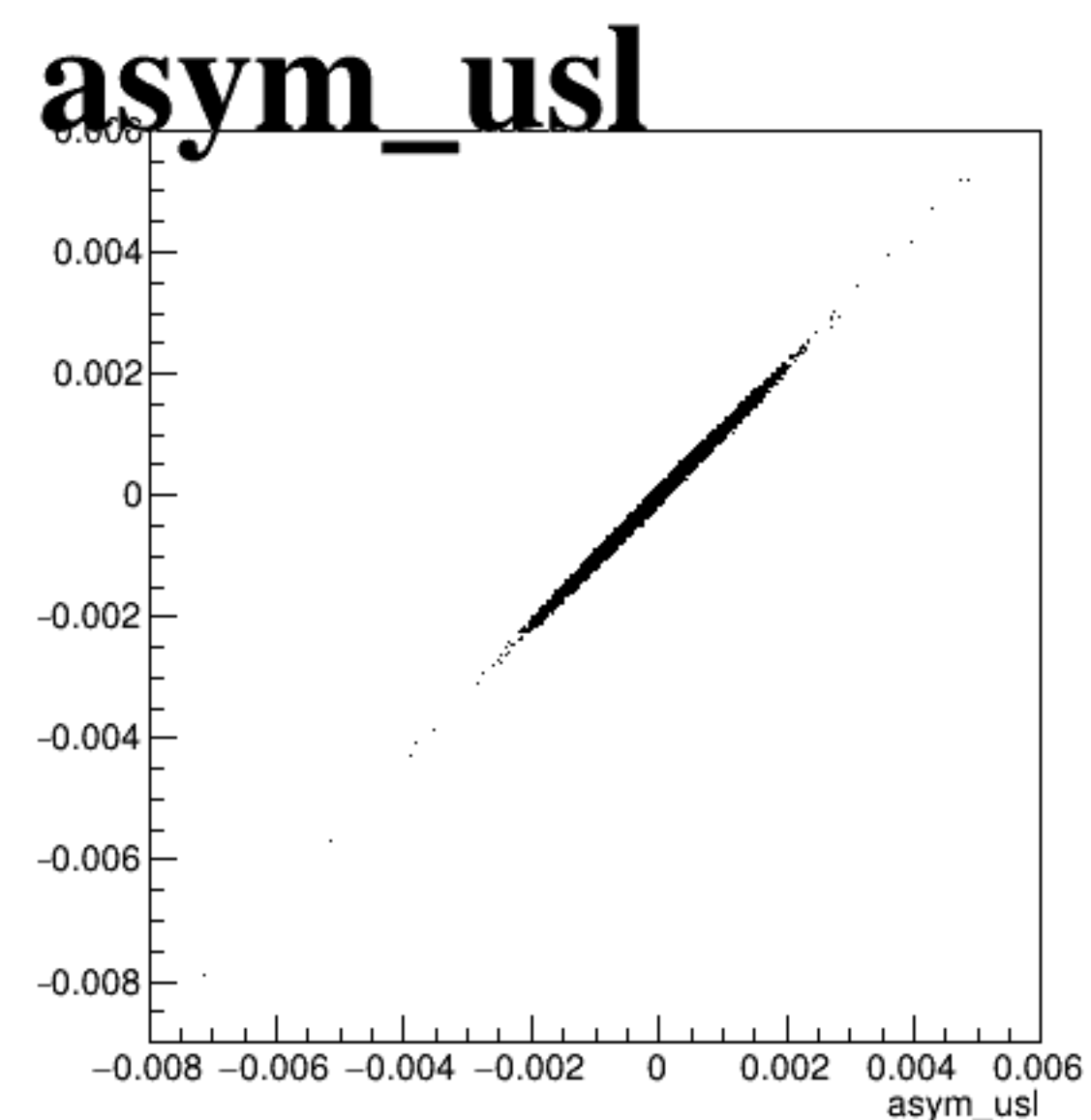
asym_dsr



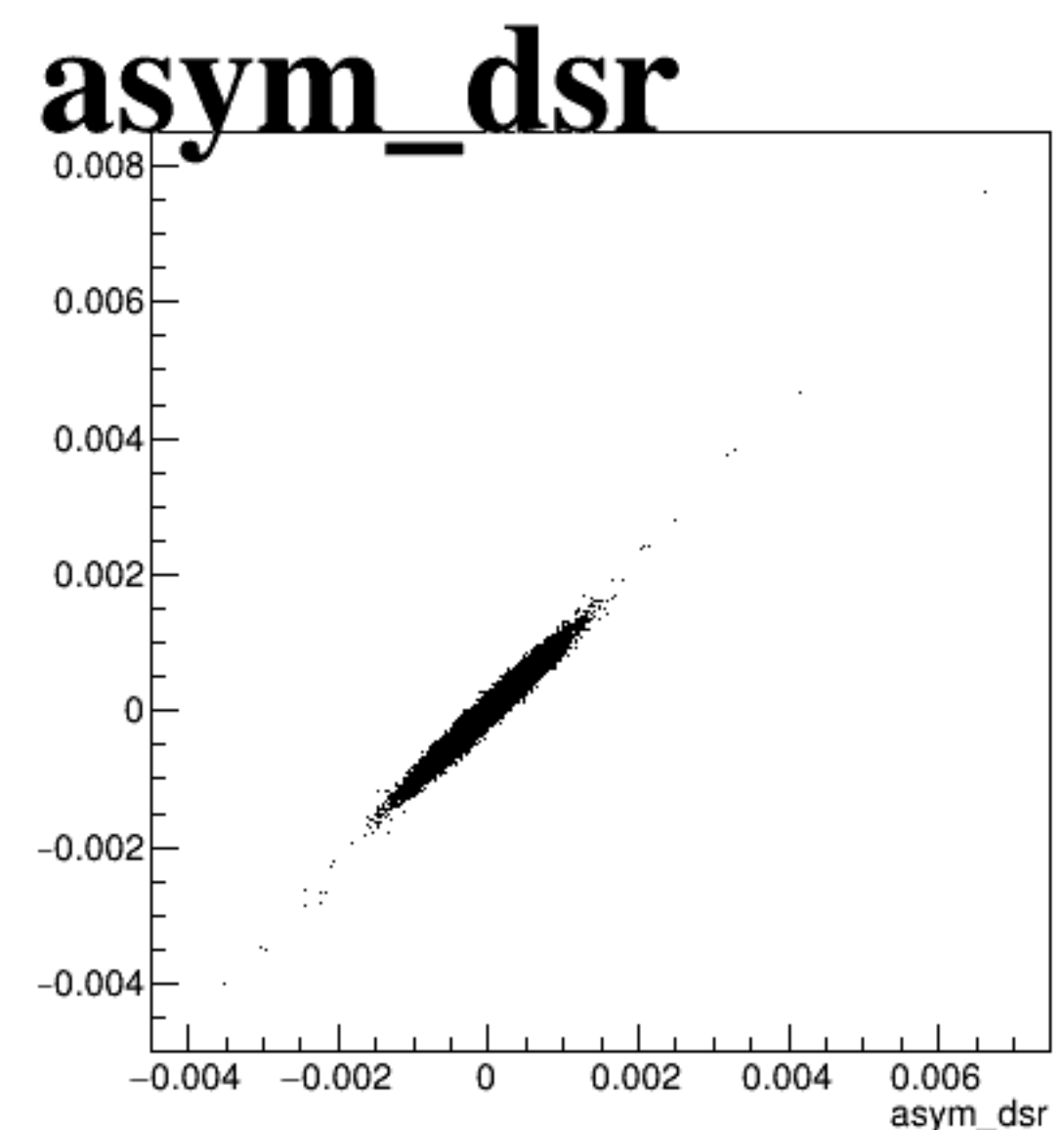
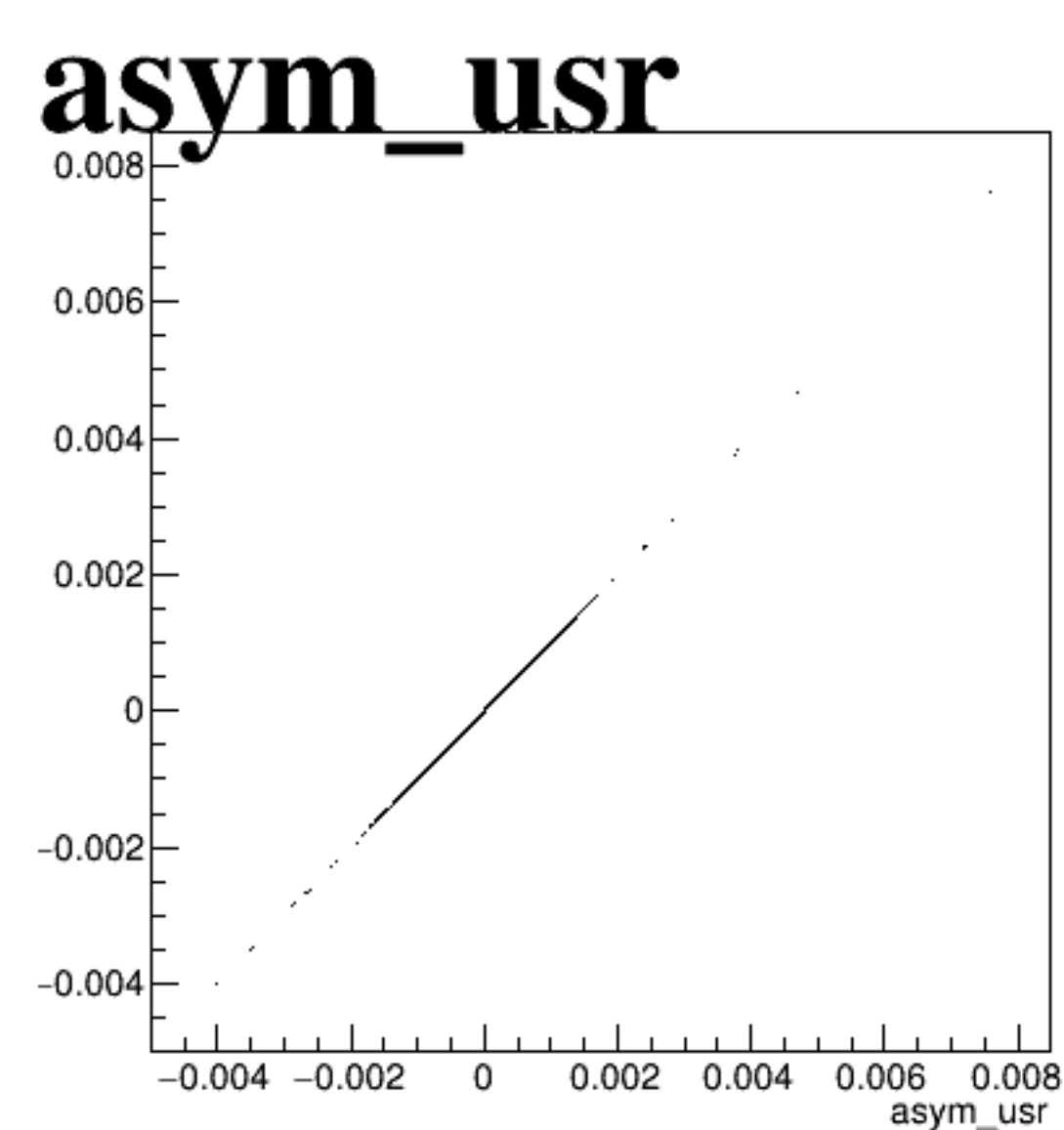
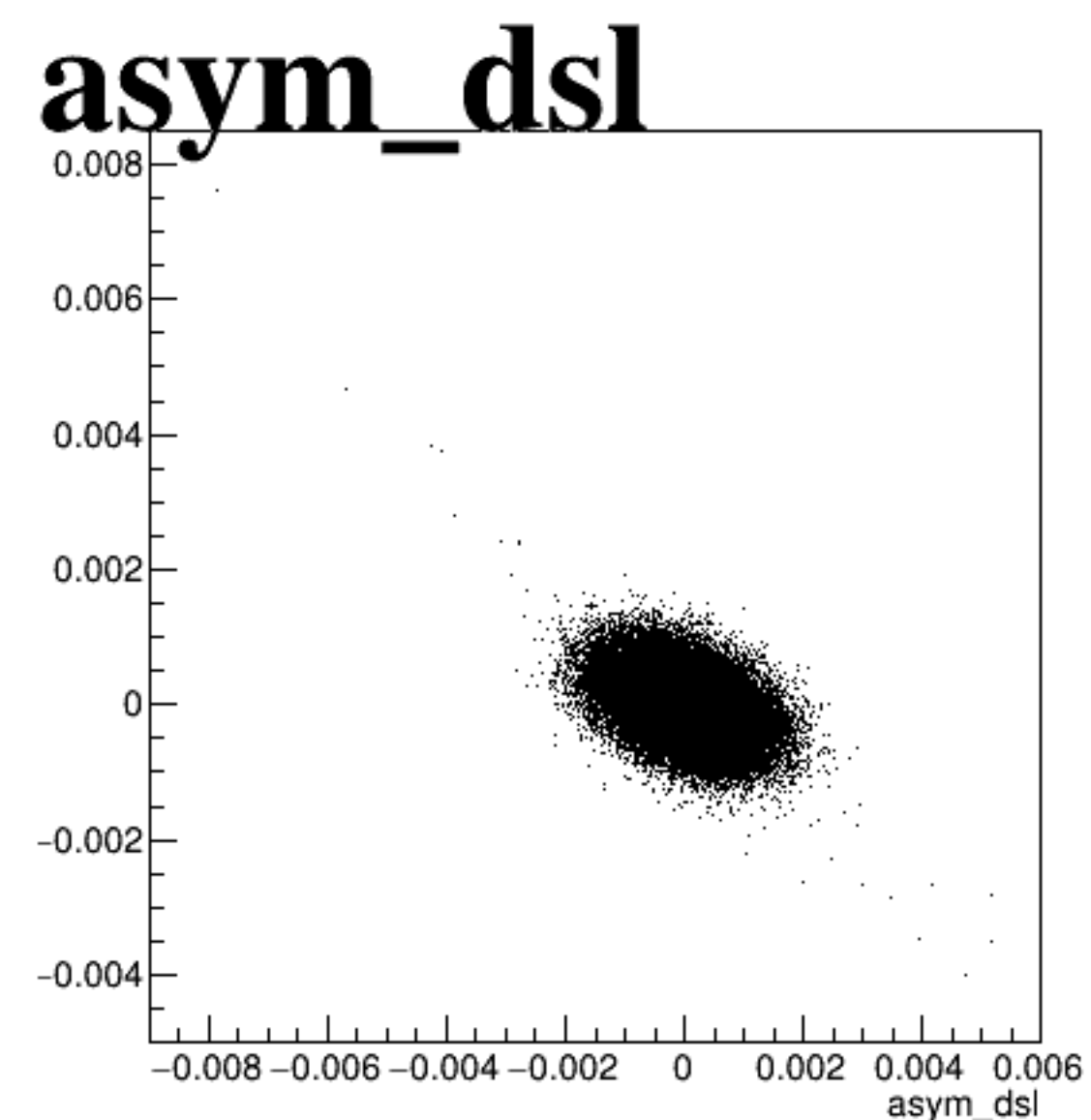
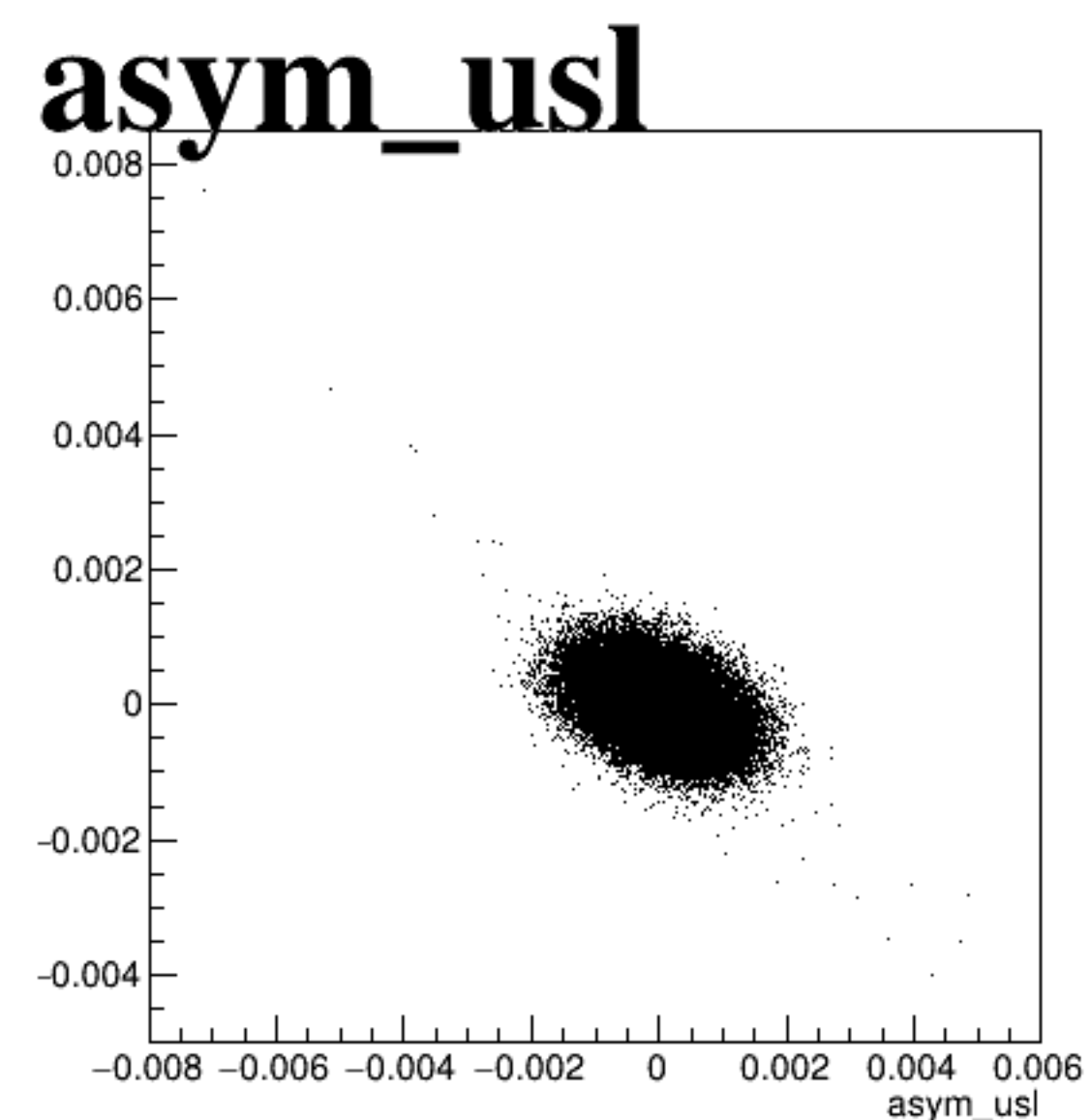
asym_usl



asym_dsl



asym_usr



asym_dsr

