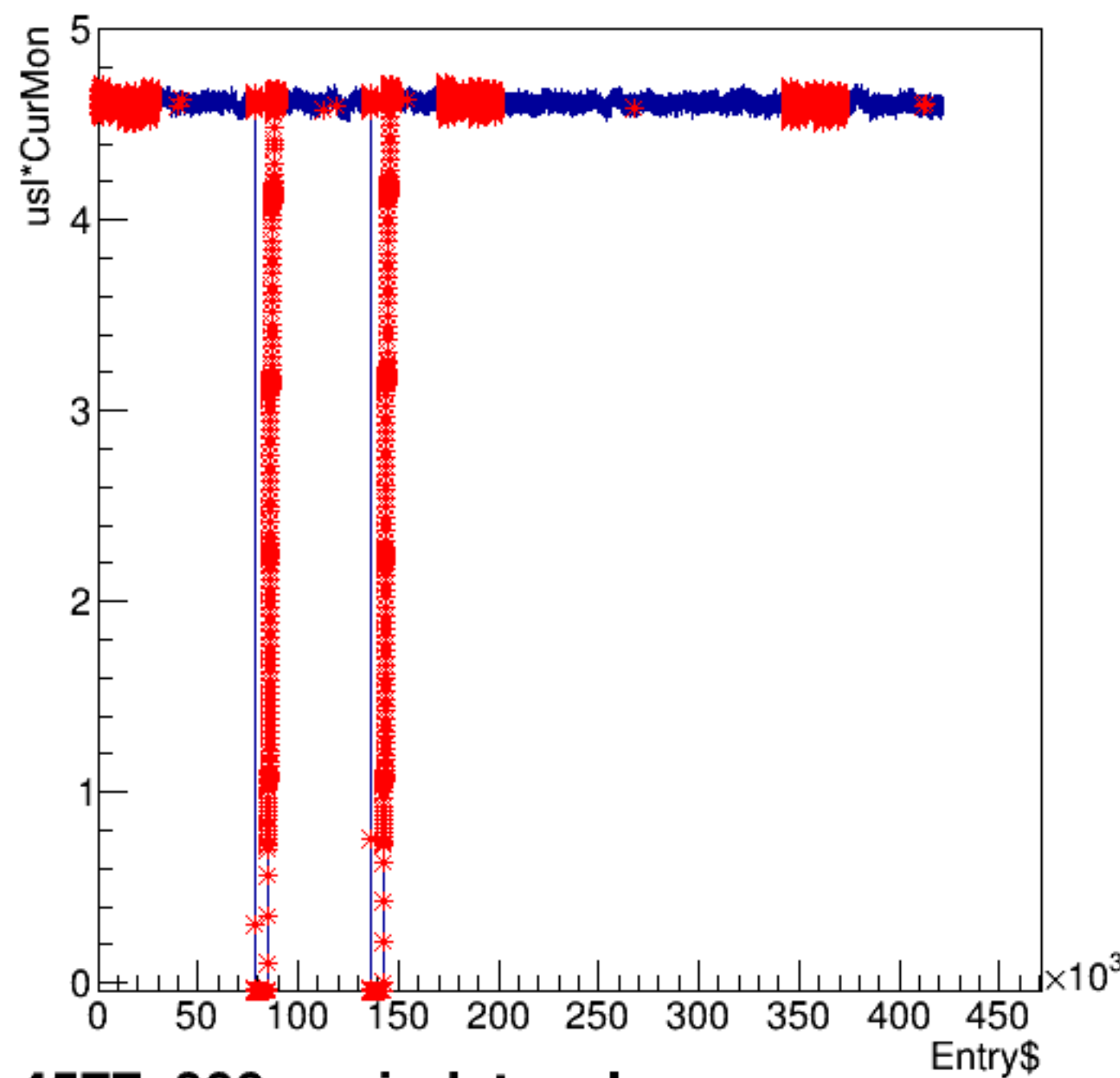
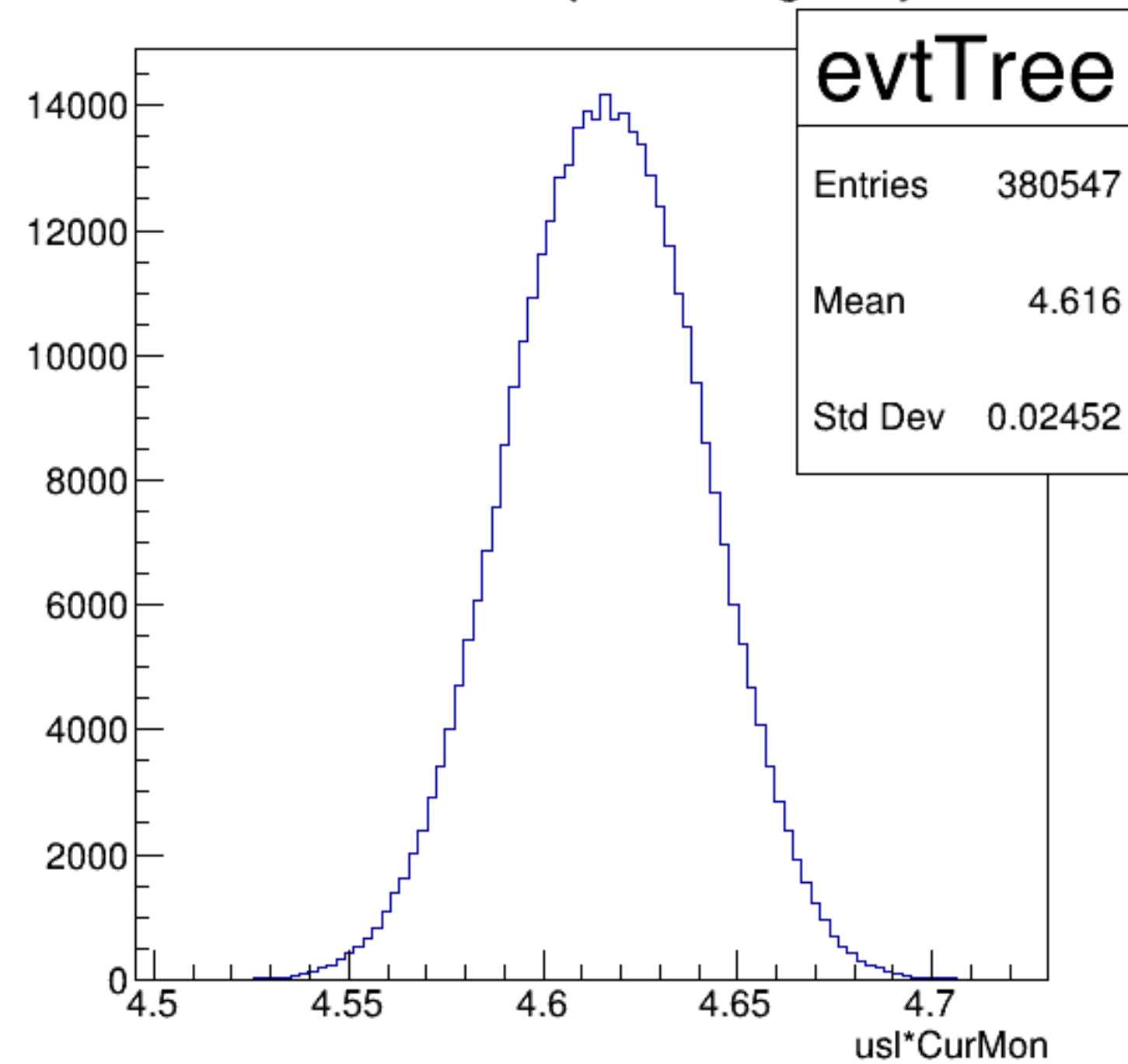


usl*CurMon:Entry\$

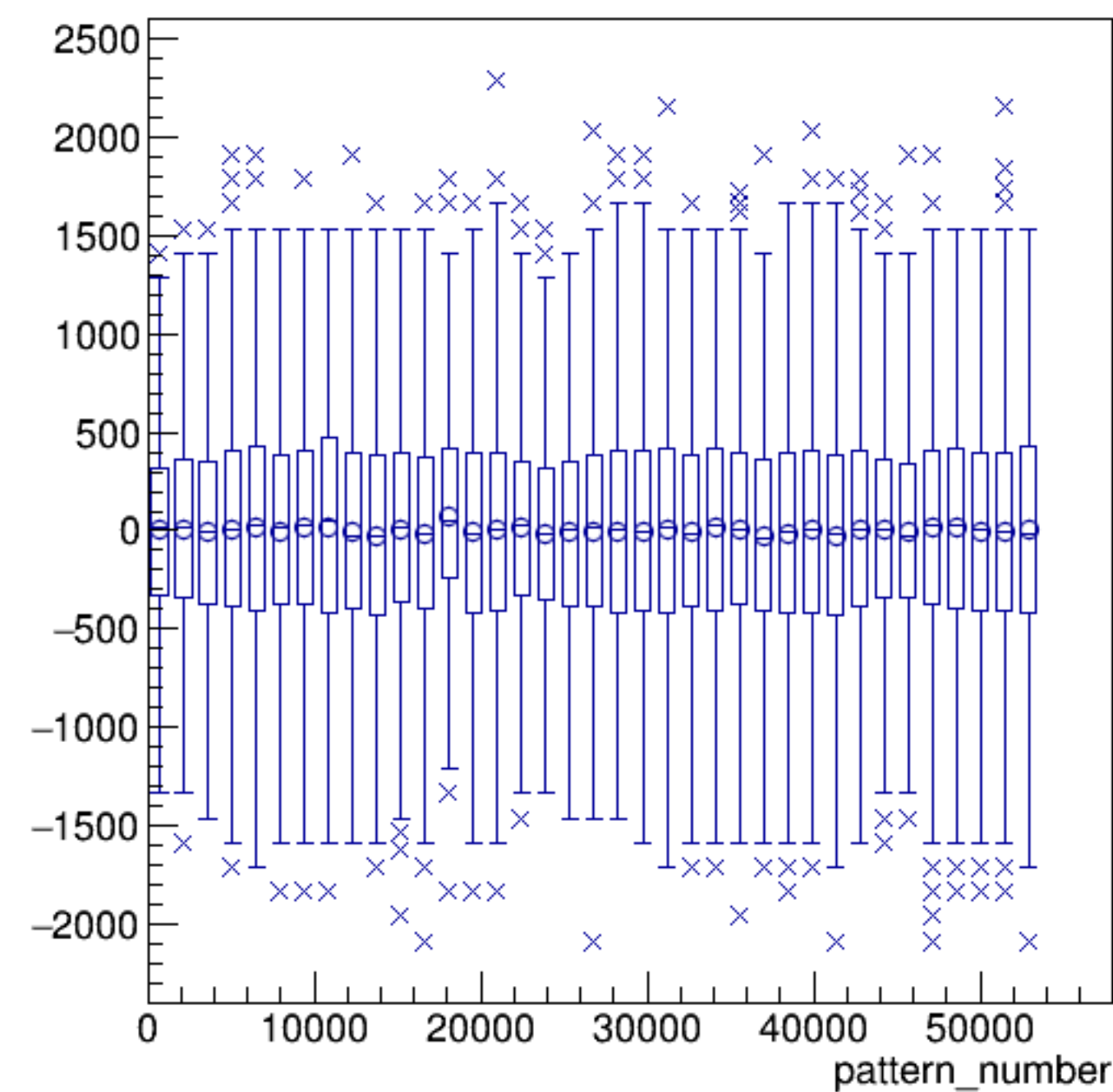


run4577_000_maindet_usl.png

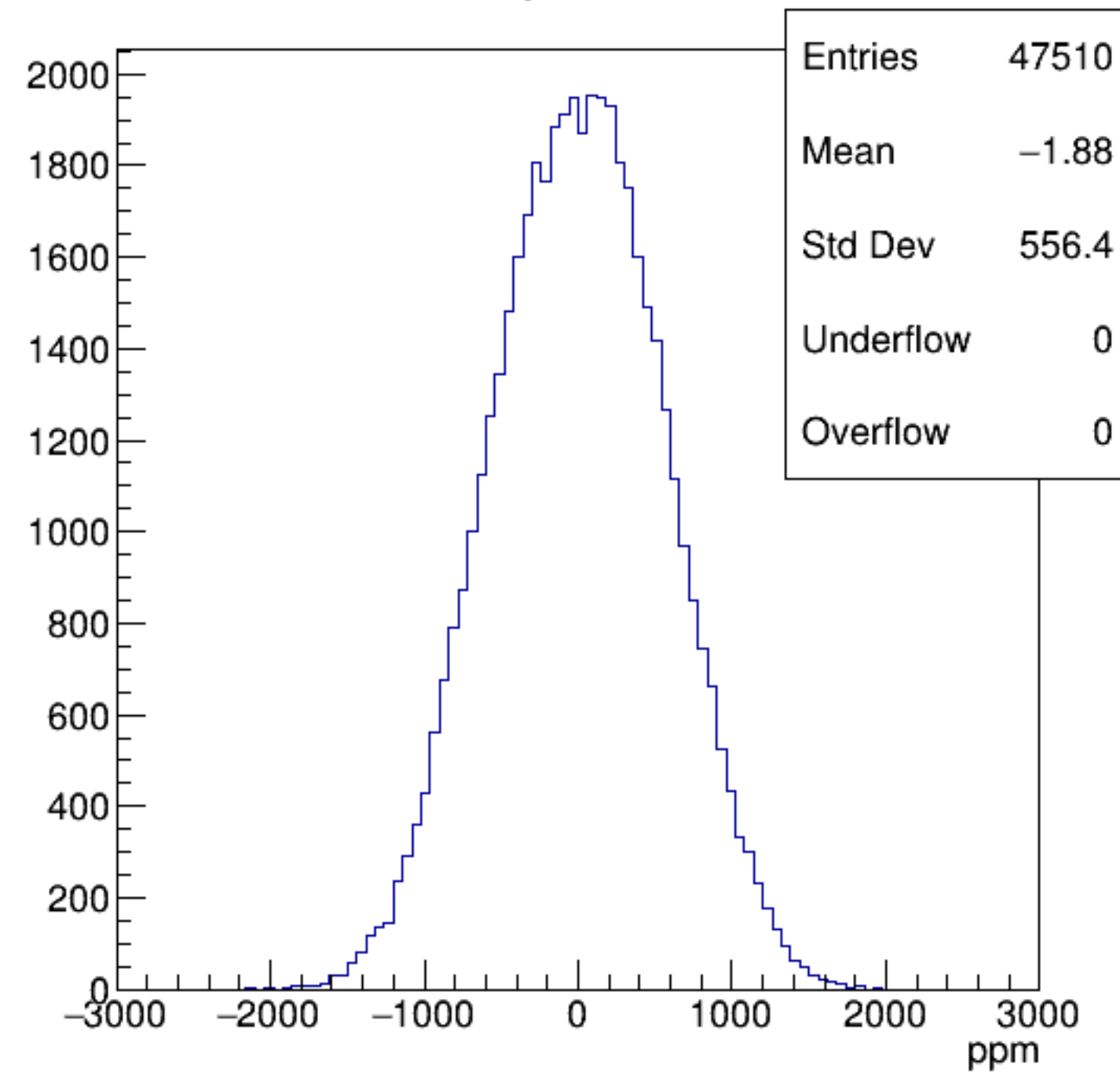
usl*CurMon {ErrorFlag==0}



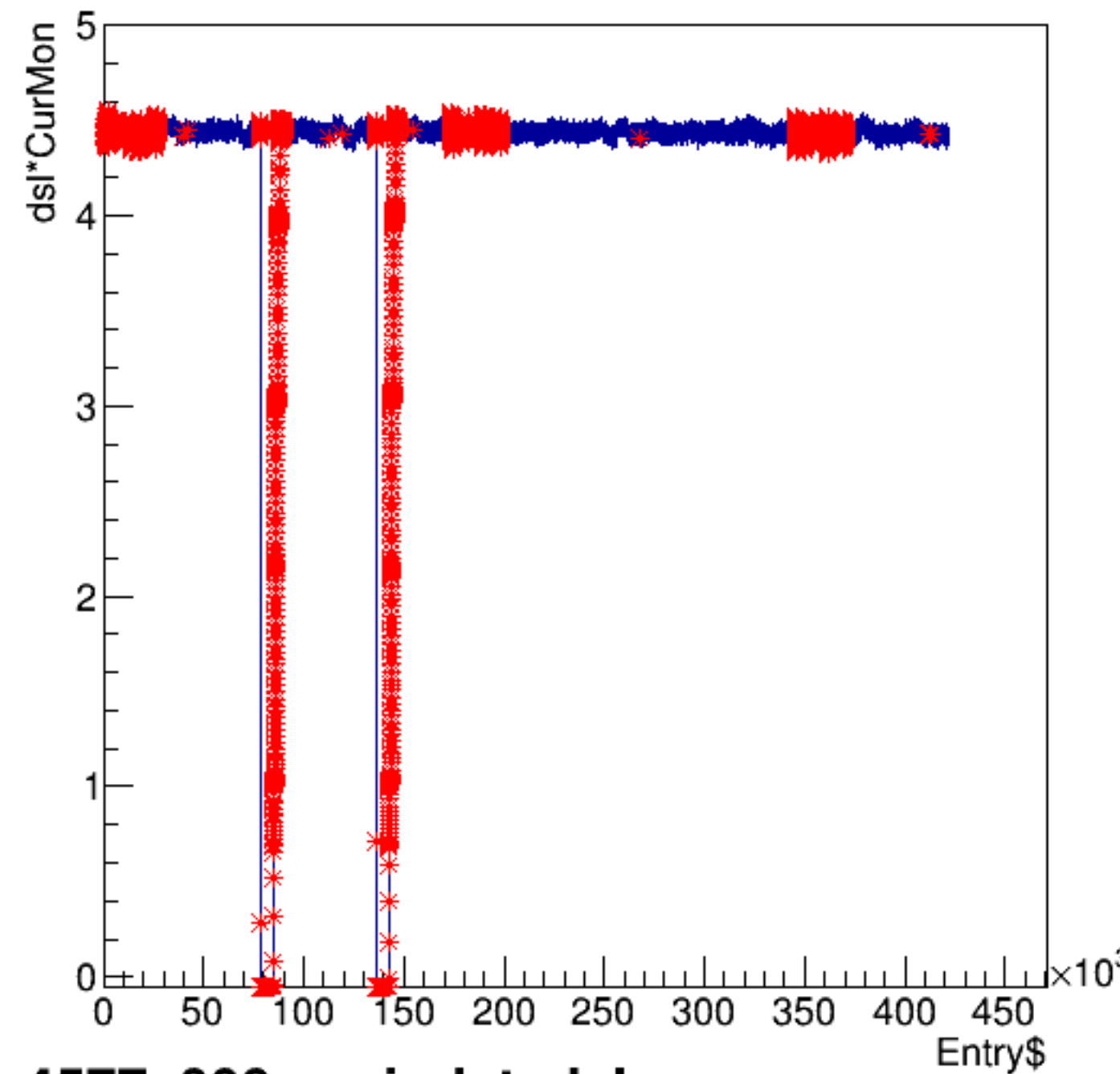
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl

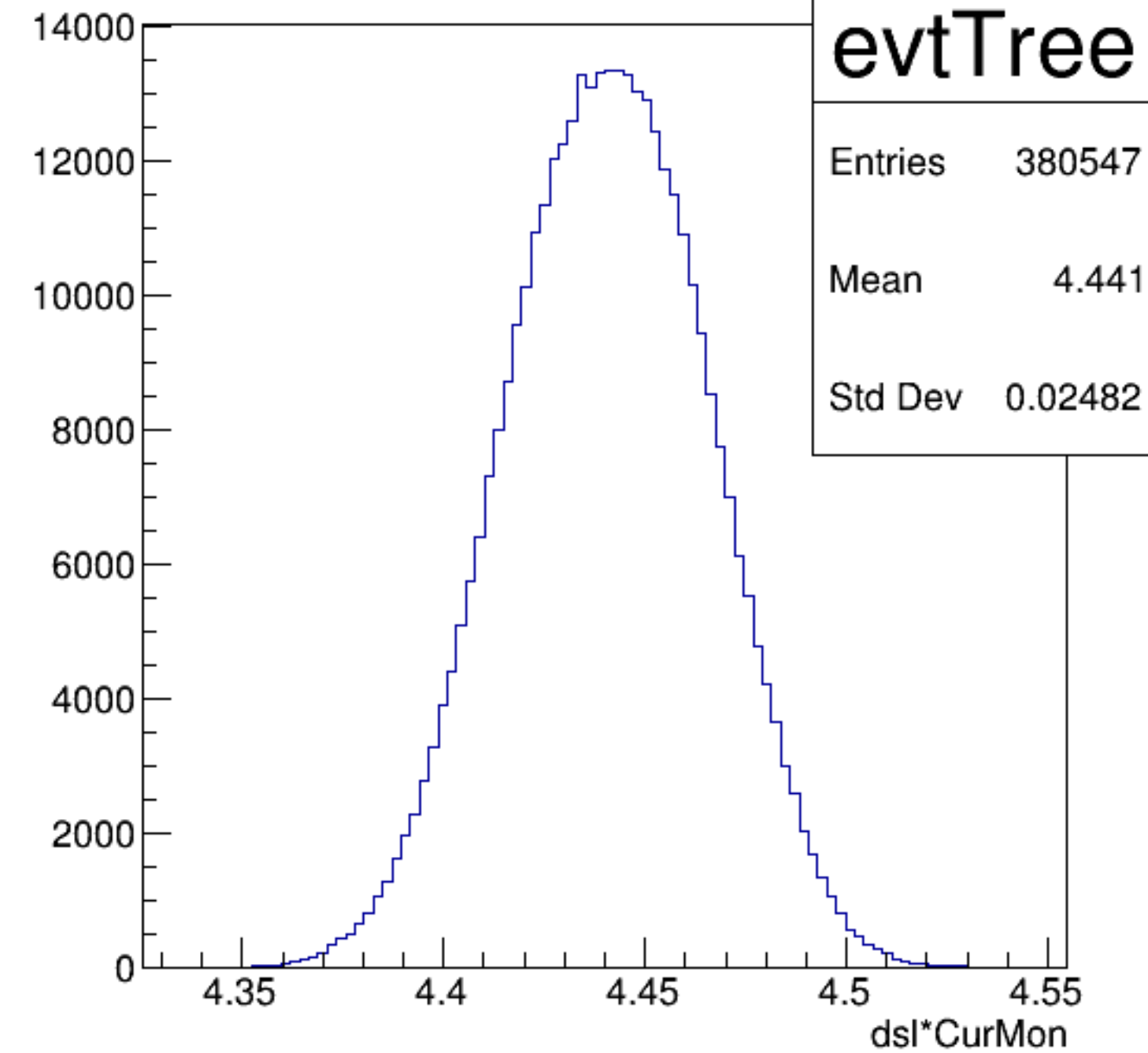


dsl*CurMon:Entry\$

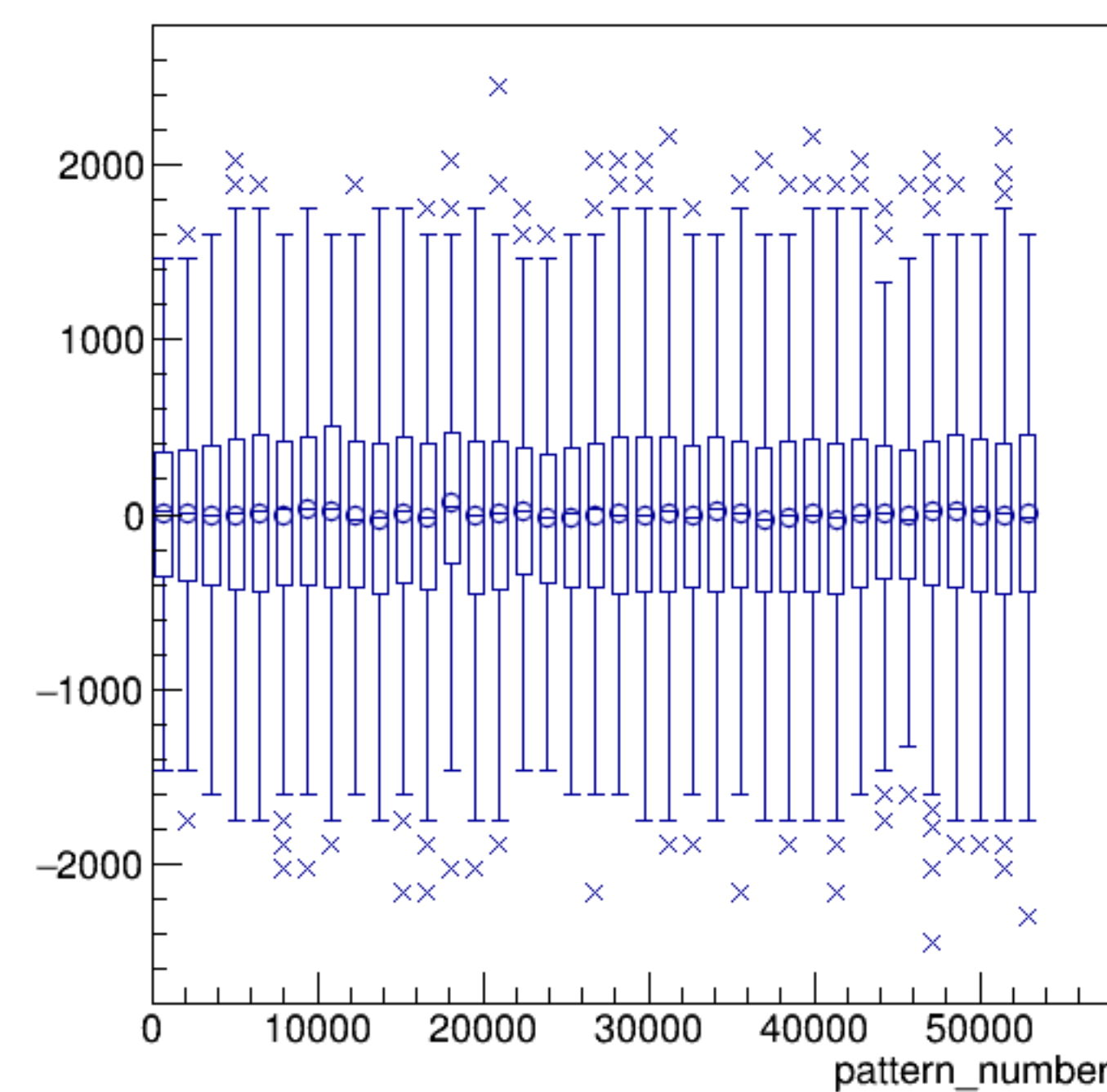


run4577_000_maindet_dsl.png

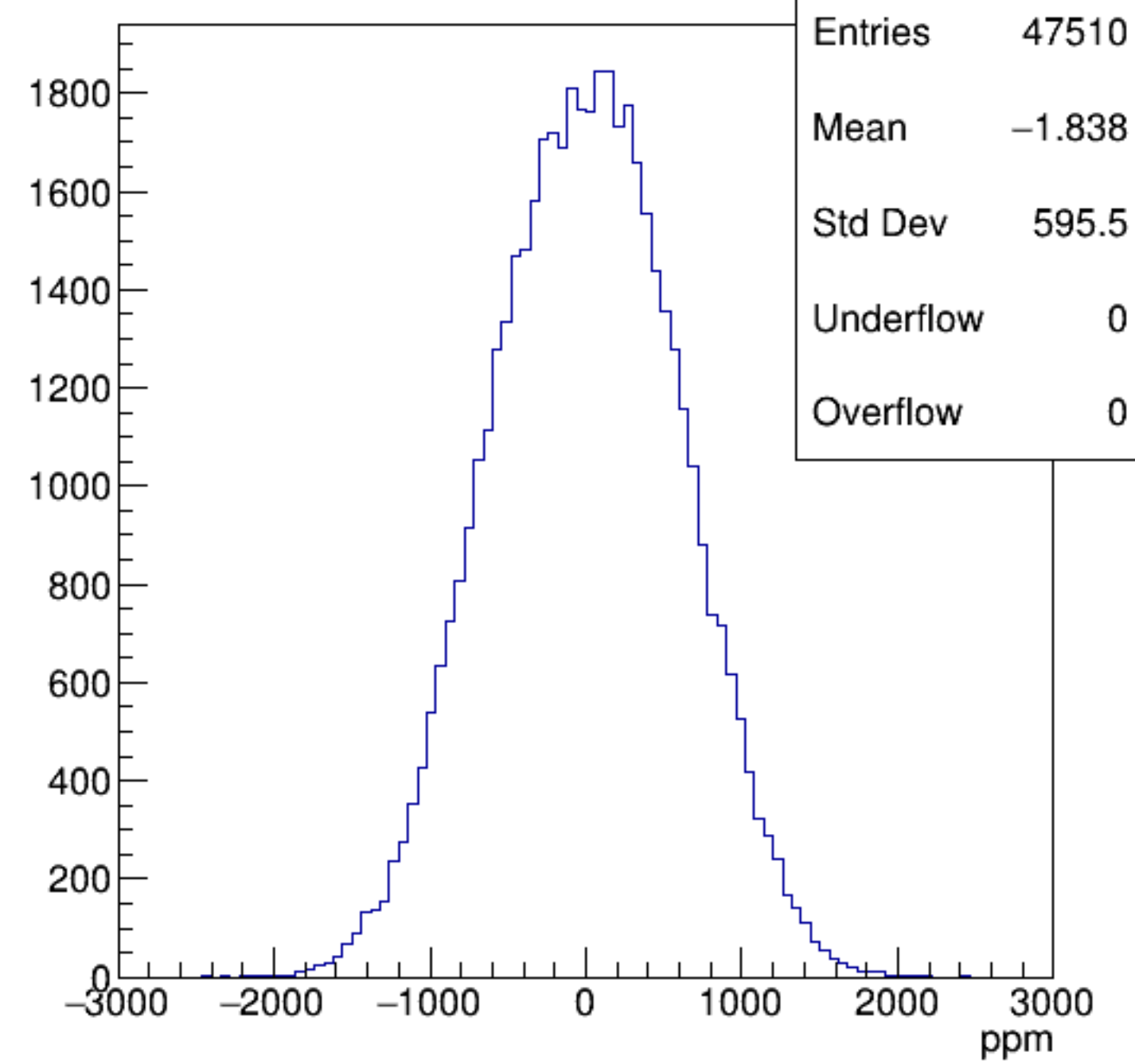
dsl*CurMon {ErrorFlag==0}



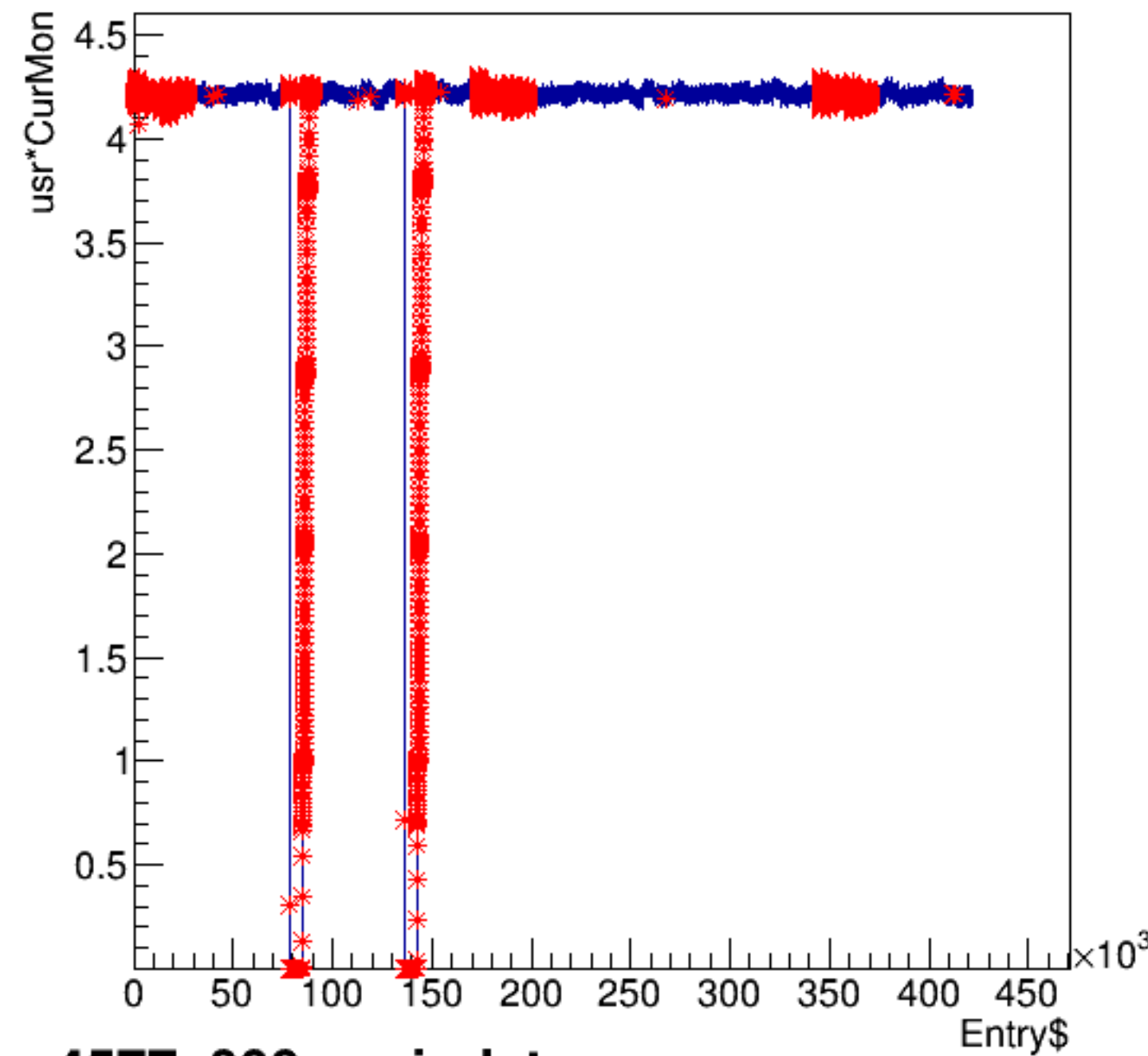
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl

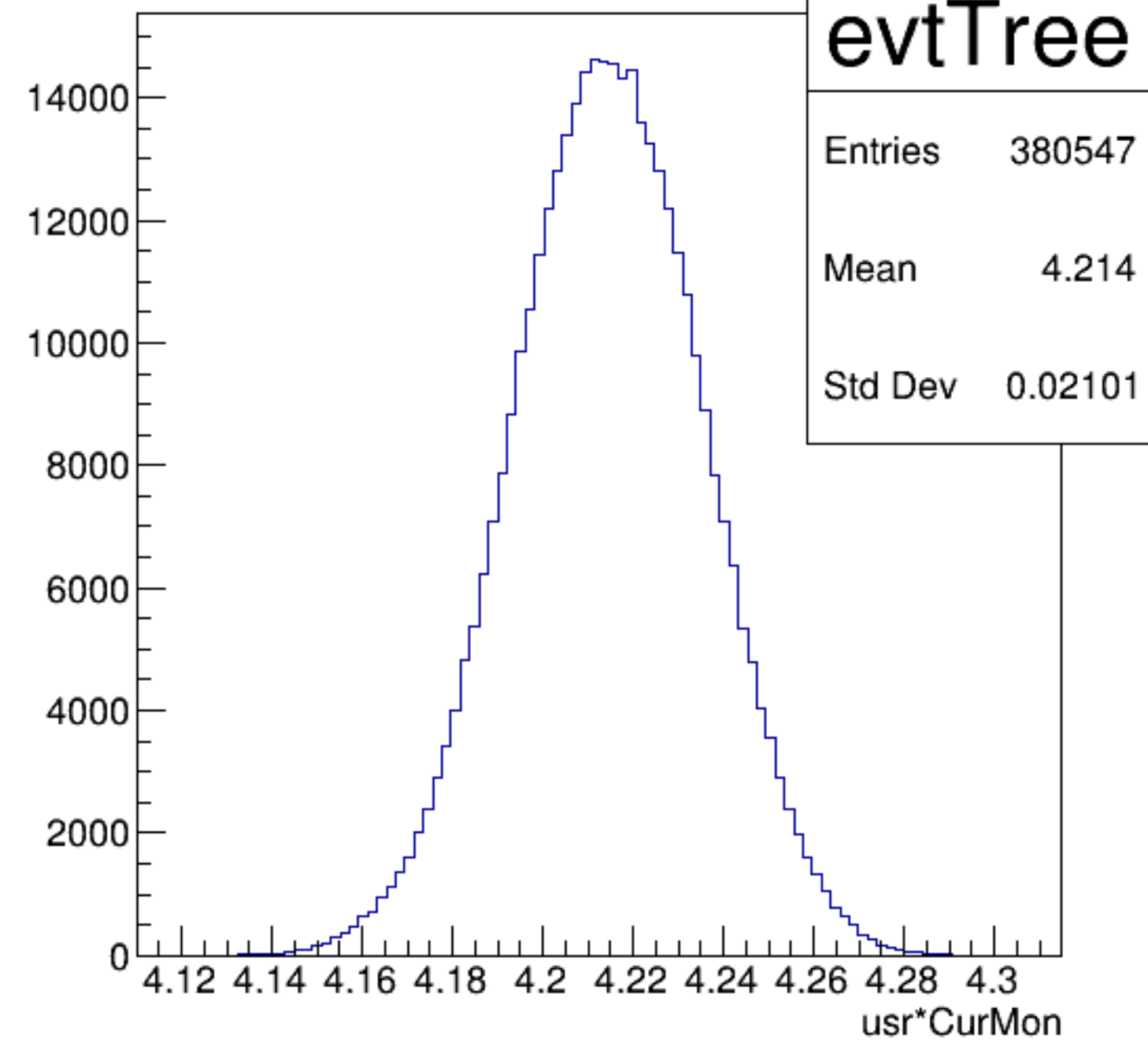


usr*CurMon:Entry\$



run4577_000_maindet_usr.png

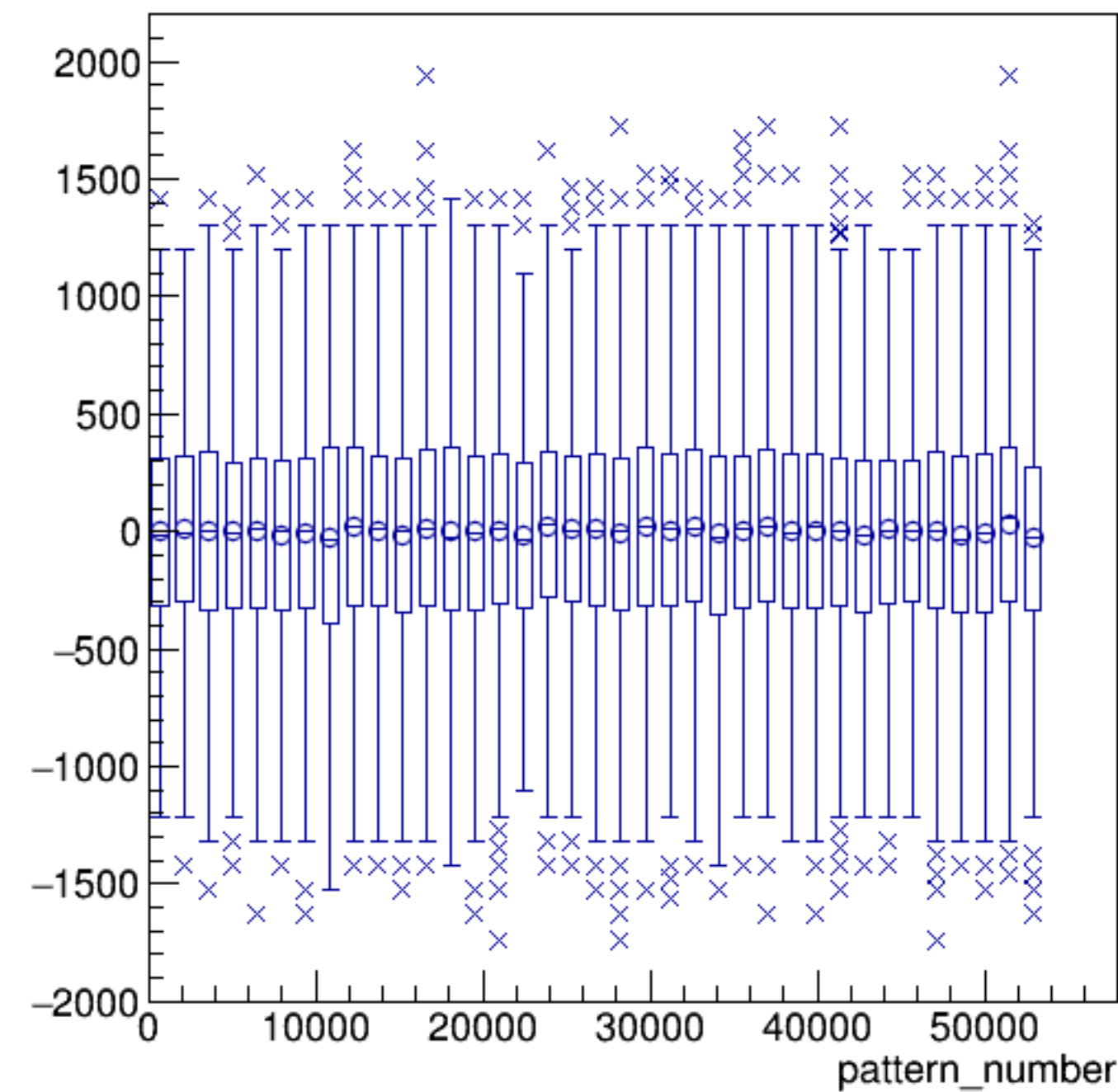
usr*CurMon {ErrorFlag==0}



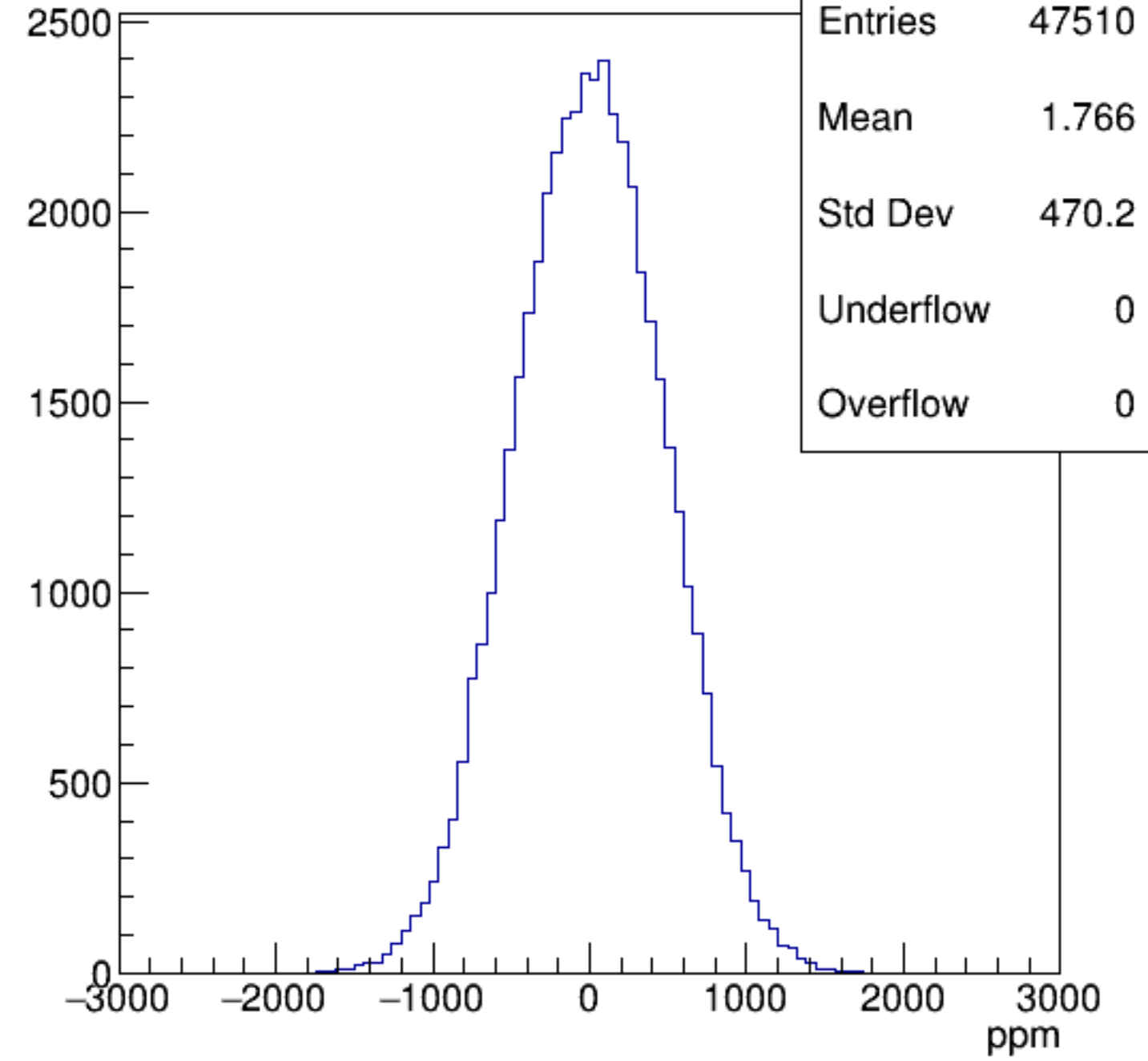
evtTree

Entries	380547
Mean	4.214
Std Dev	0.02101

asym_usr/ppm:pattern_number {ErrorFlag==0}

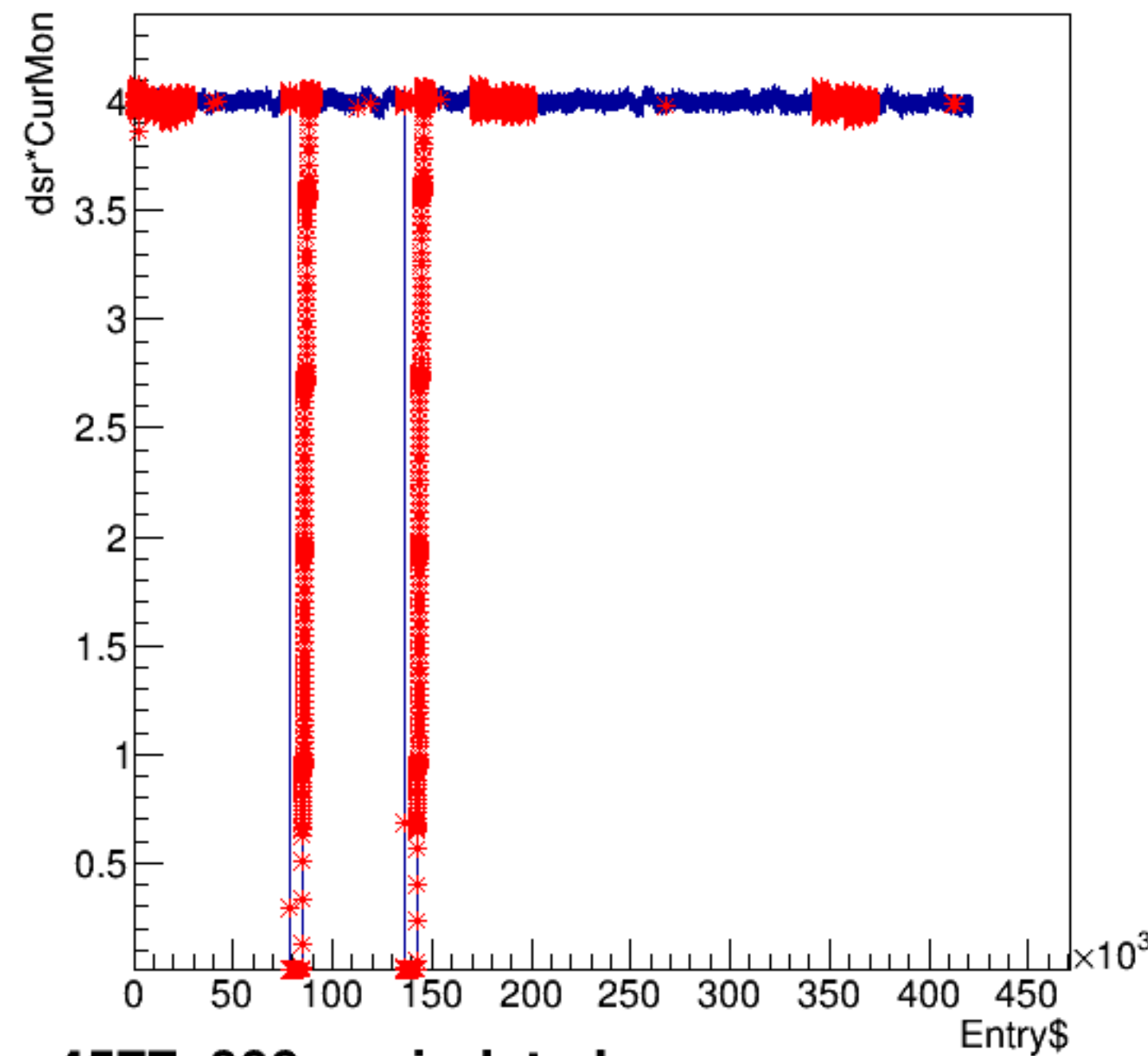


asym_usr



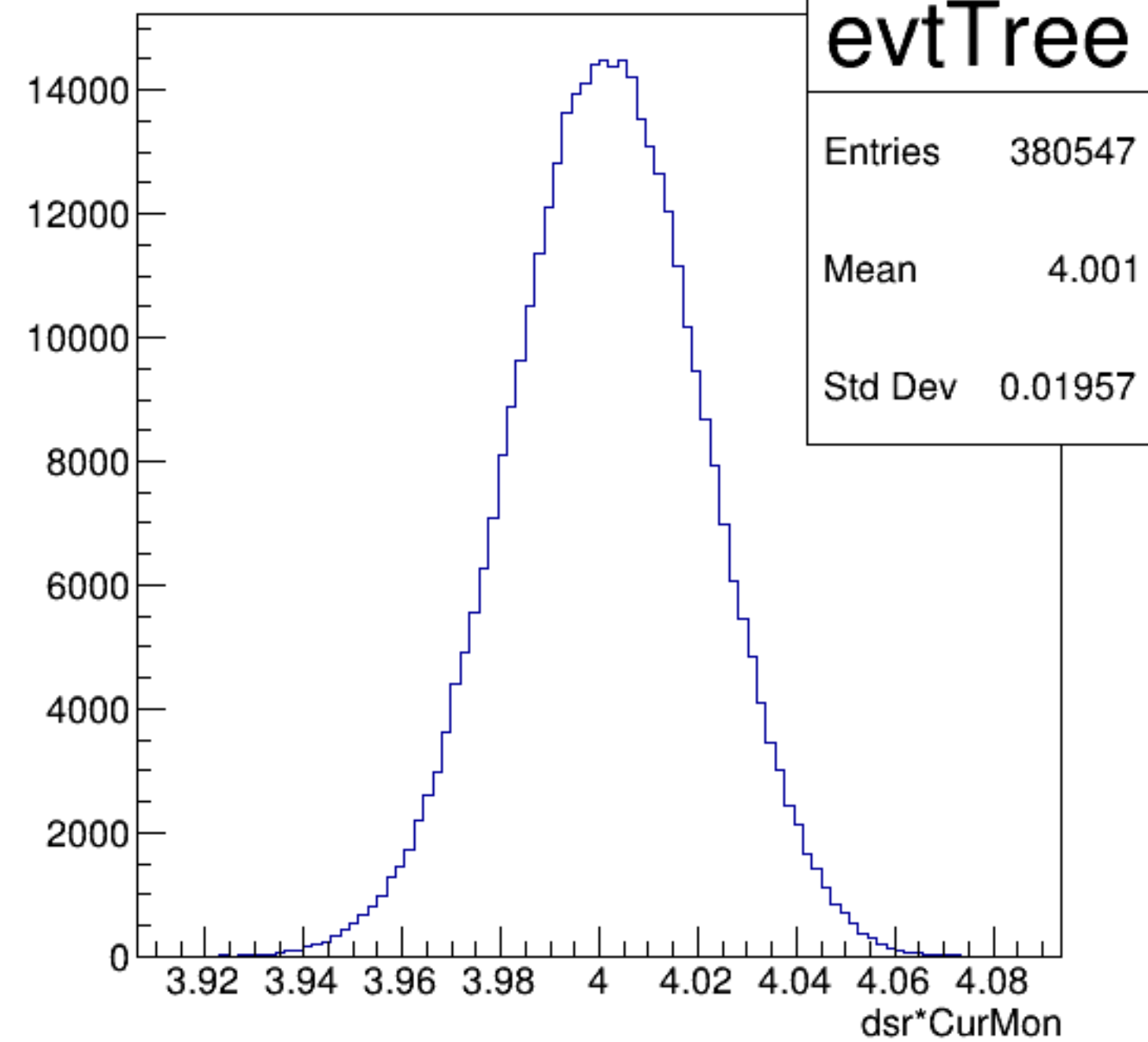
Entries	47510
Mean	1.766
Std Dev	470.2
Underflow	0
Overflow	0

dsr*CurMon:Entry\$

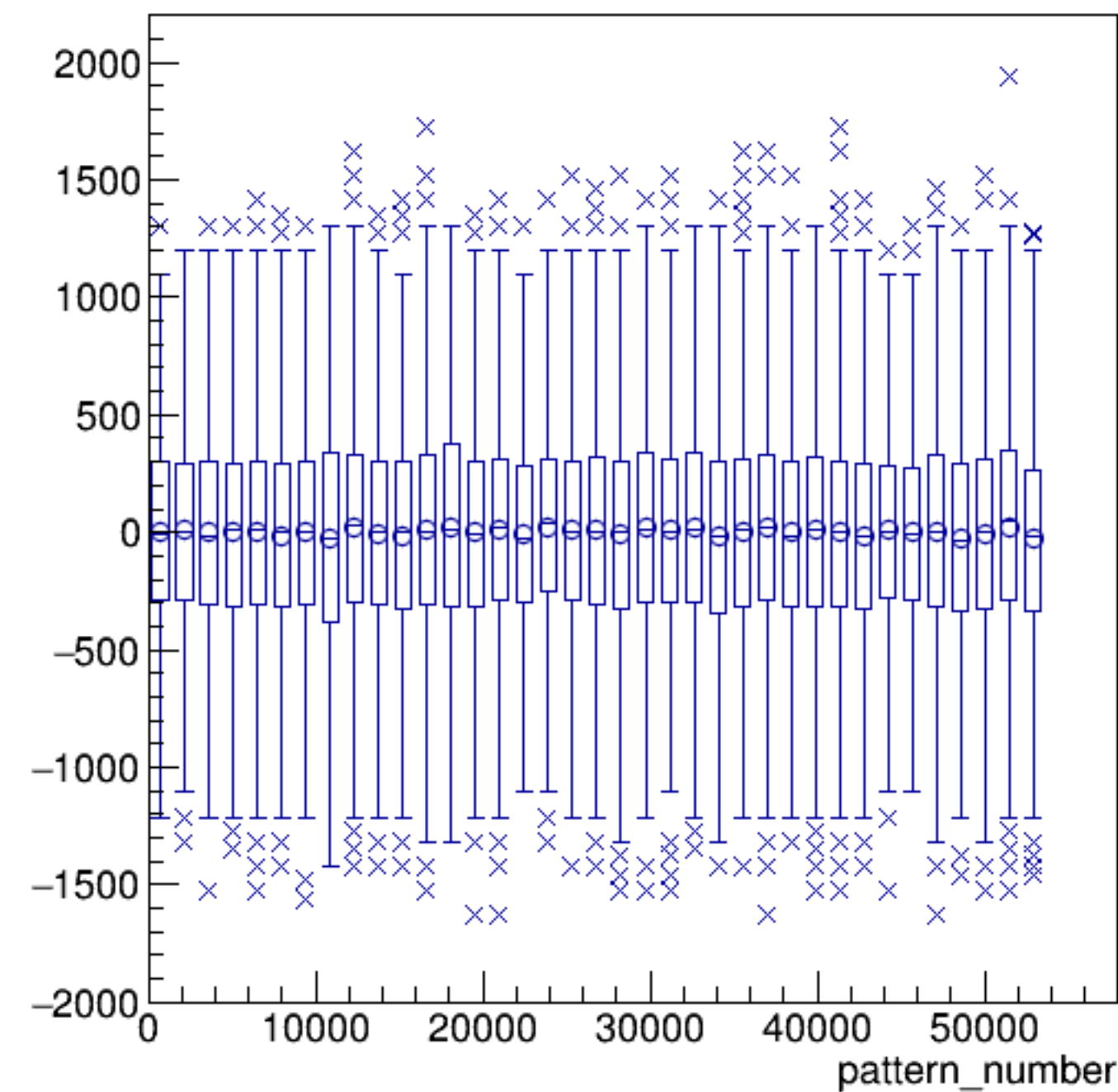


run4577_000_maindet_dsr.png

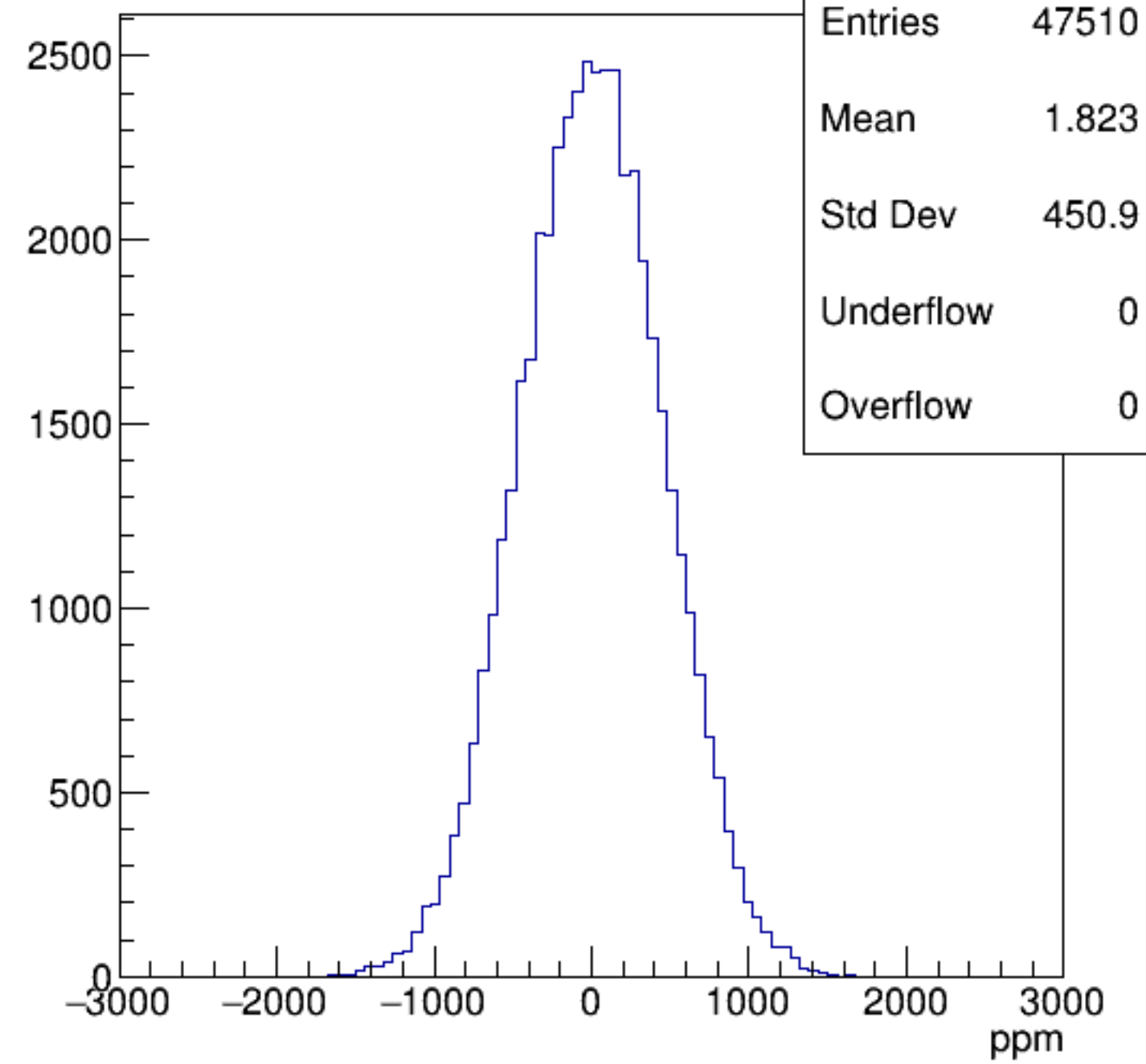
dsr*CurMon {ErrorFlag==0}



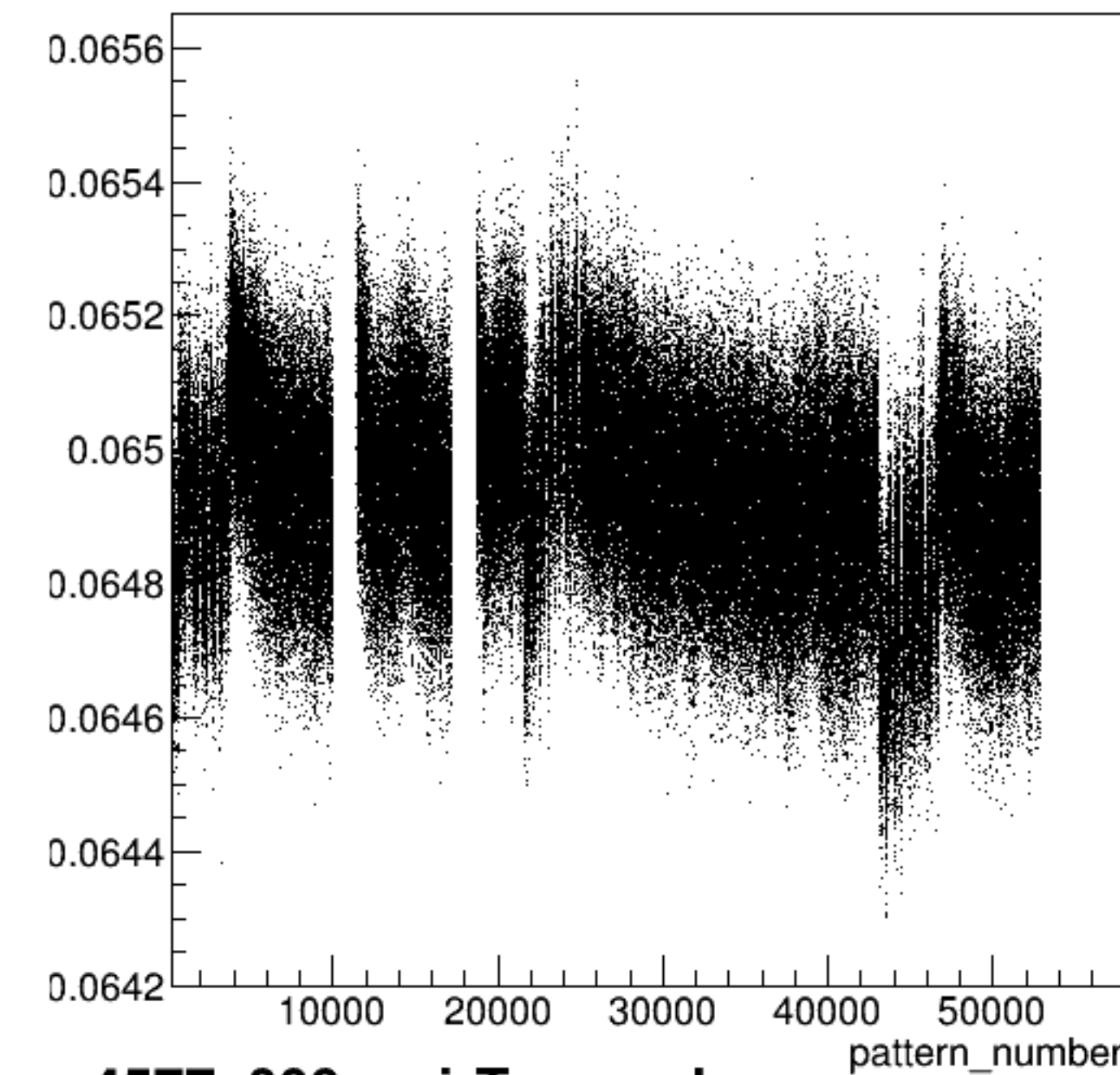
asym_dsr/ppm:pattern_number {ErrorFlag==0}



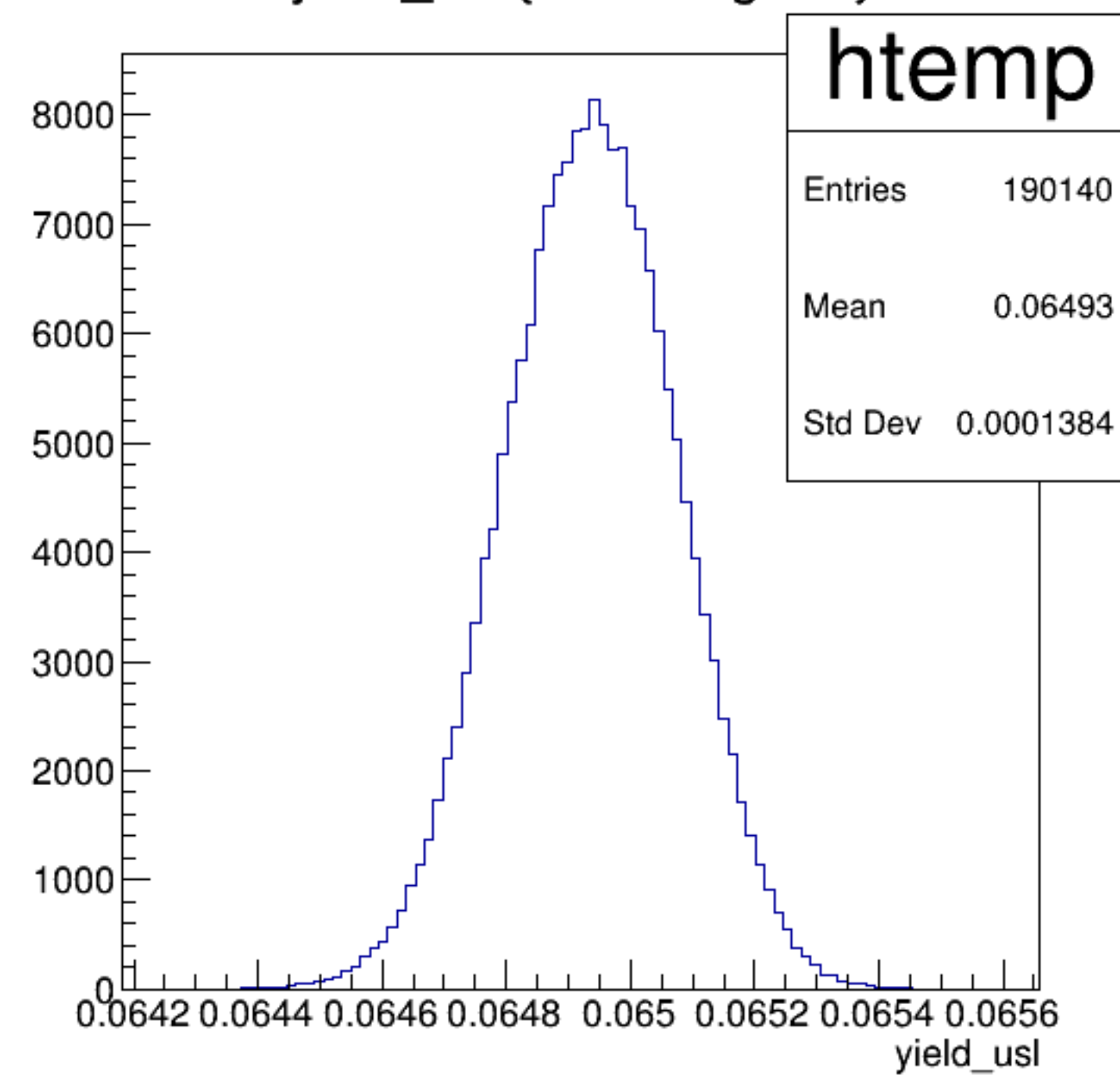
asym_dsr



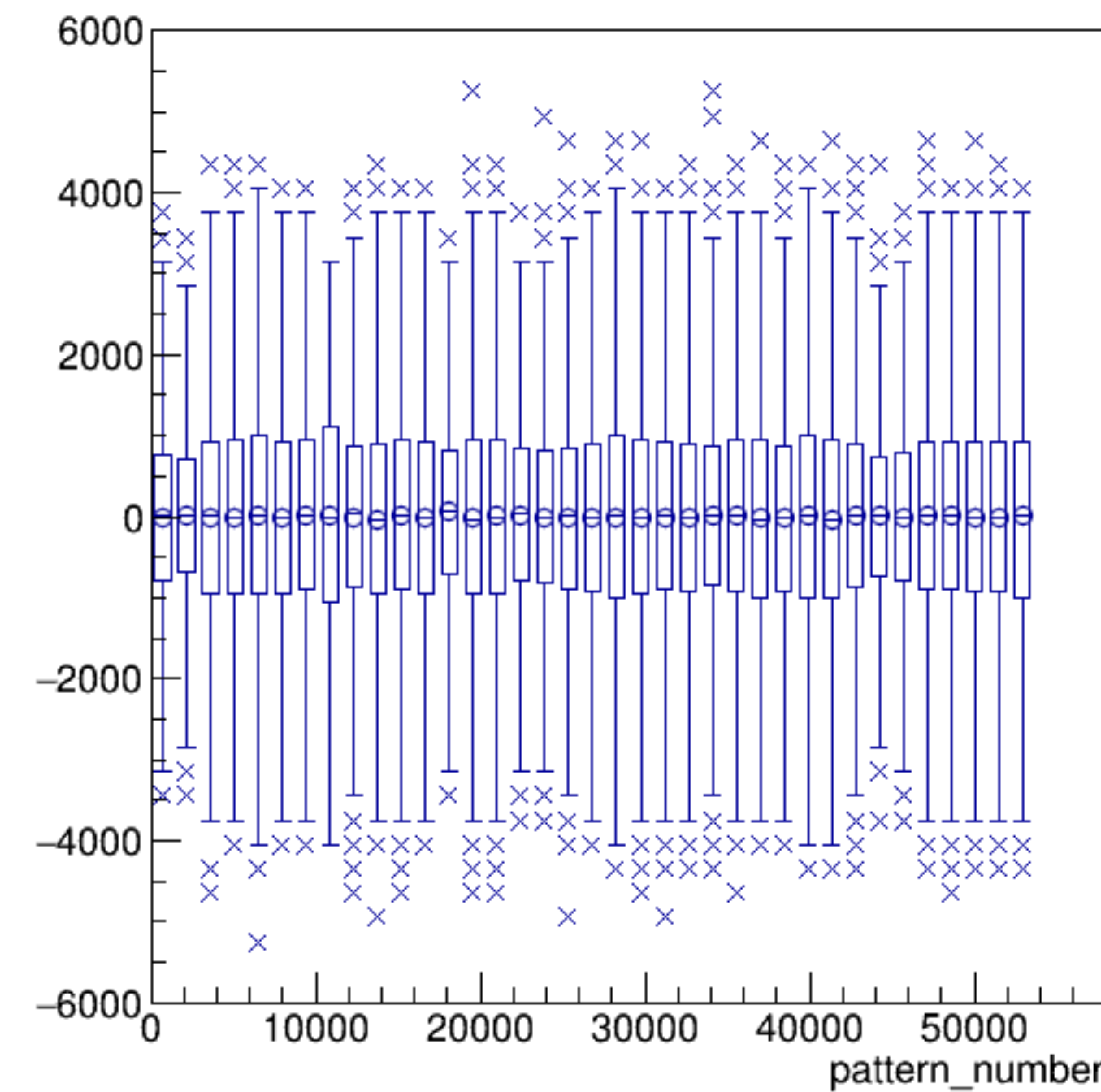
yield_usl:pattern_number {ErrorFlag==0}



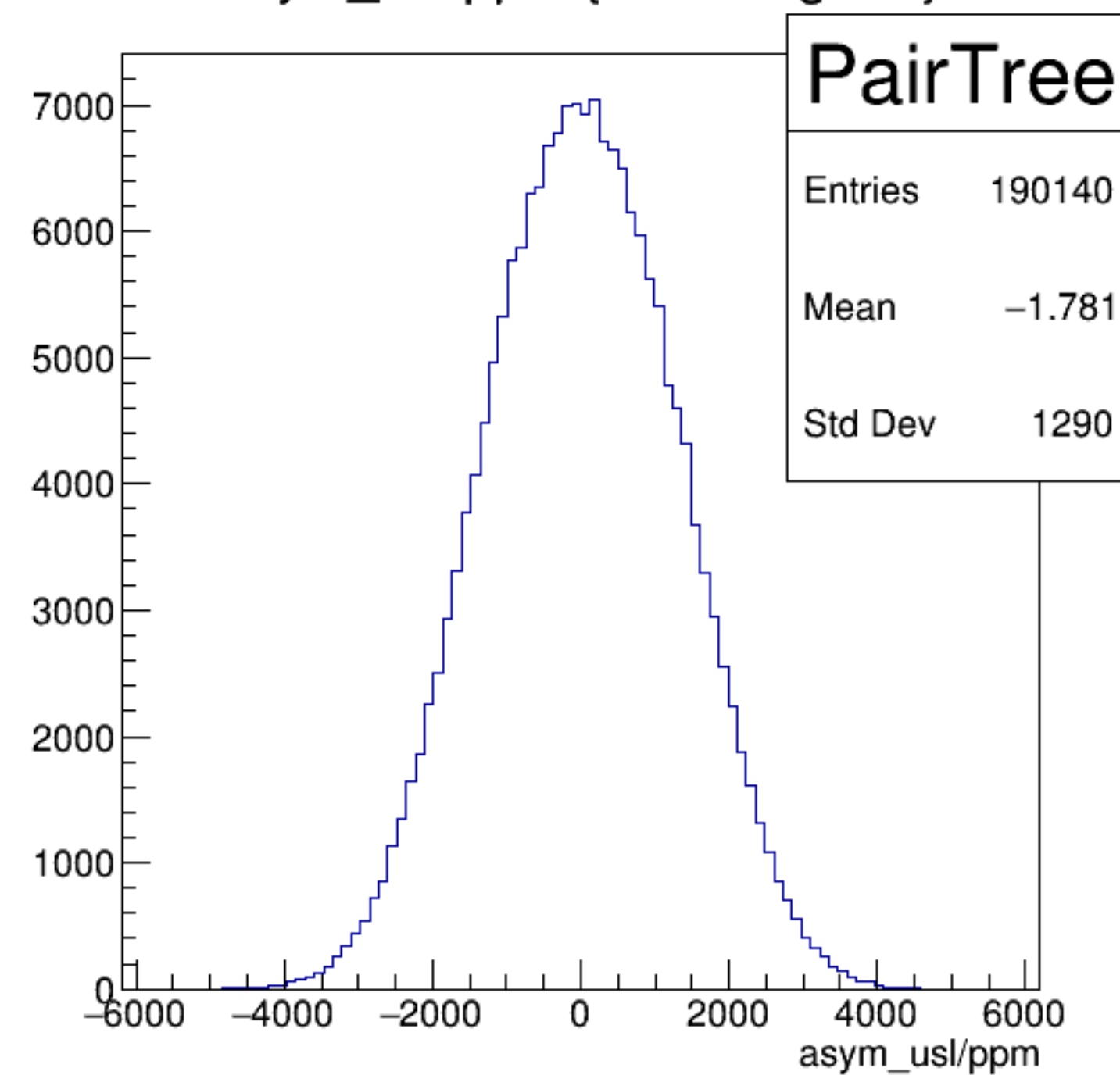
yield_usl {ErrorFlag==0}



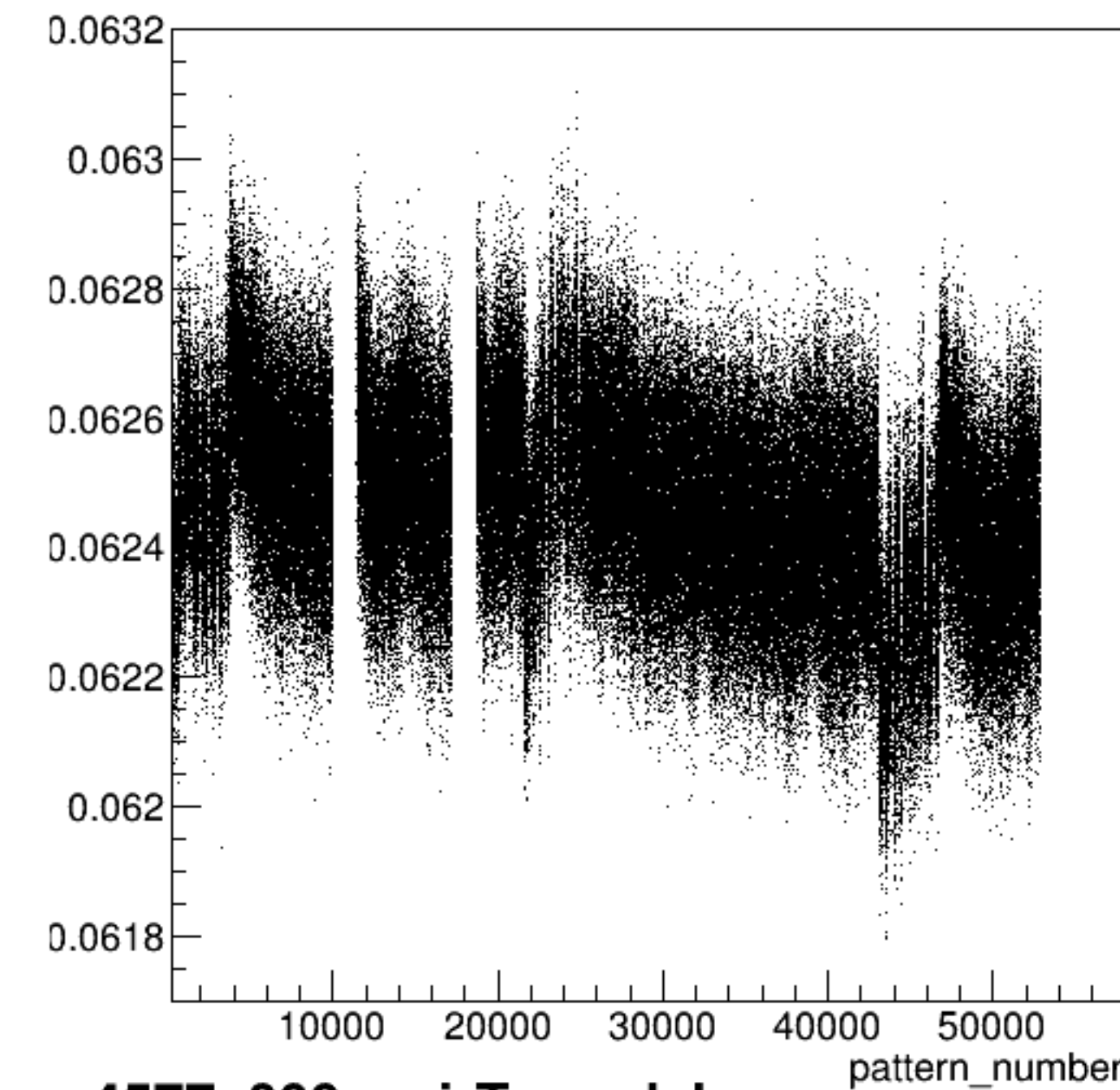
asym_usl/ppm:pattern_number {ErrorFlag==0}



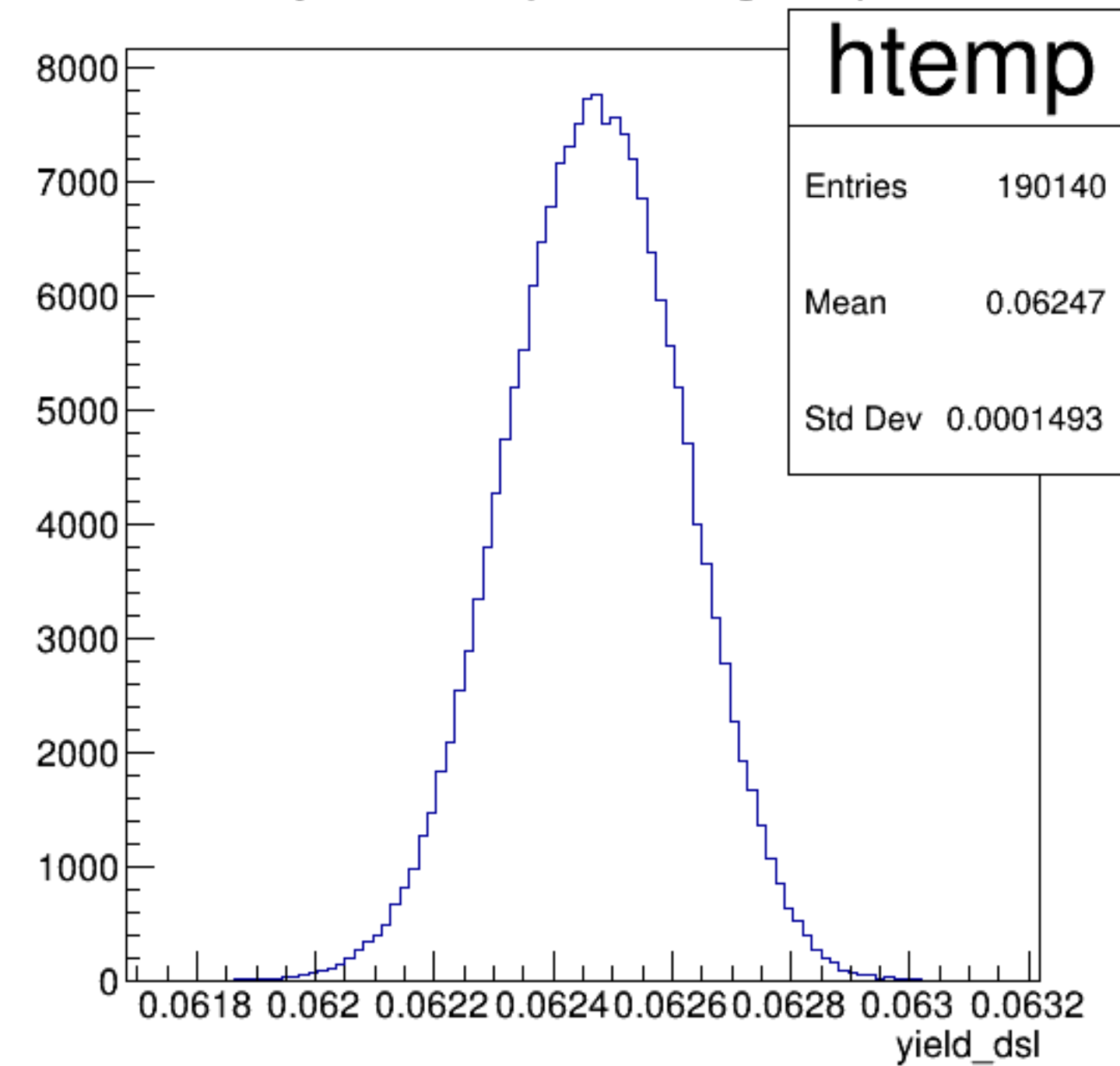
asym_usl/ppm {ErrorFlag==0}



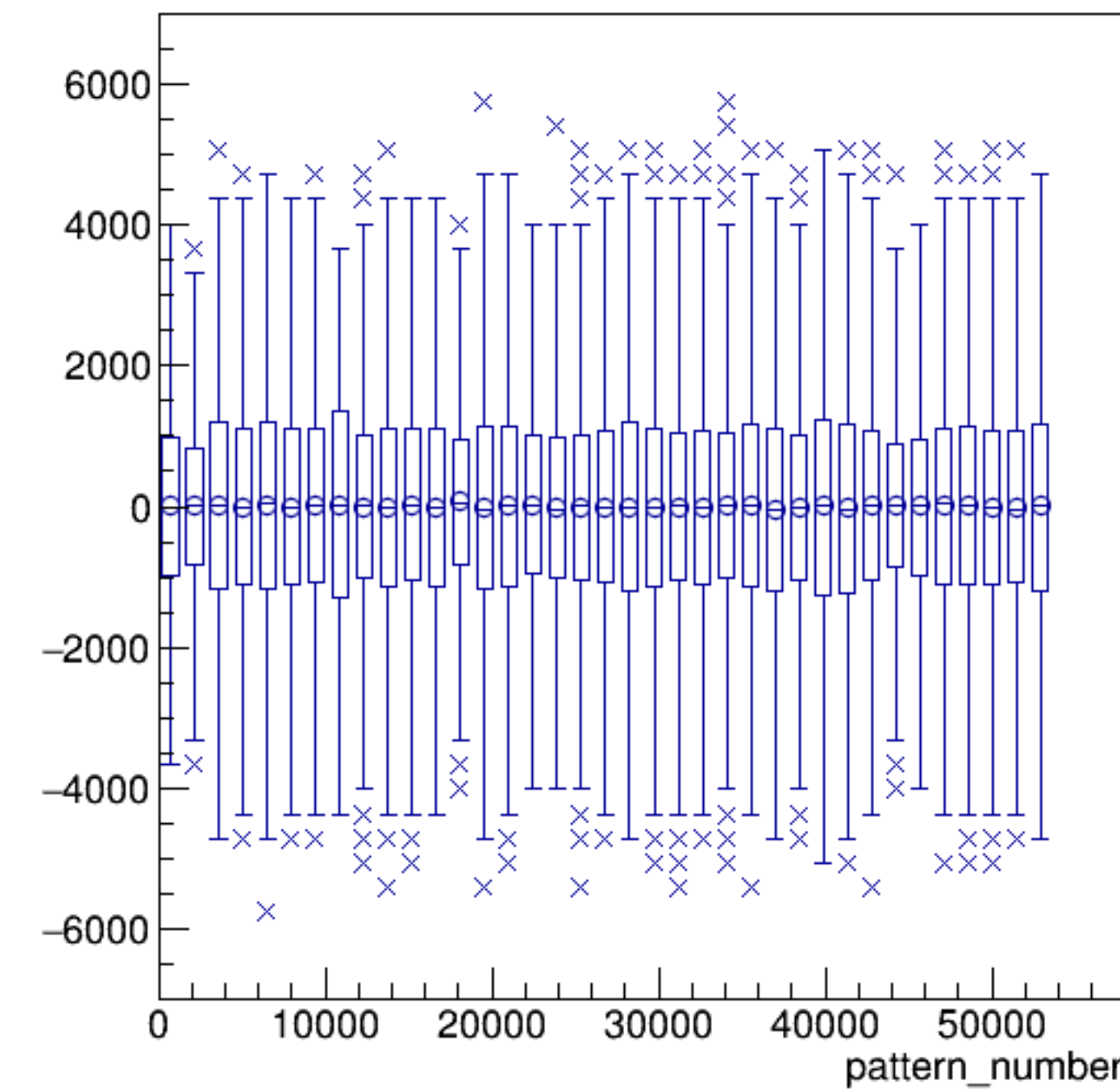
yield_dsl:pattern_number {ErrorFlag==0}



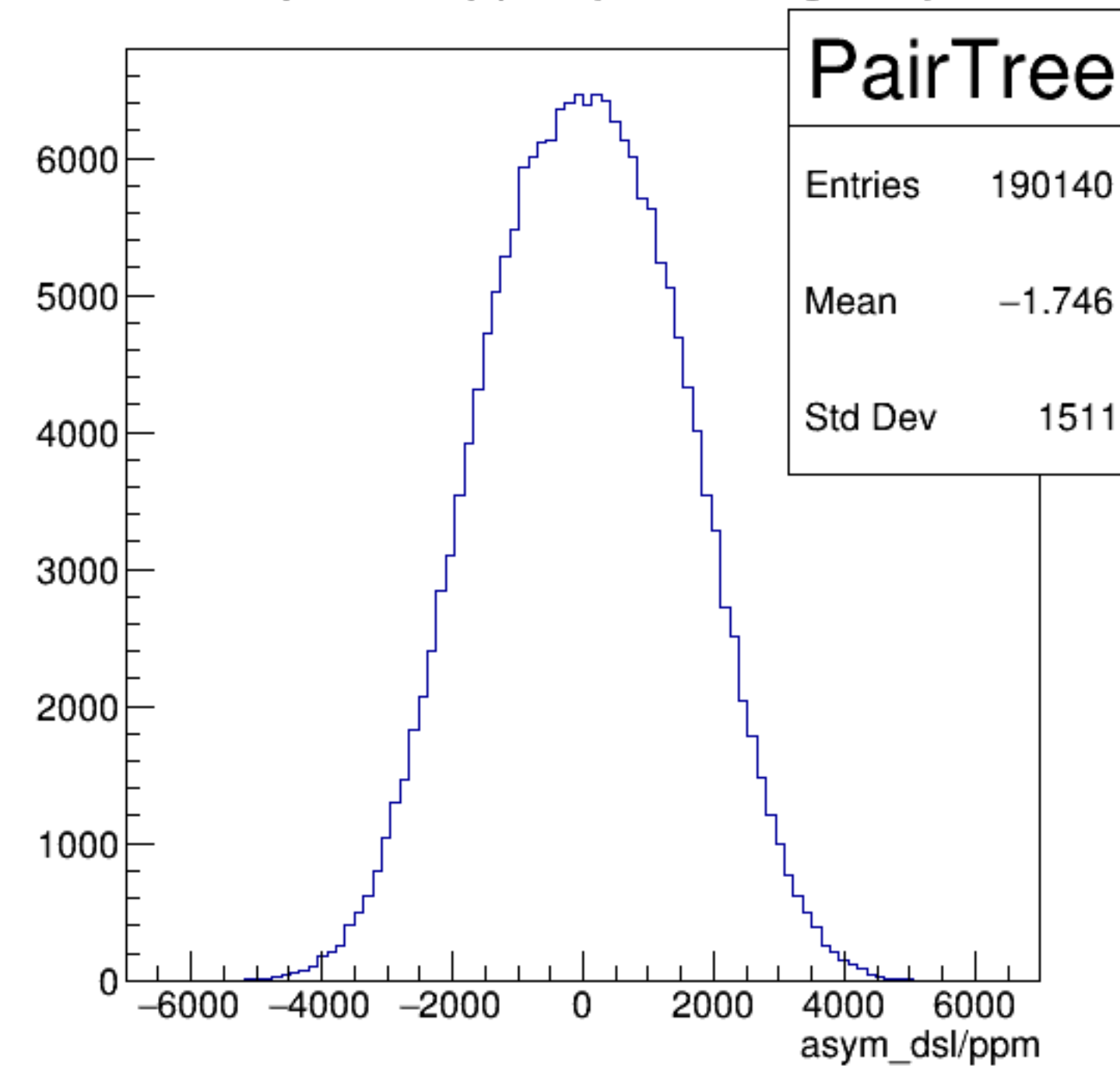
yield_dsl {ErrorFlag==0}



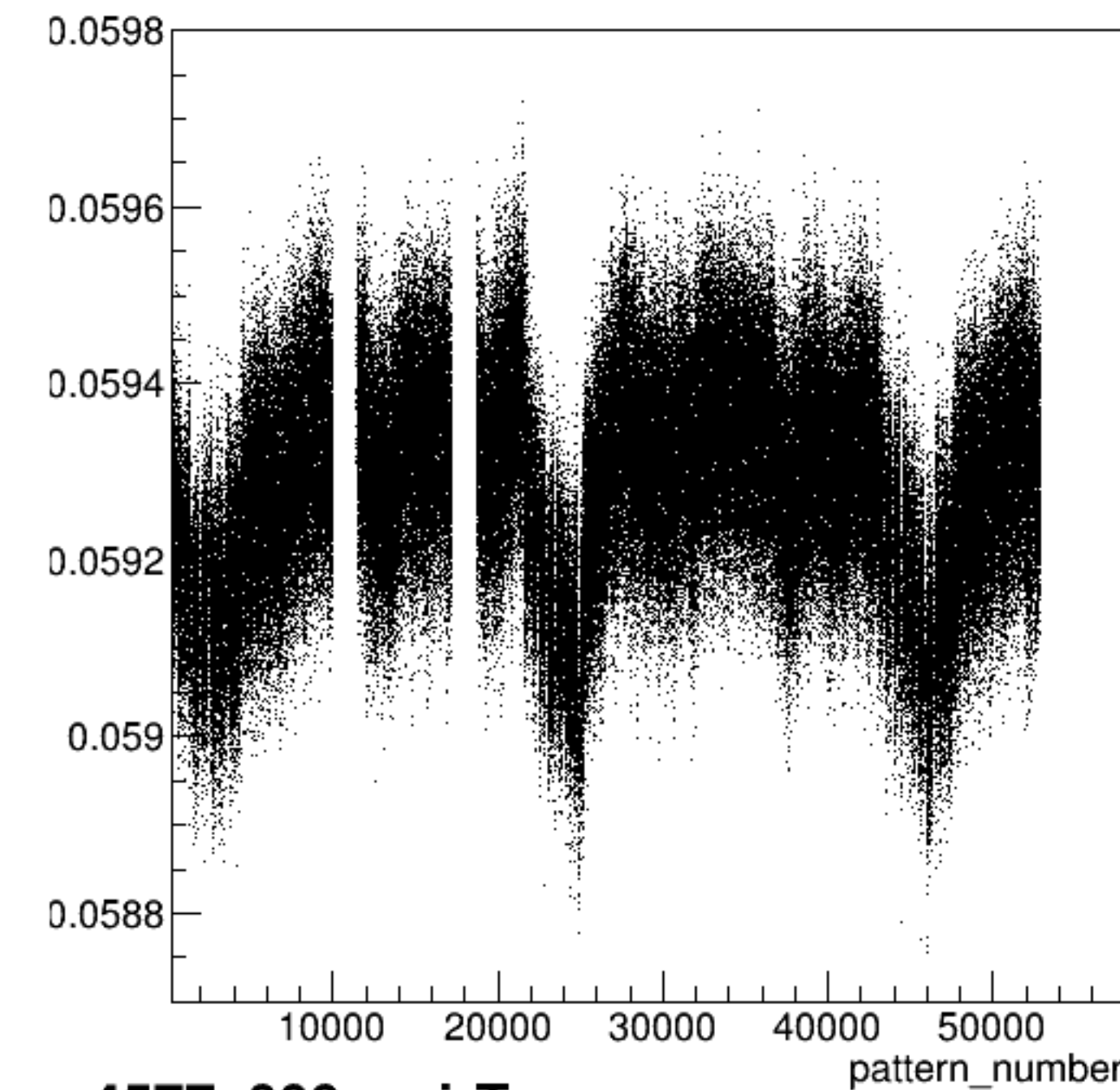
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl/ppm {ErrorFlag==0}

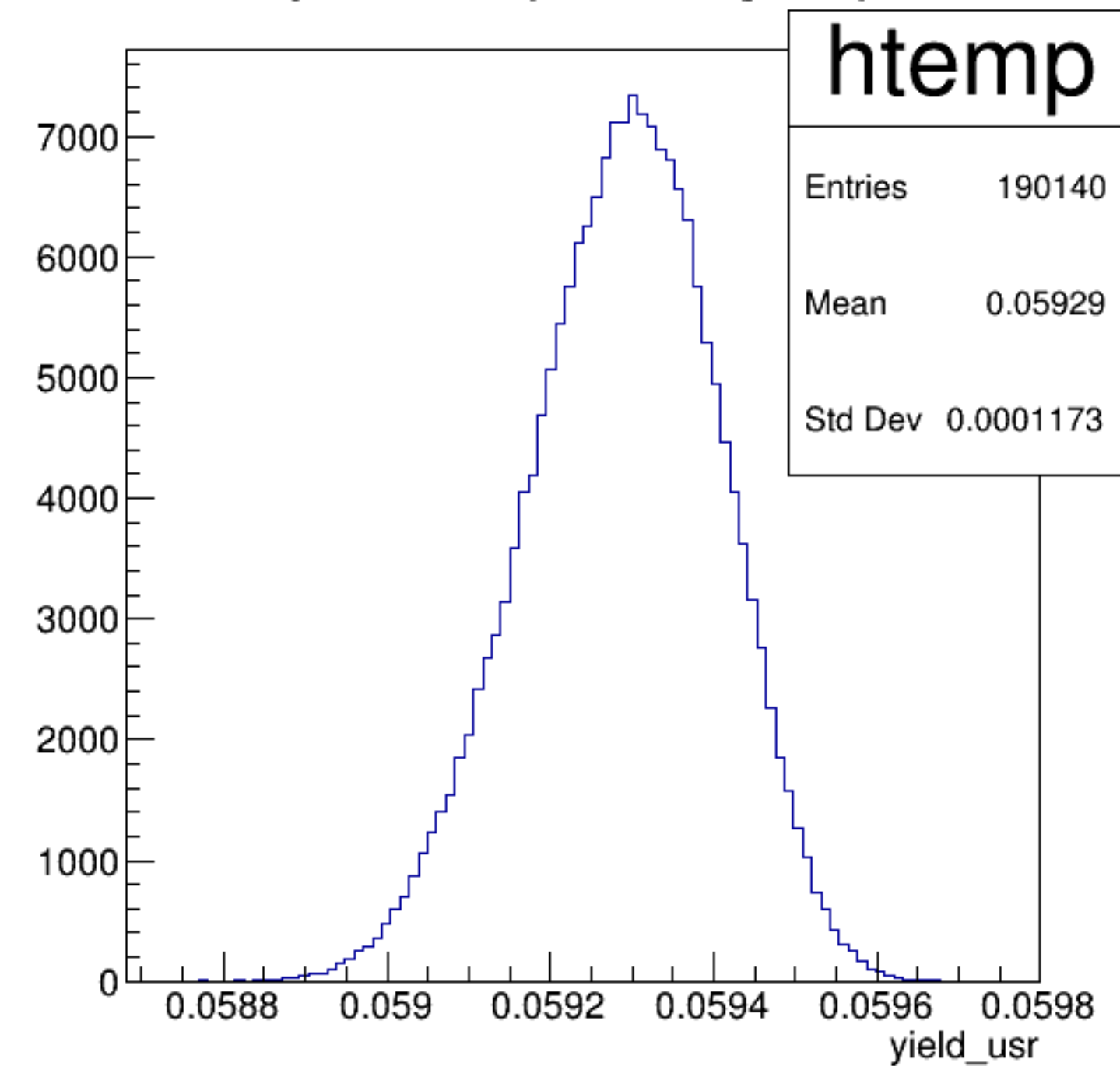


yield_usr:pattern_number {ErrorFlag==0}

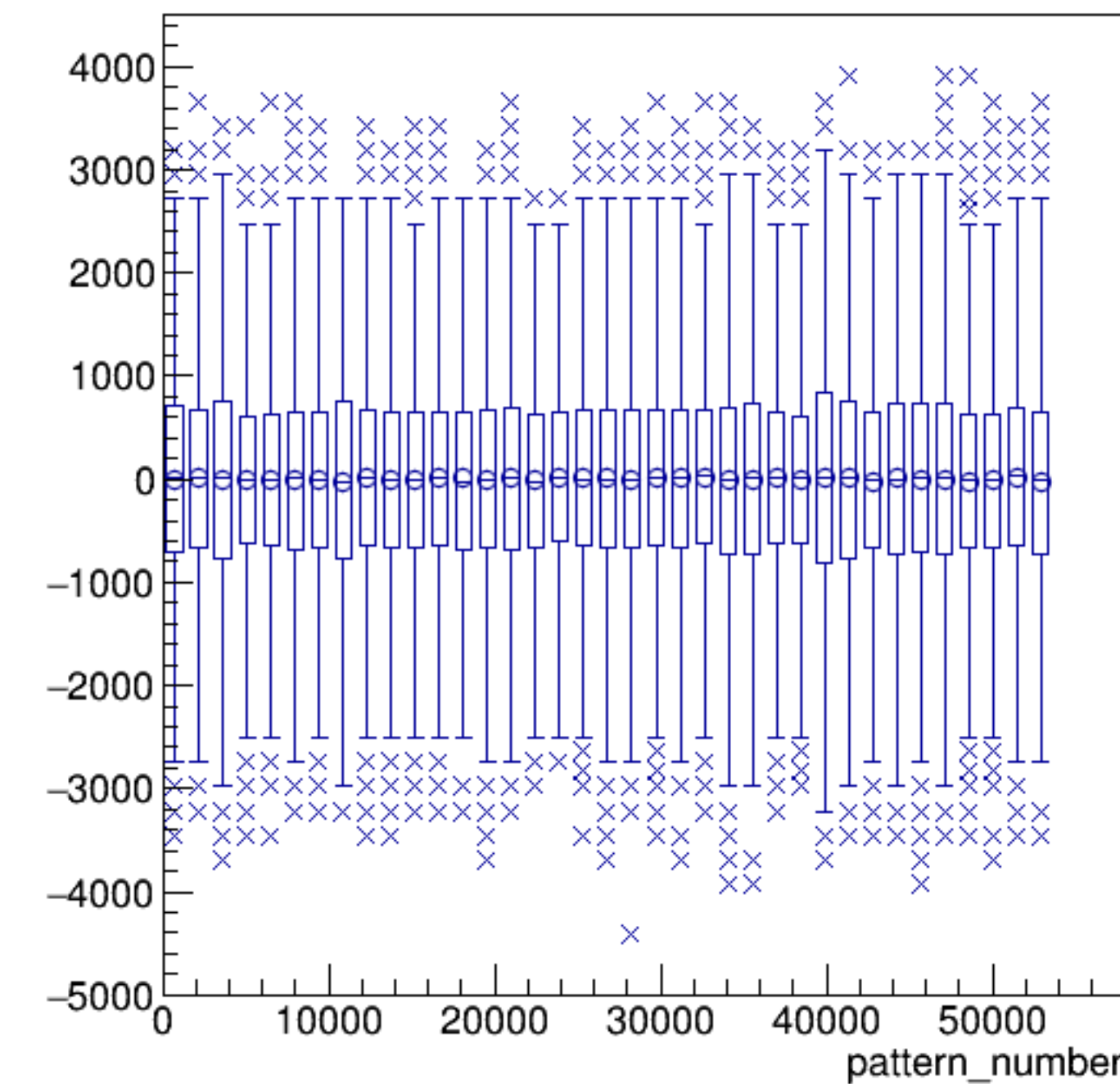


run4577_000_pairTree_usr.png

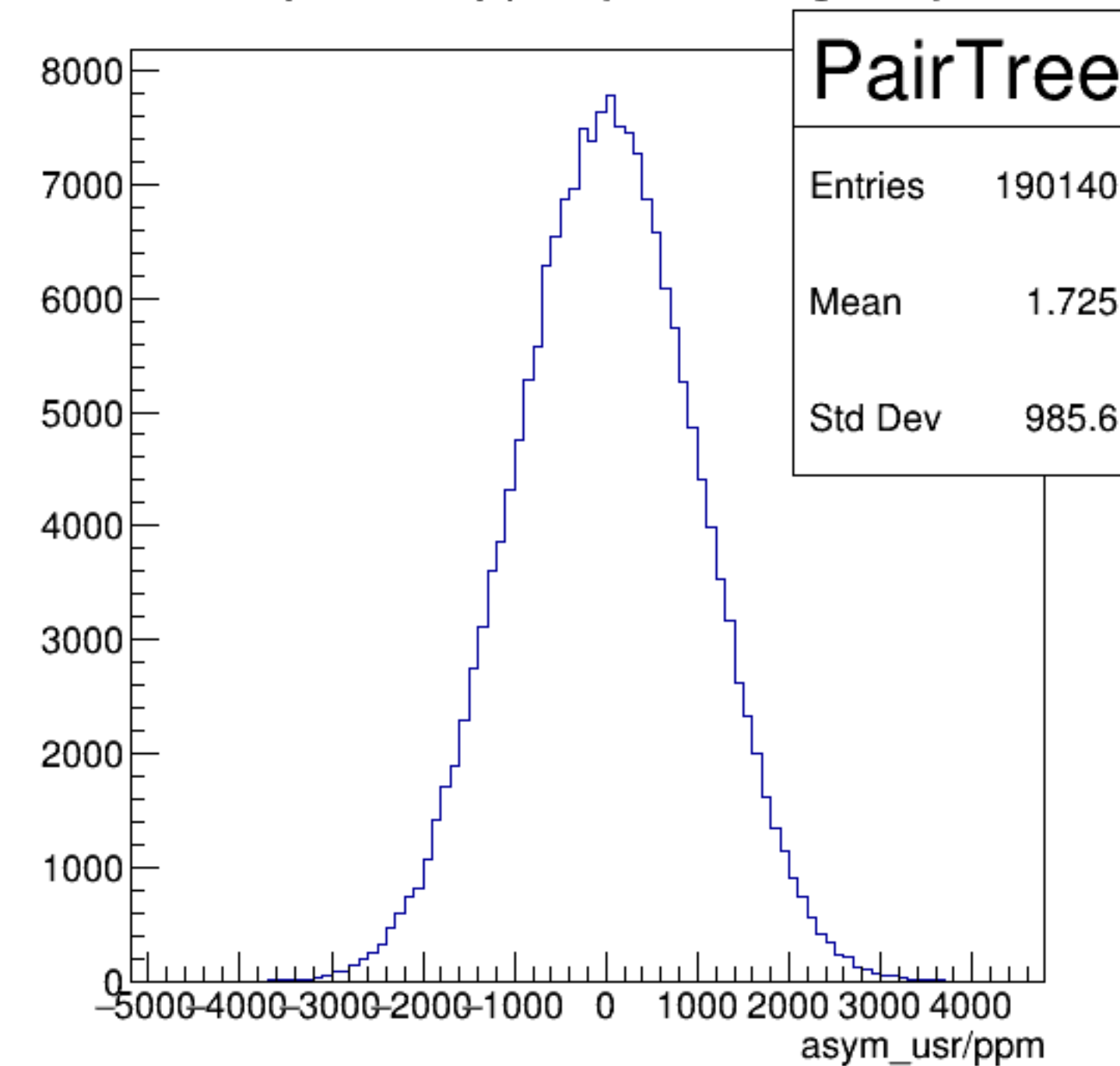
yield_usr {ErrorFlag==0}



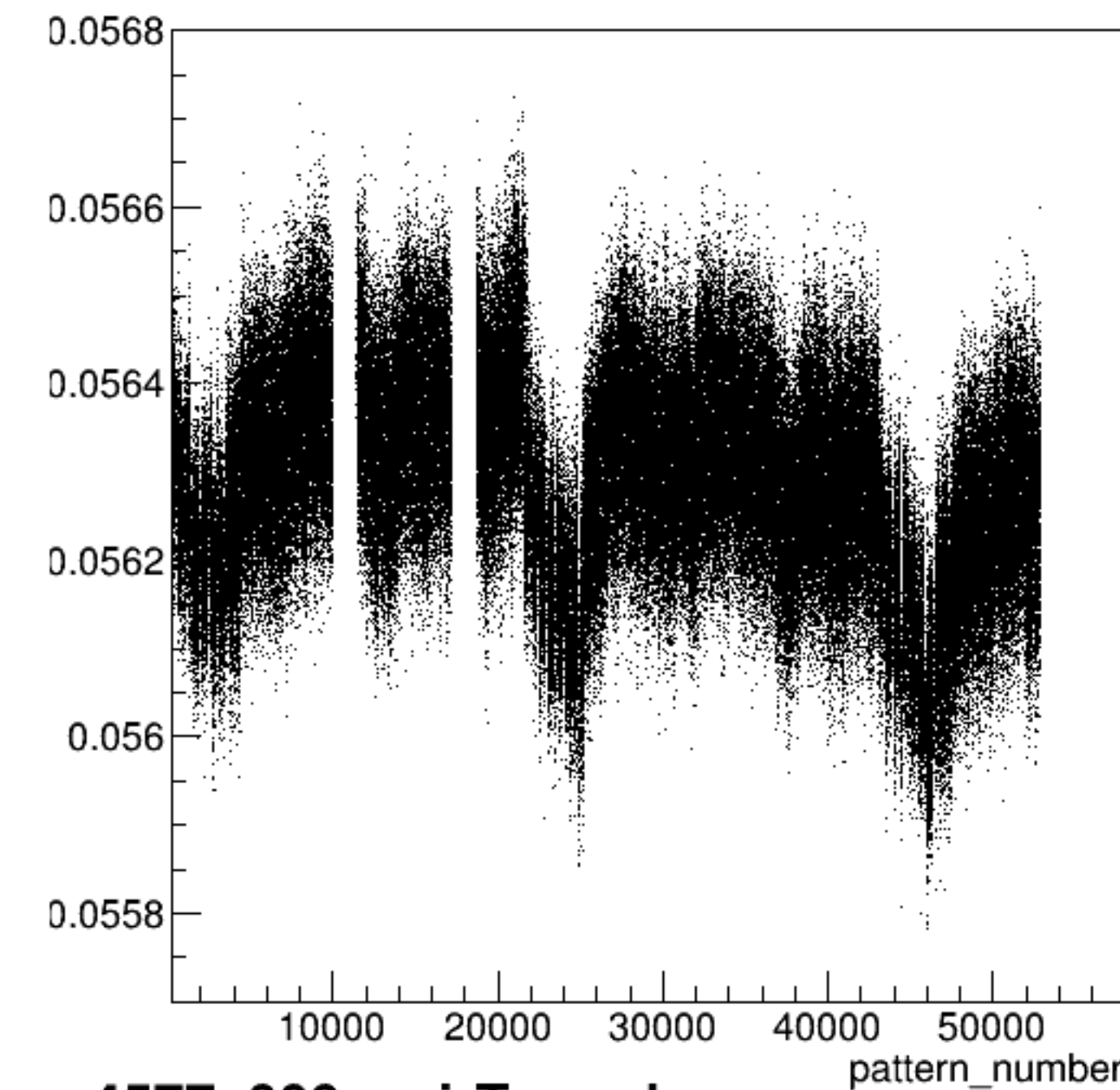
asym_usr/ppm:pattern_number {ErrorFlag==0}



asym_usr/ppm {ErrorFlag==0}

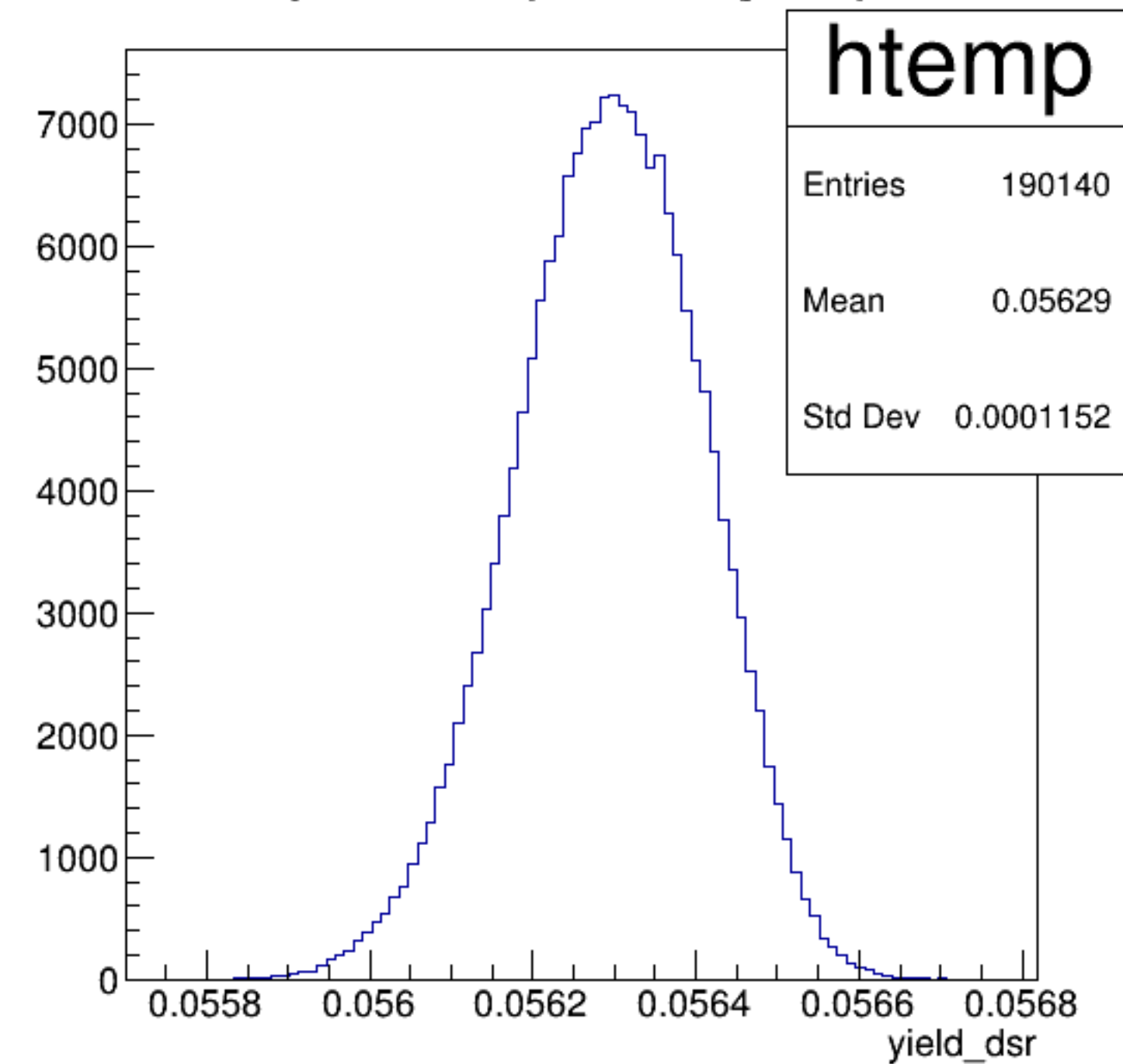


yield_dsr:pattern_number {ErrorFlag==0}

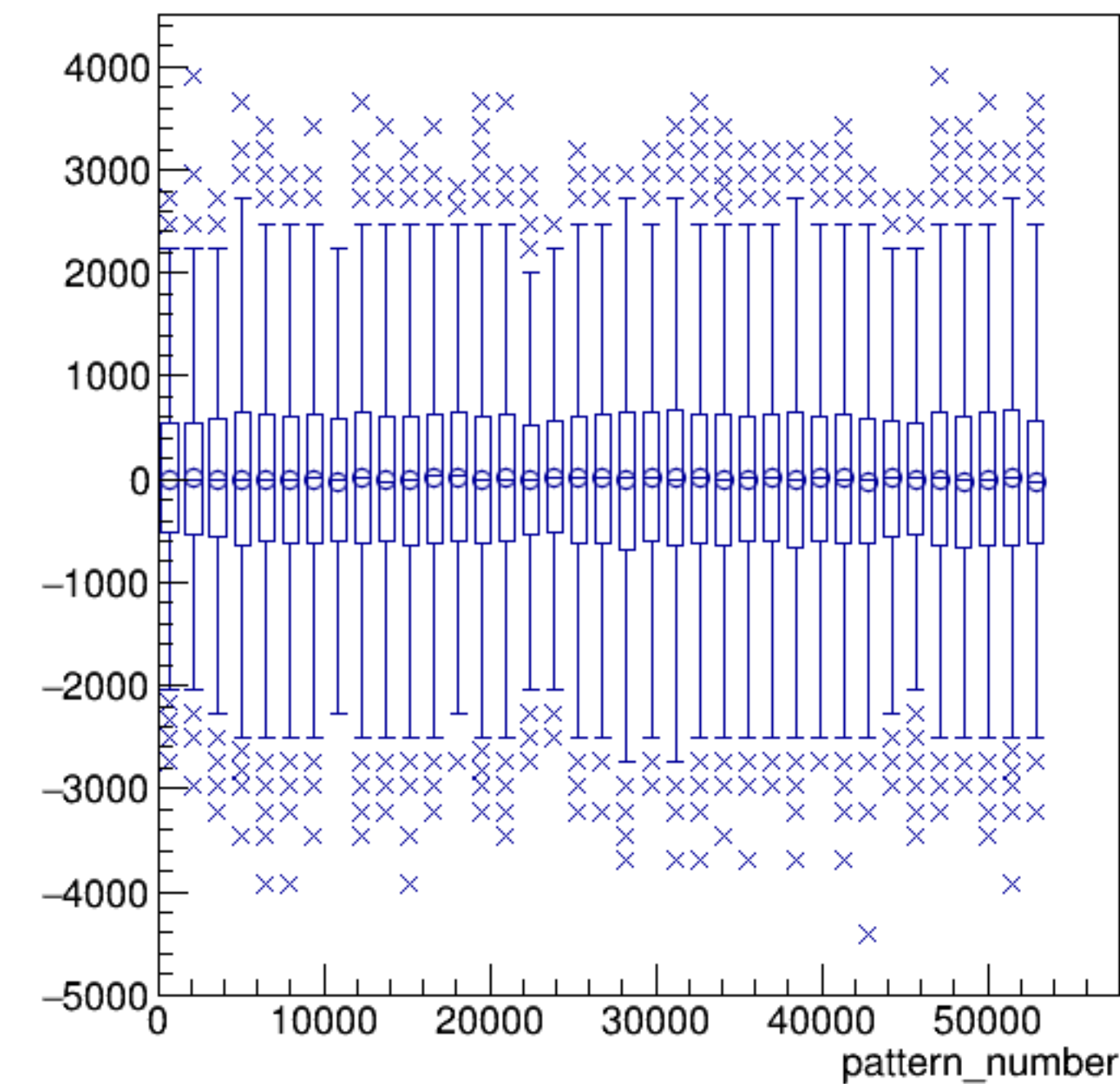


run4577_000_pairTree_dsr.png

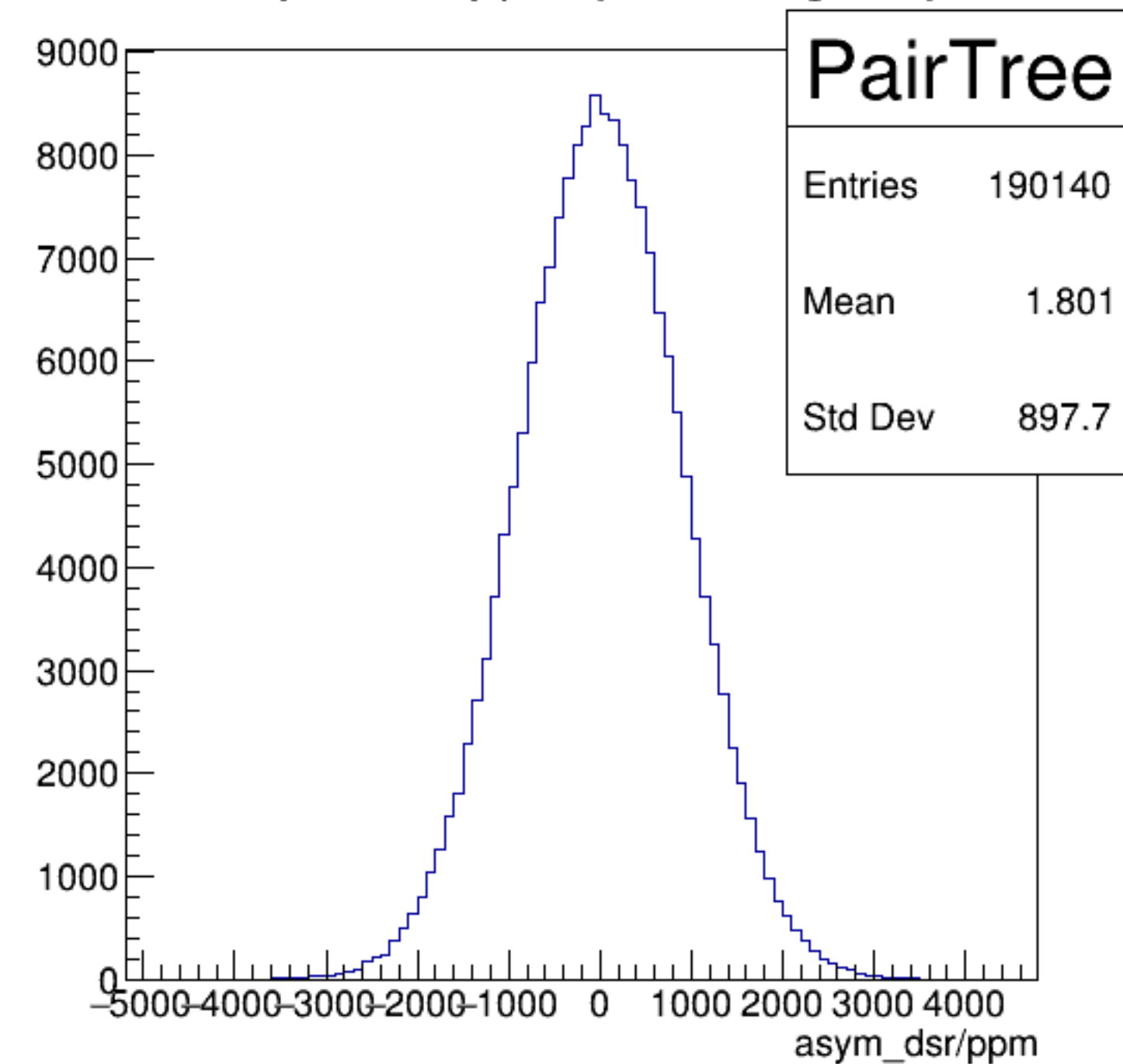
yield_dsr {ErrorFlag==0}



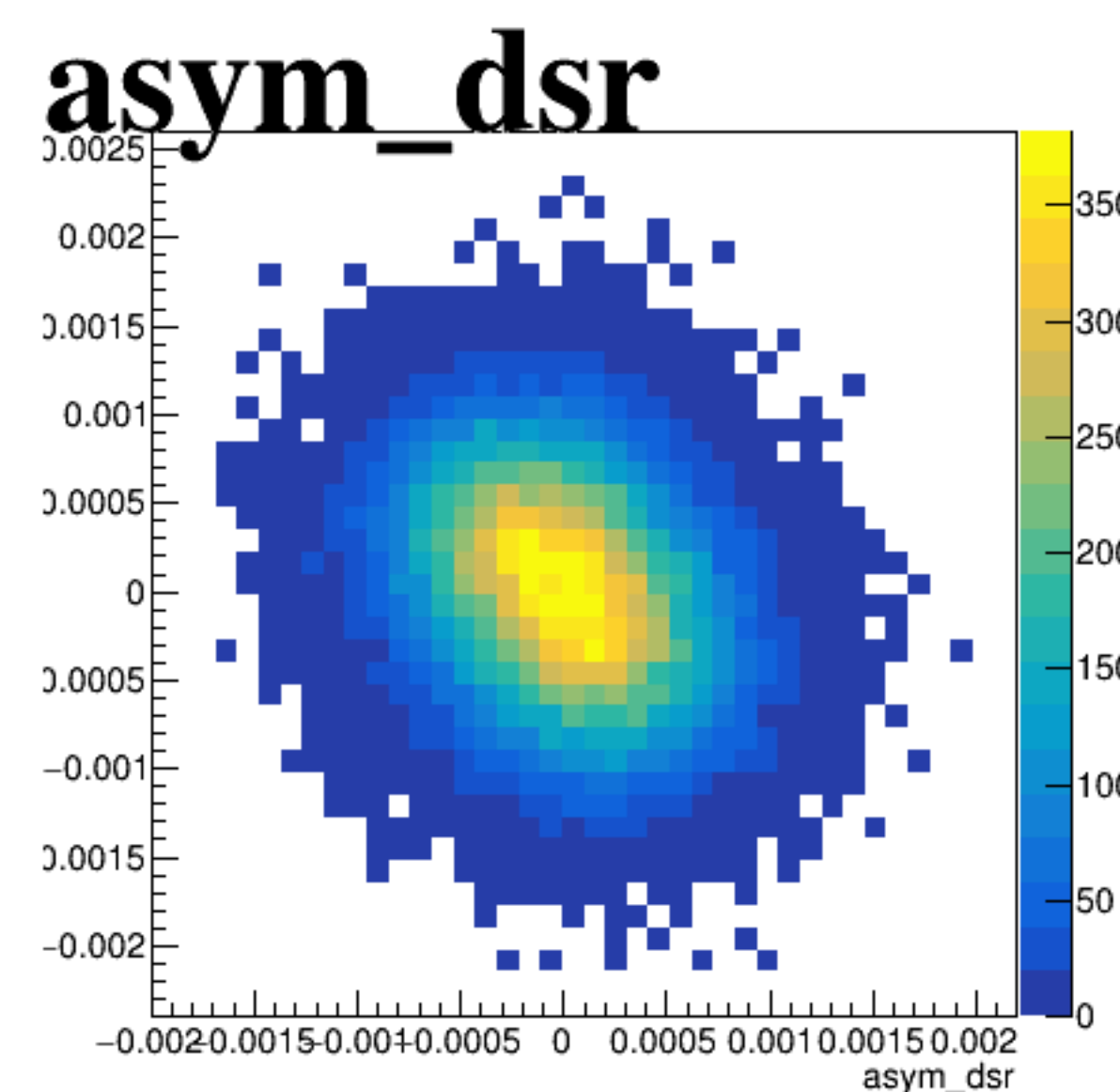
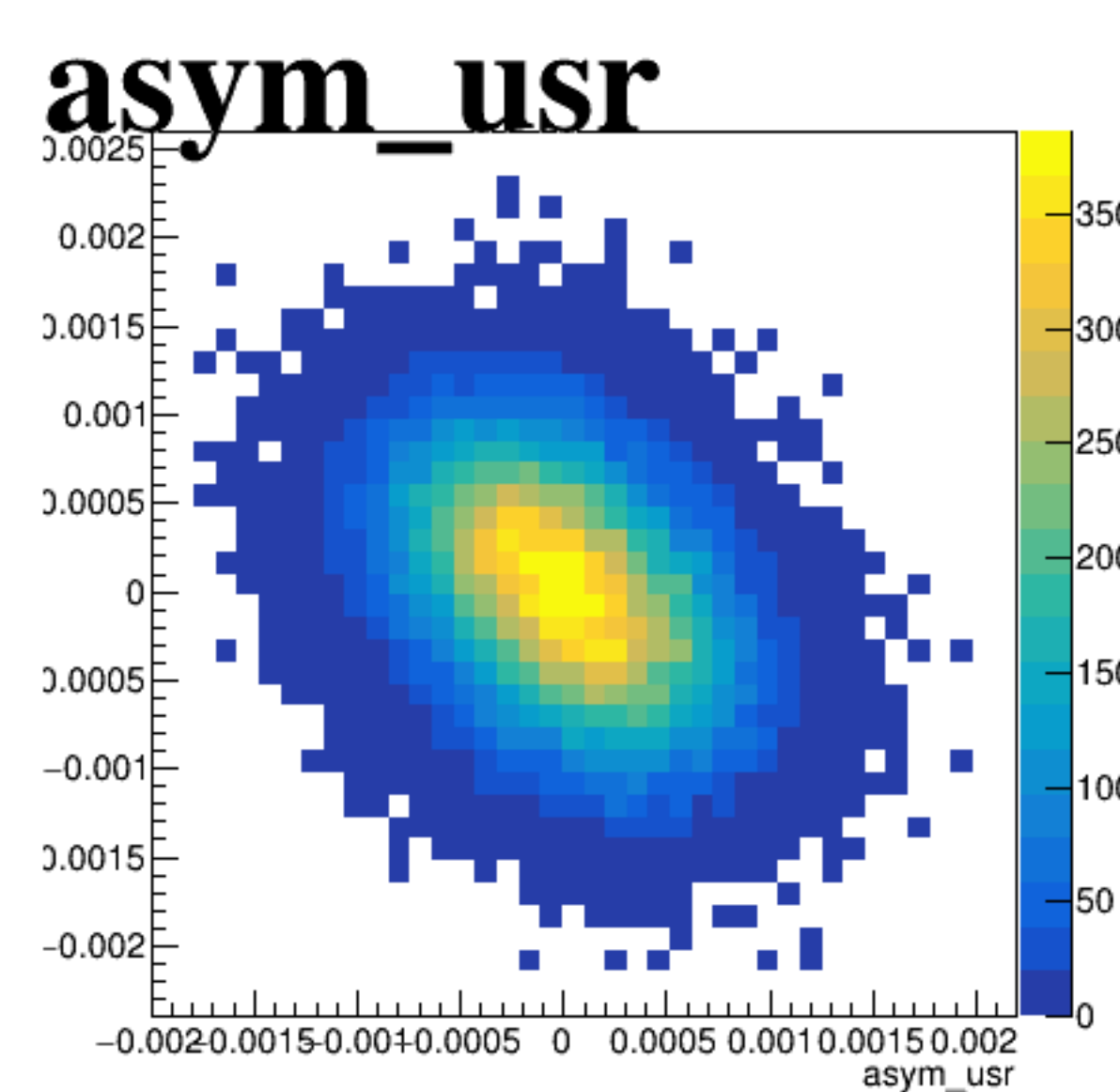
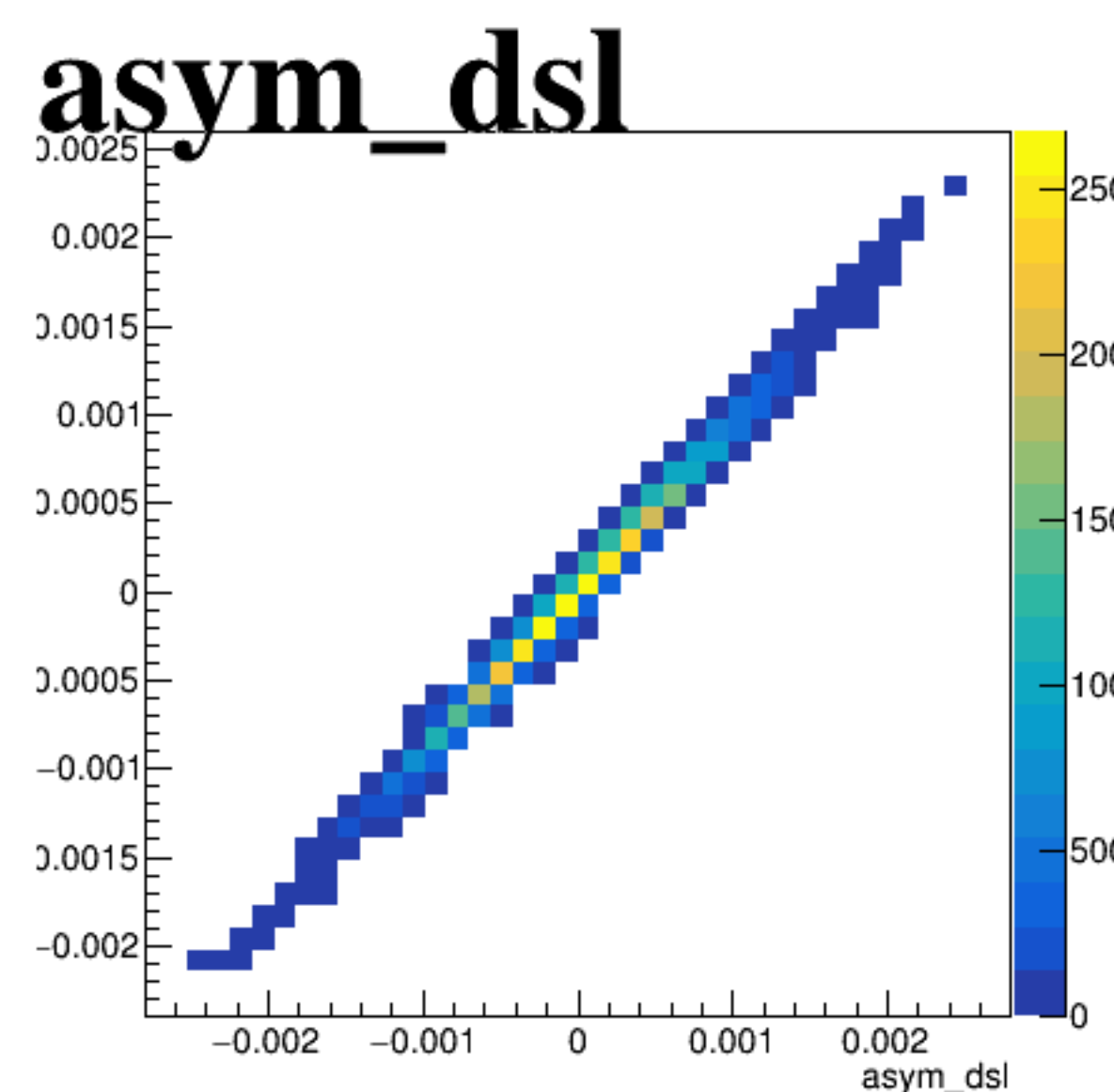
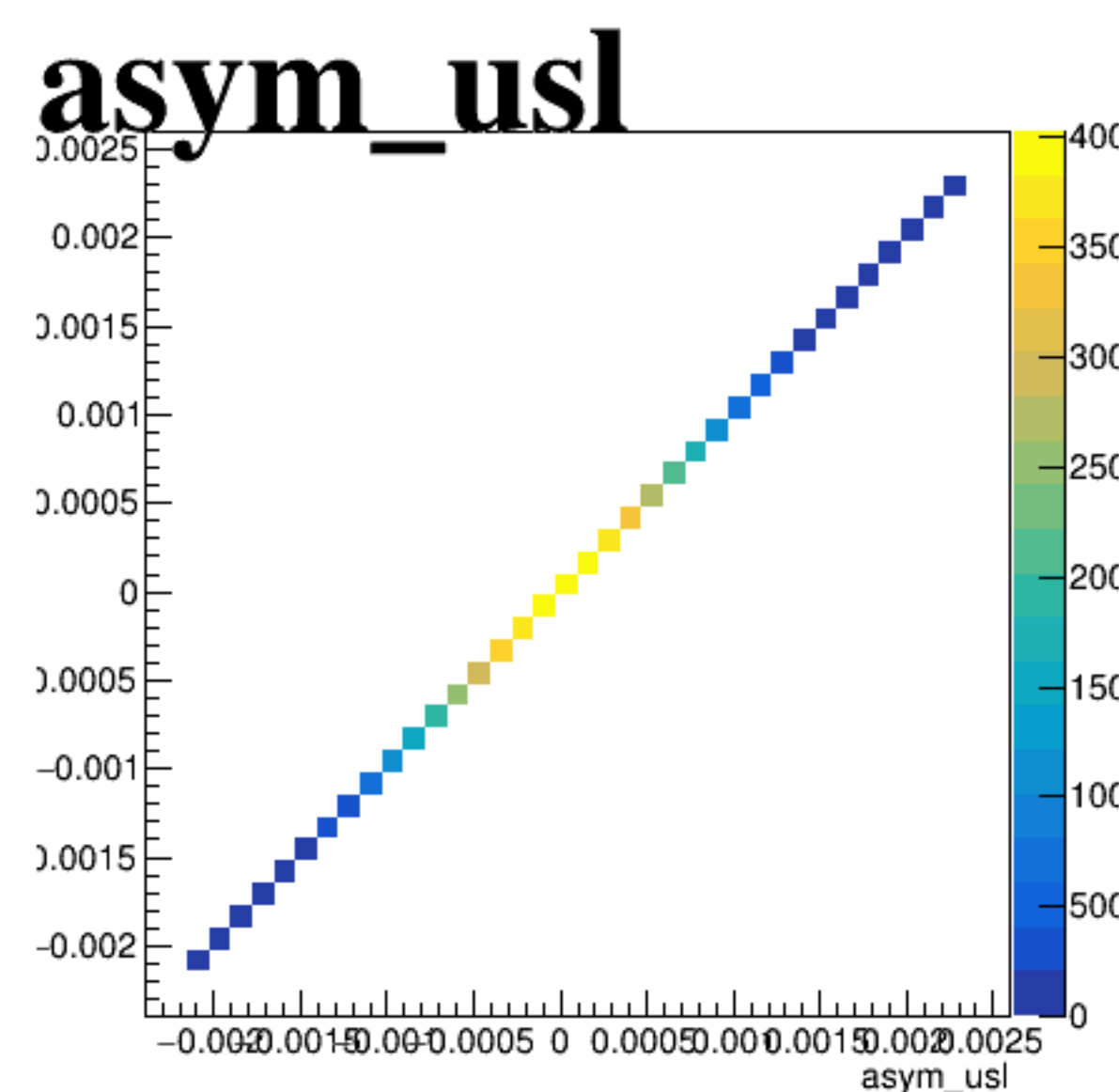
asym_dsr/ppm:pattern_number {ErrorFlag==0}



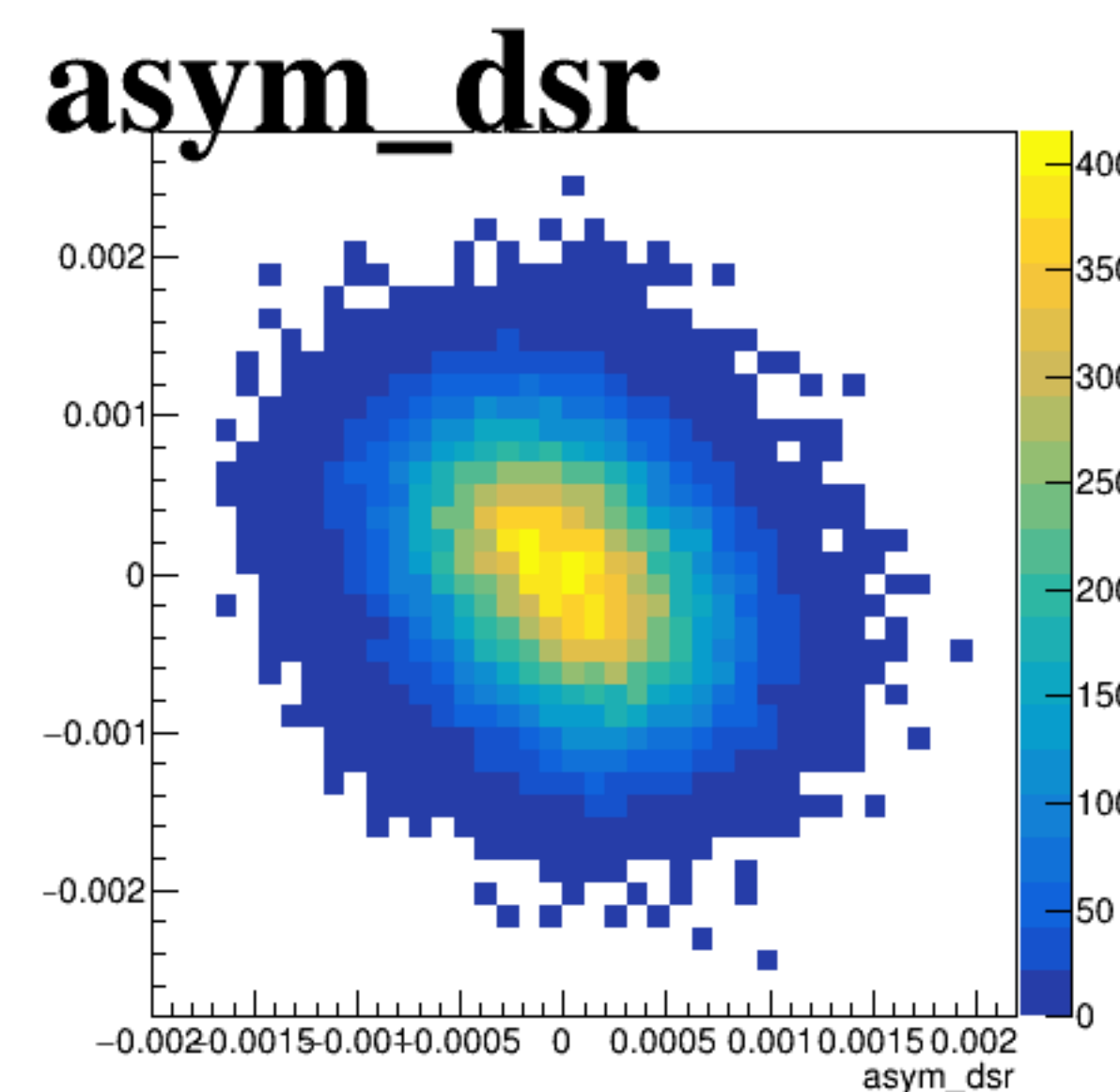
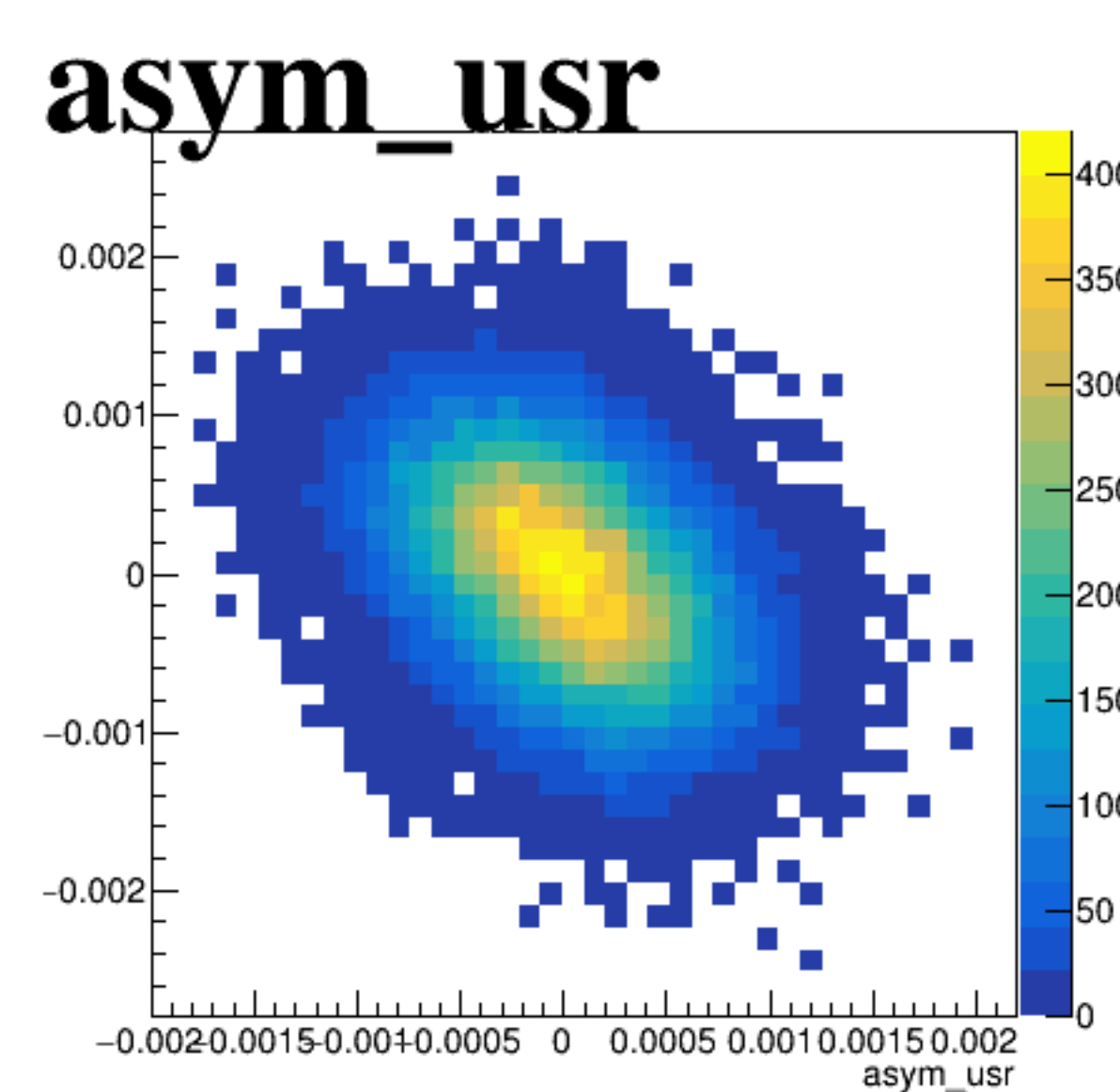
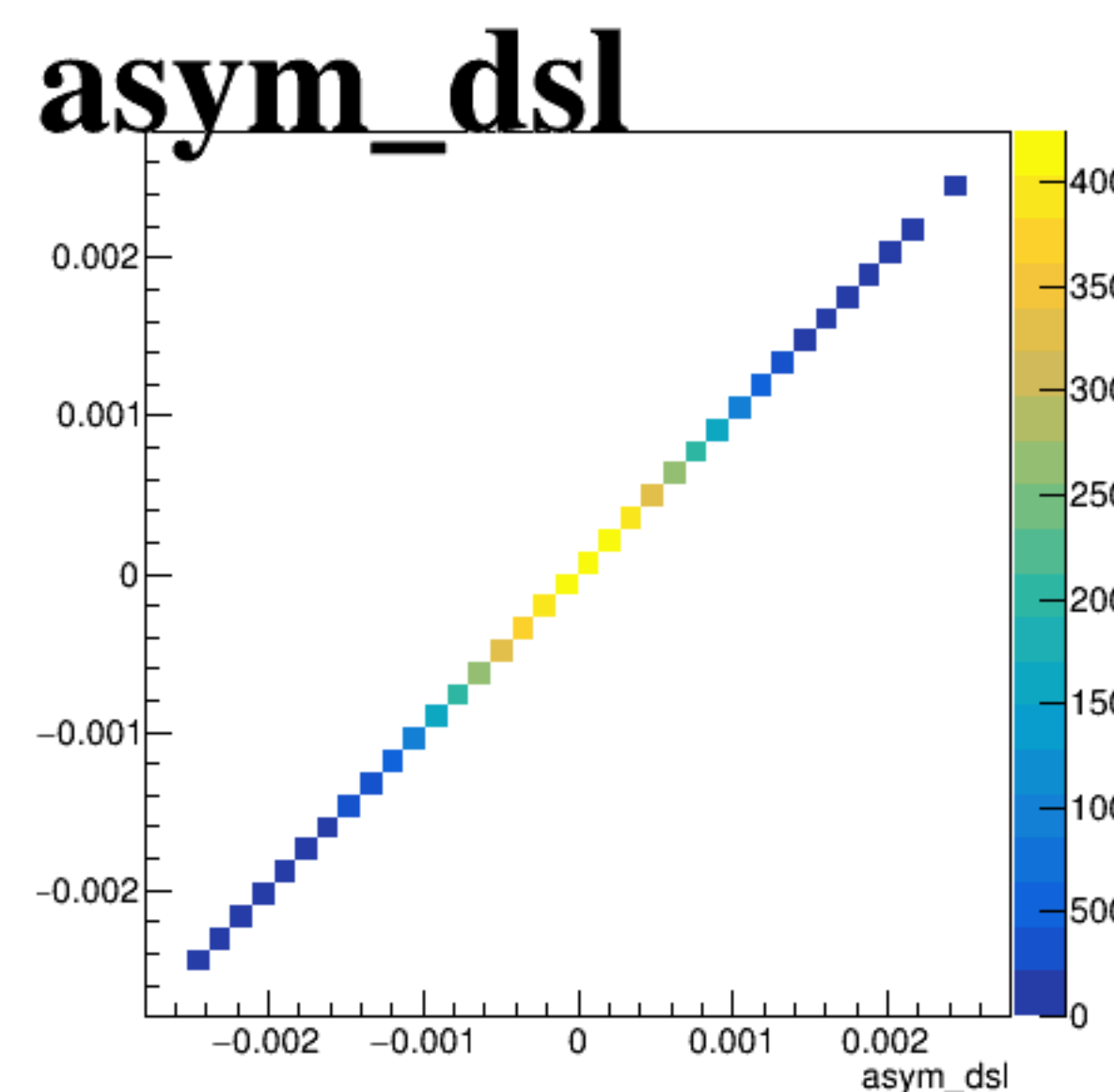
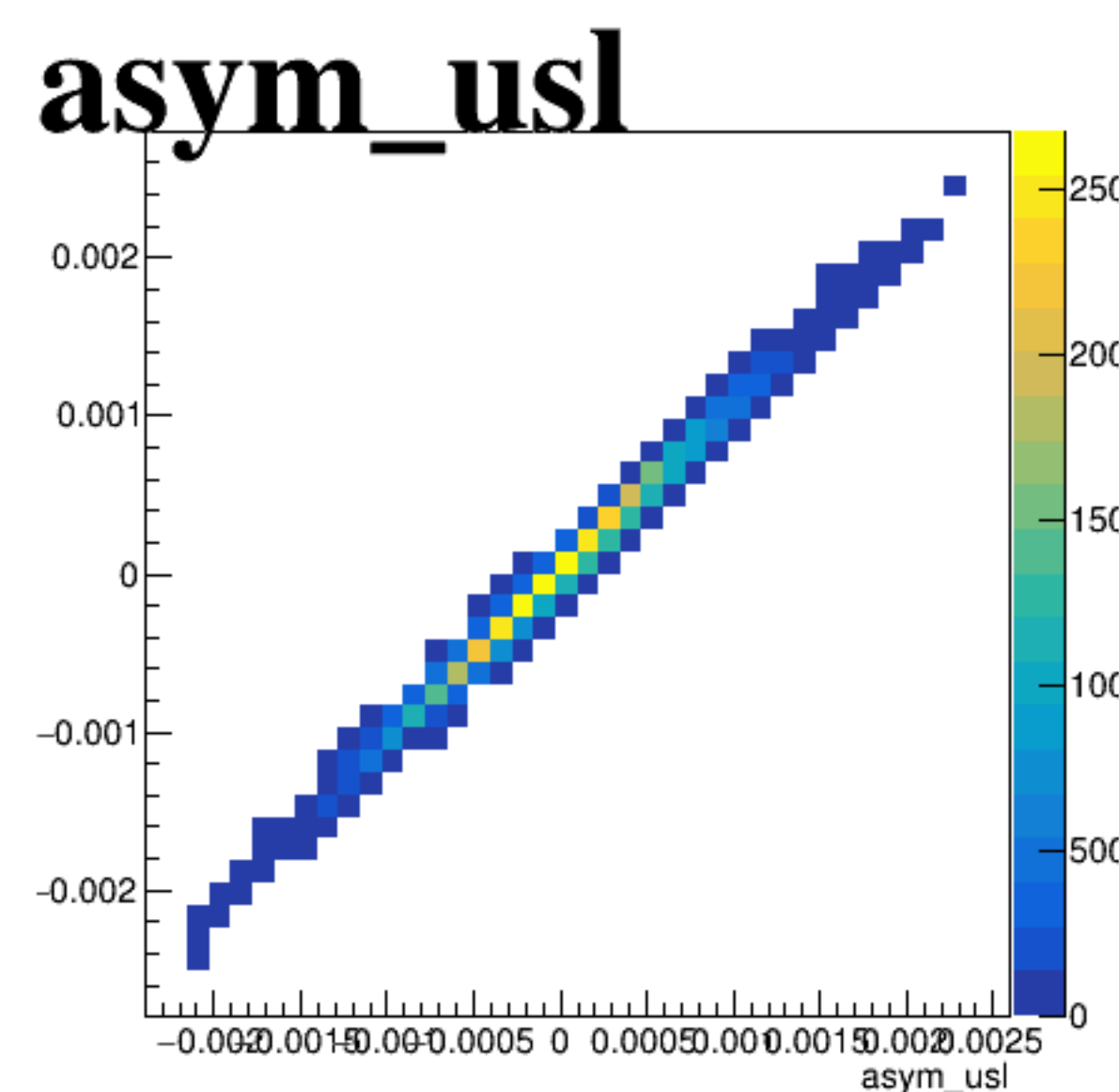
asym_dsr/ppm {ErrorFlag==0}



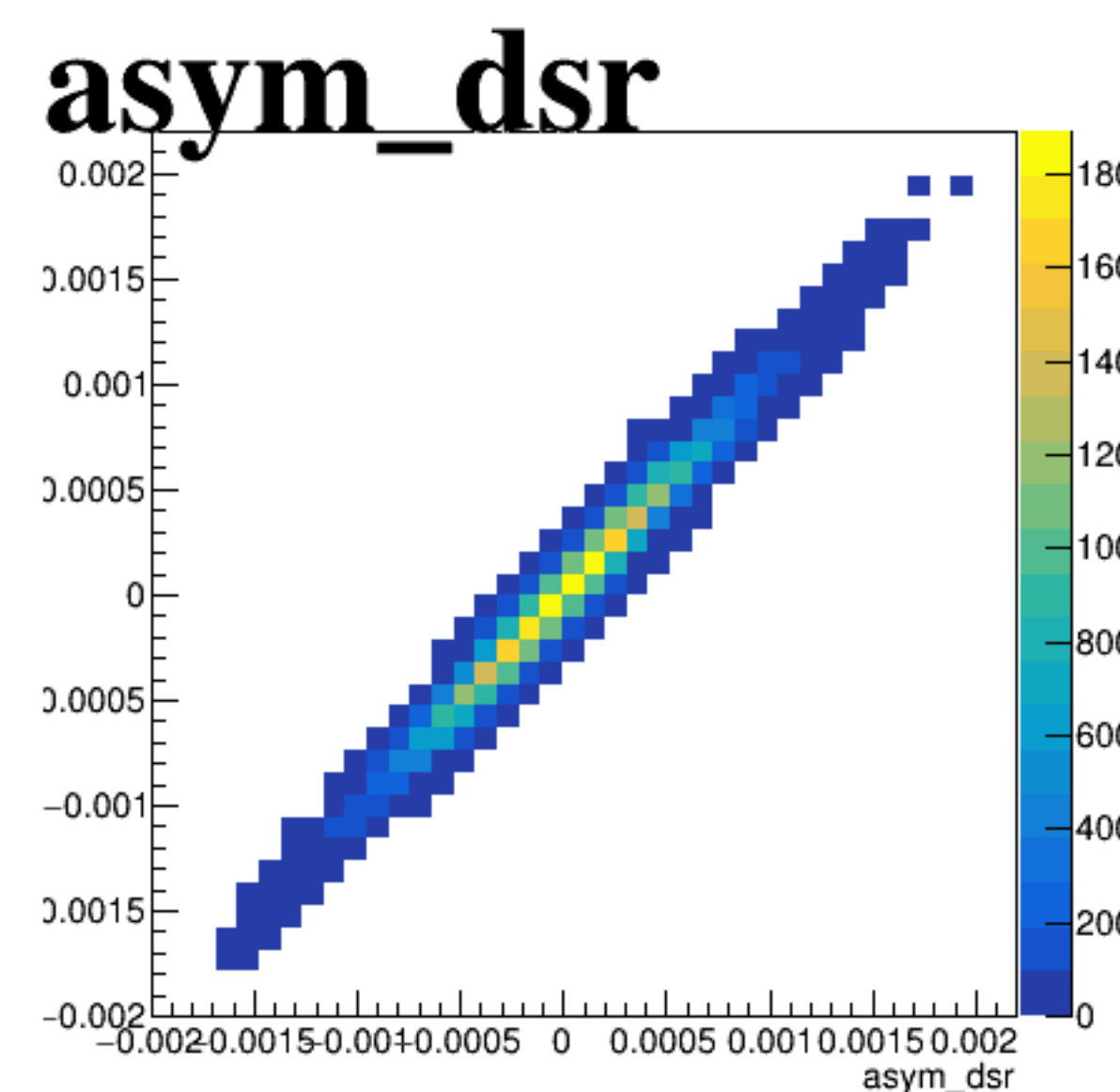
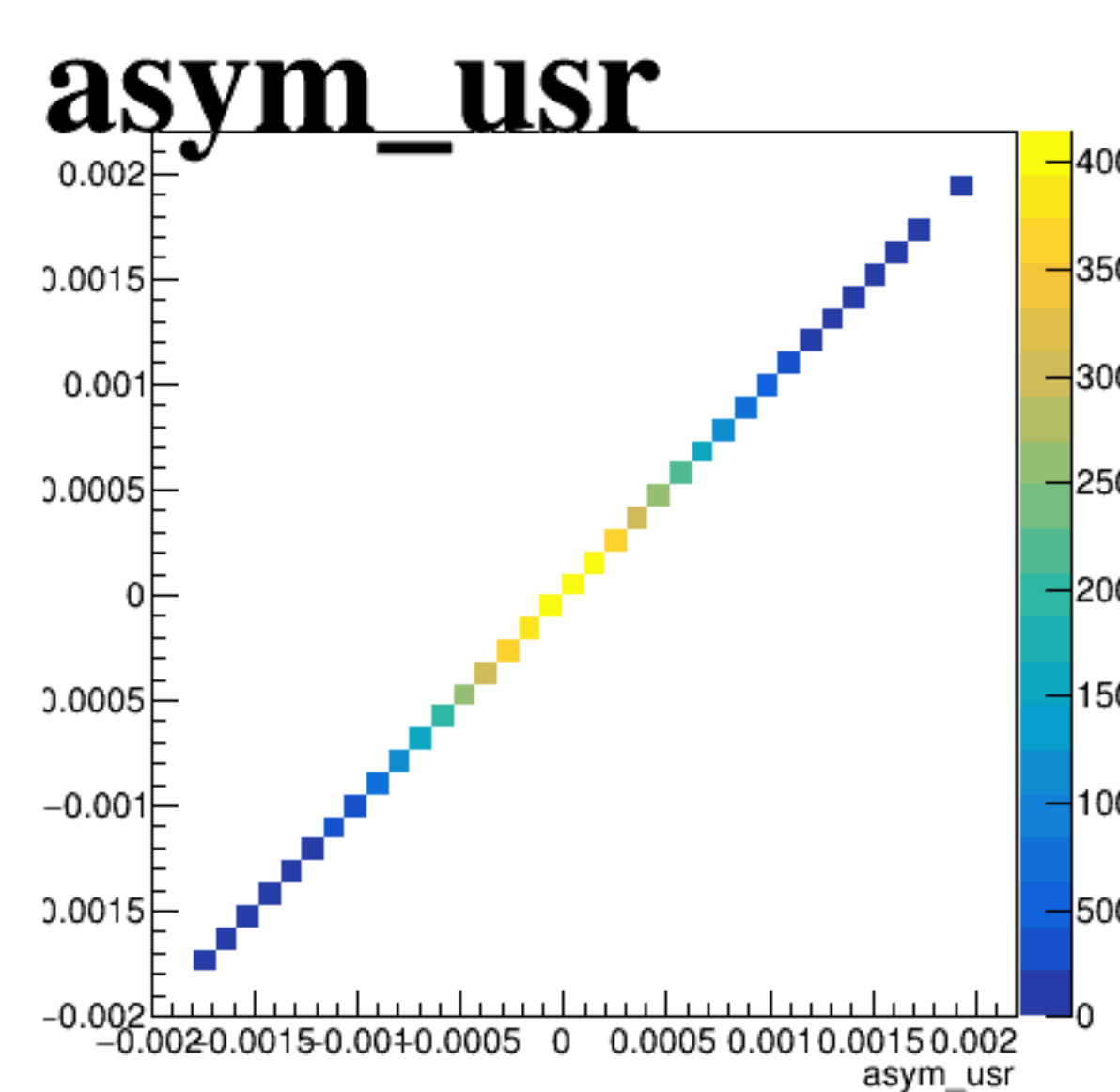
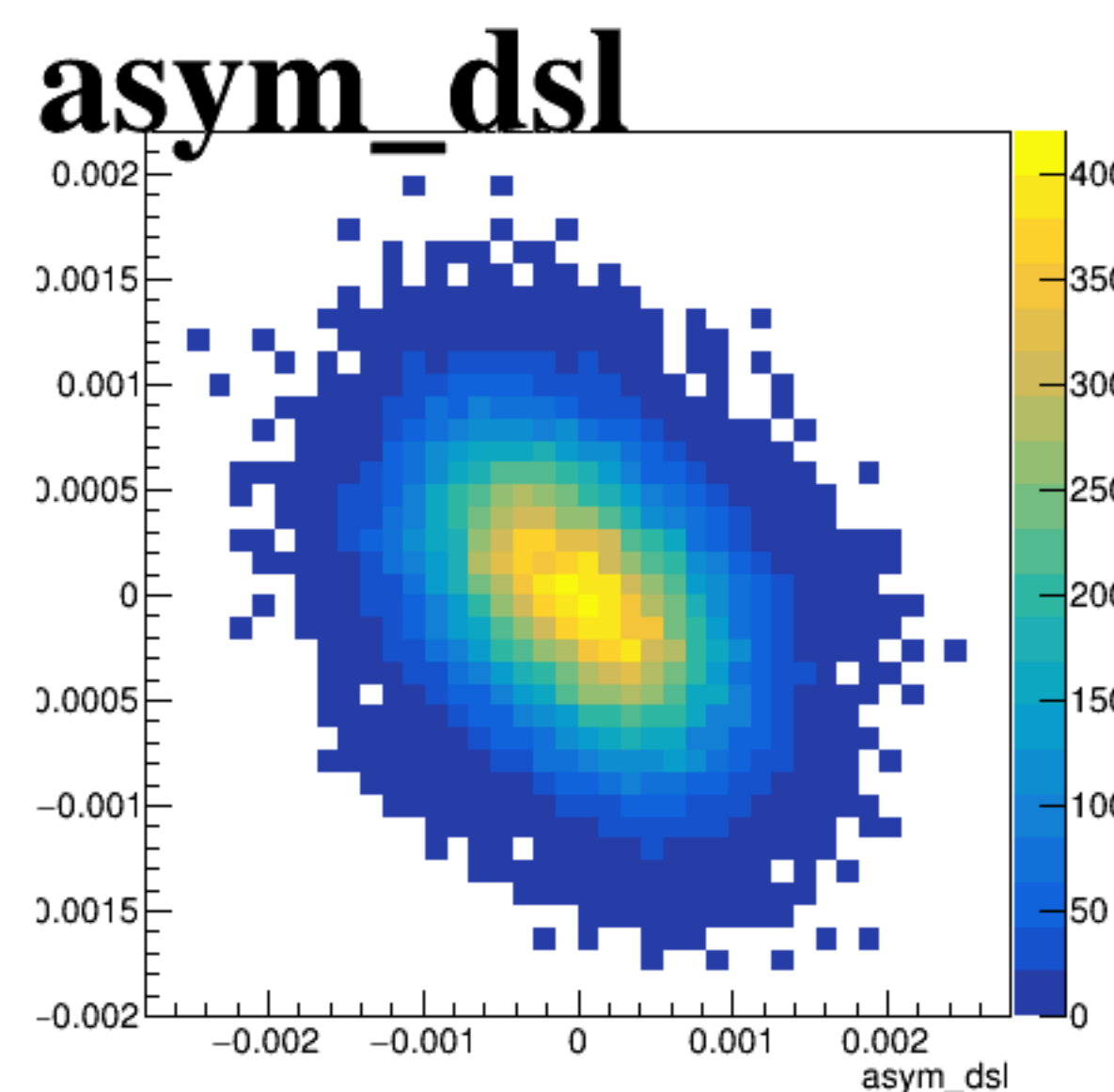
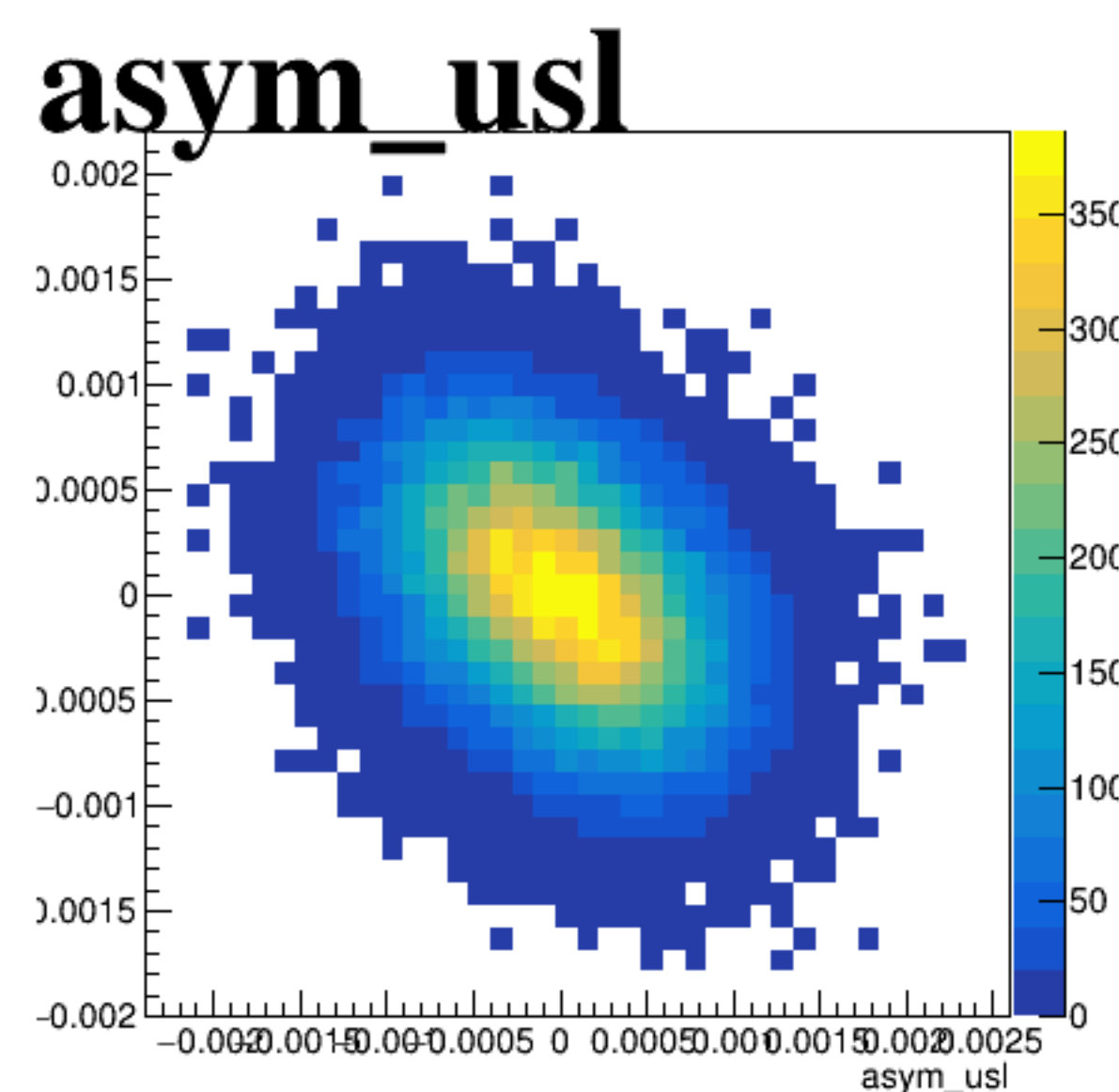
asym_usl



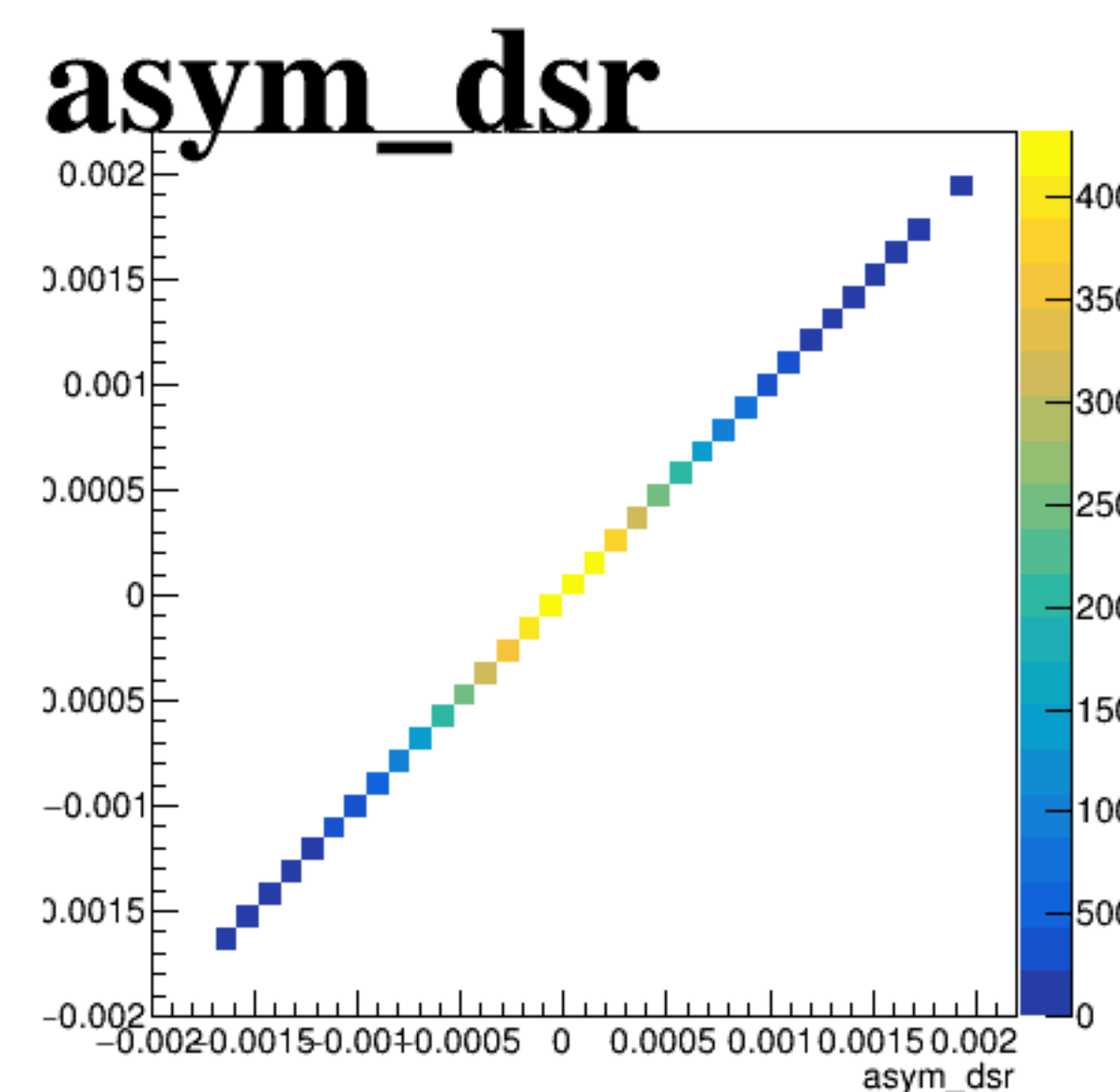
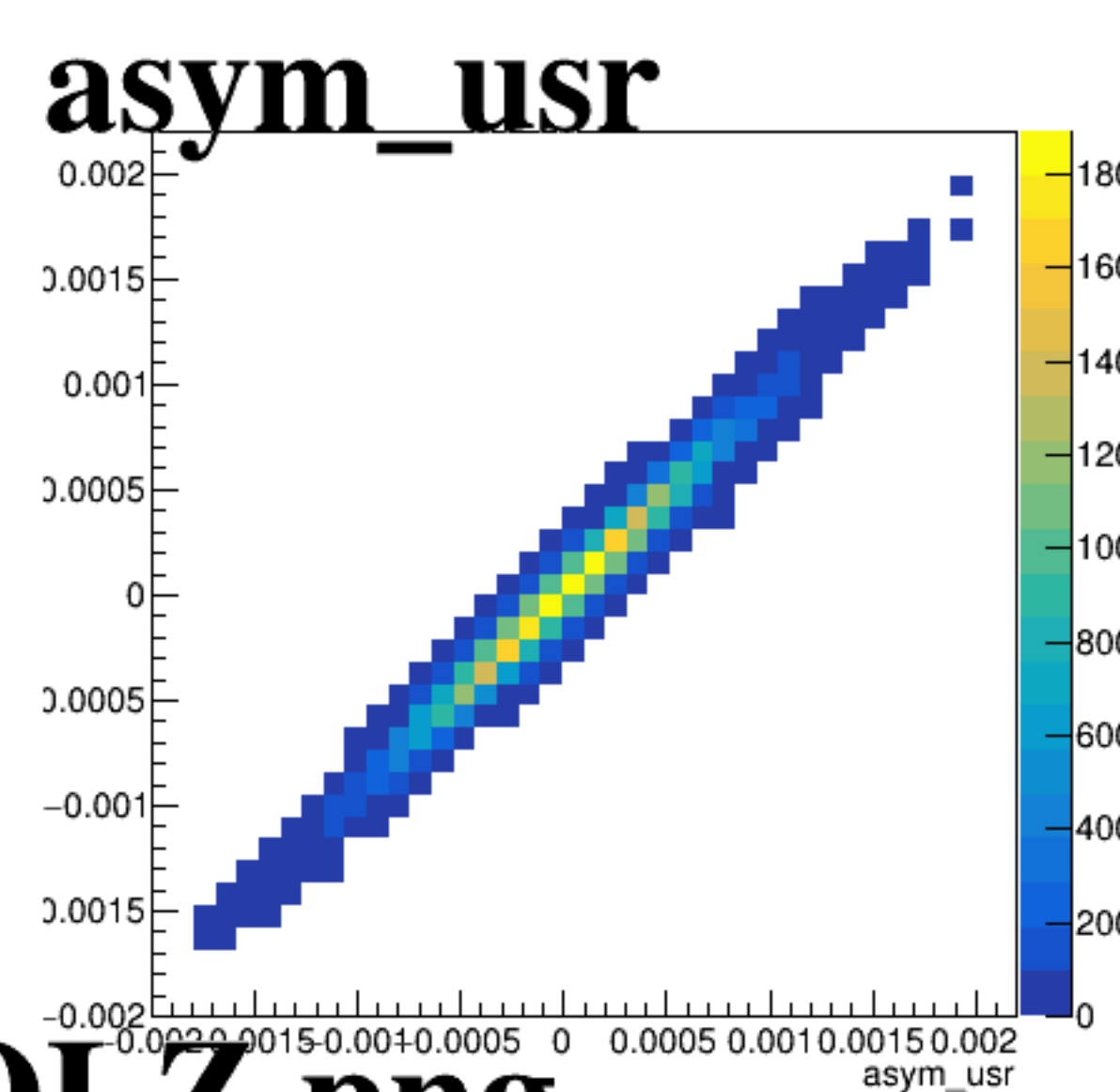
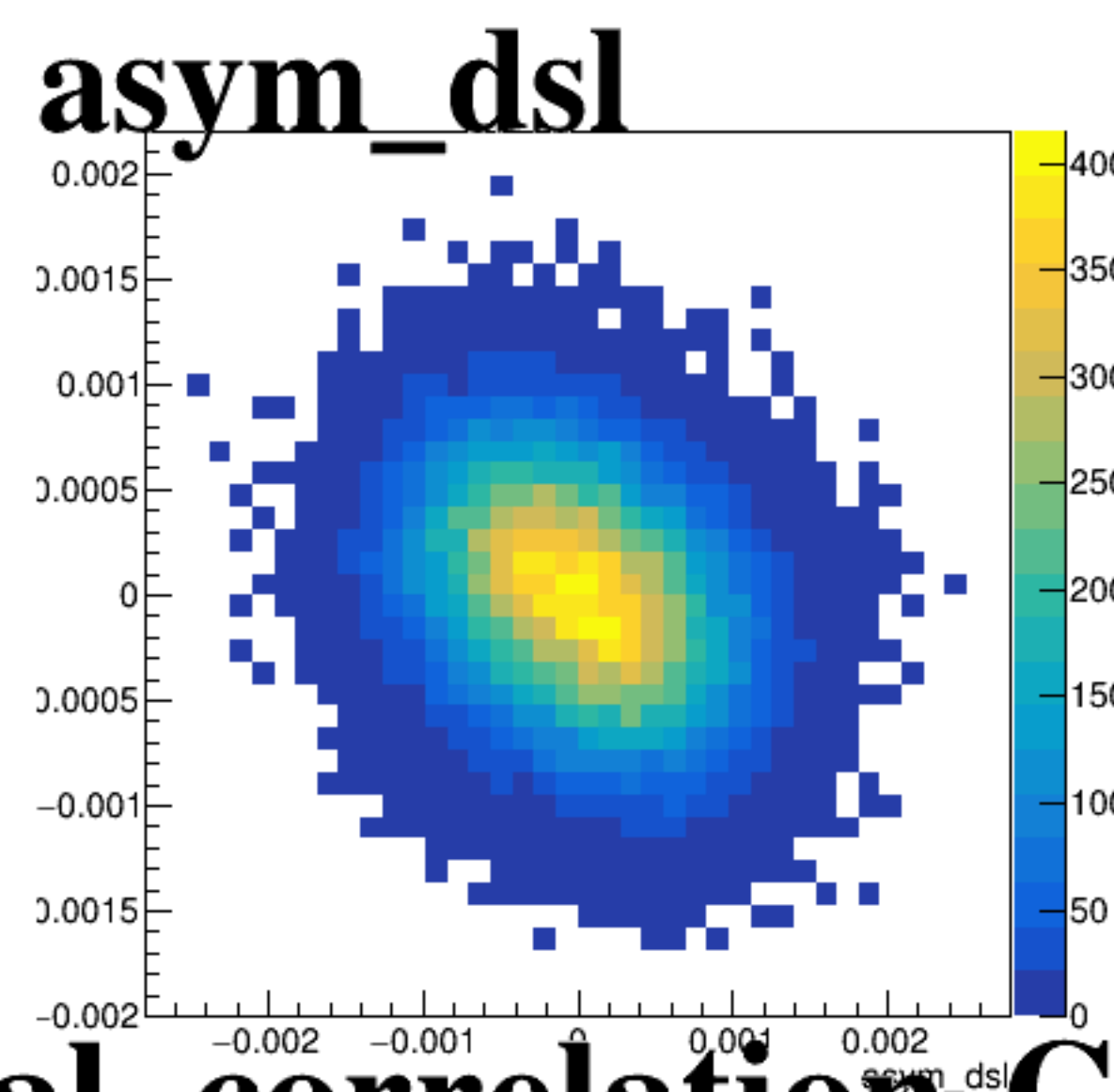
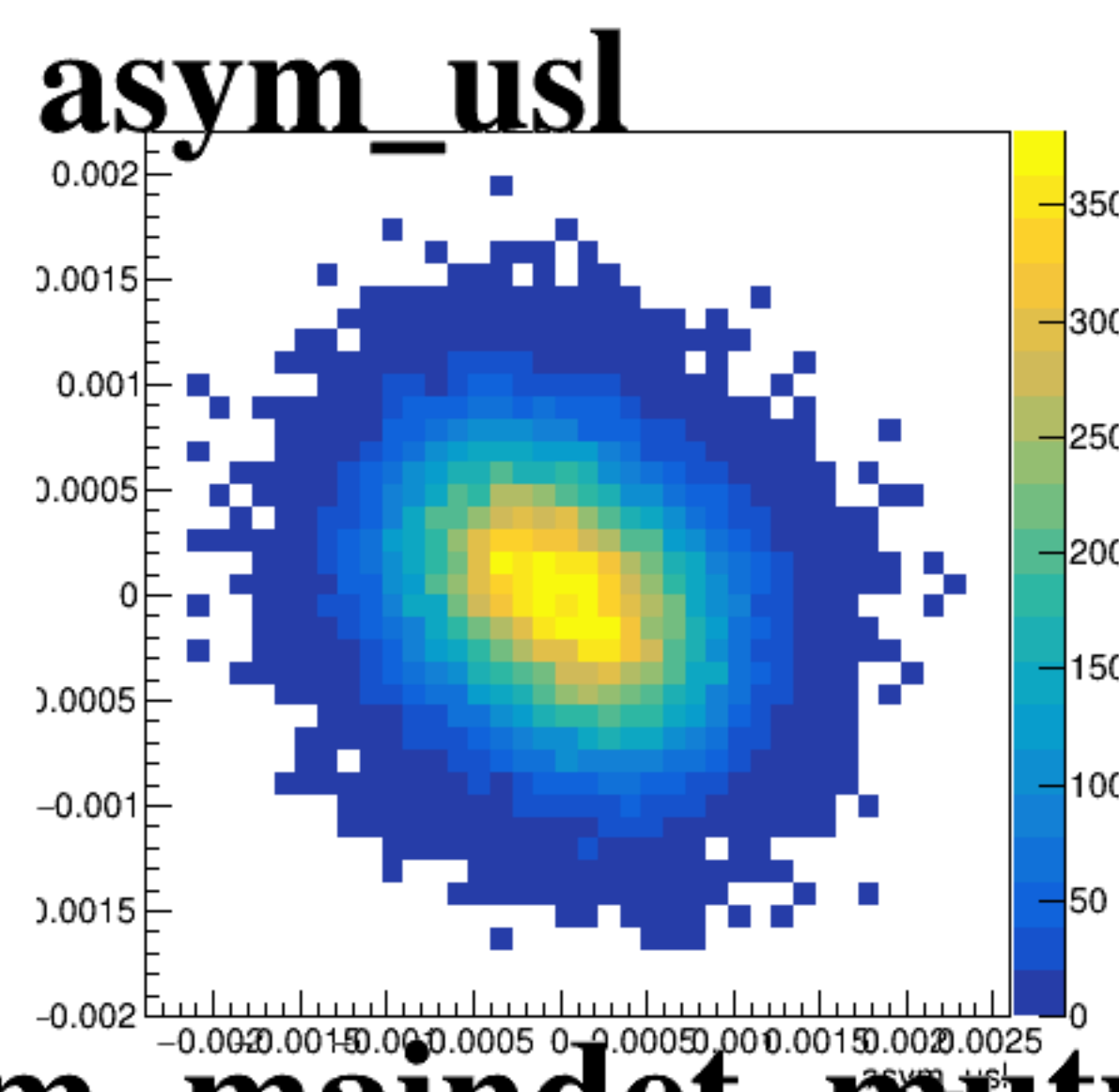
asym_dsl



asym_usr

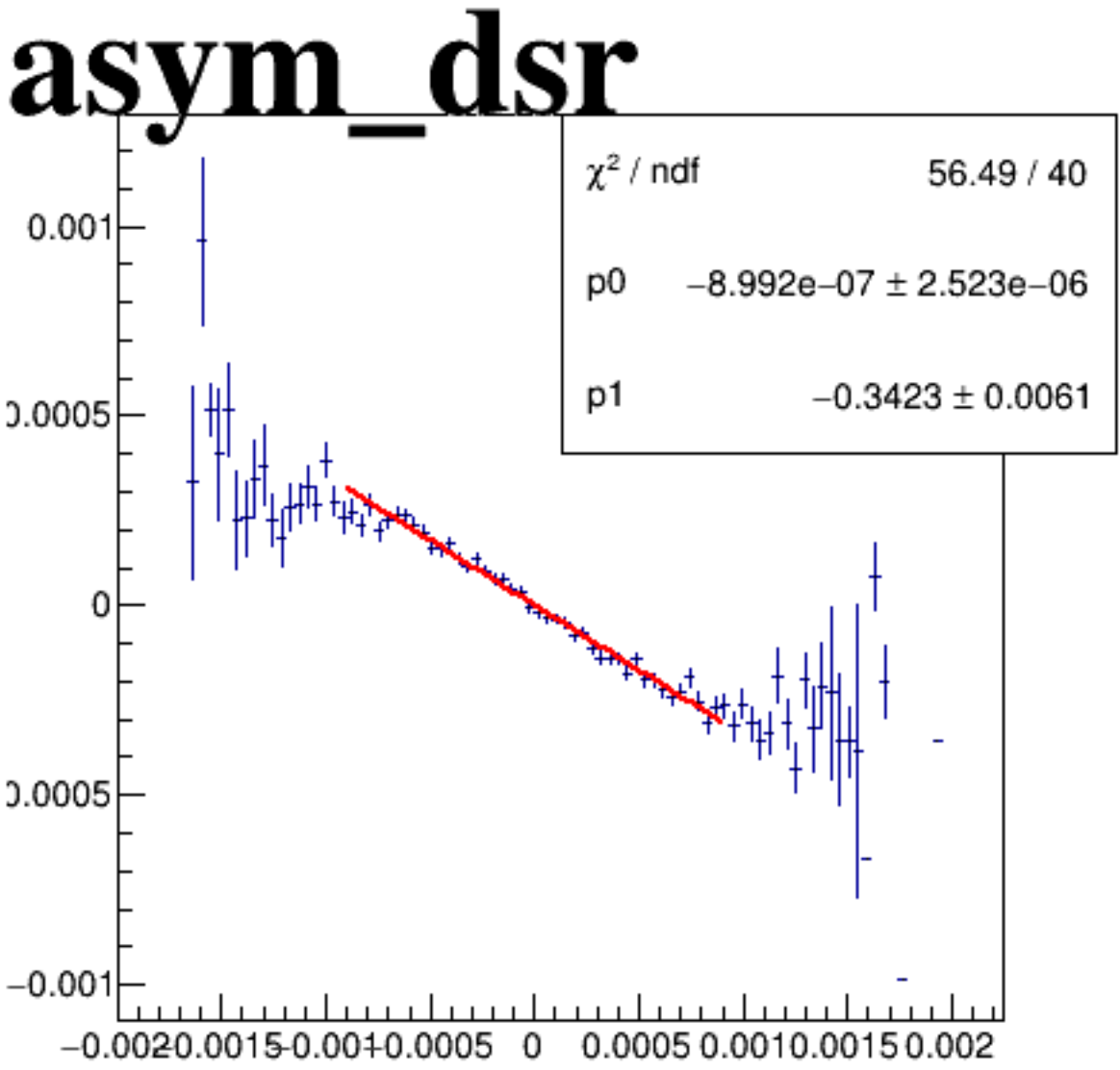
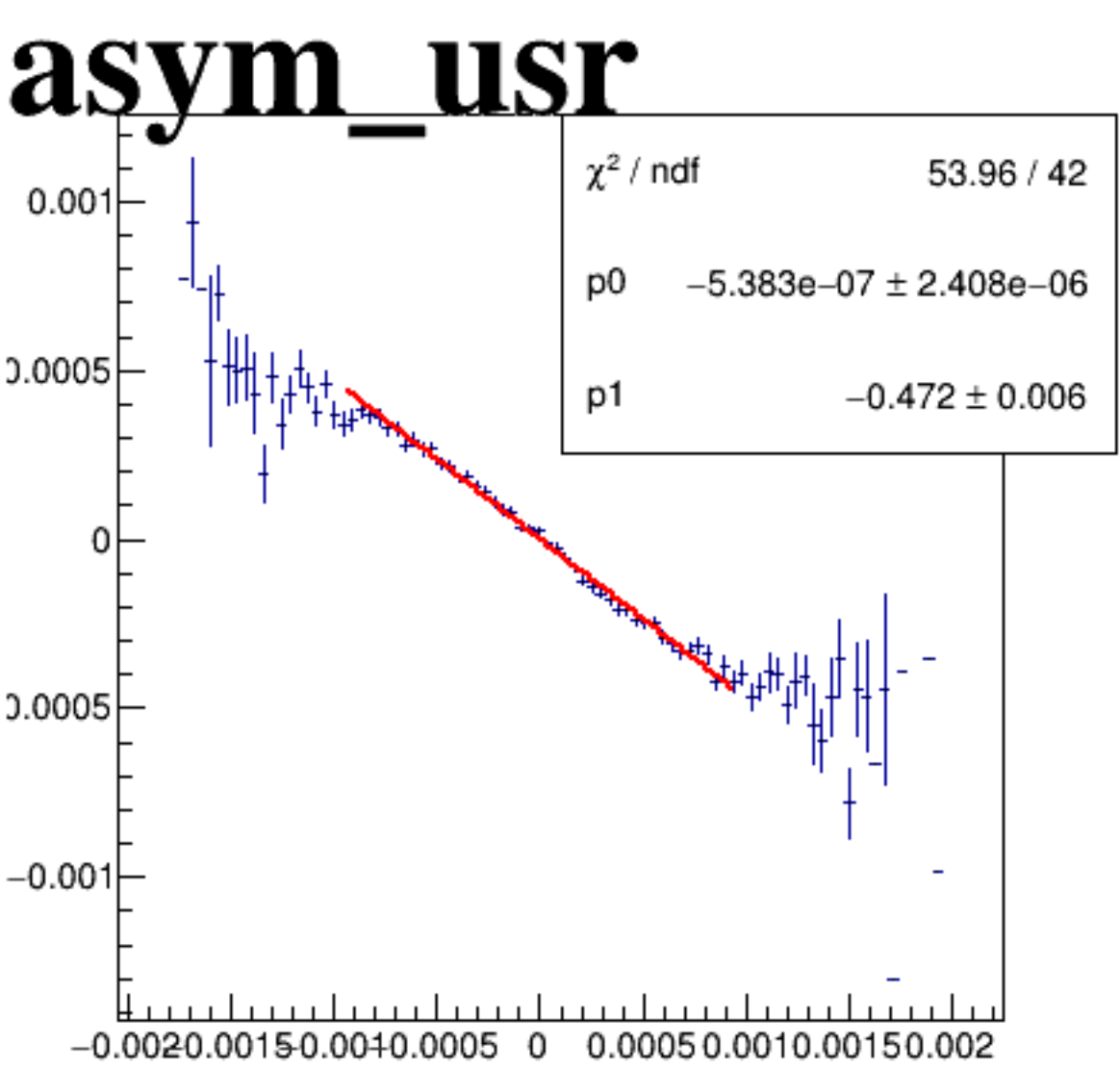
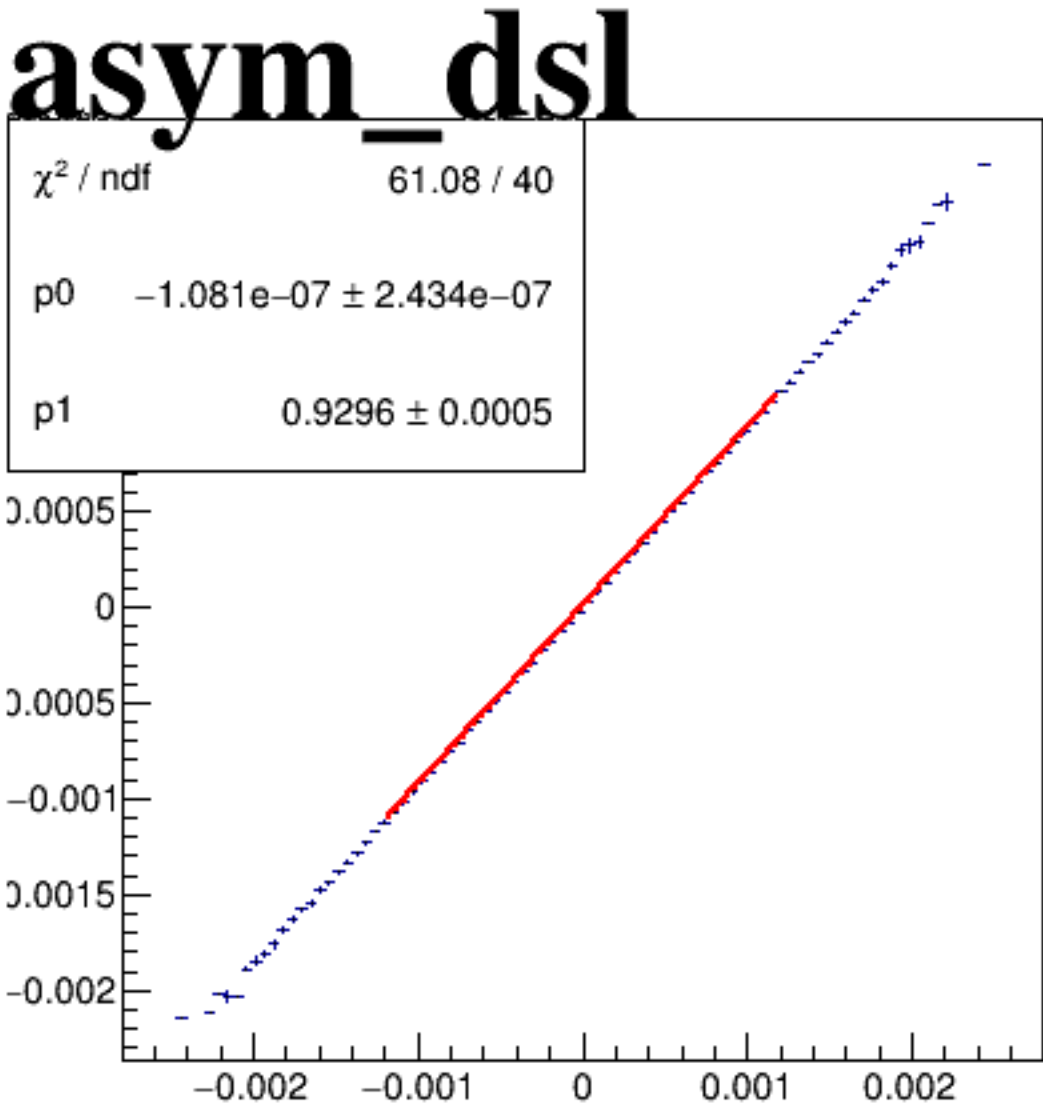
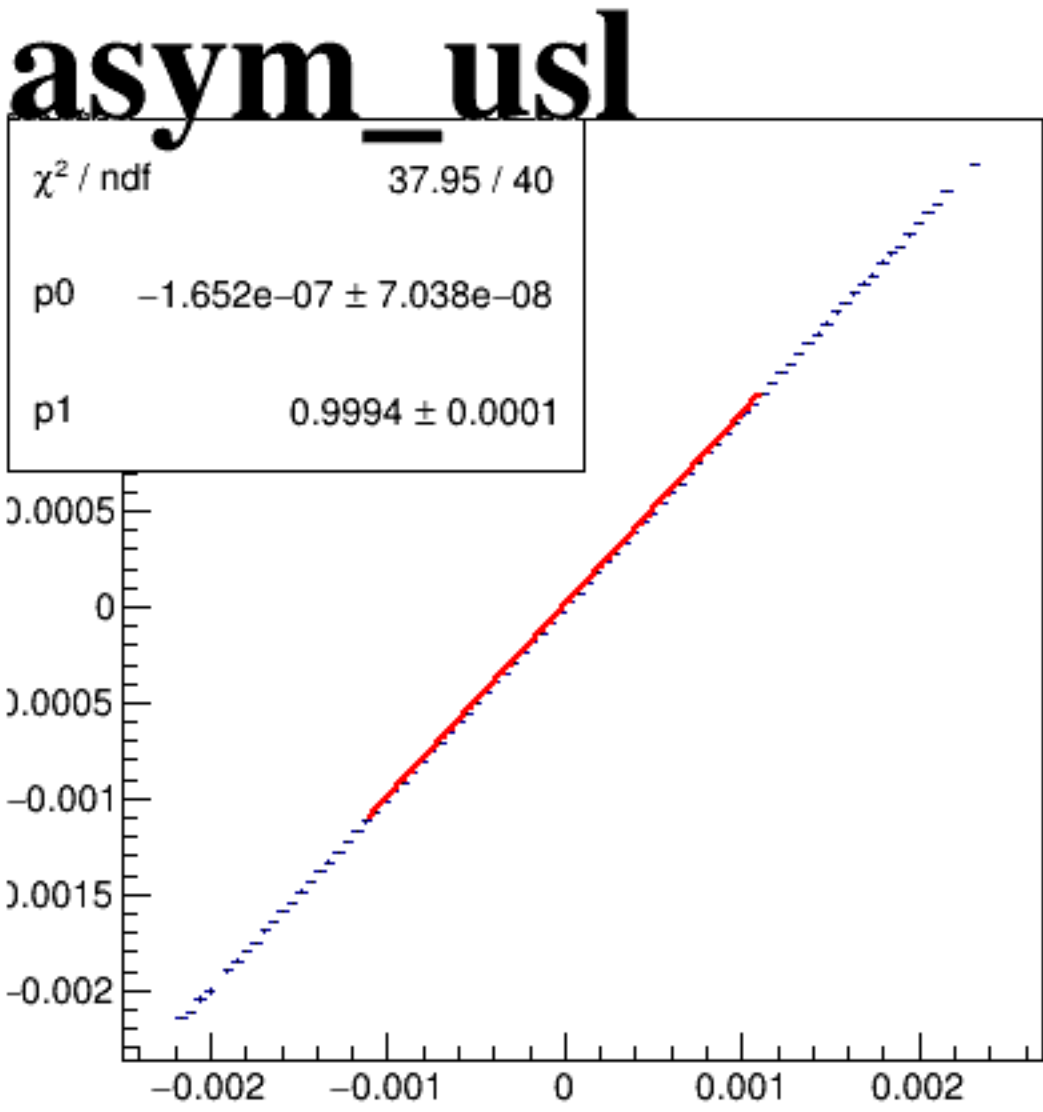


asym_dsr

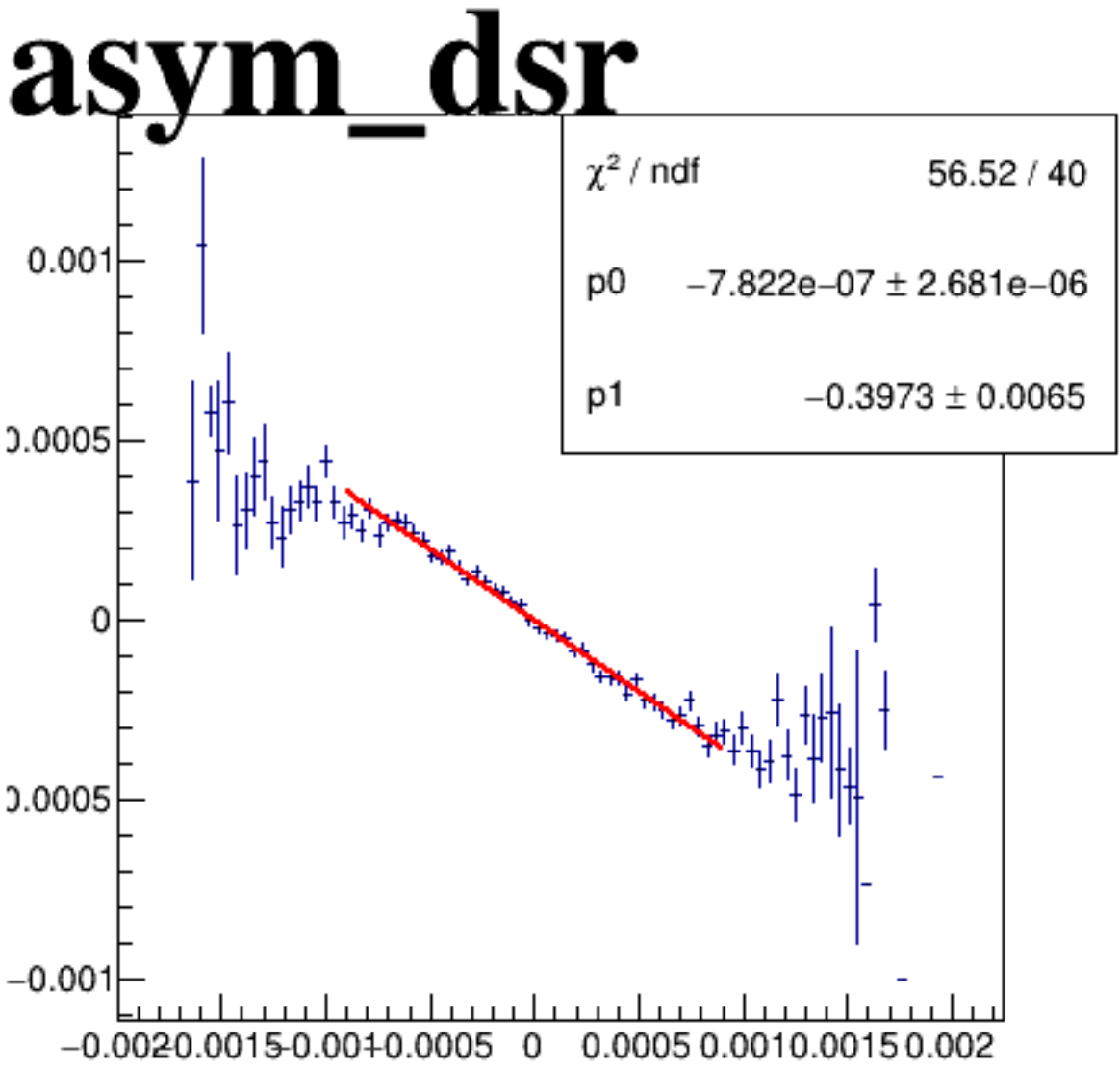
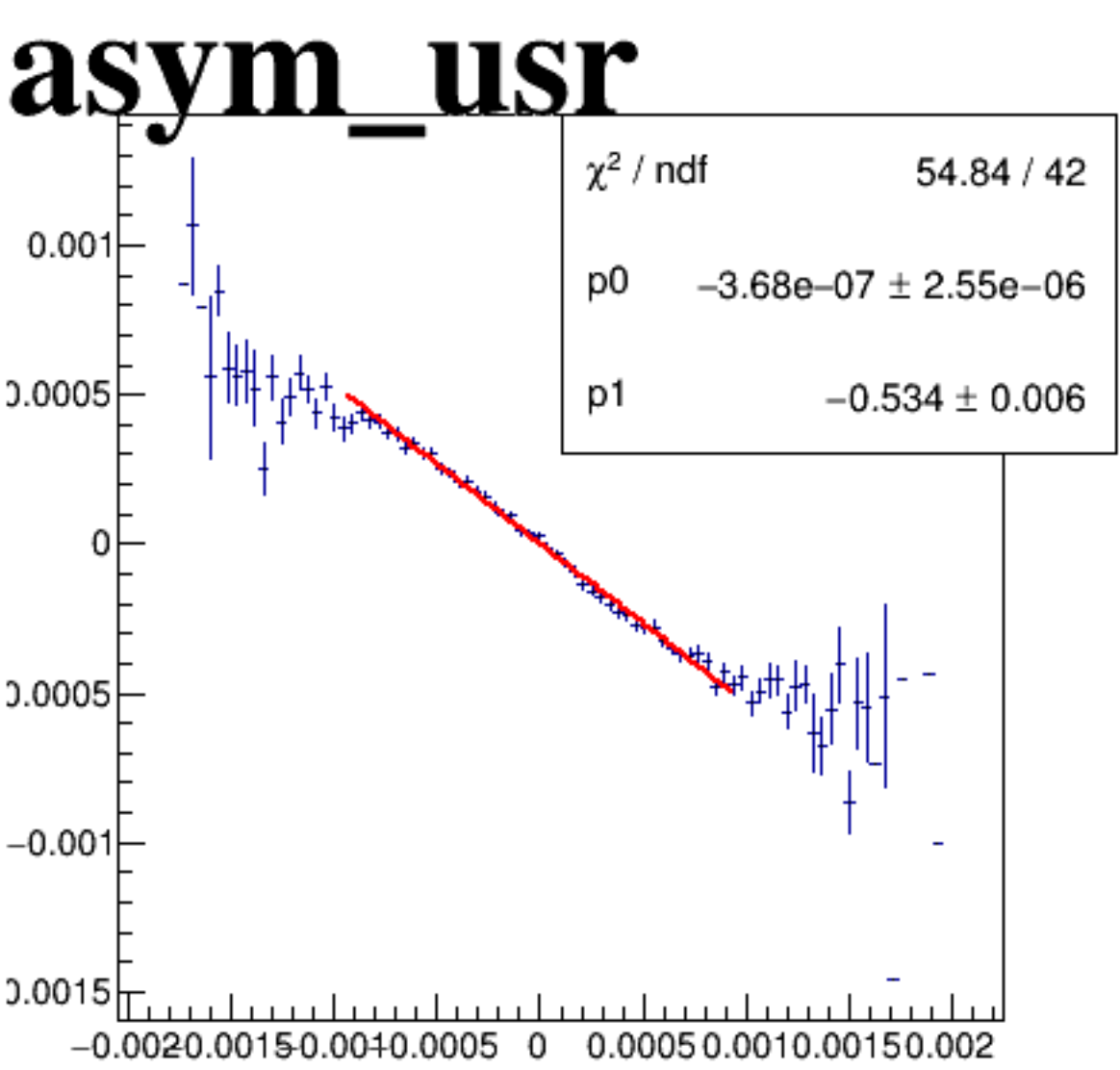
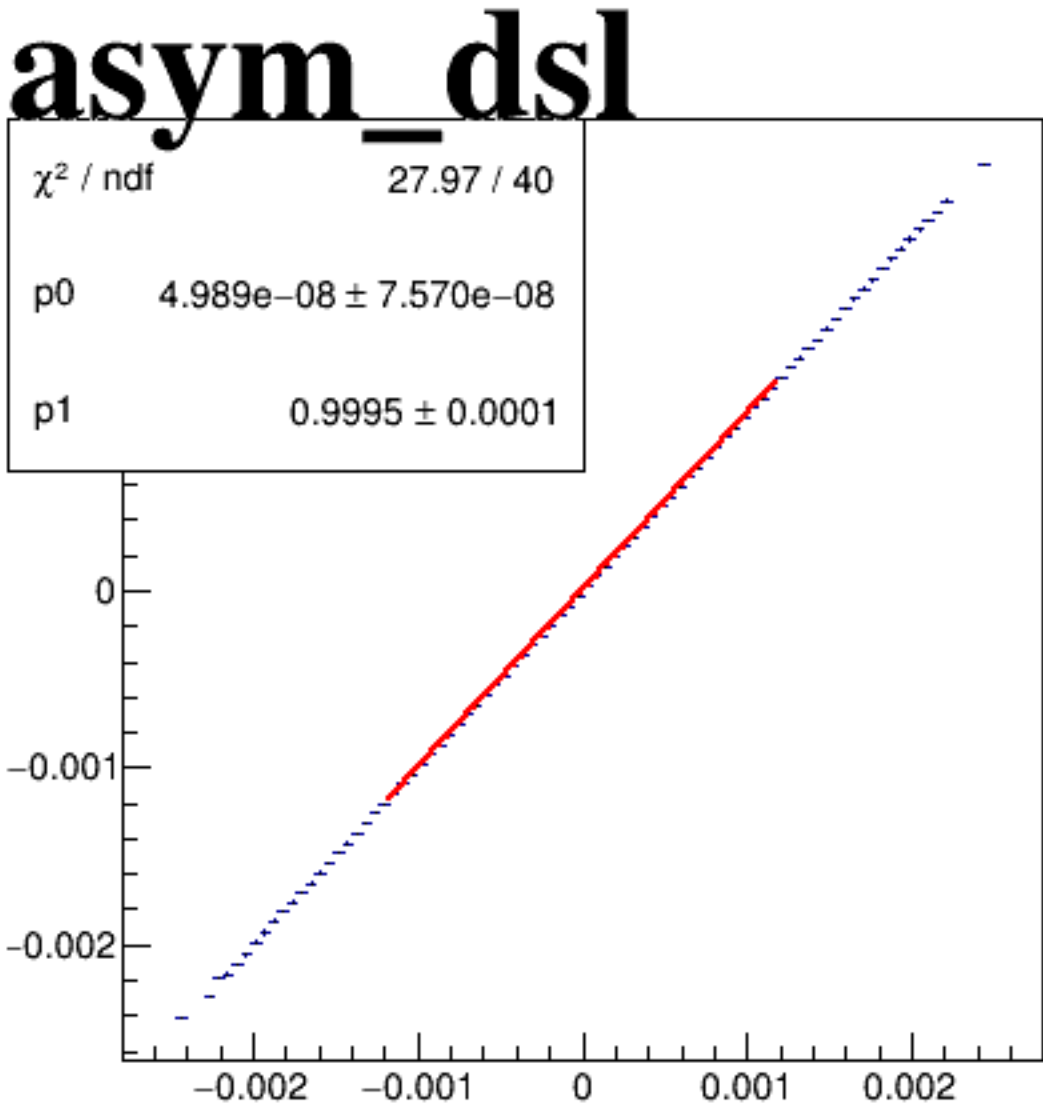
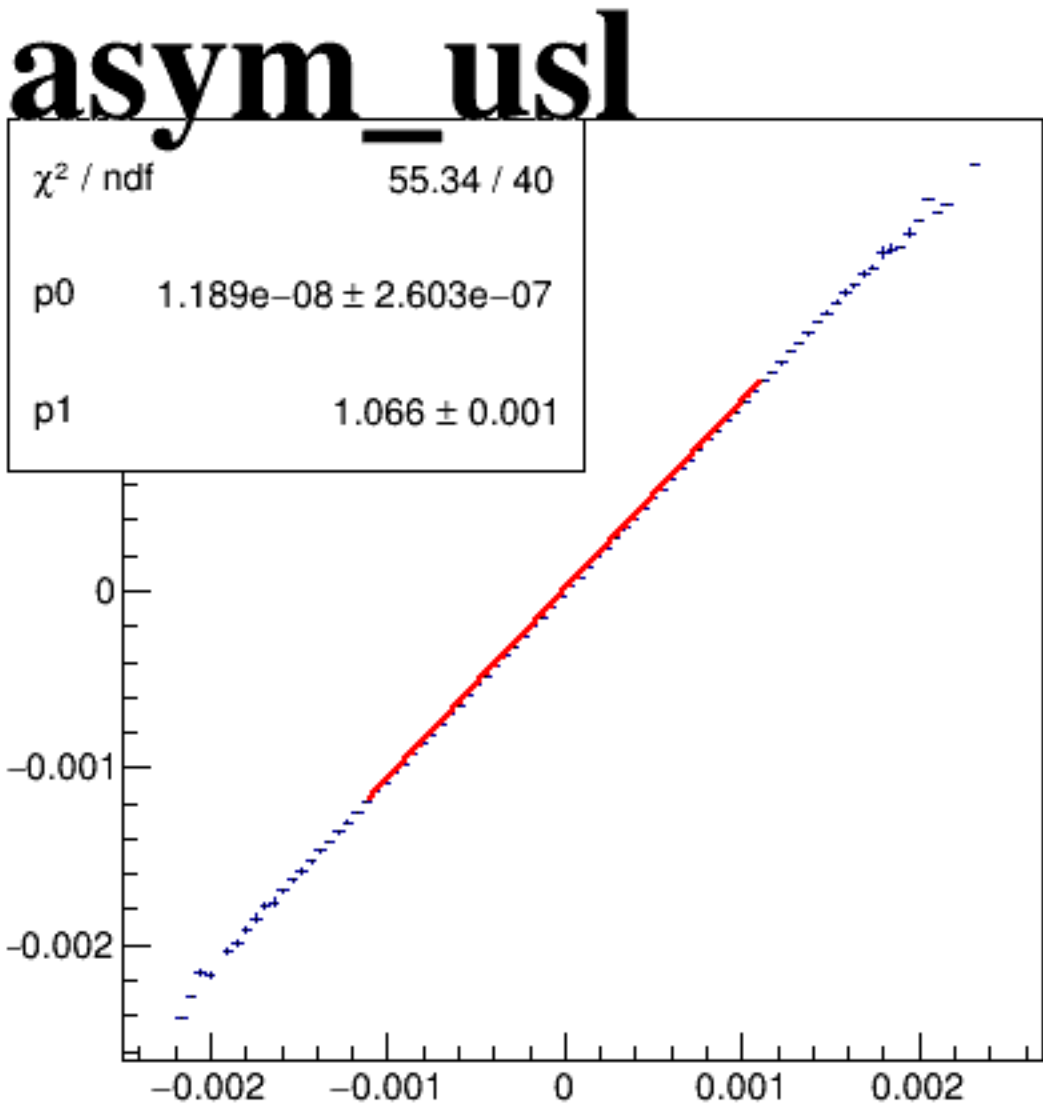


run4577_000_asym_maindet_mutual_correlation-COLZ.png

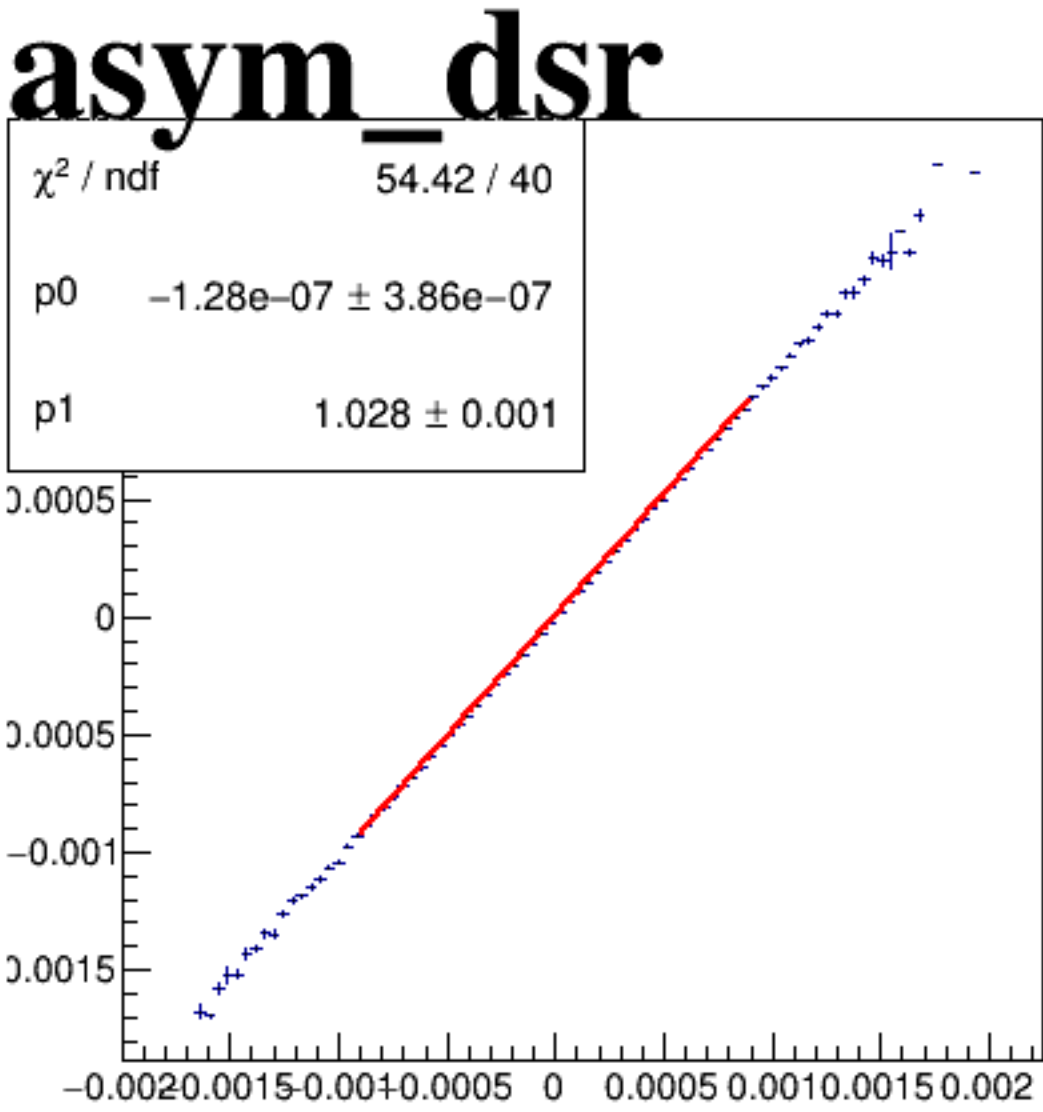
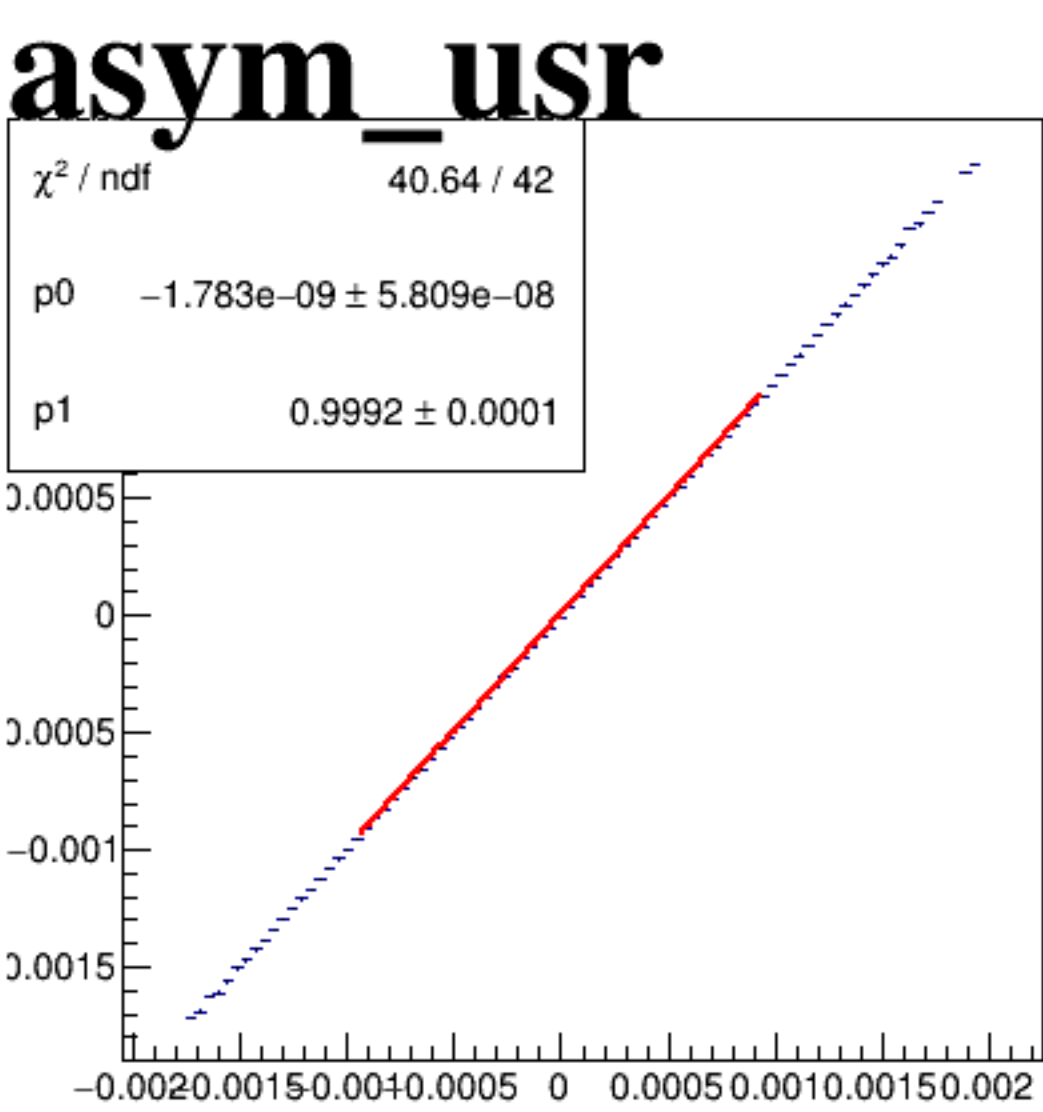
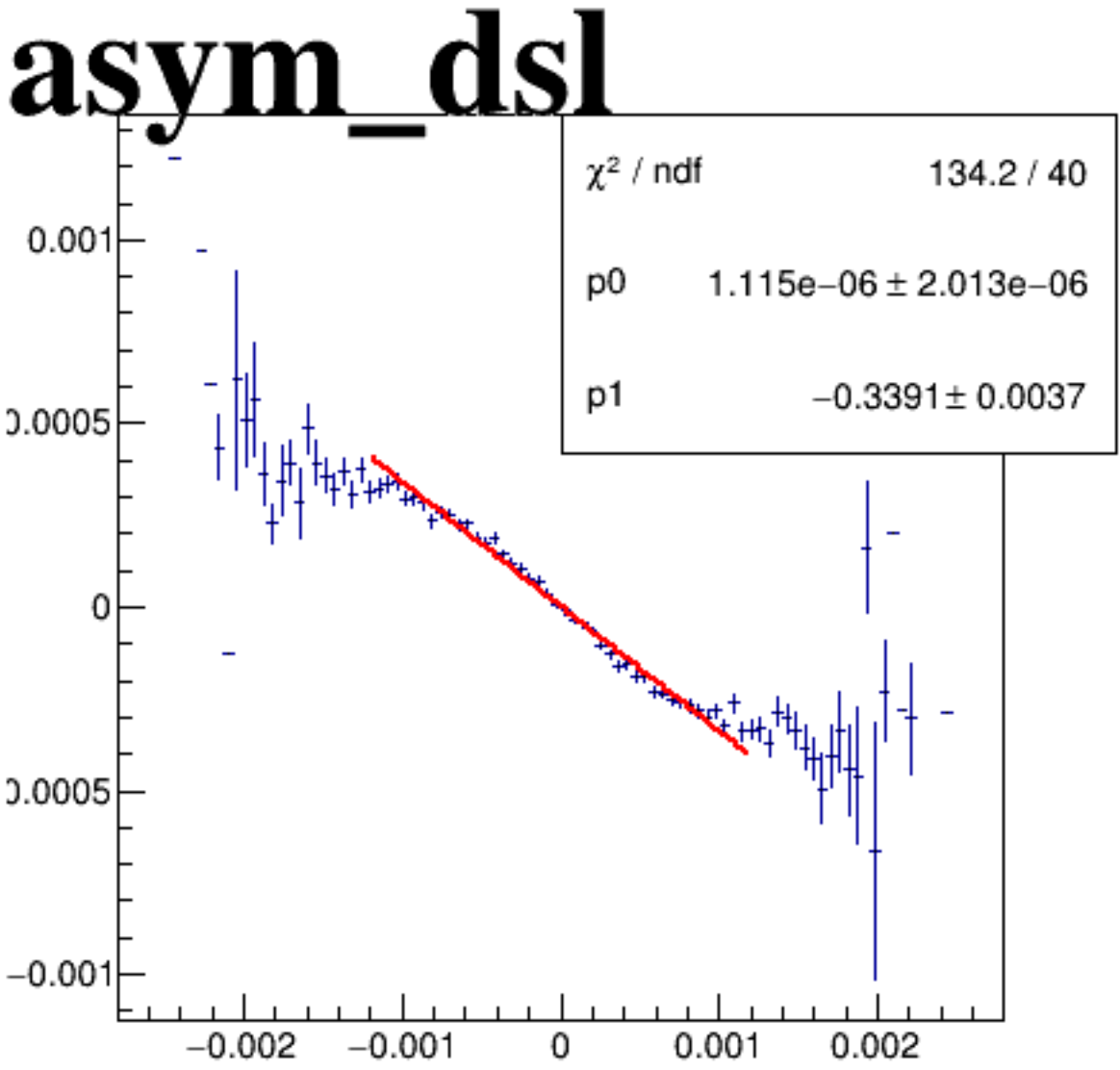
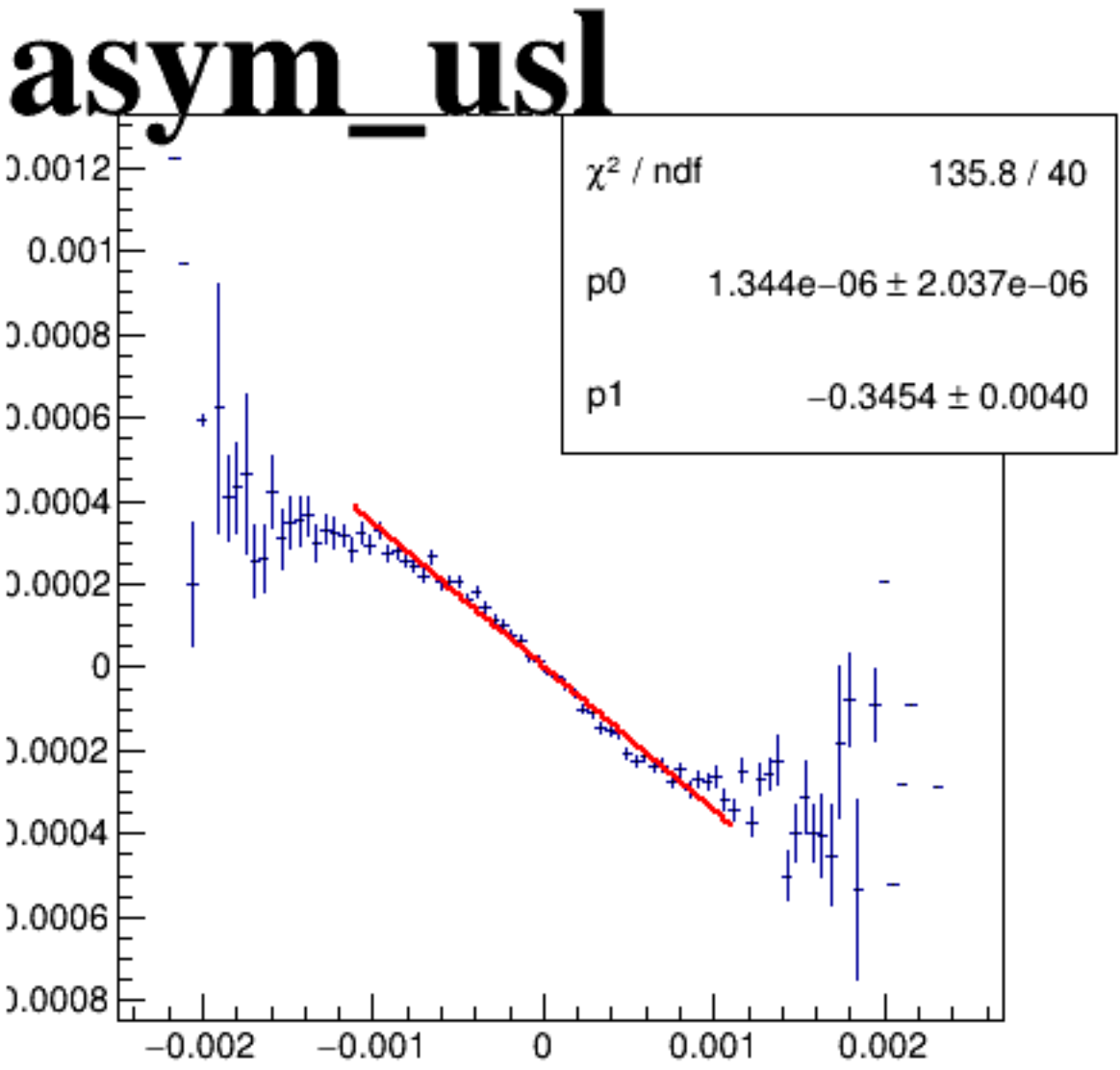
asym_usl



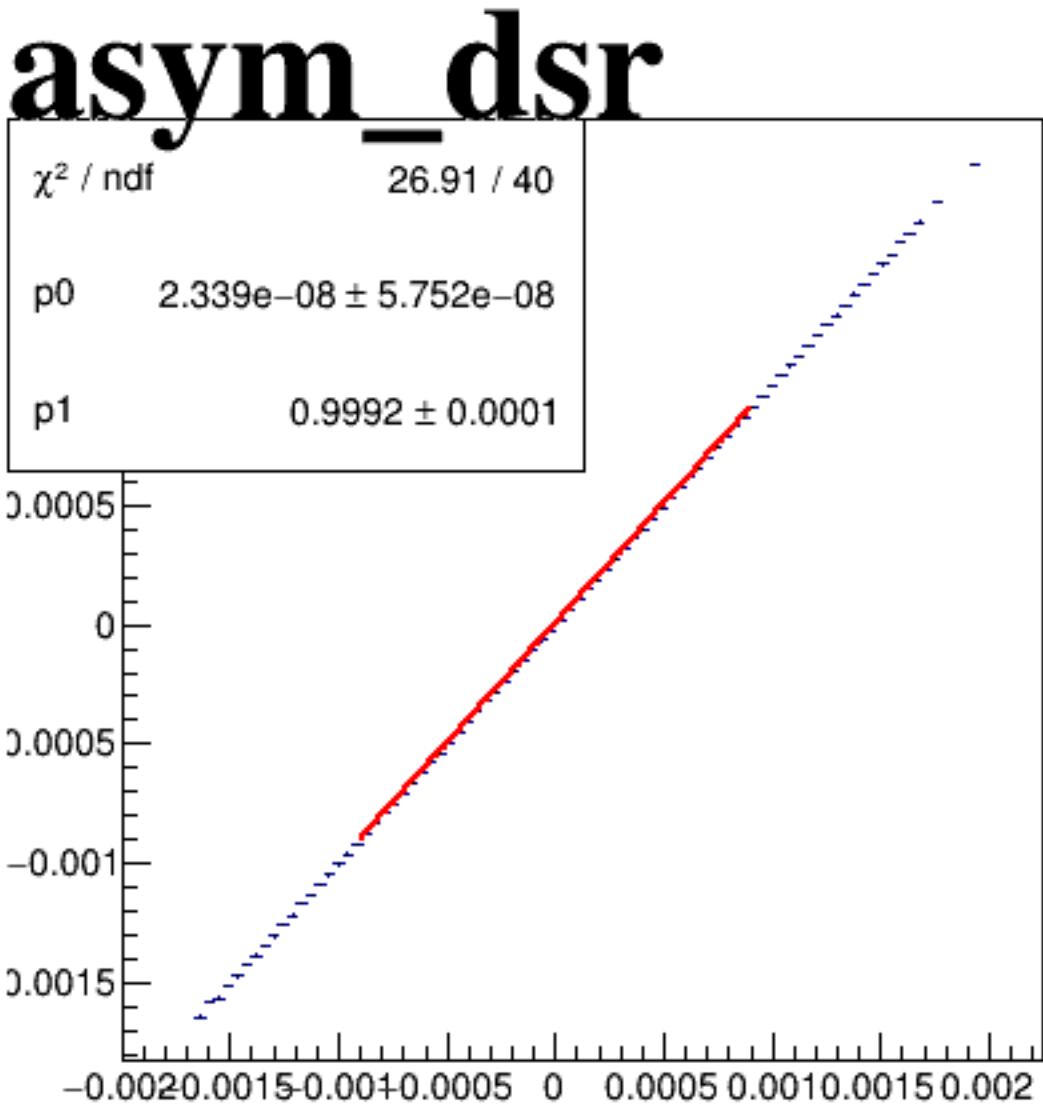
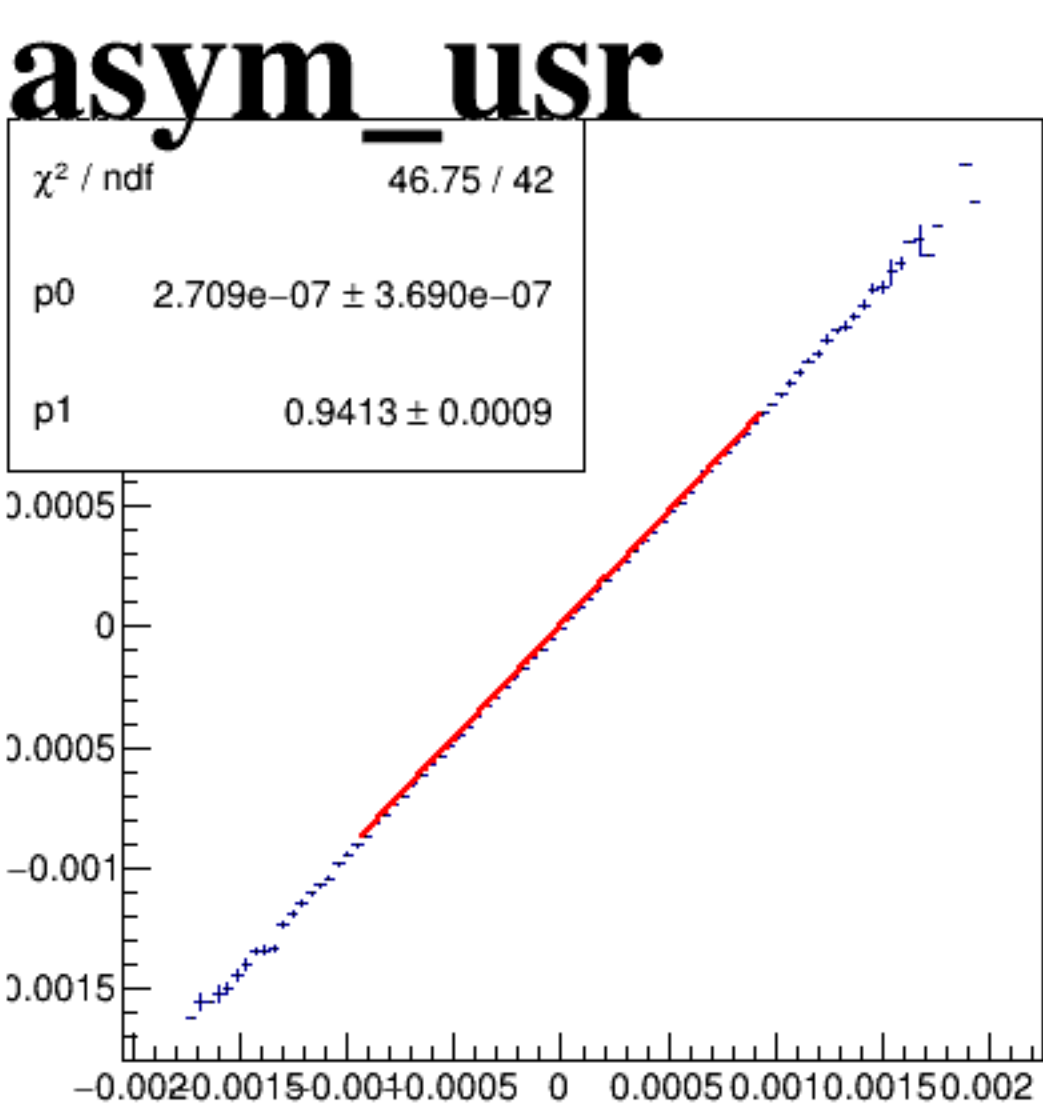
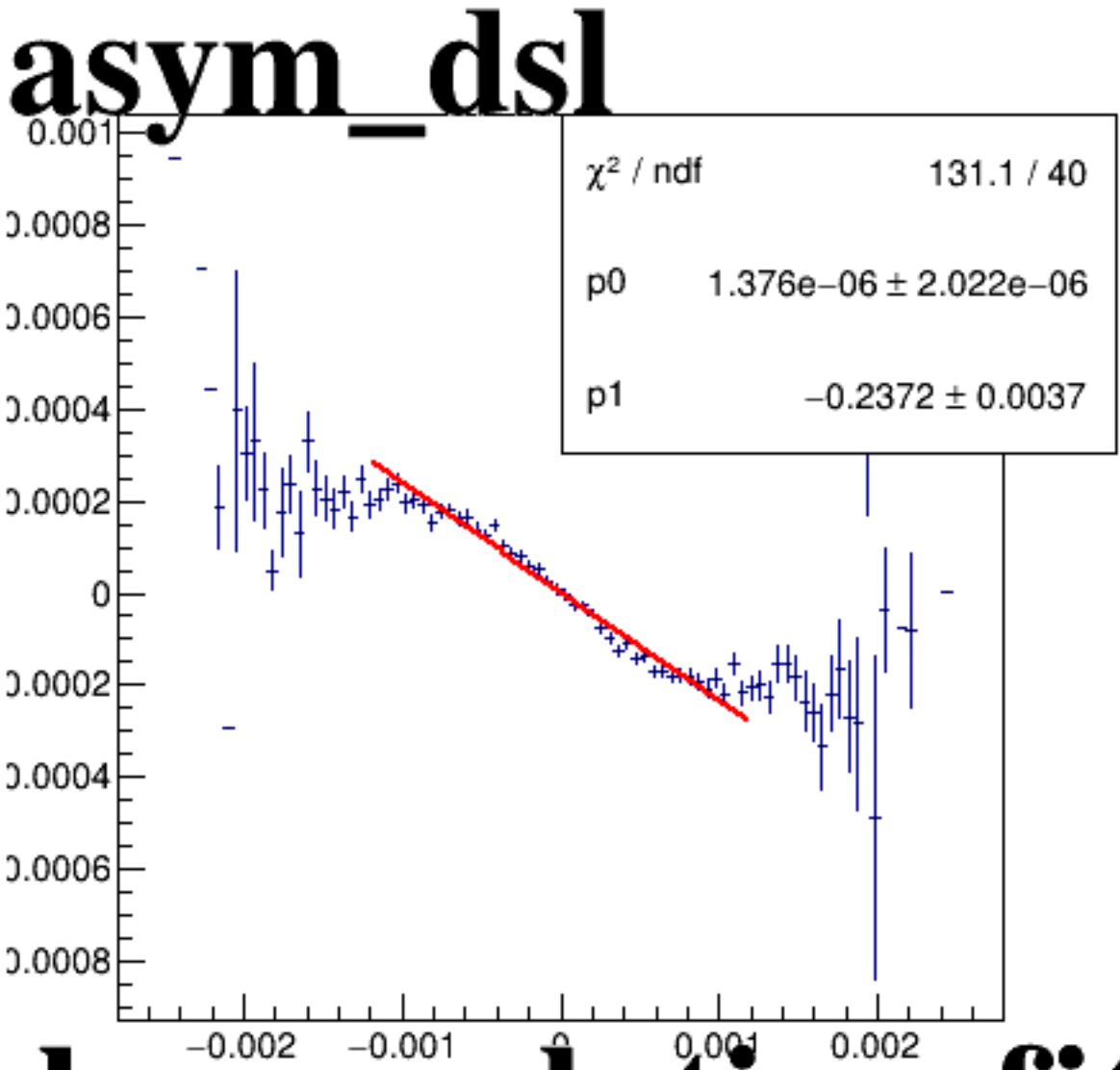
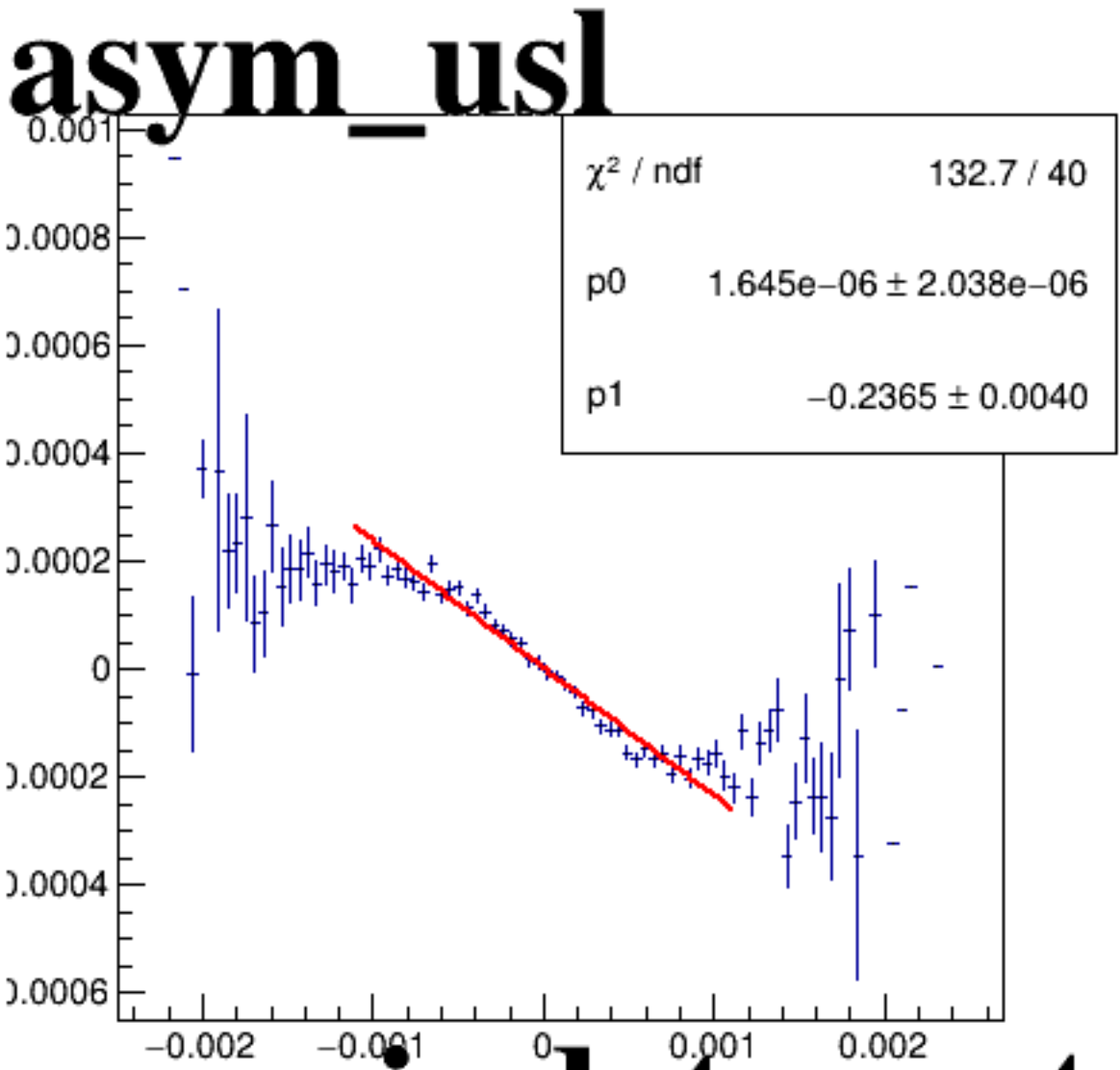
asym_dsl



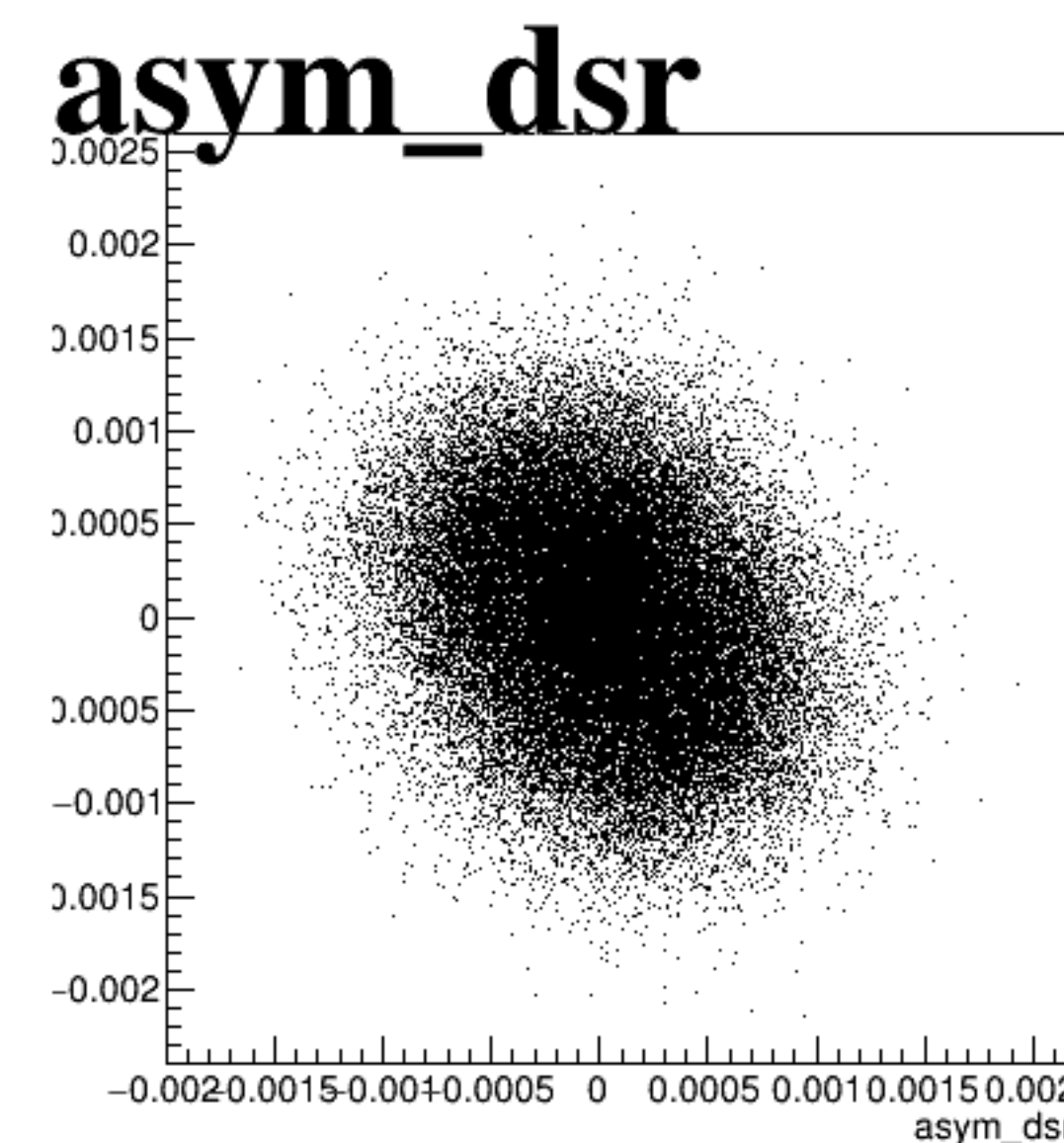
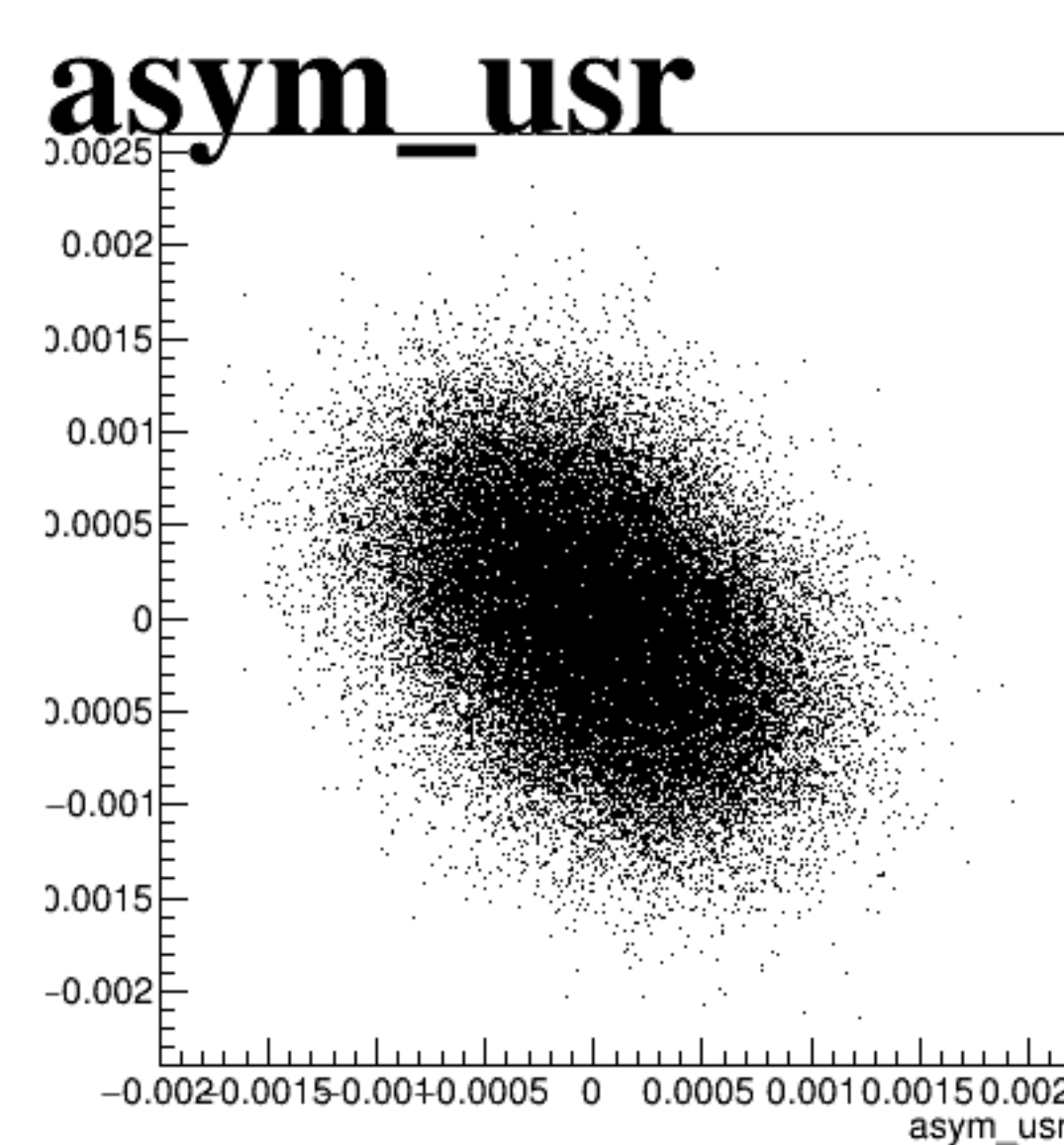
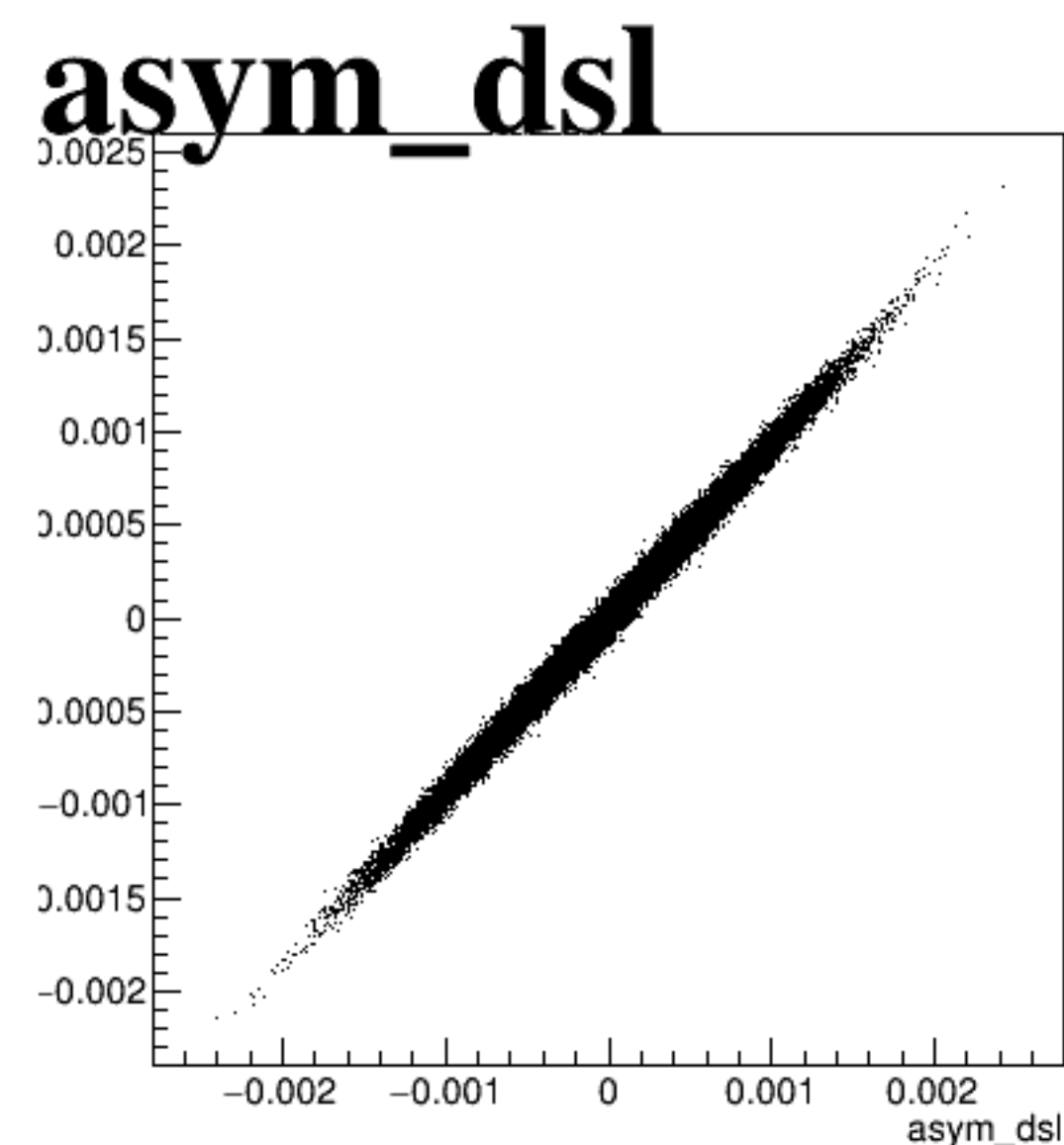
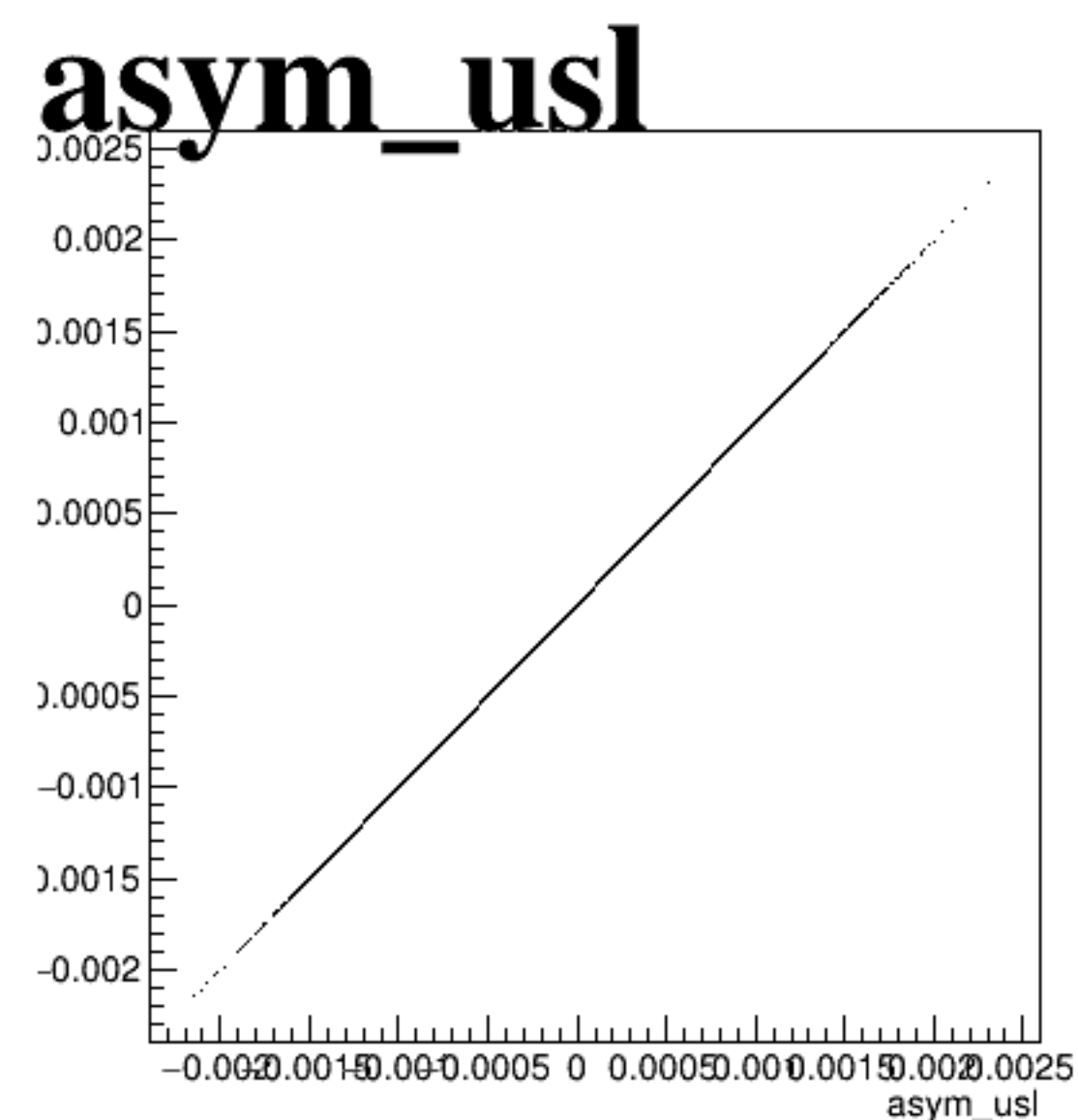
asym_usr



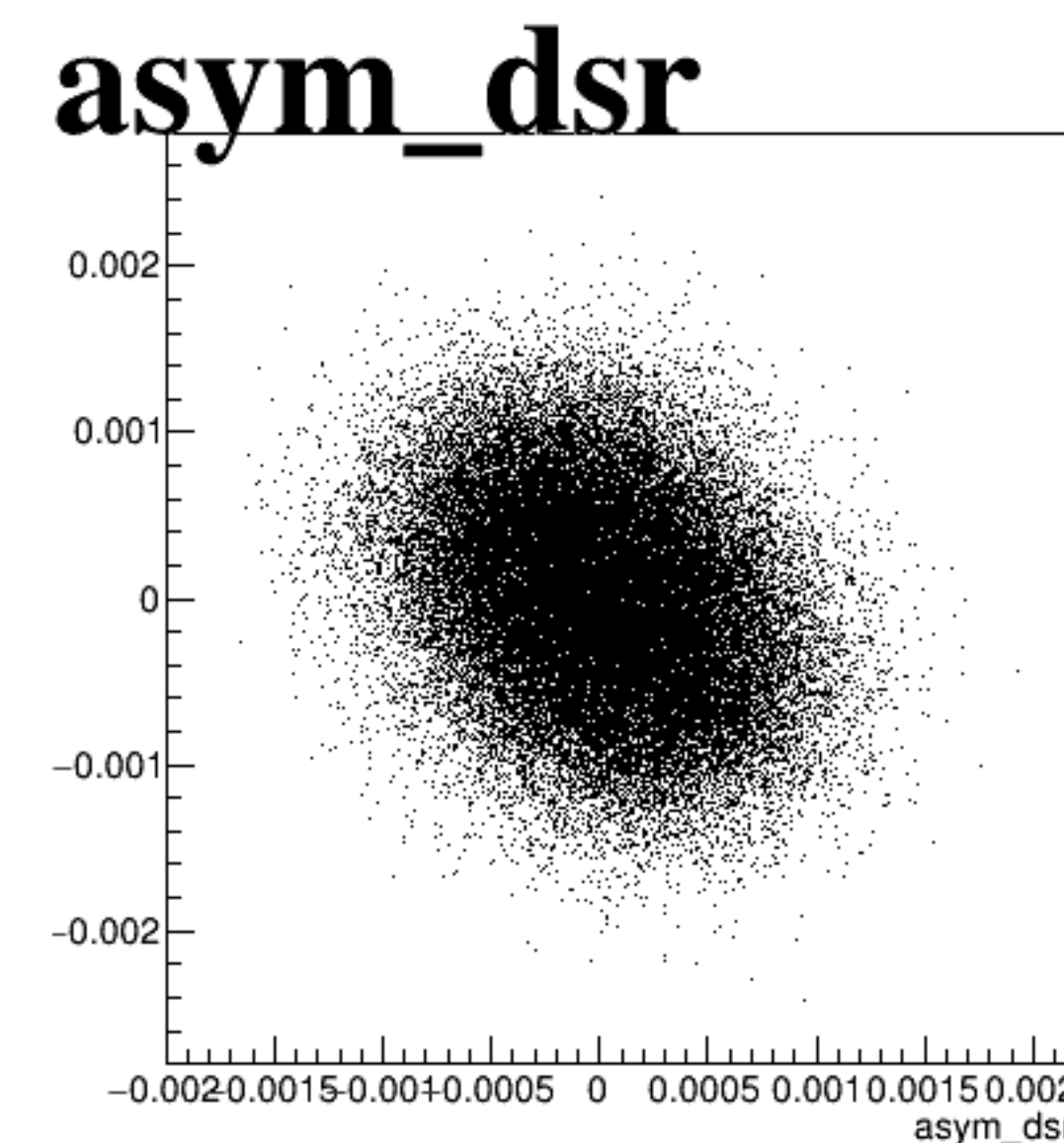
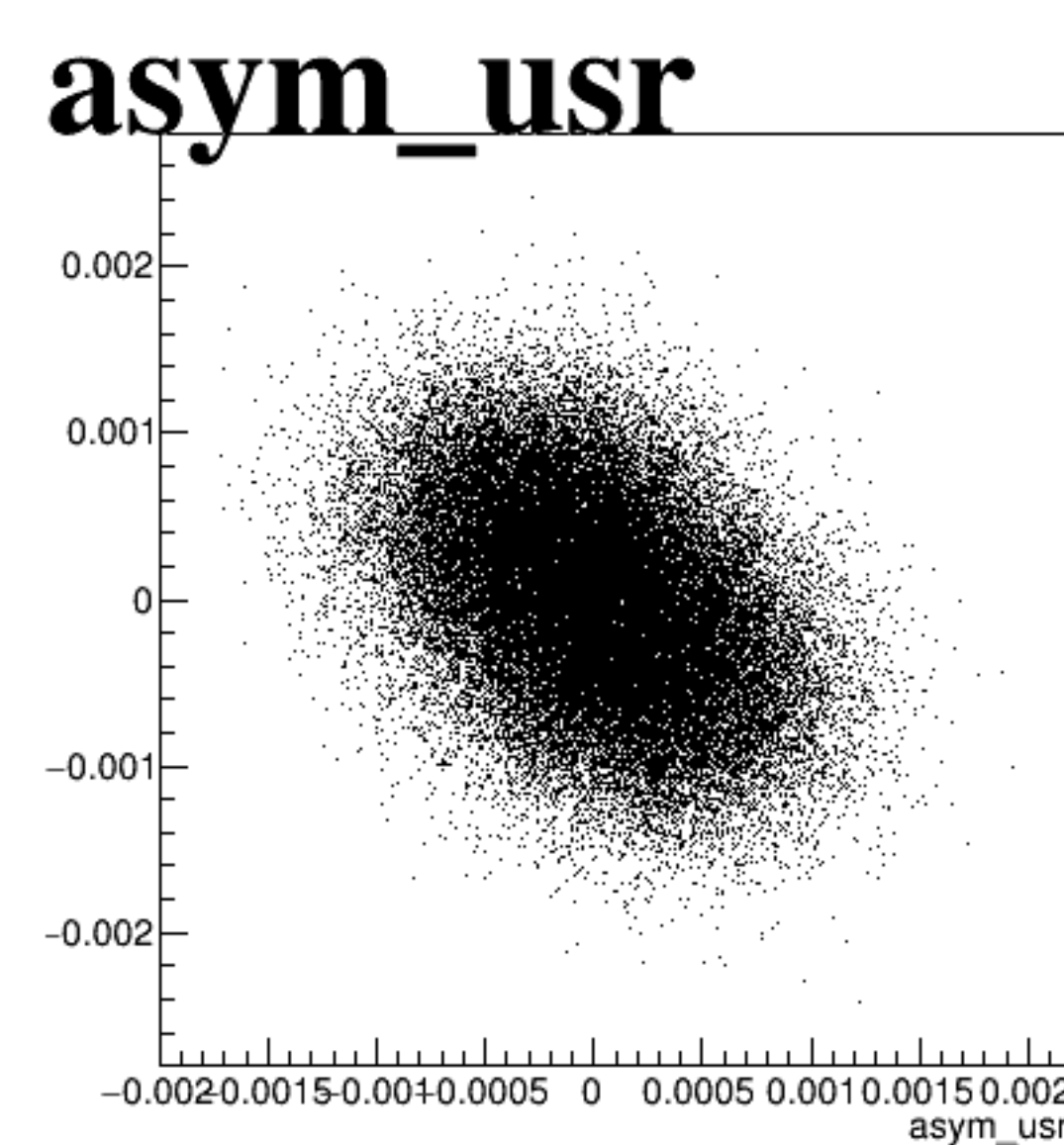
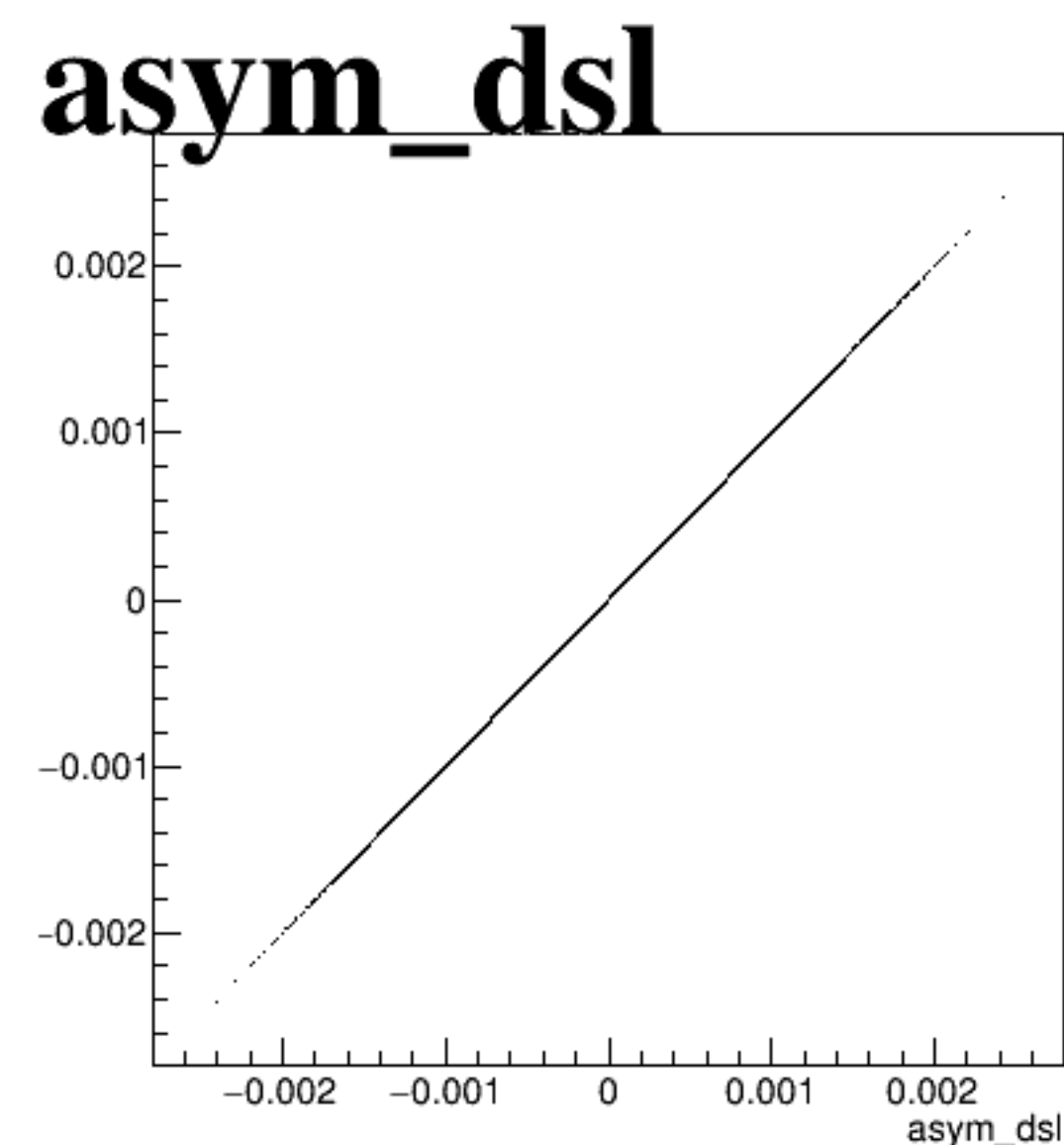
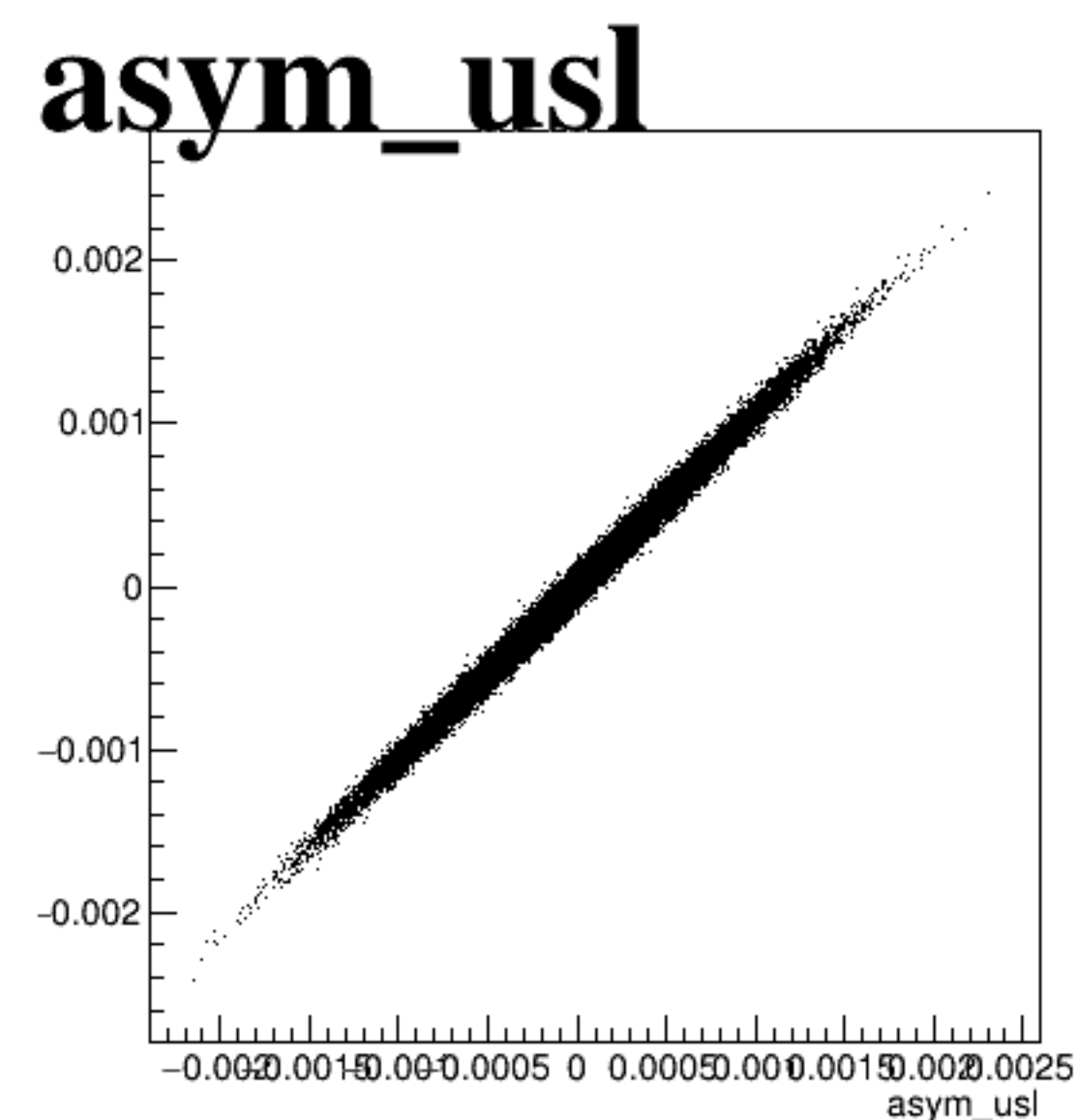
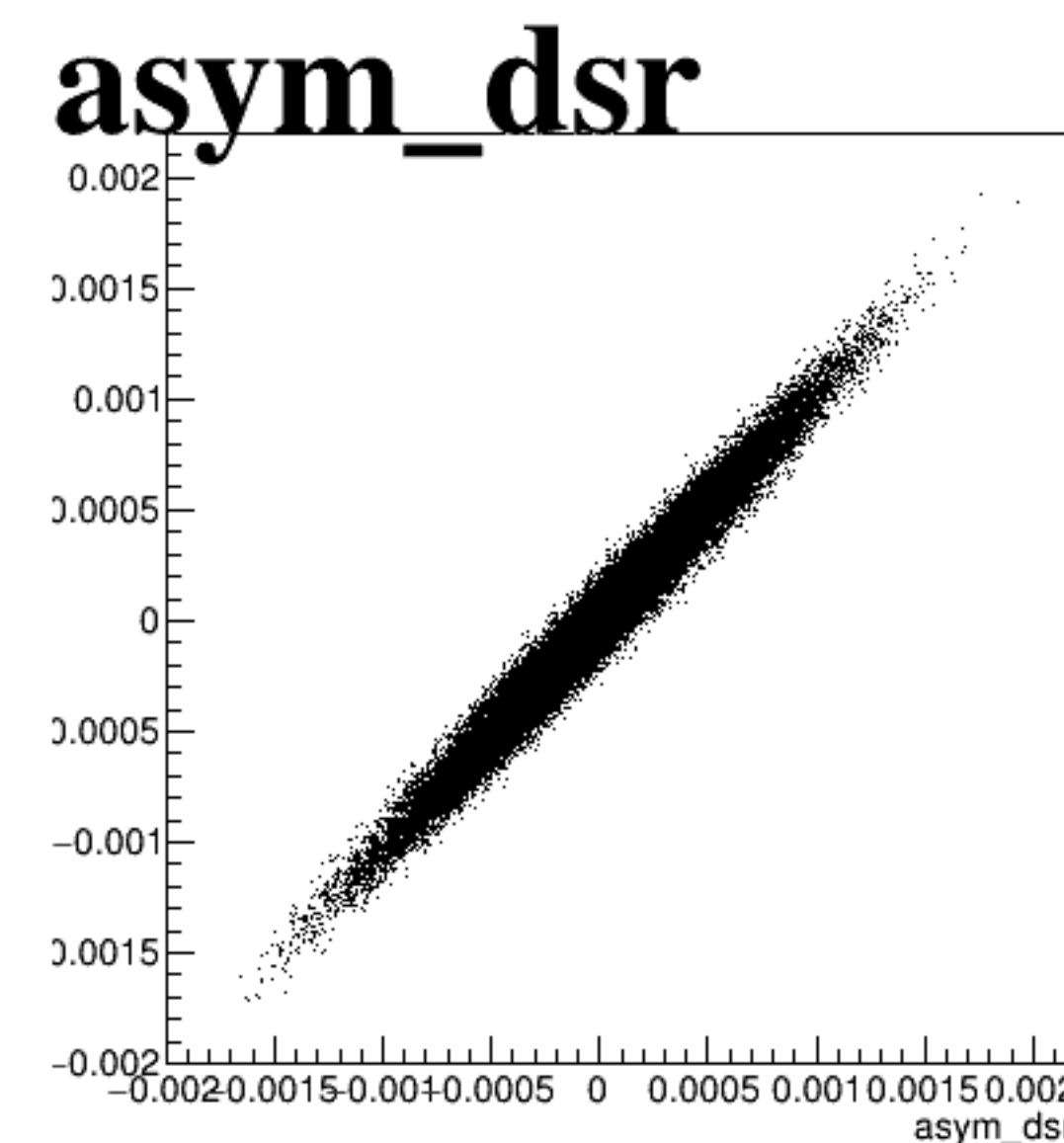
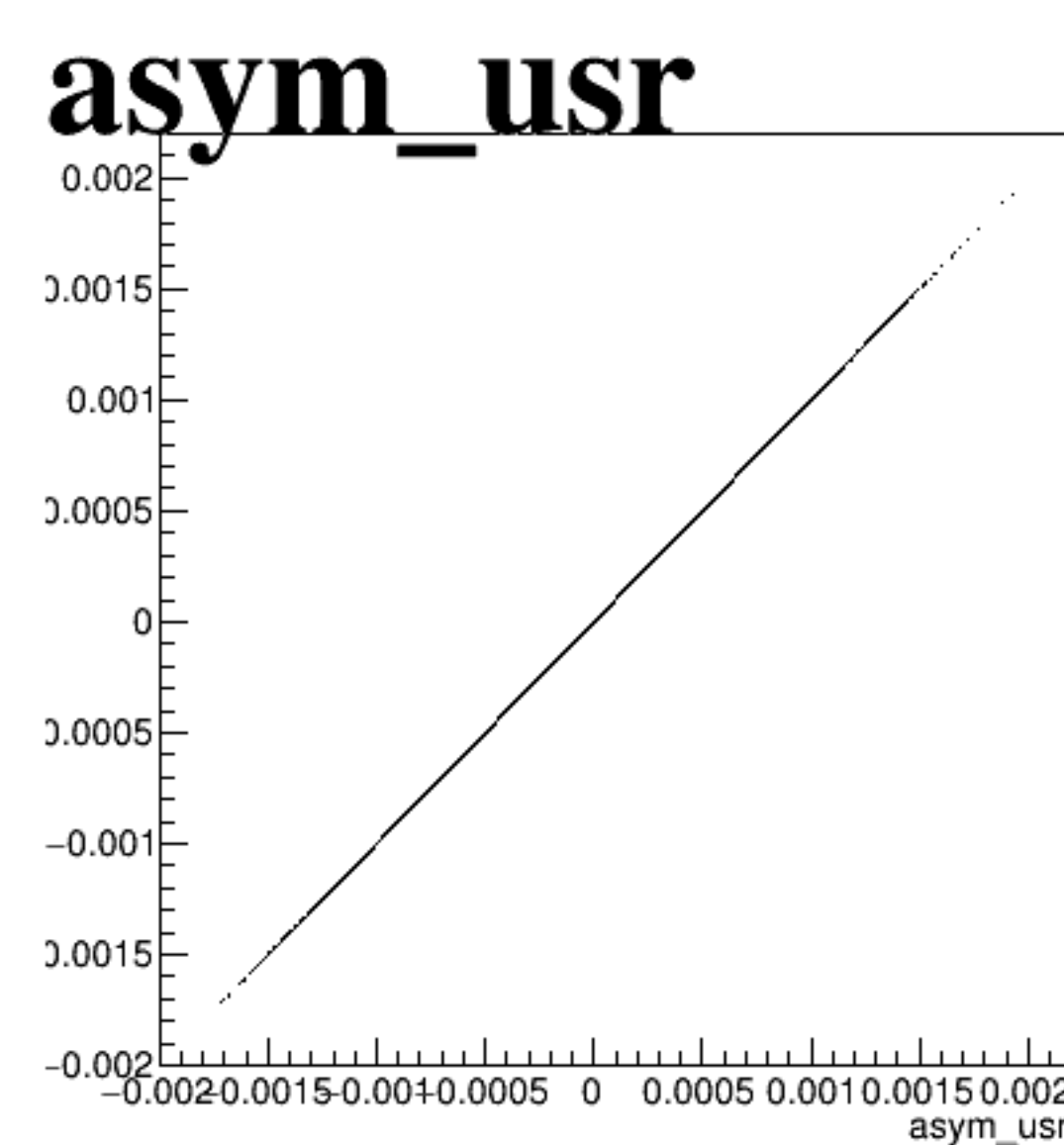
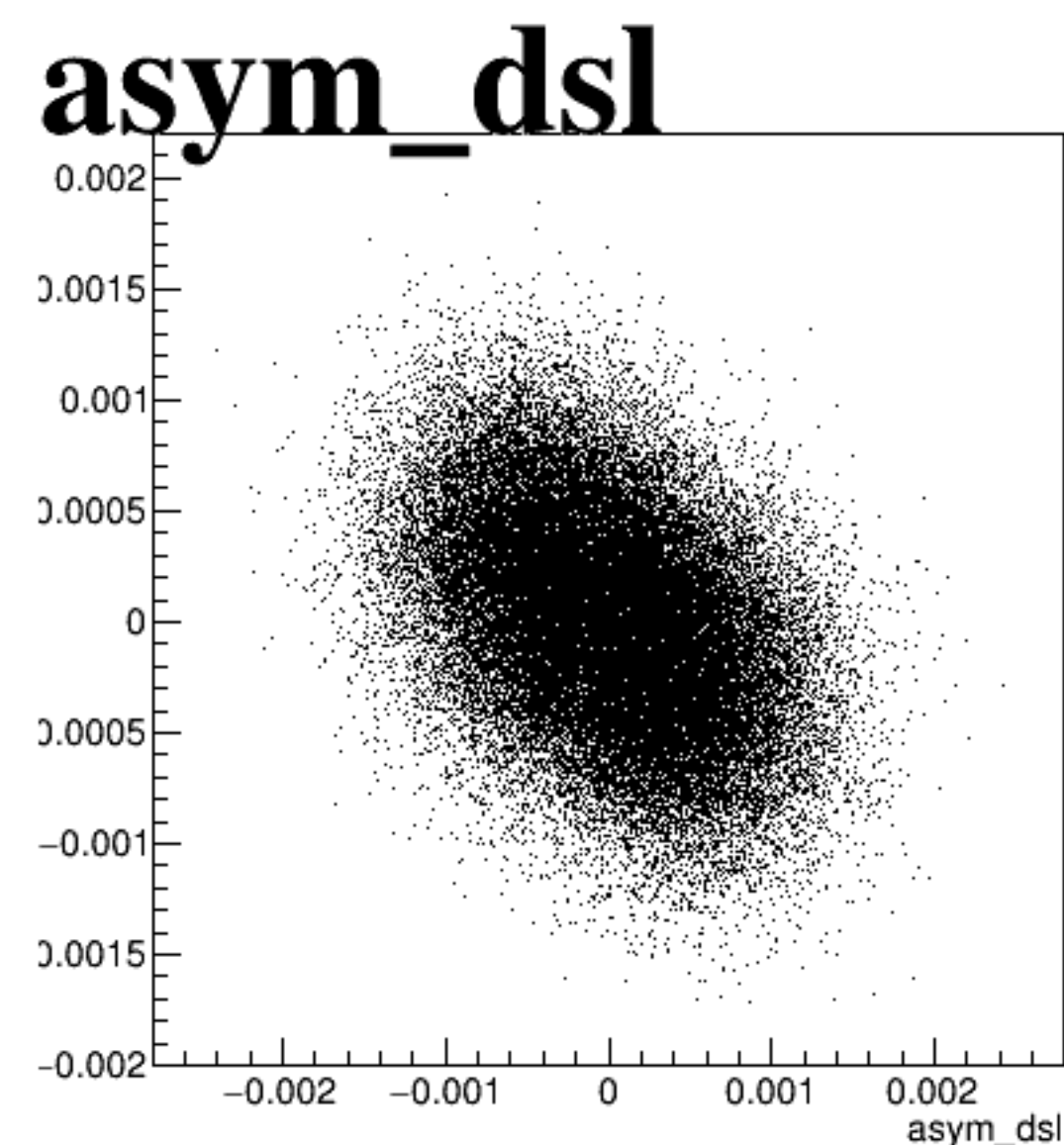
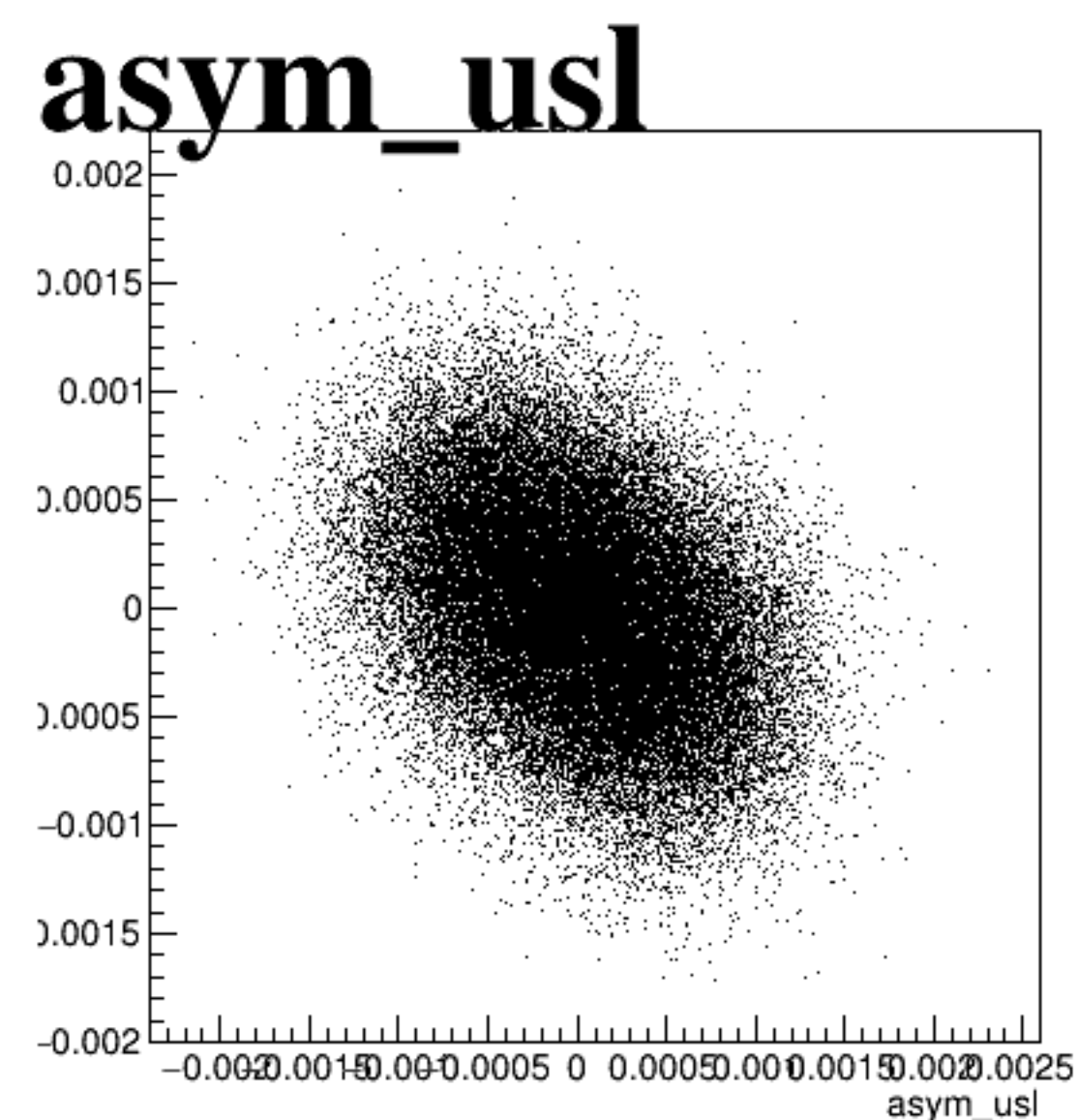
asym_dsr



asym_usl



asym_dsl

**asym_usr****asym_dsr**