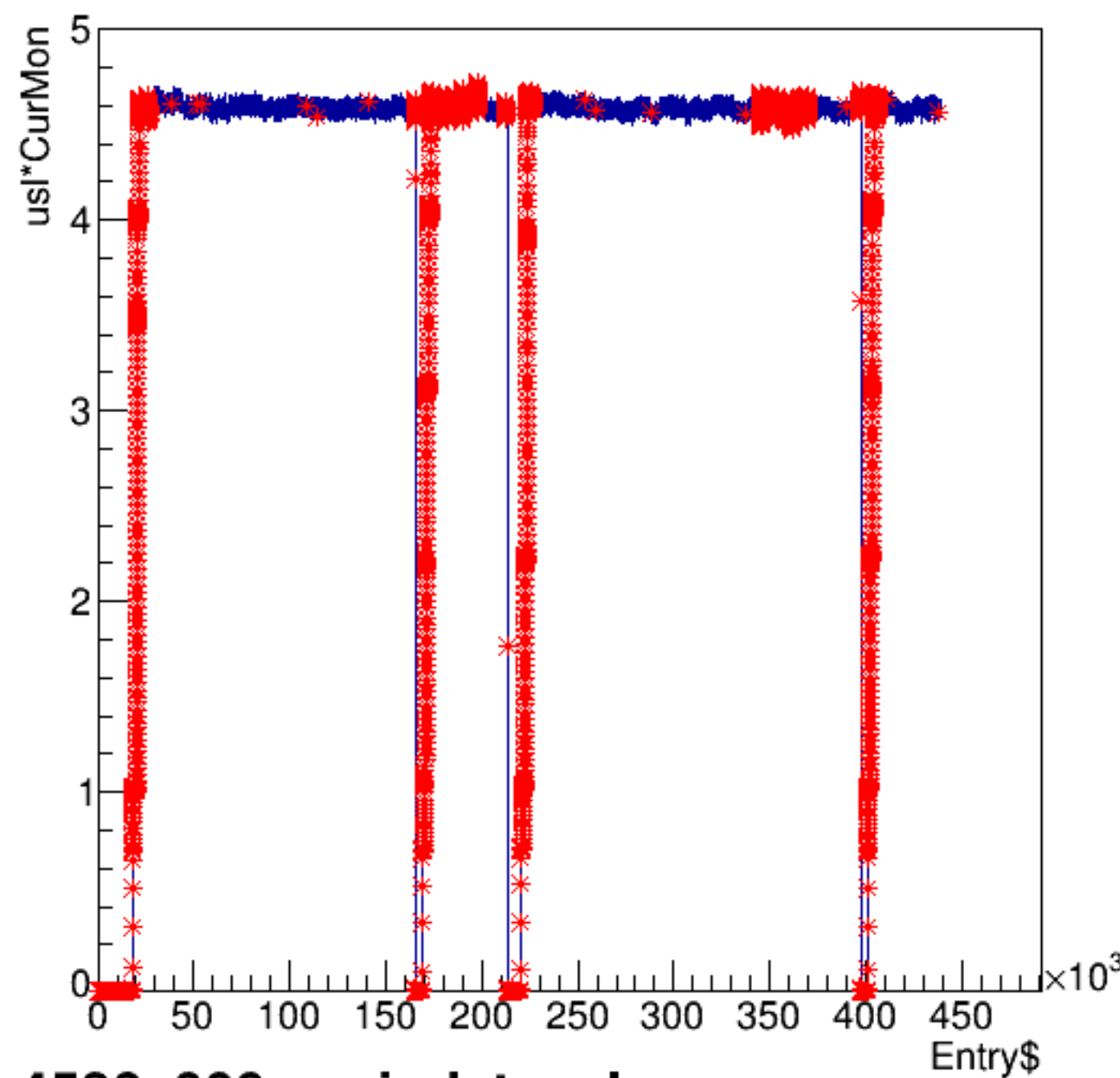
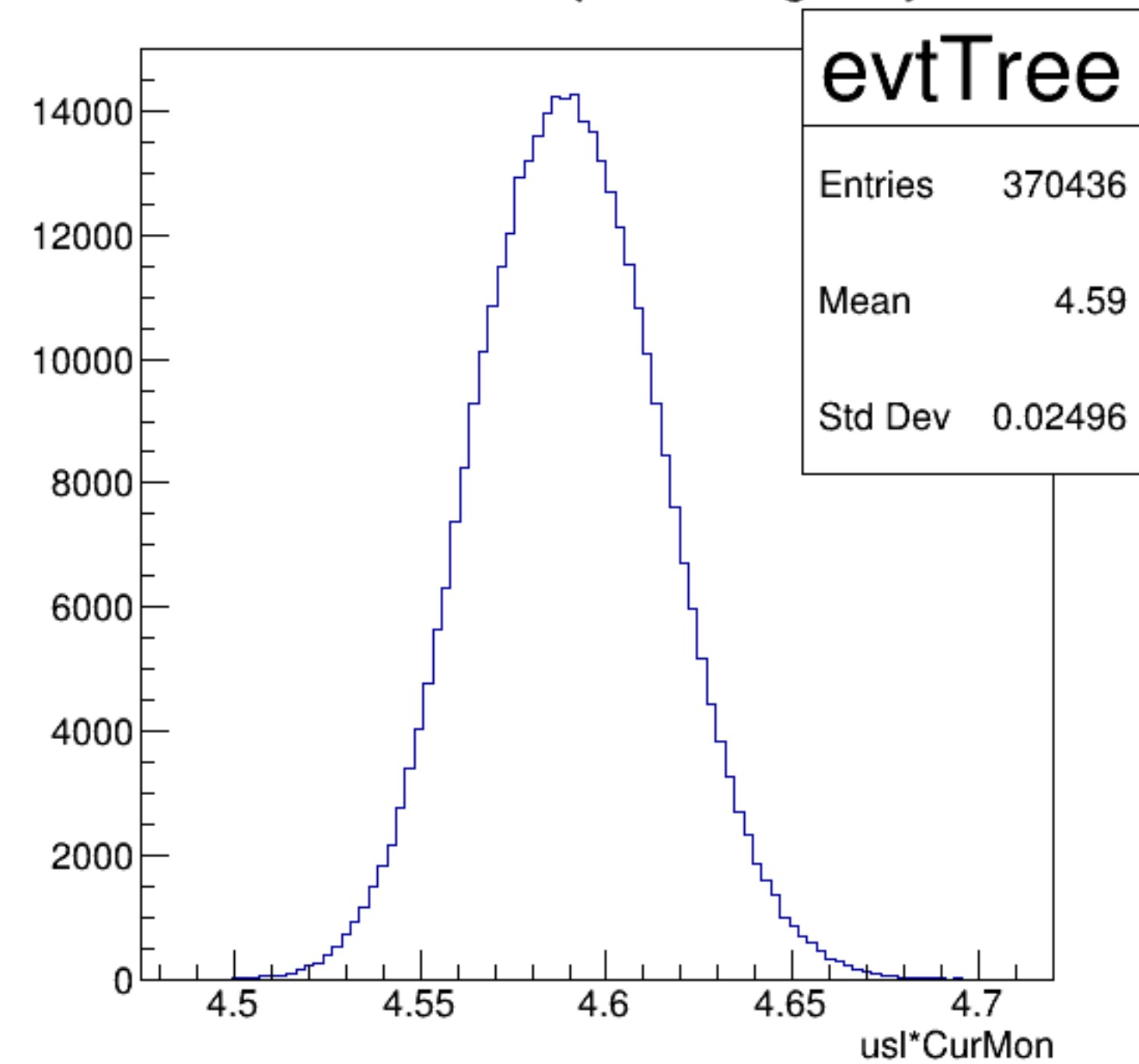


usl\*CurMon:Entry\$

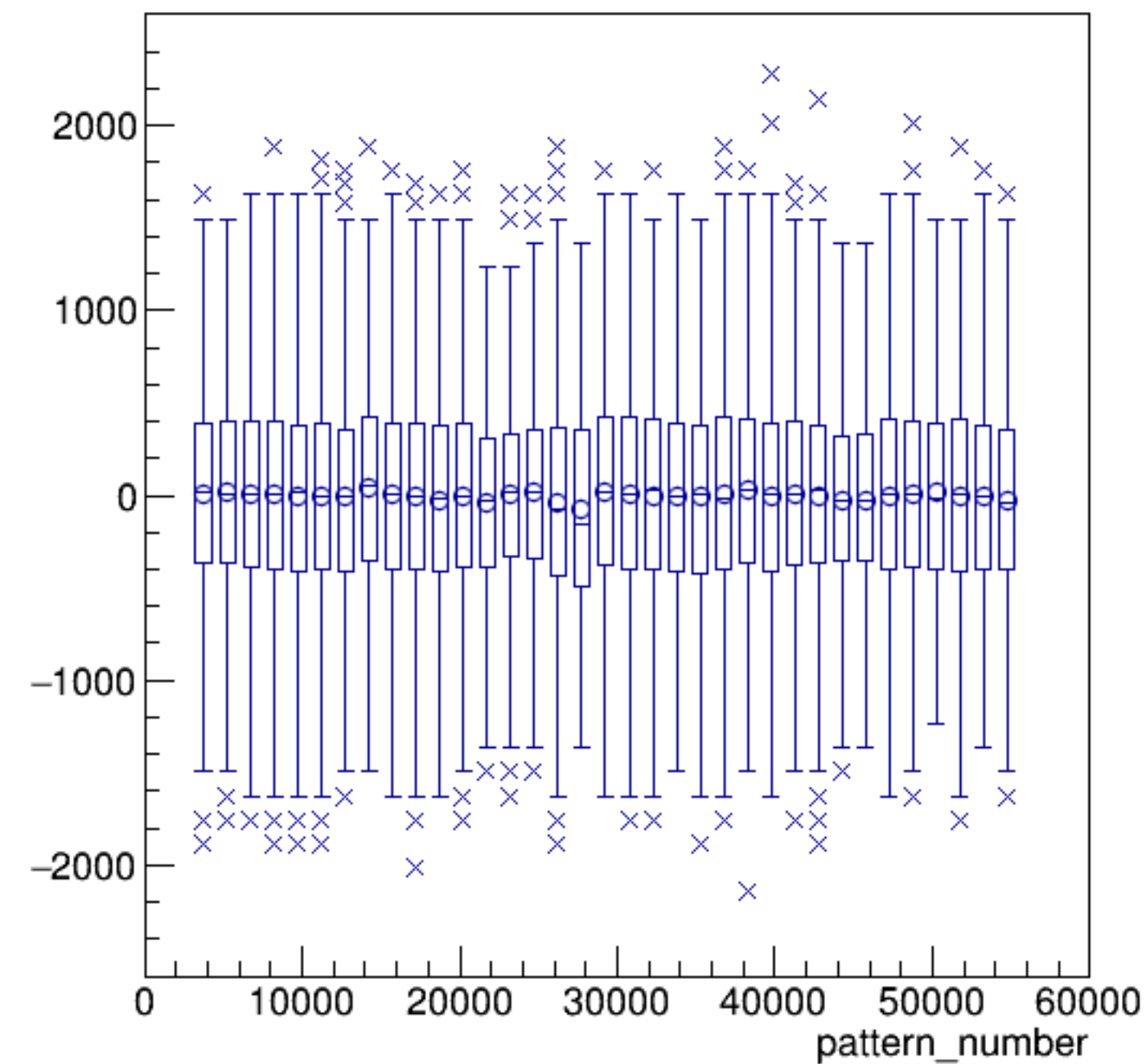


run4586\_000\_maindet\_usl.png

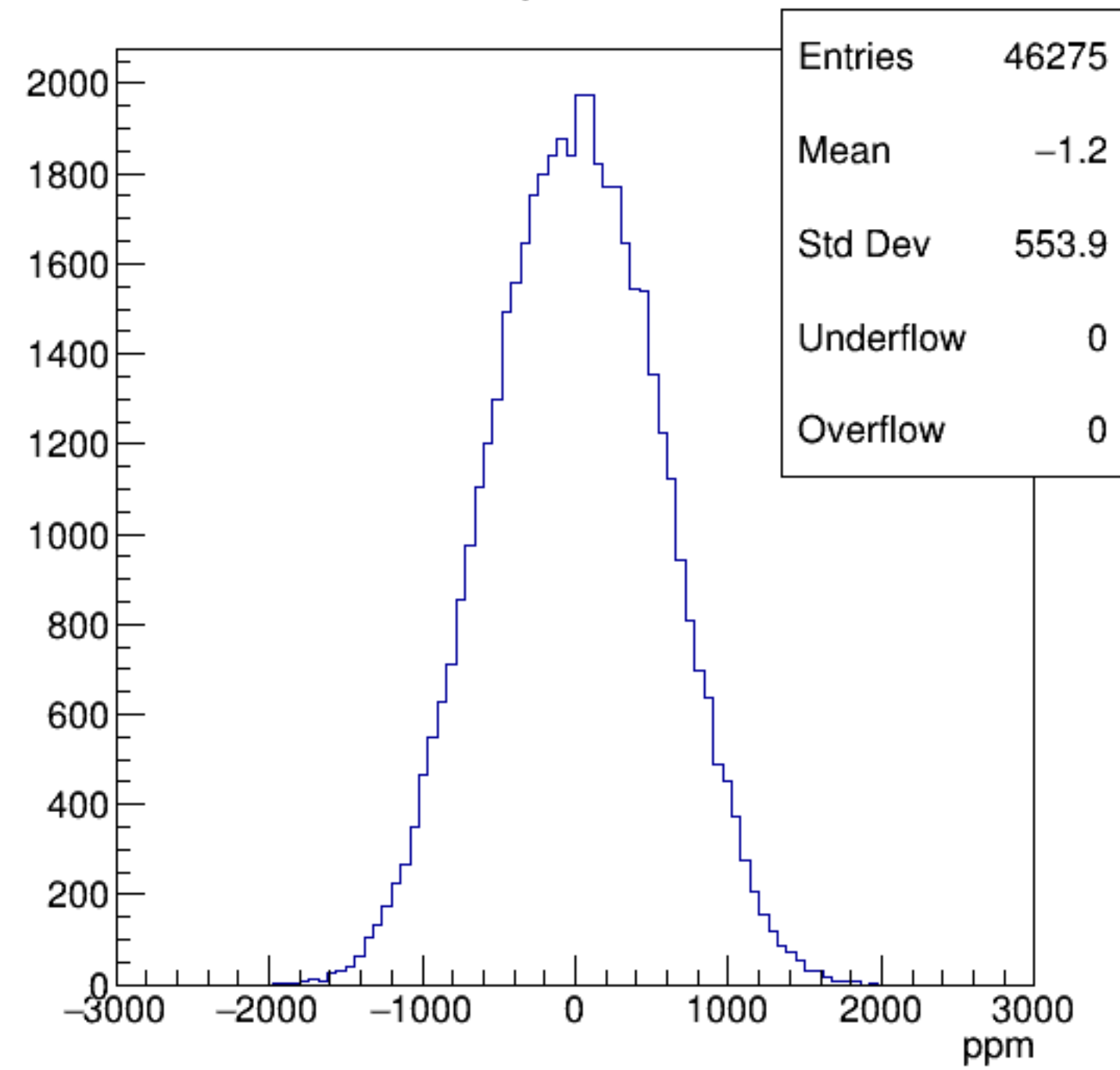
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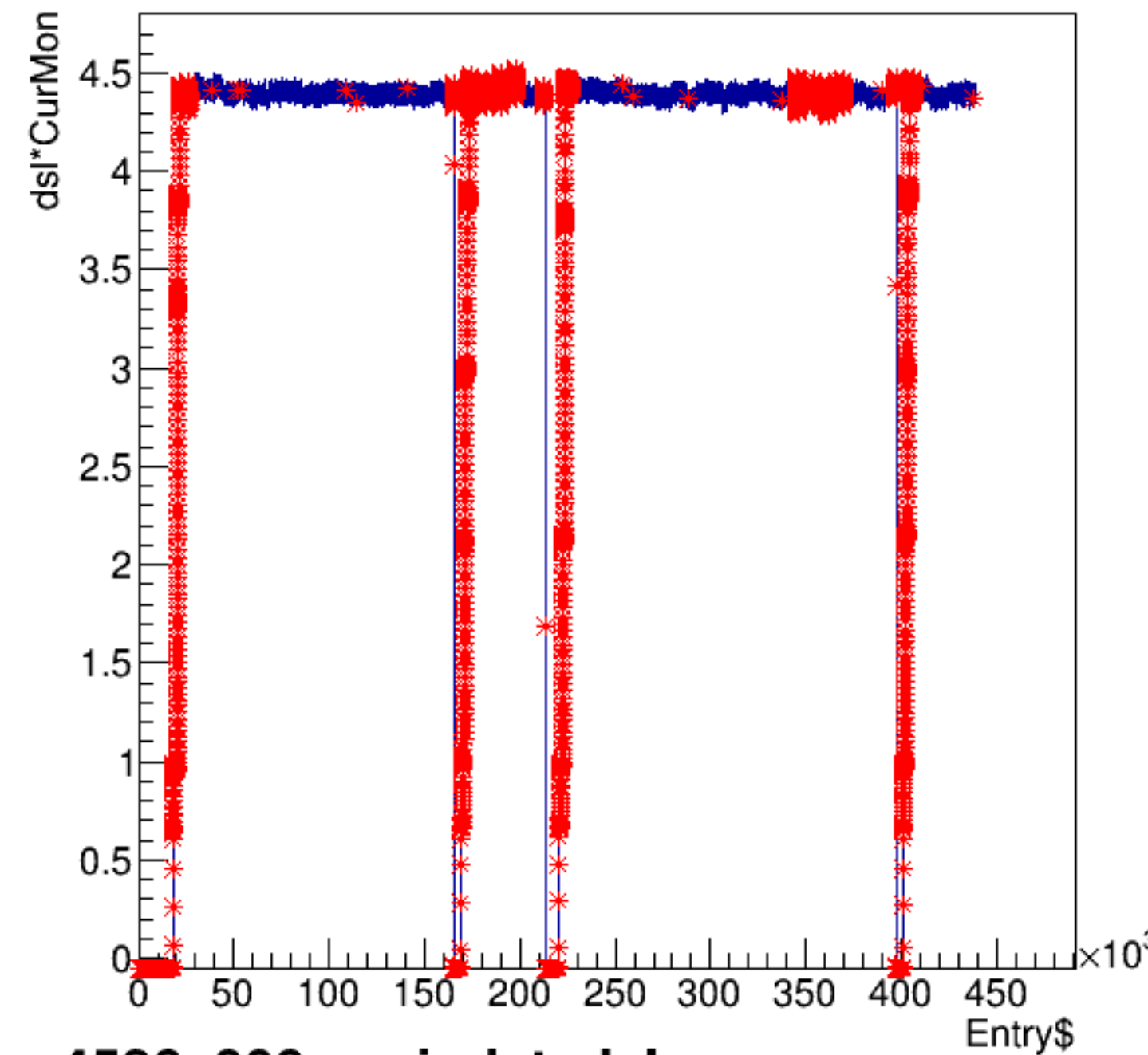
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asym\_usl

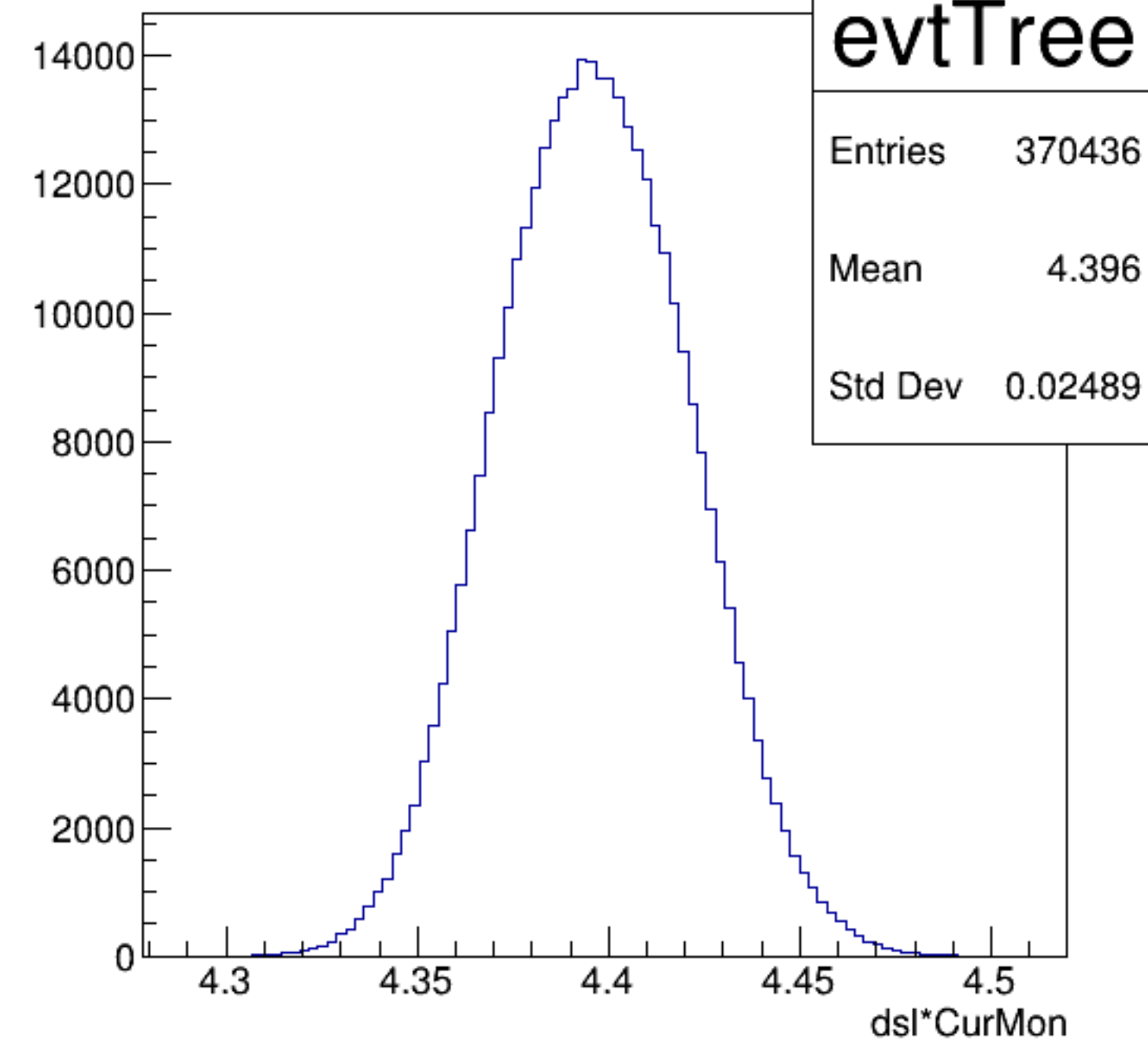


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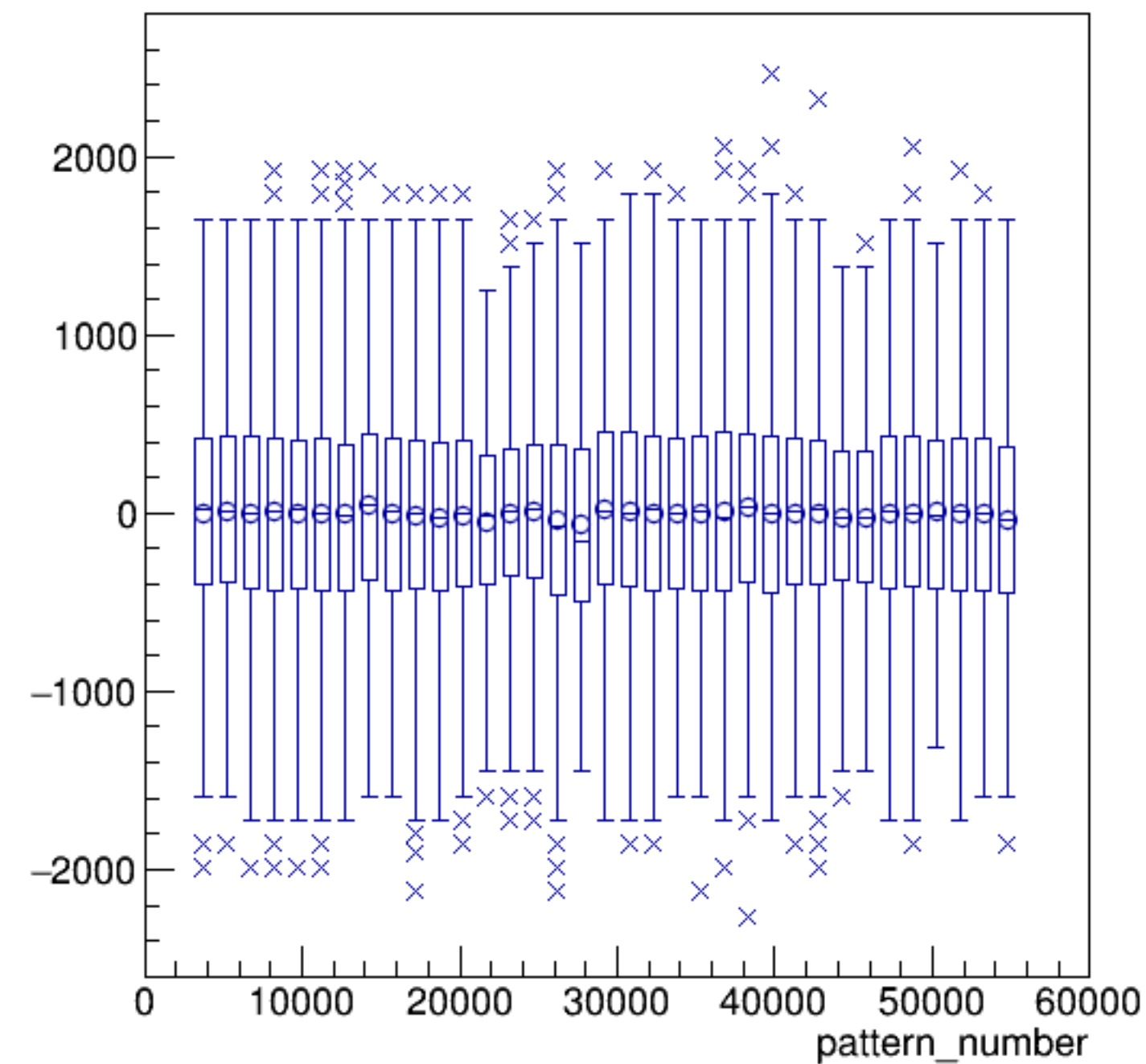


run4586\_000\_maindet\_dsl.png

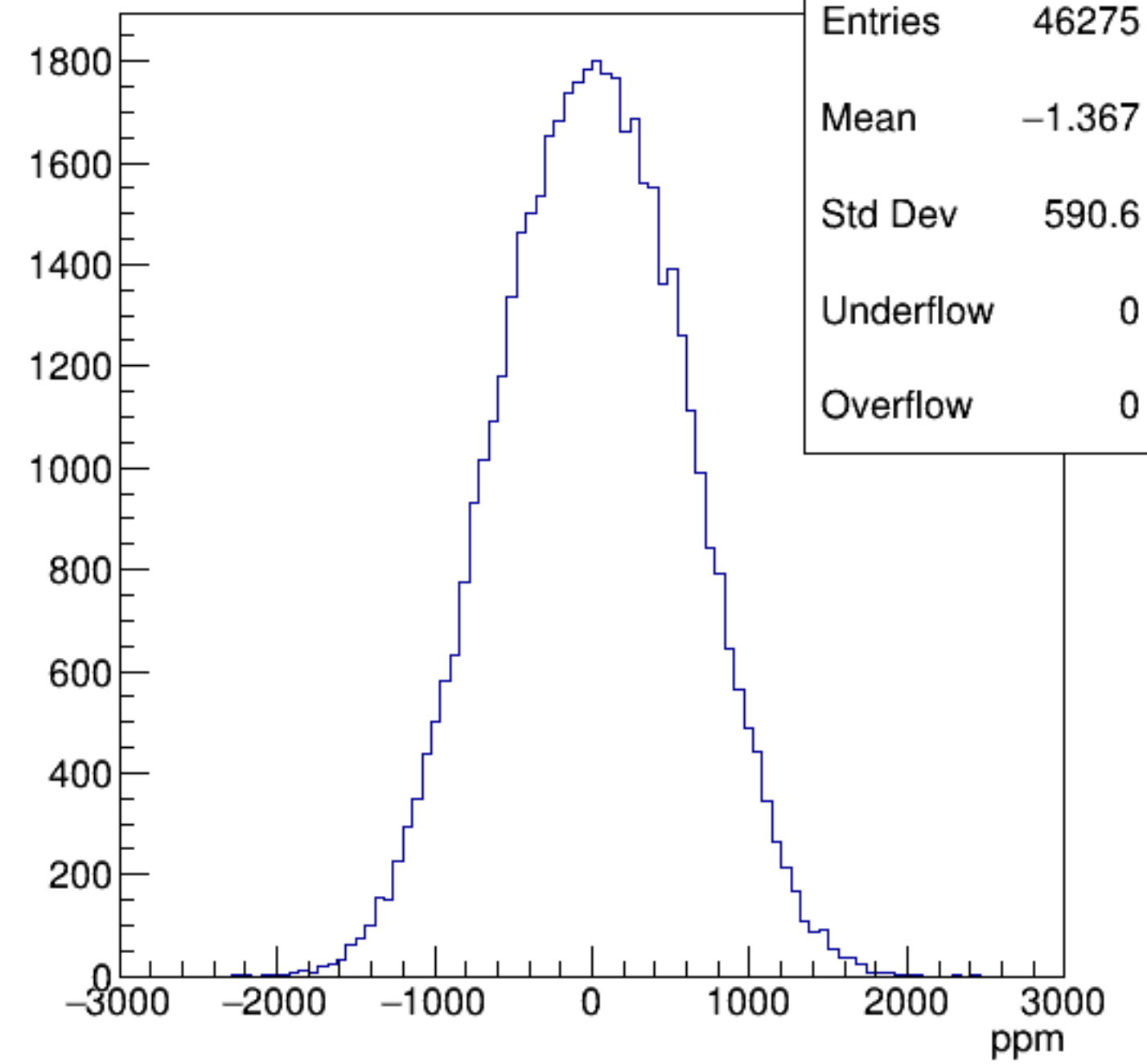
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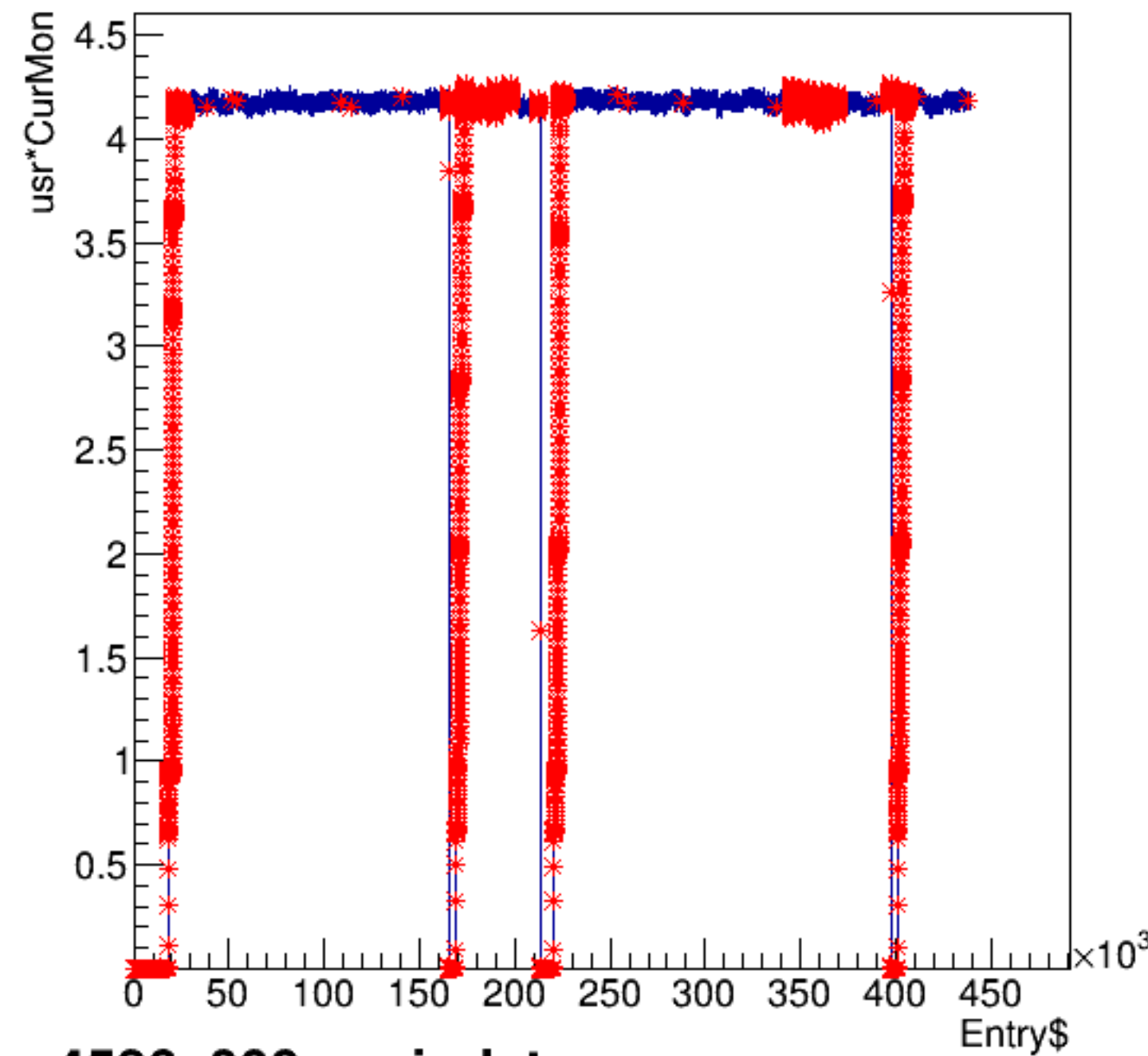
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asym\_dsl

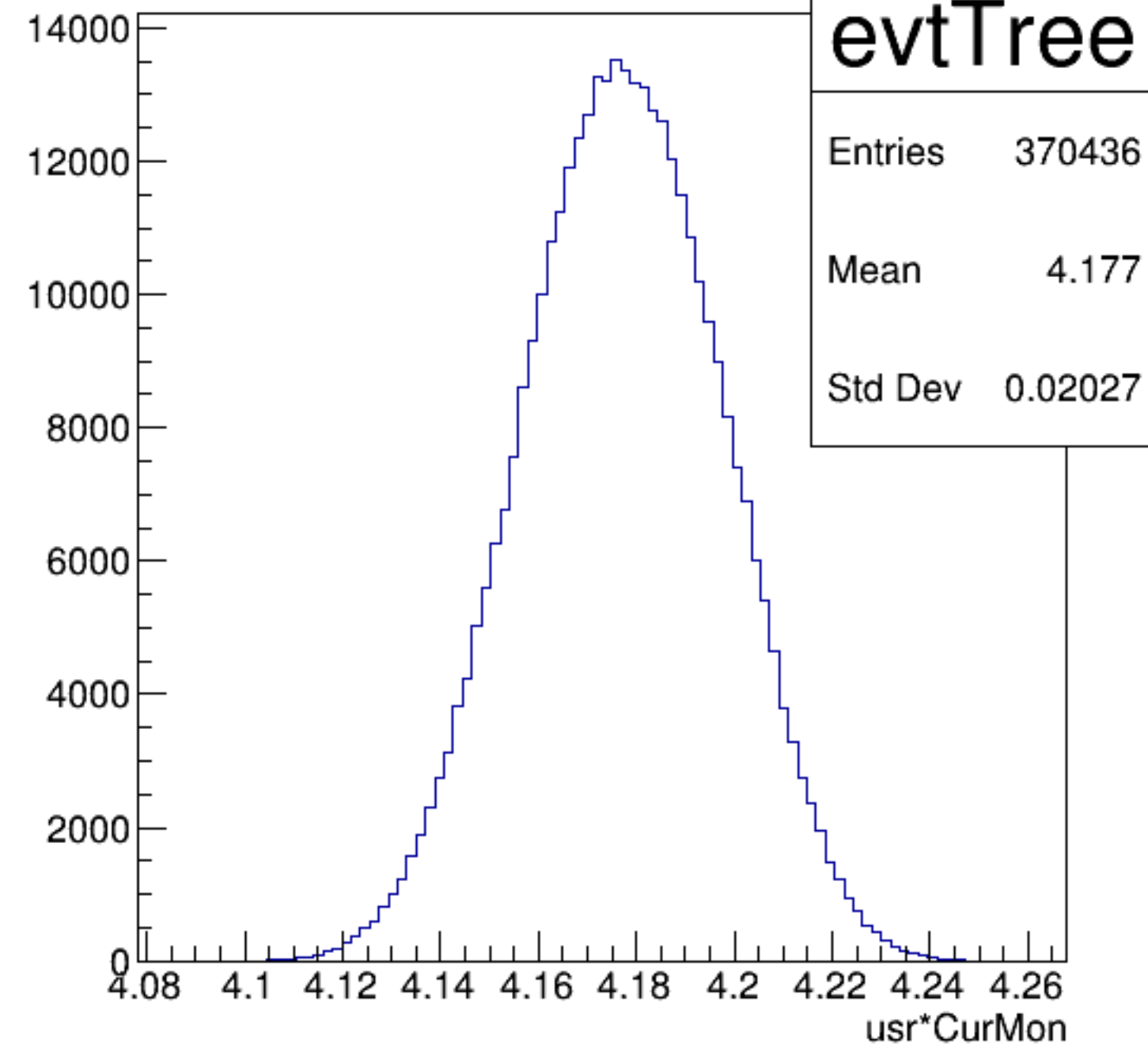


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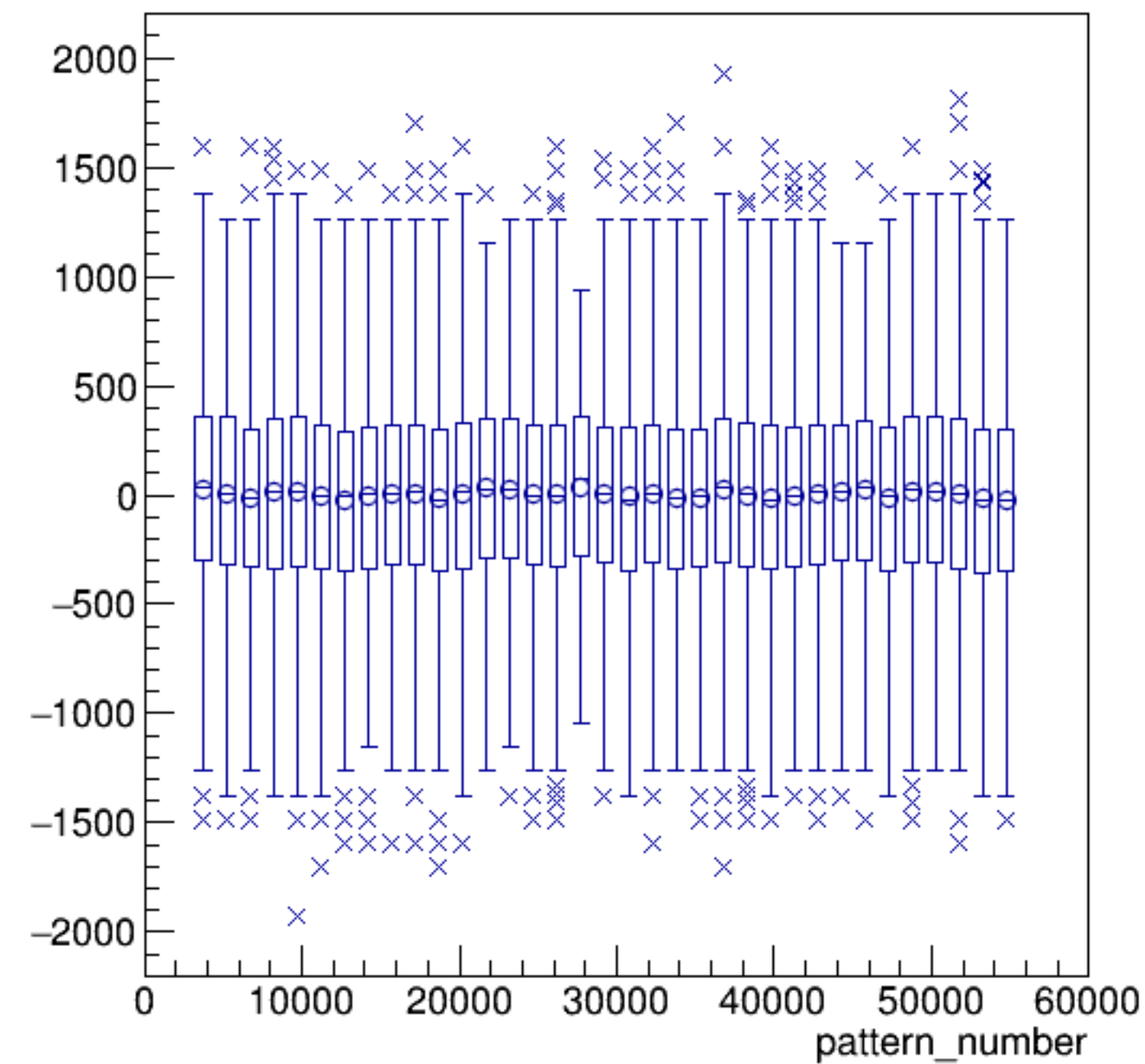


run4586\_000\_maindet\_usr.png

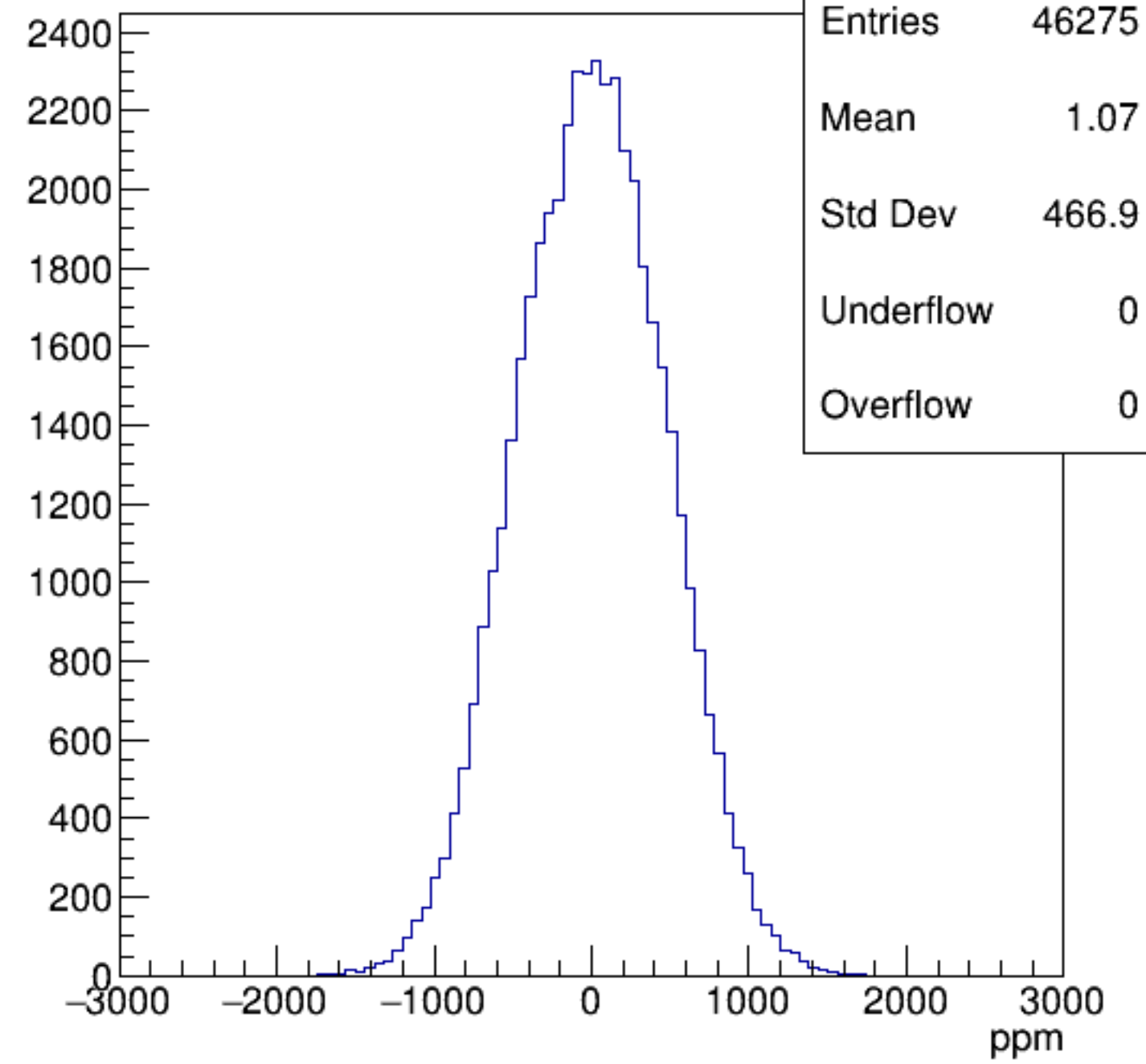
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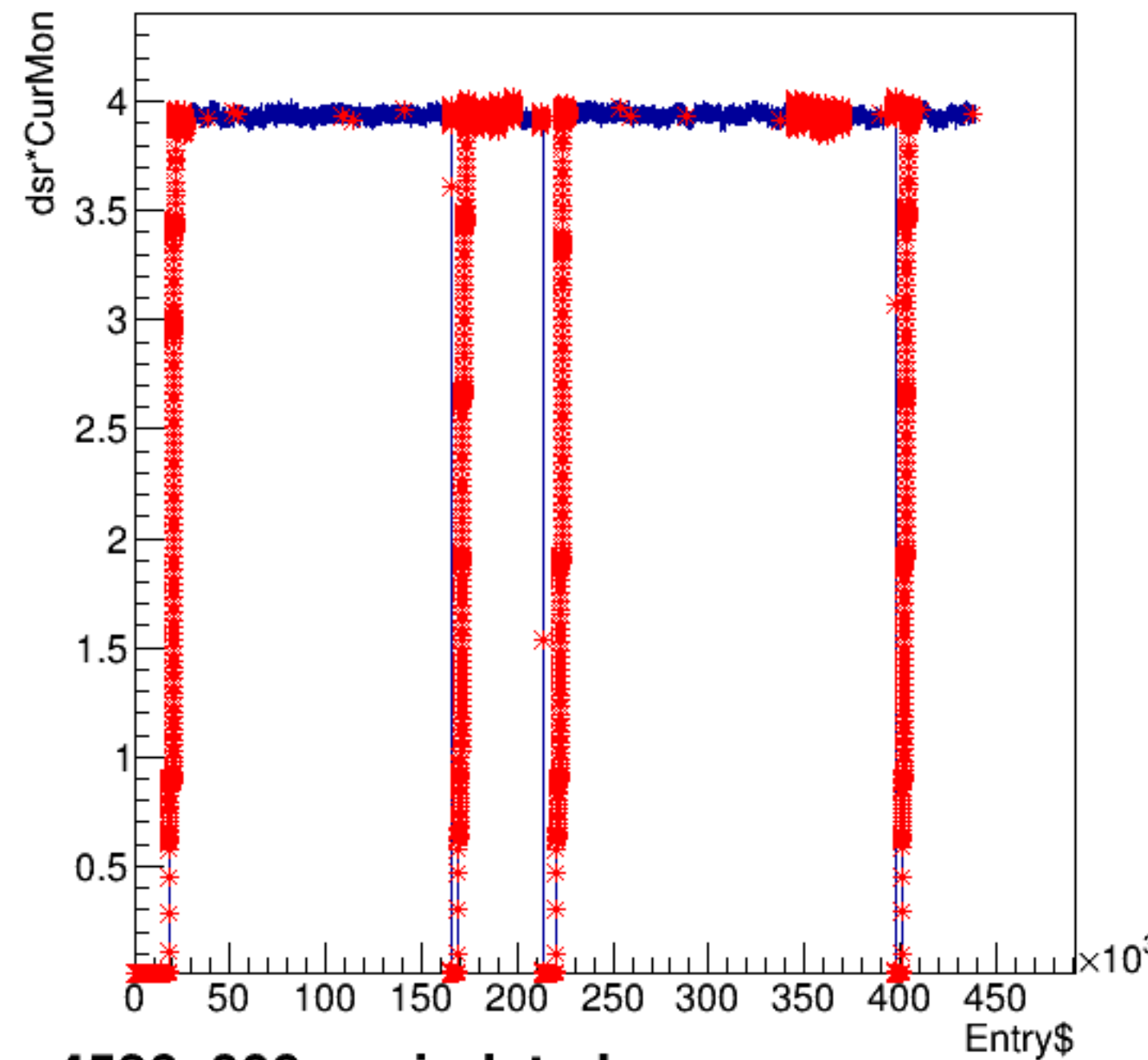
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asym\_usr

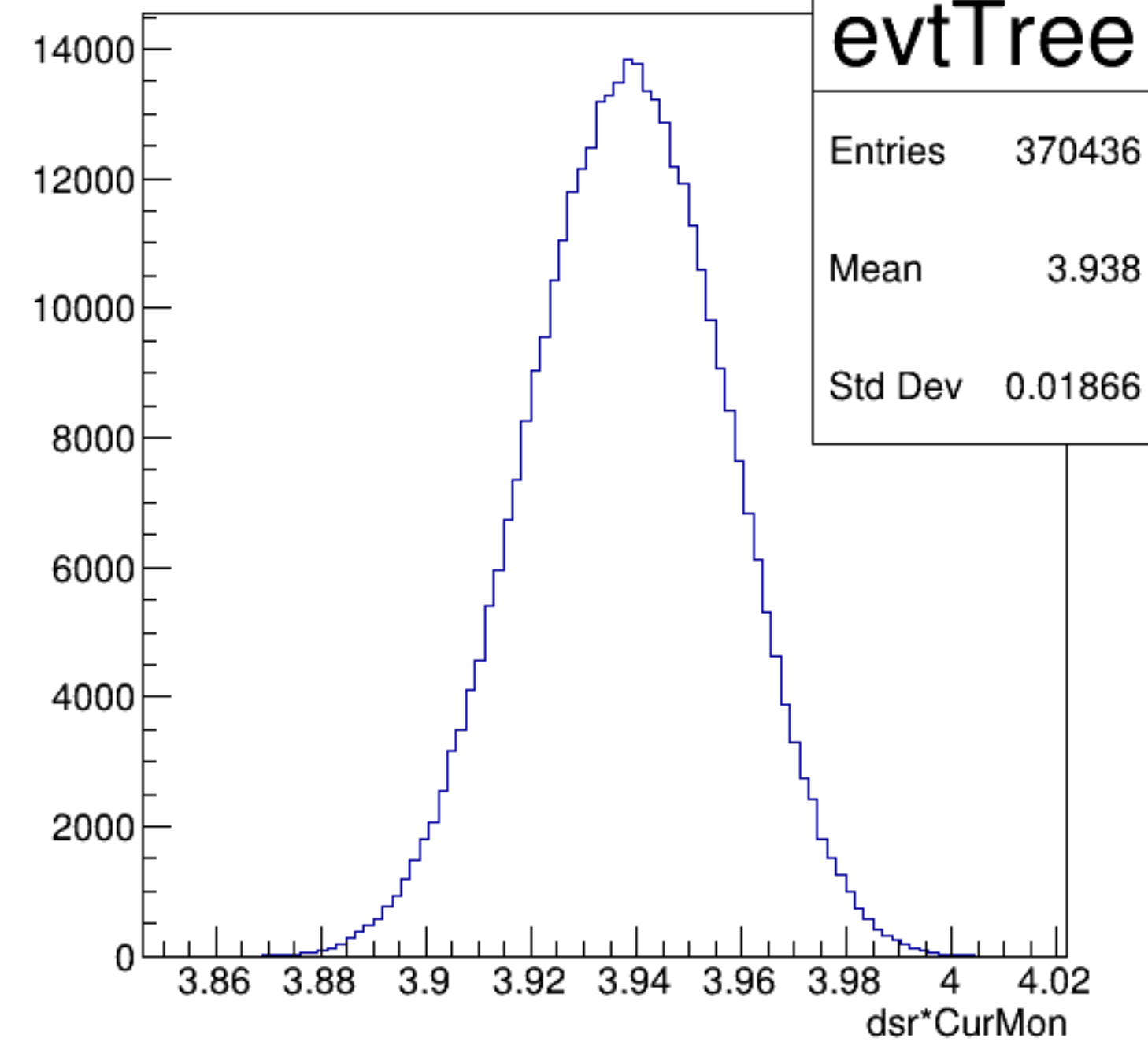


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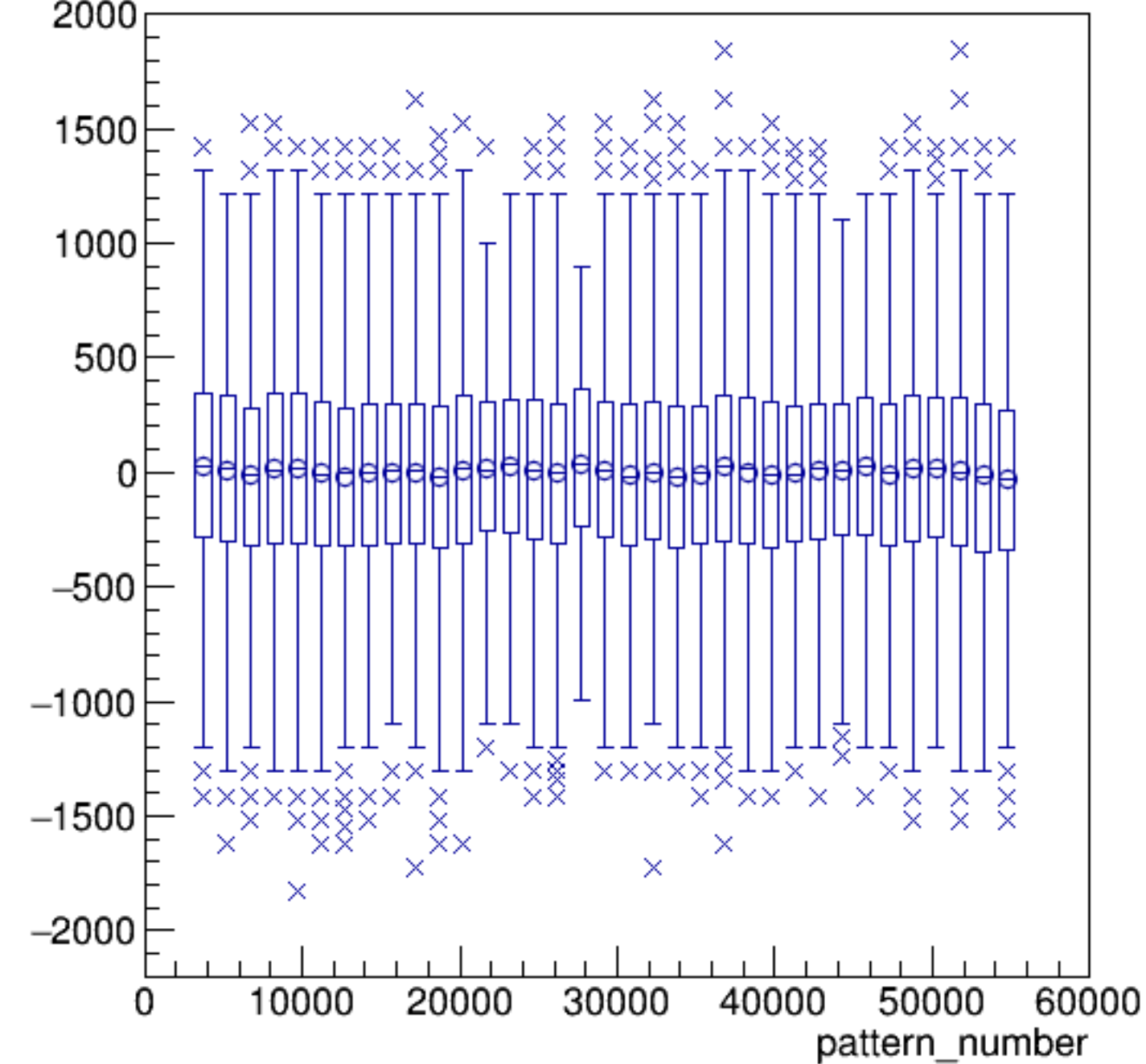


run4586\_000\_maindet\_dsr.png

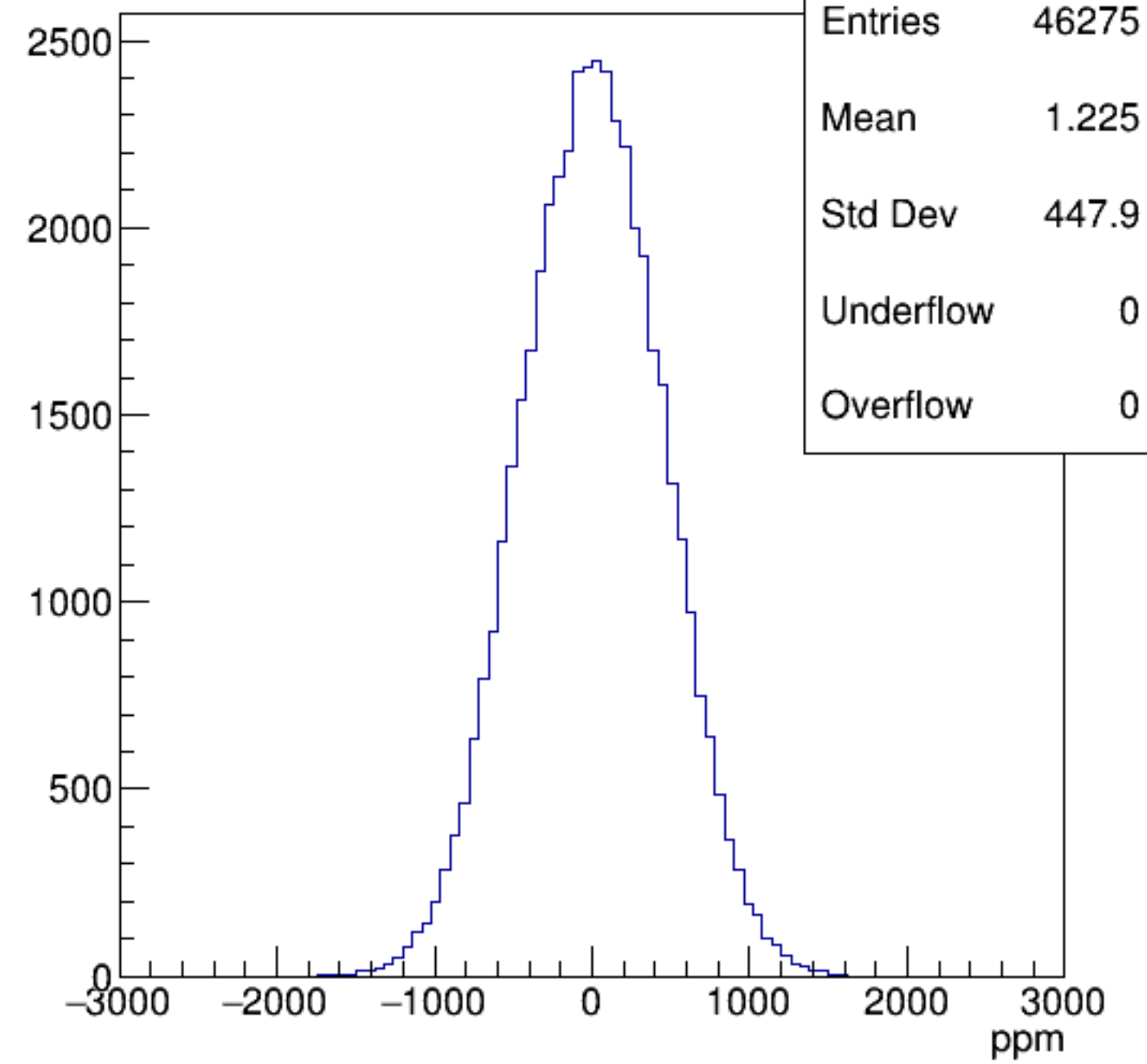
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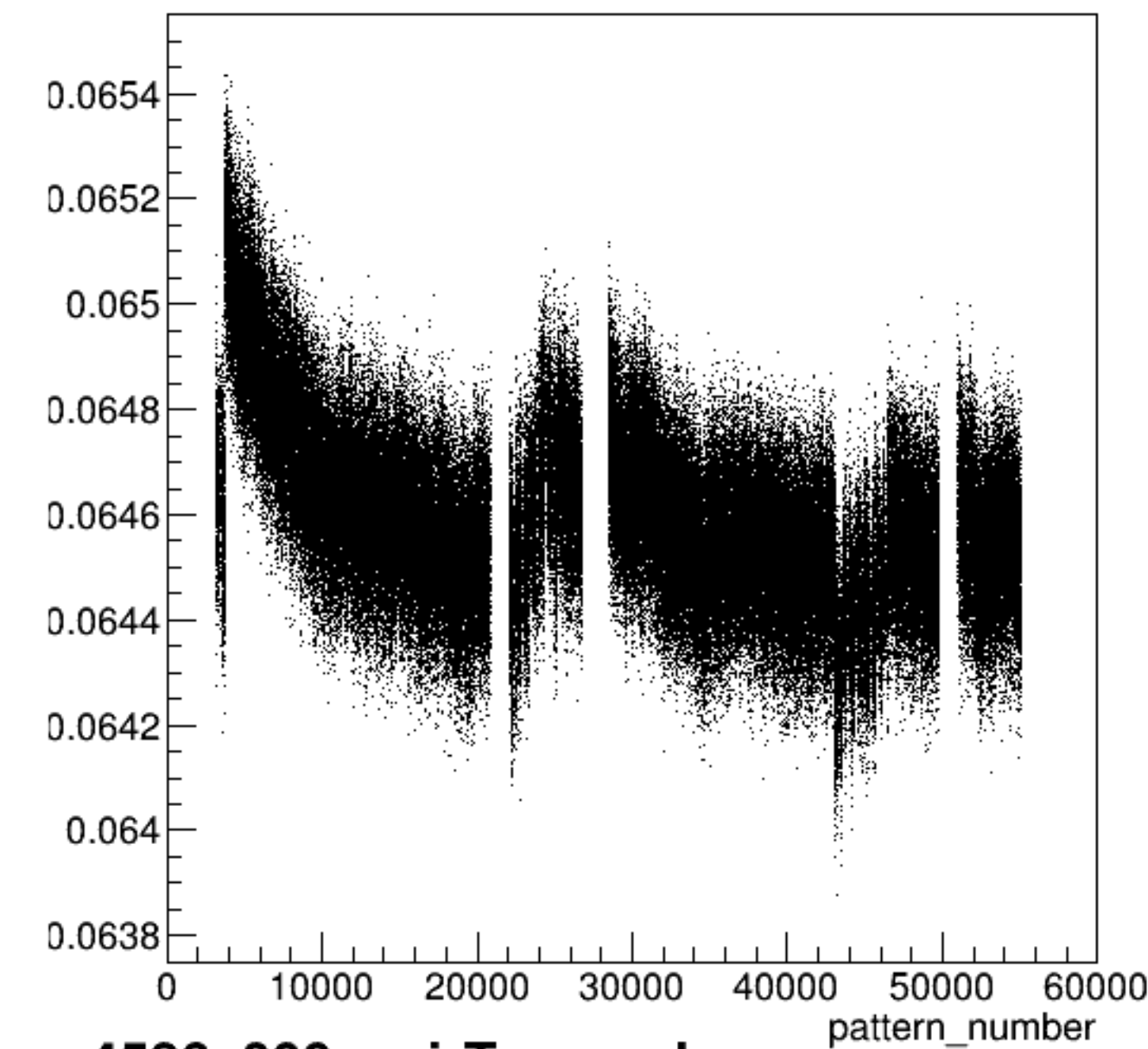


asym\_dsr

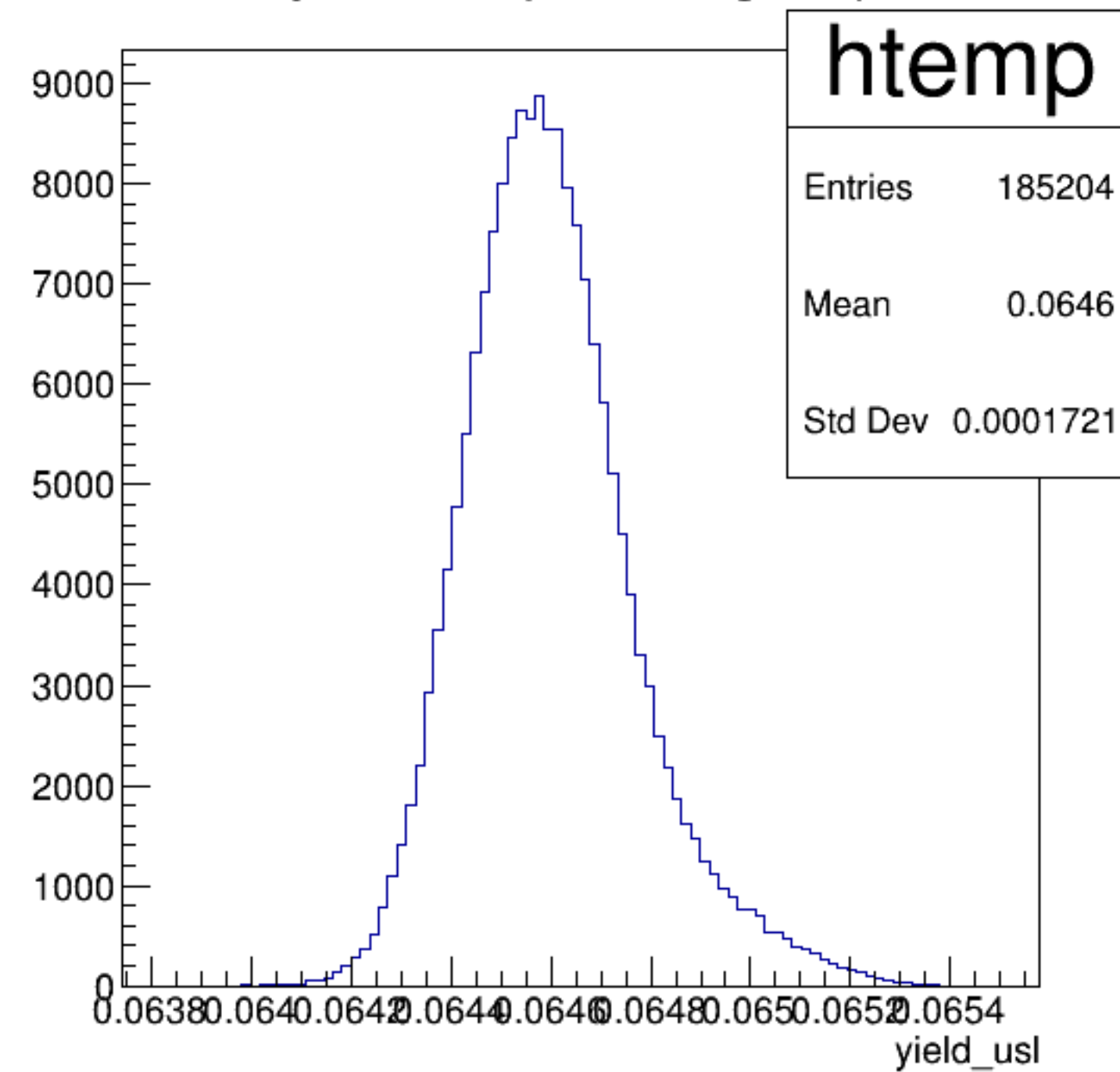




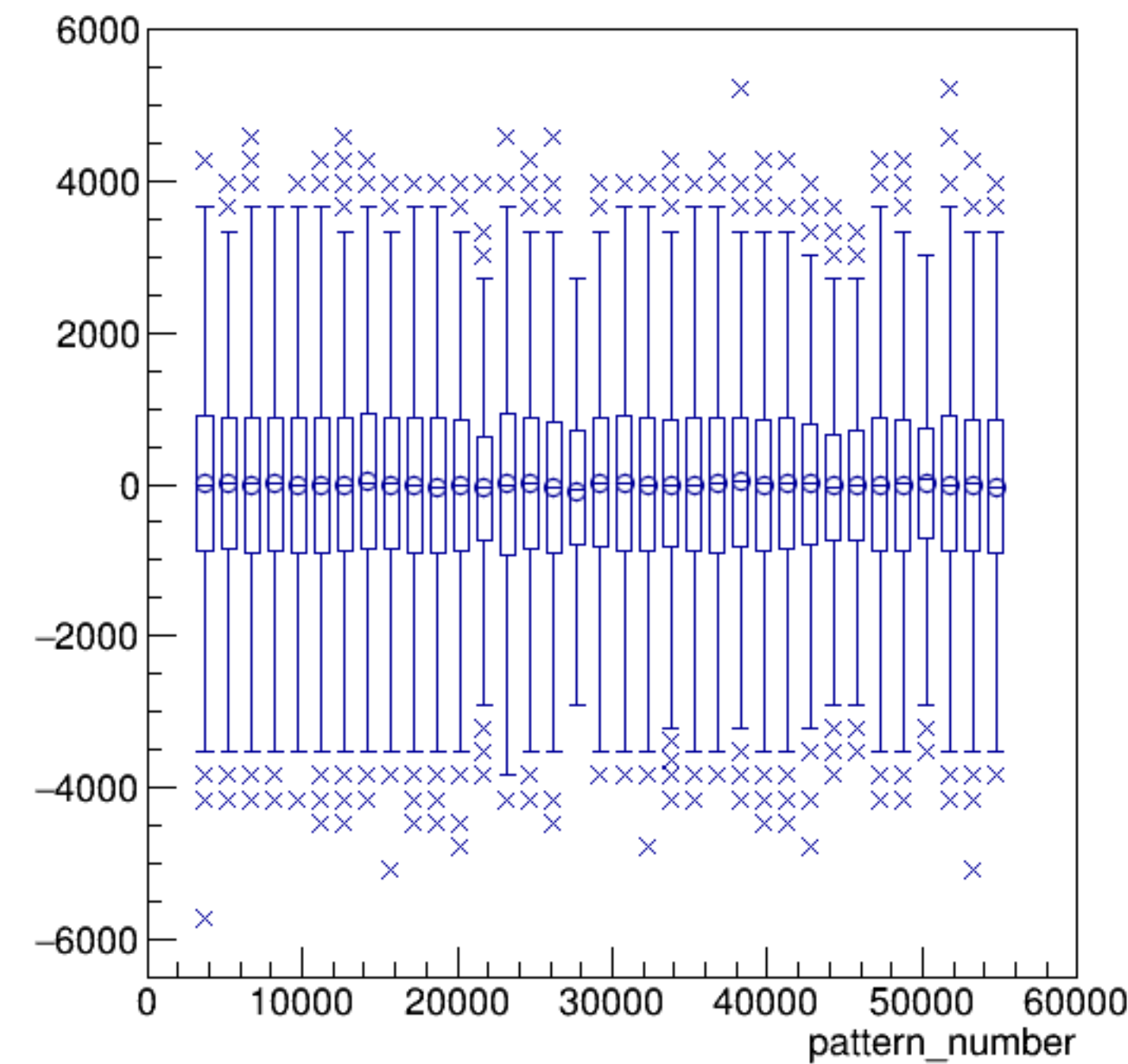
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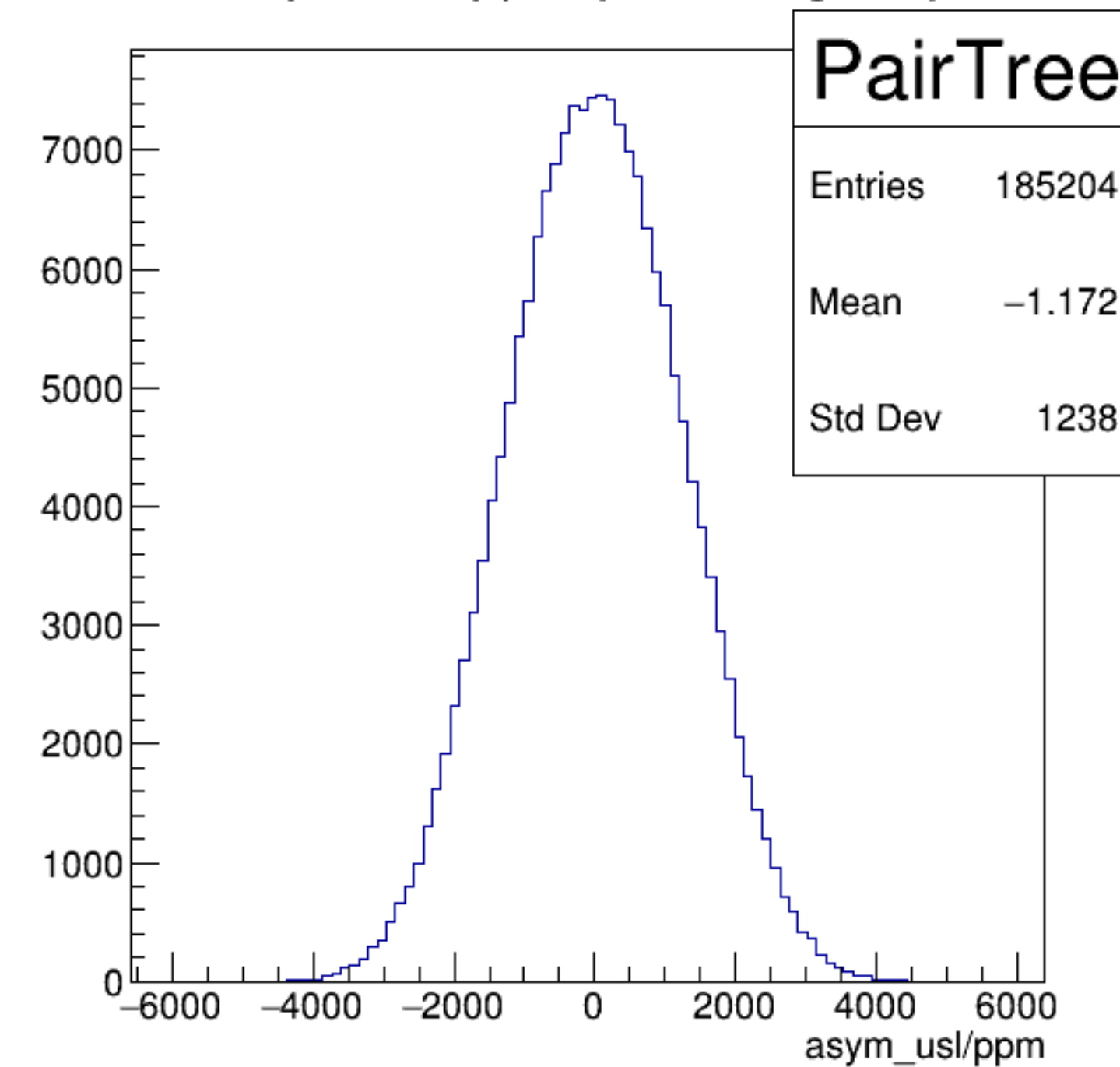
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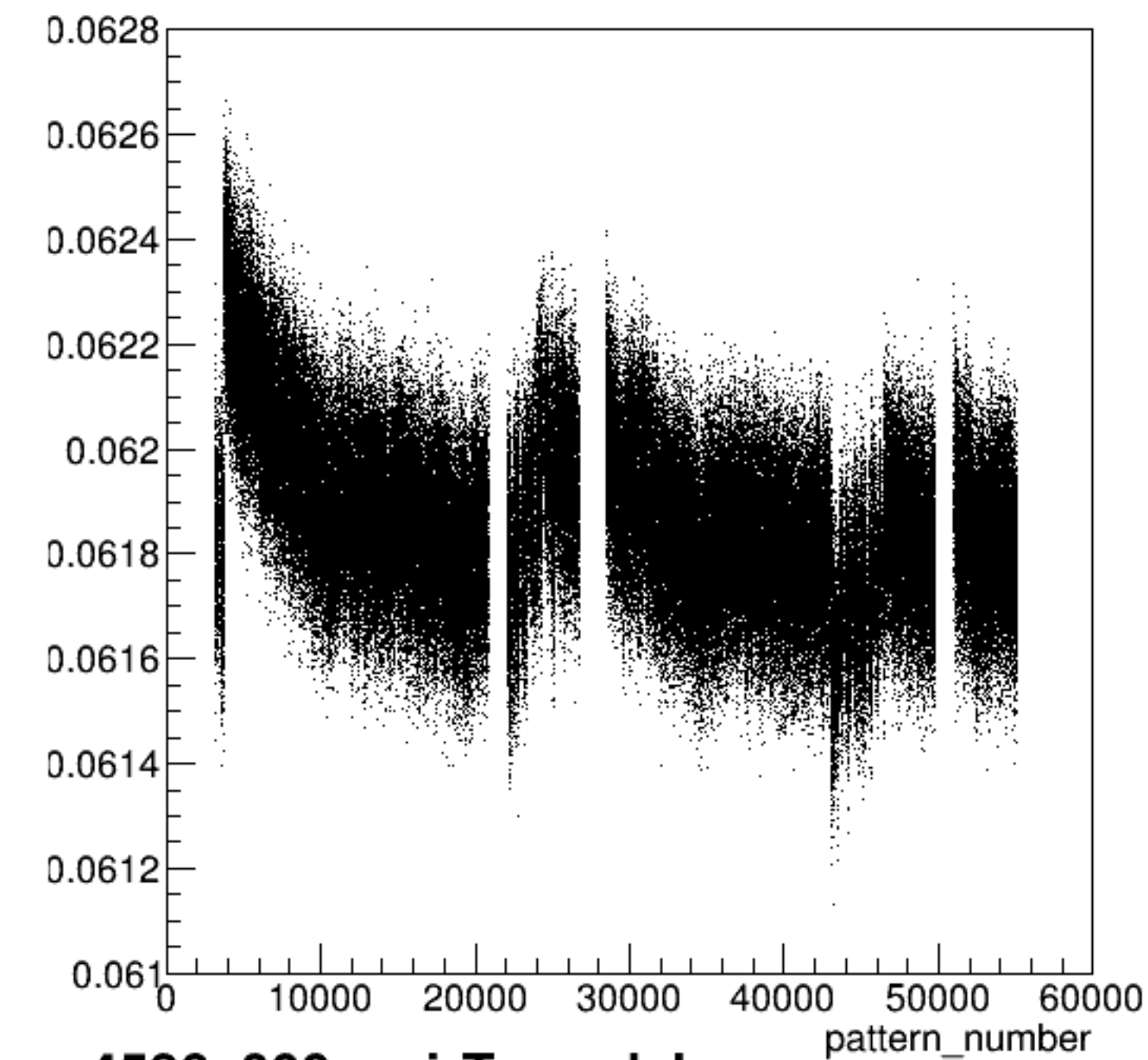
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asym\_usl/ppm {ErrorFlag==0}

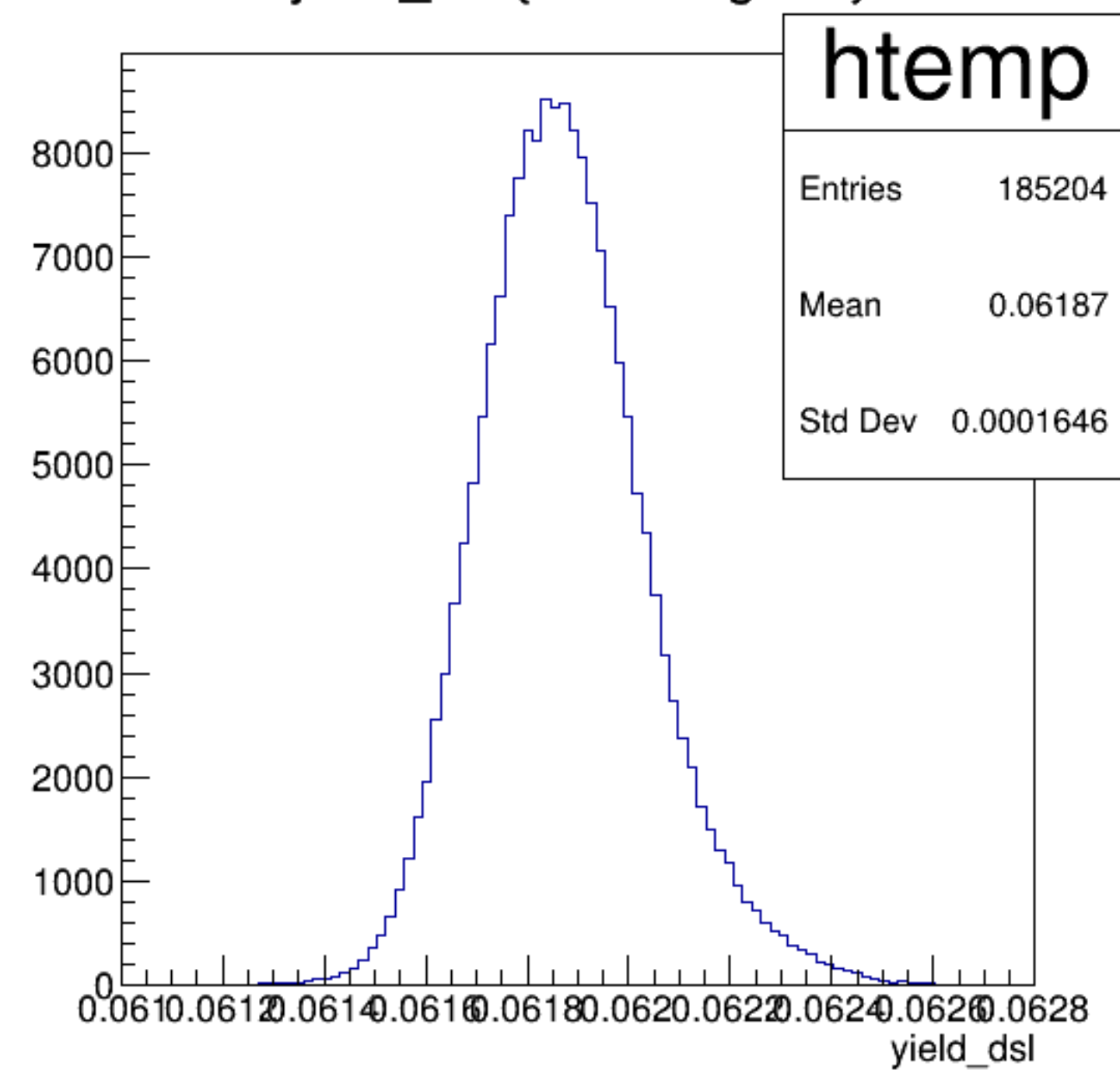


yield\_dsl:pattern\_number {ErrorFlag==0}

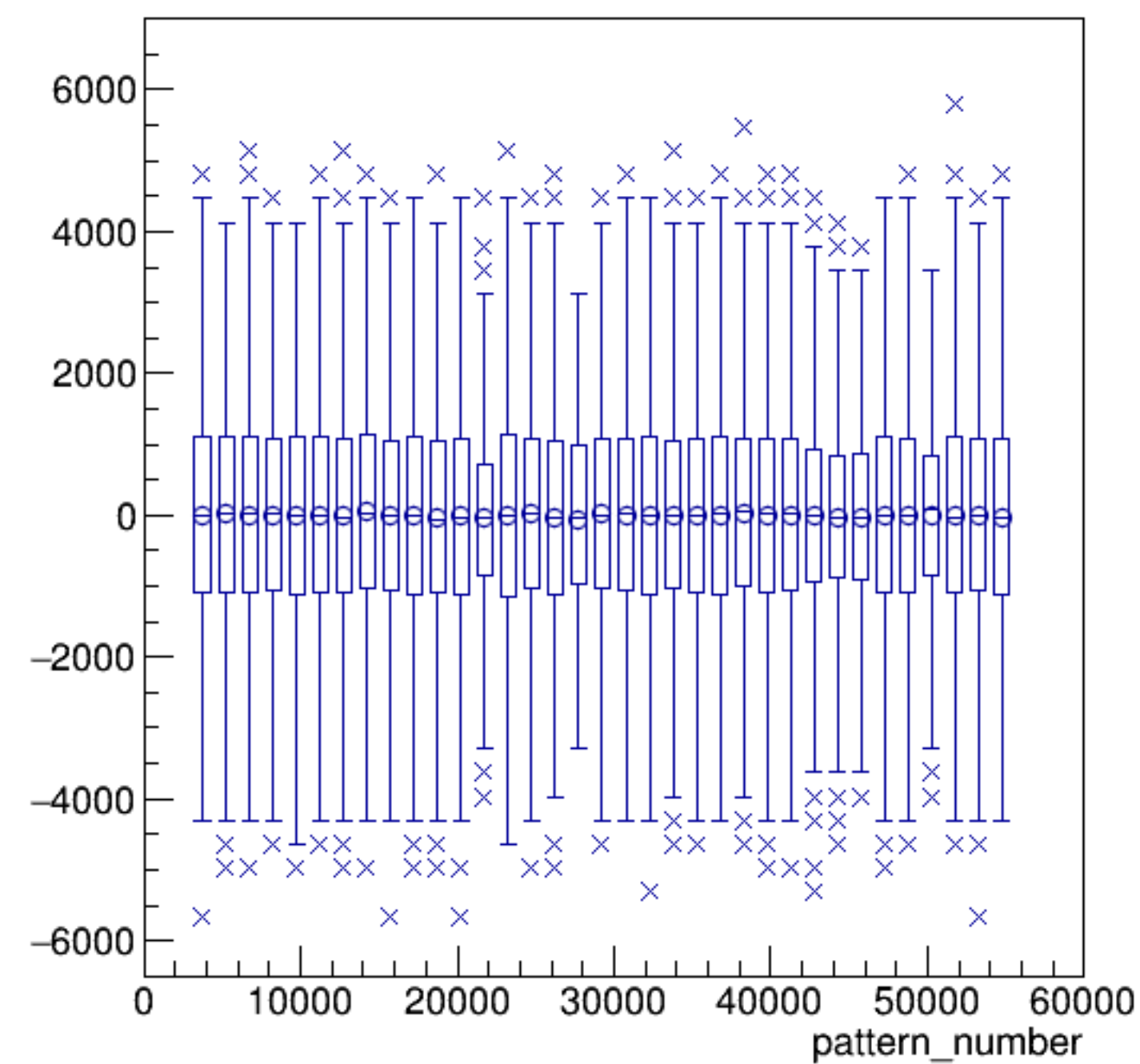


run4586\_000\_pairTree\_dsl.png

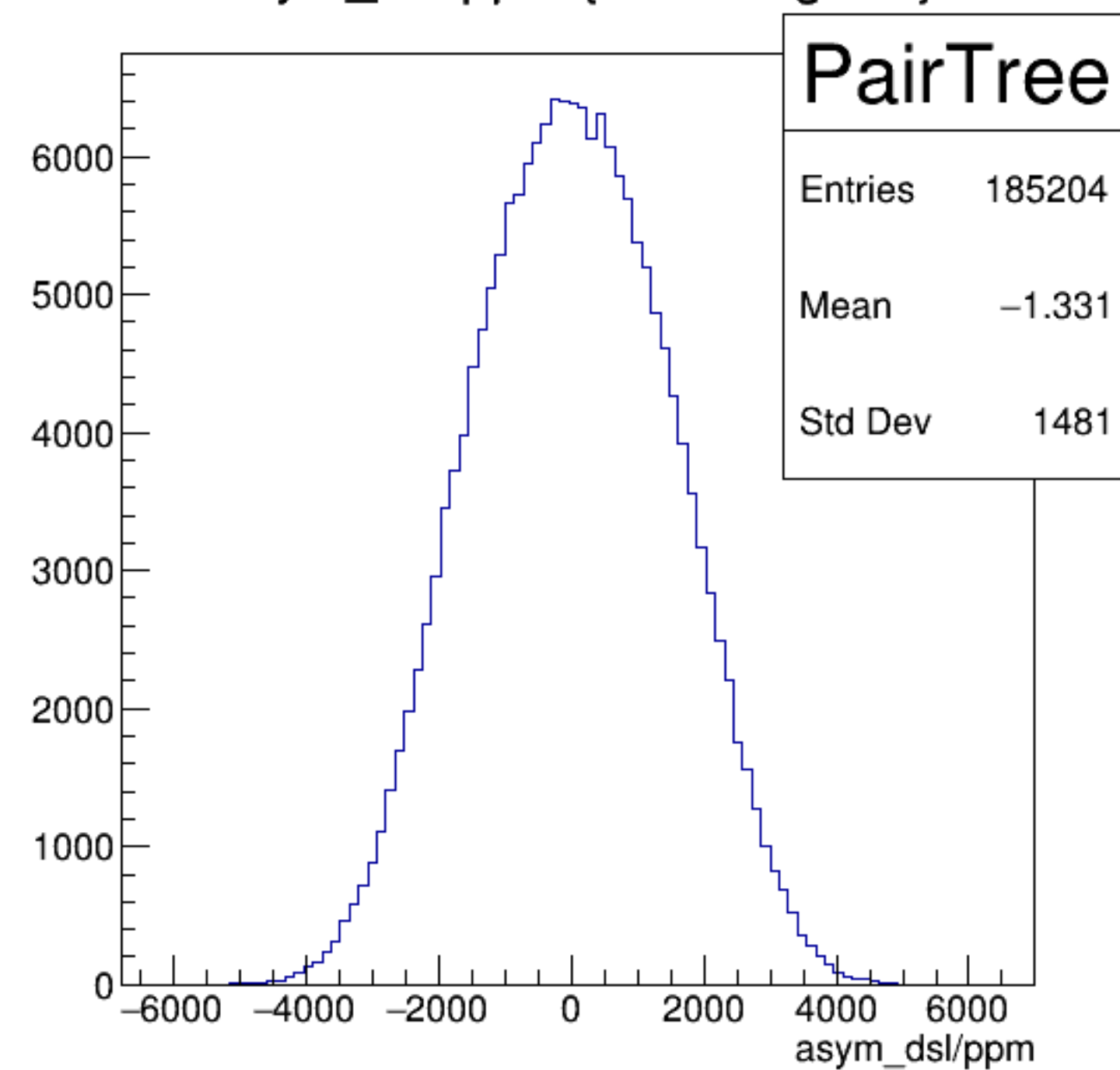
yield\_dsl {ErrorFlag==0}



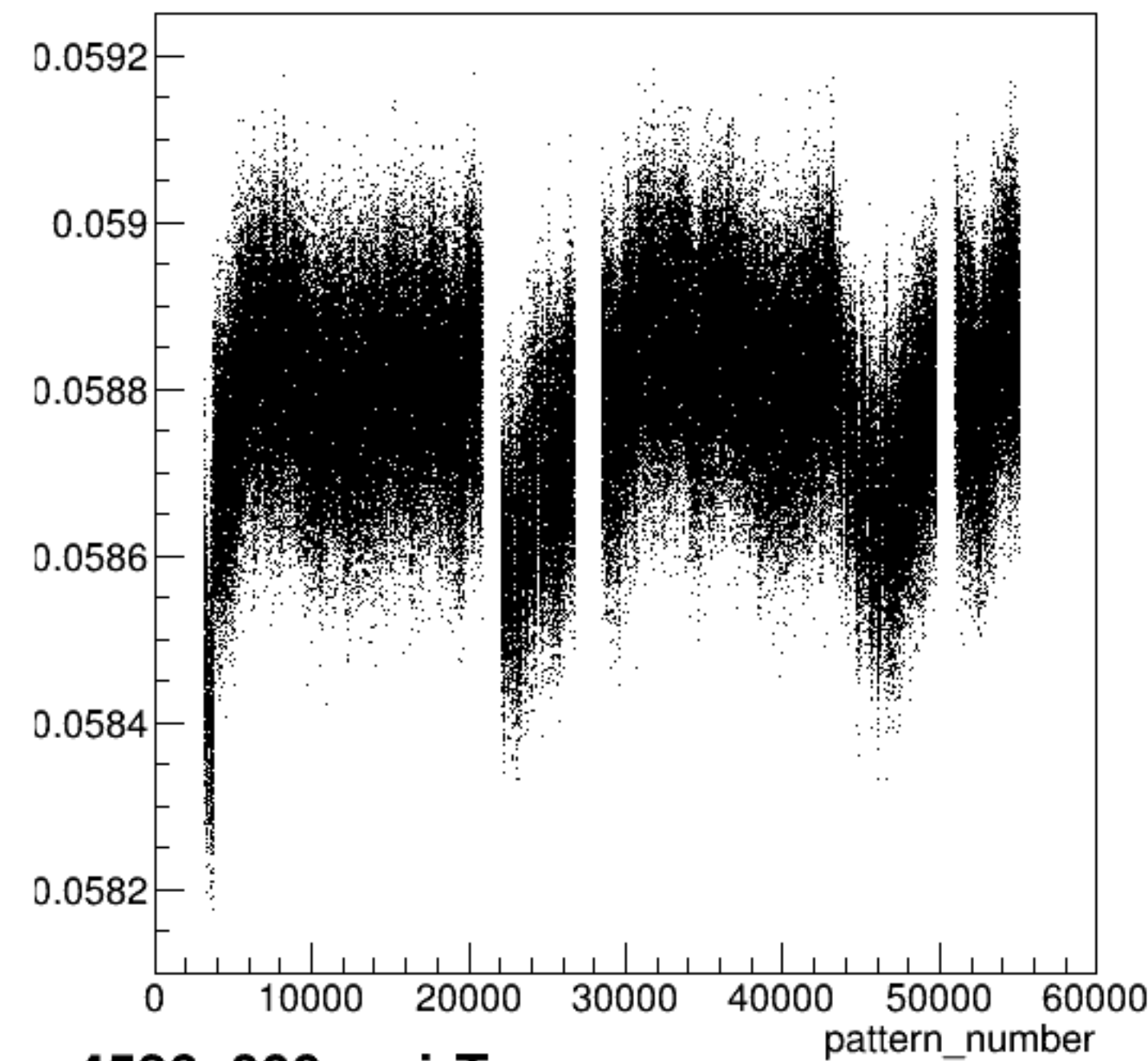
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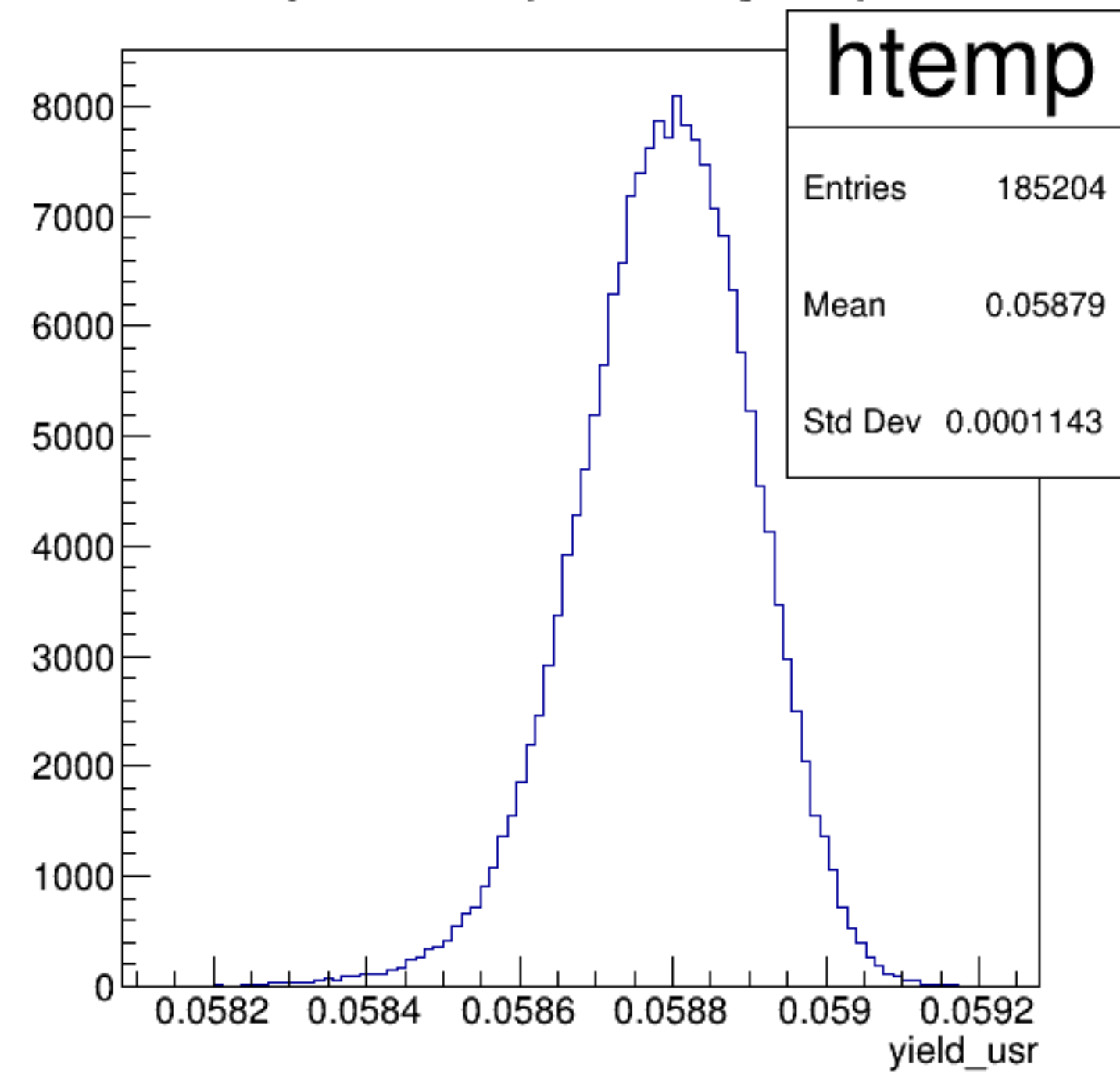


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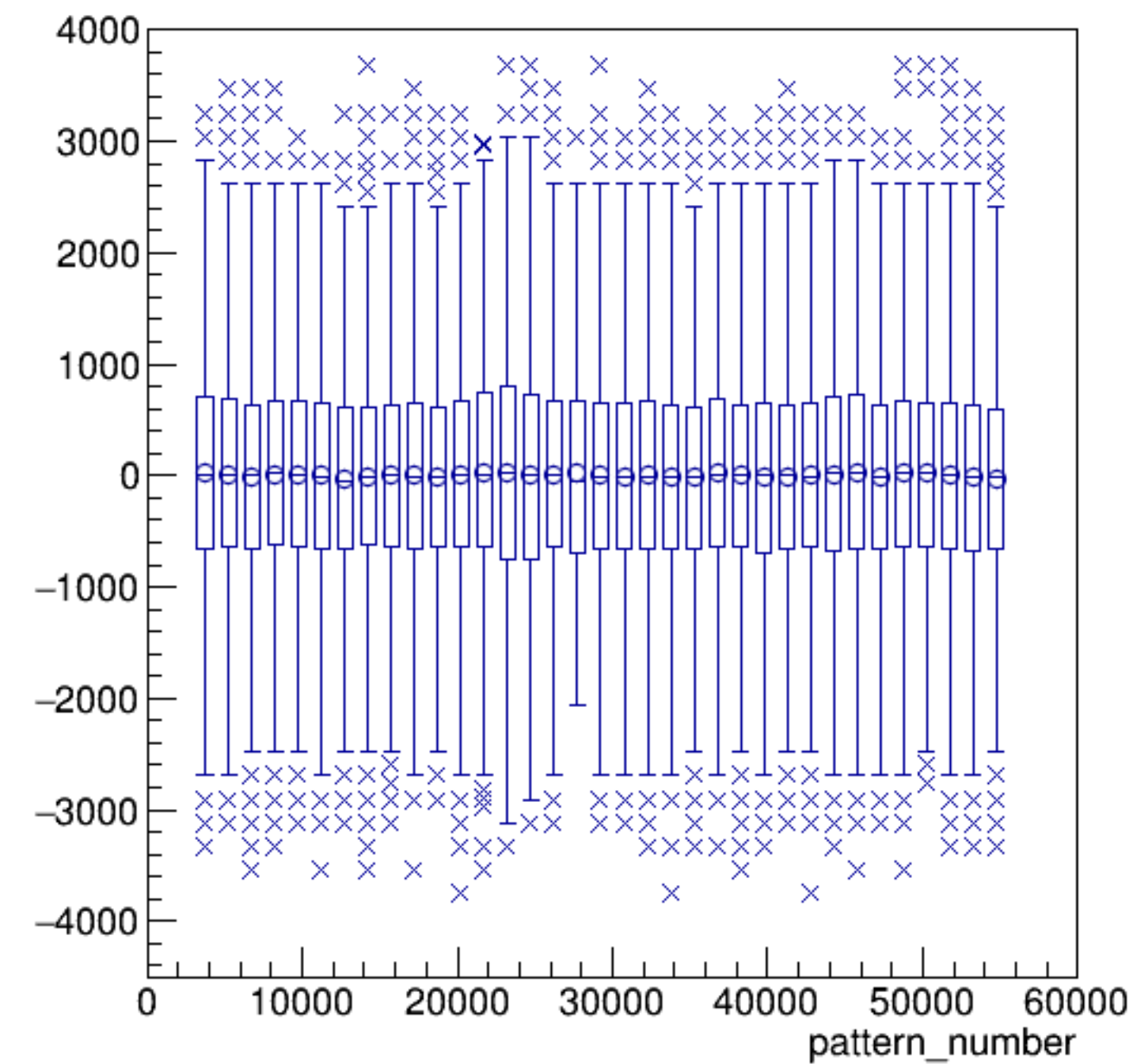


run4586\_000\_pairTree\_usr.png

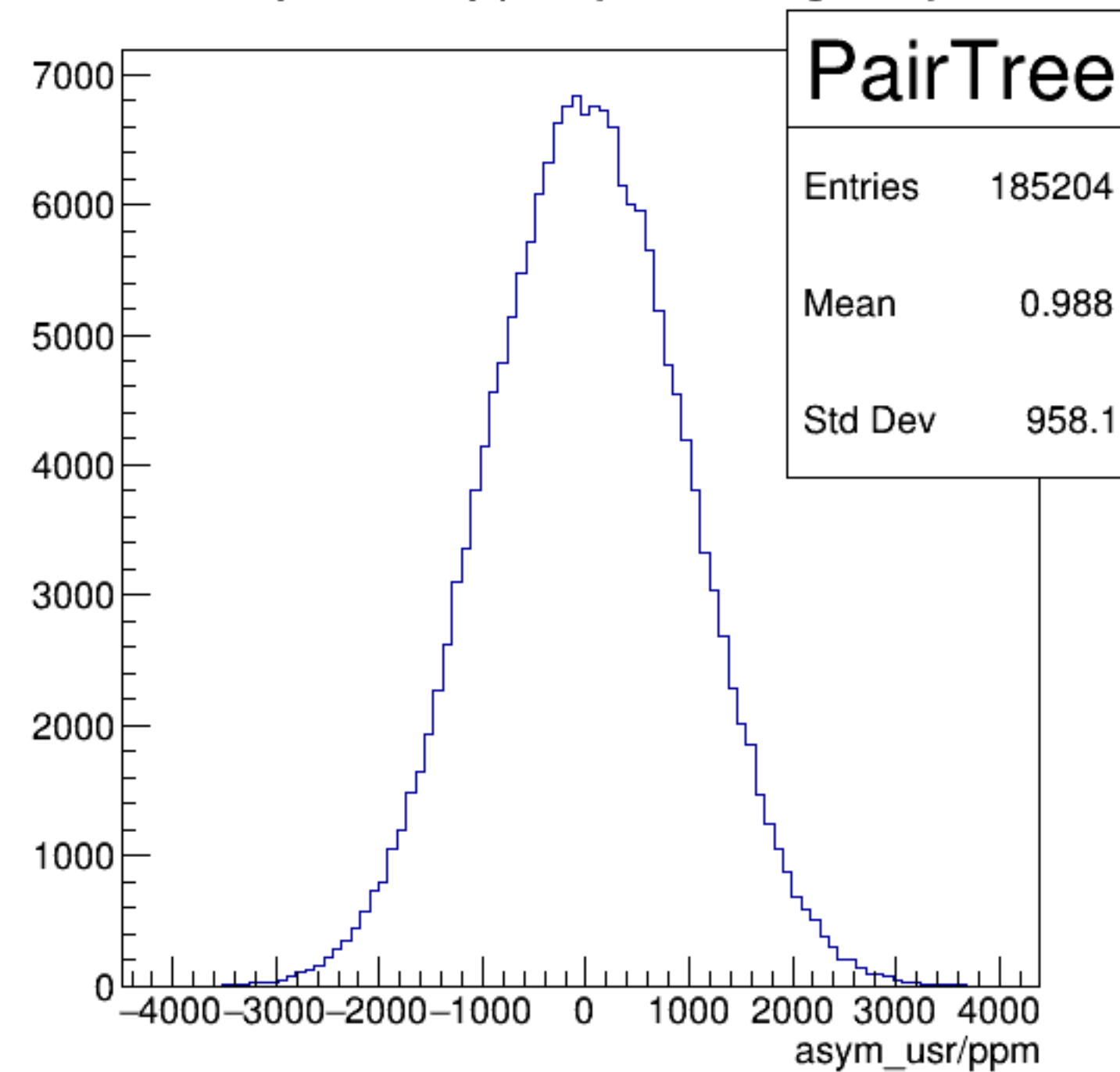
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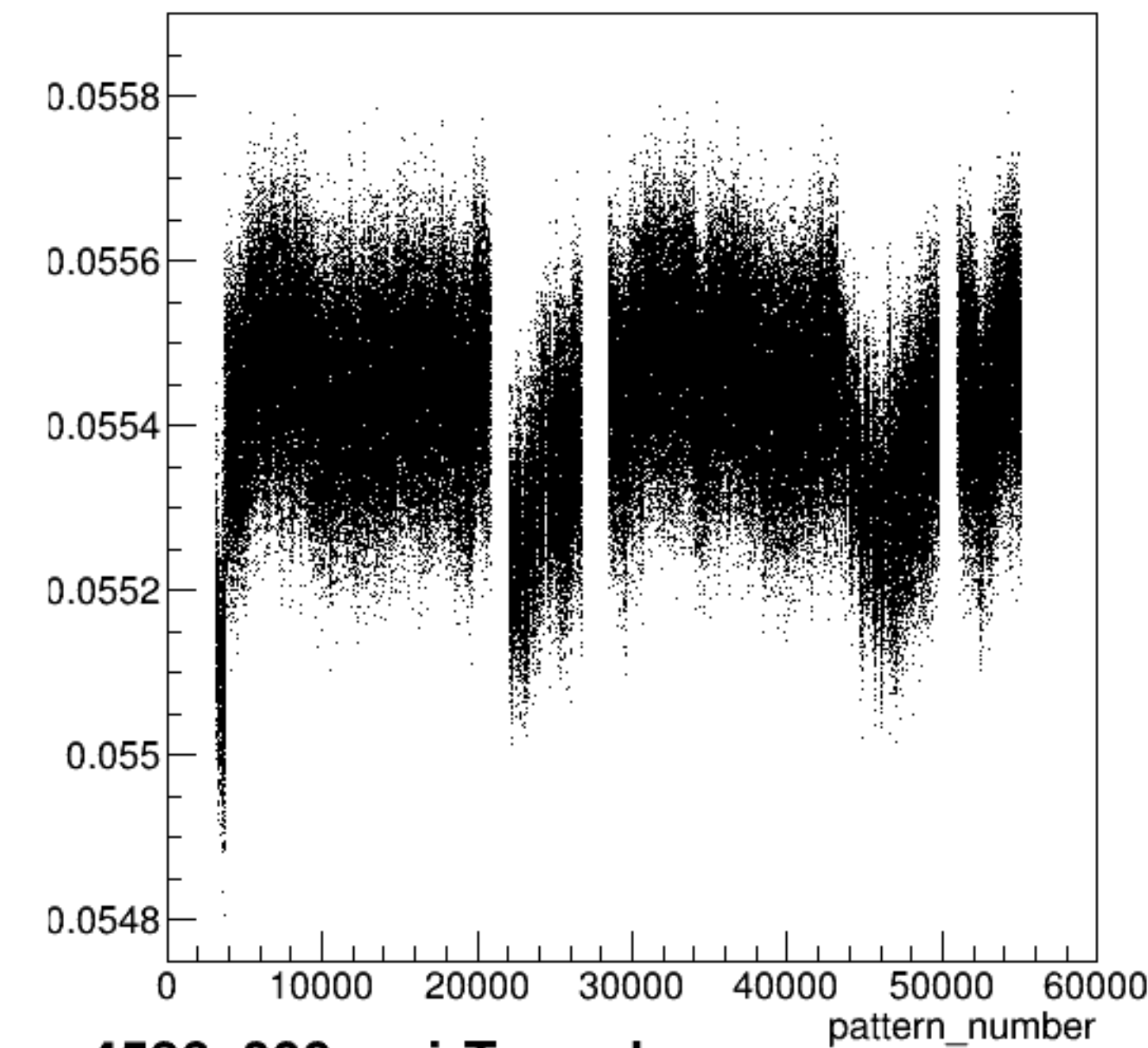


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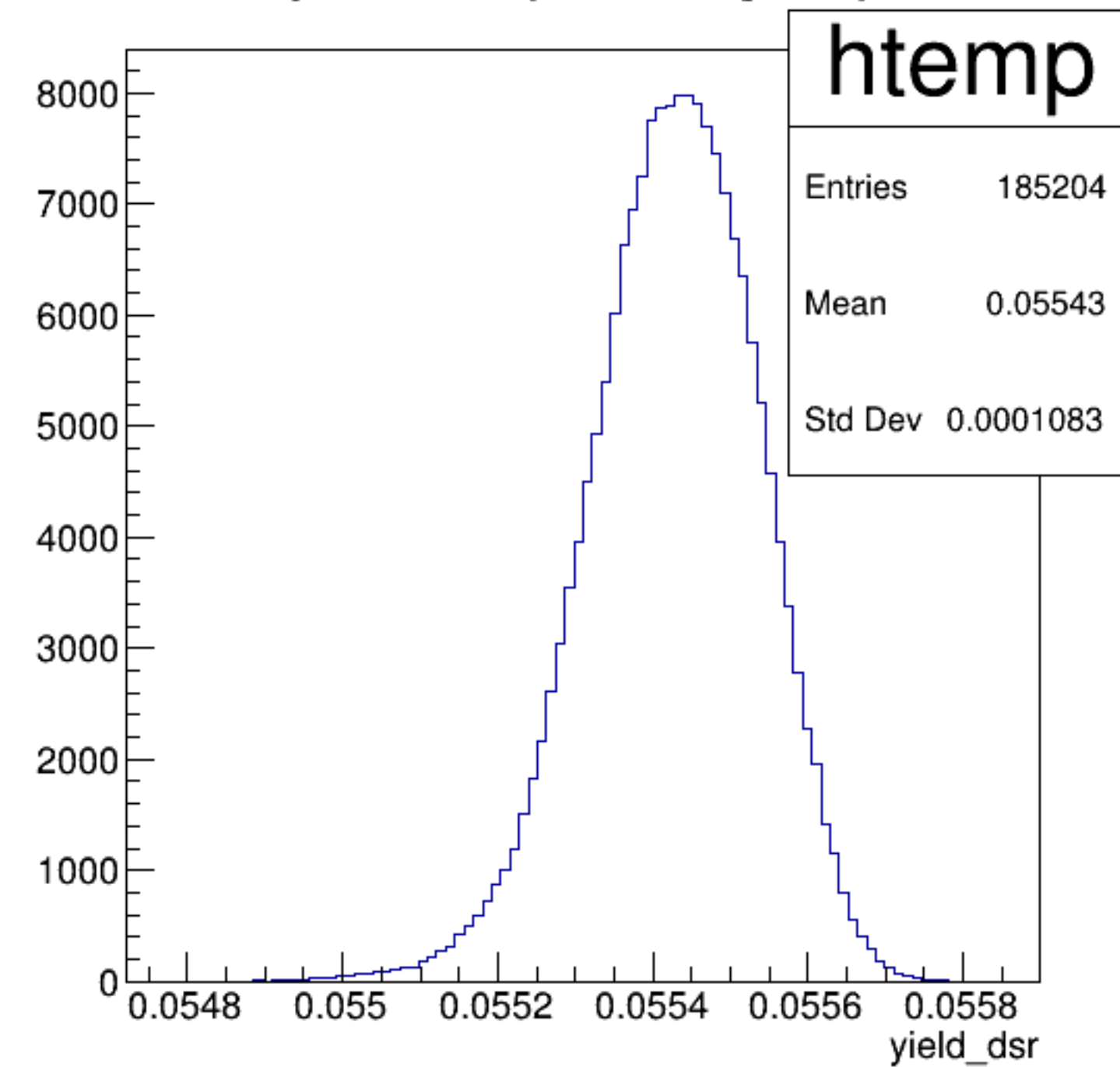


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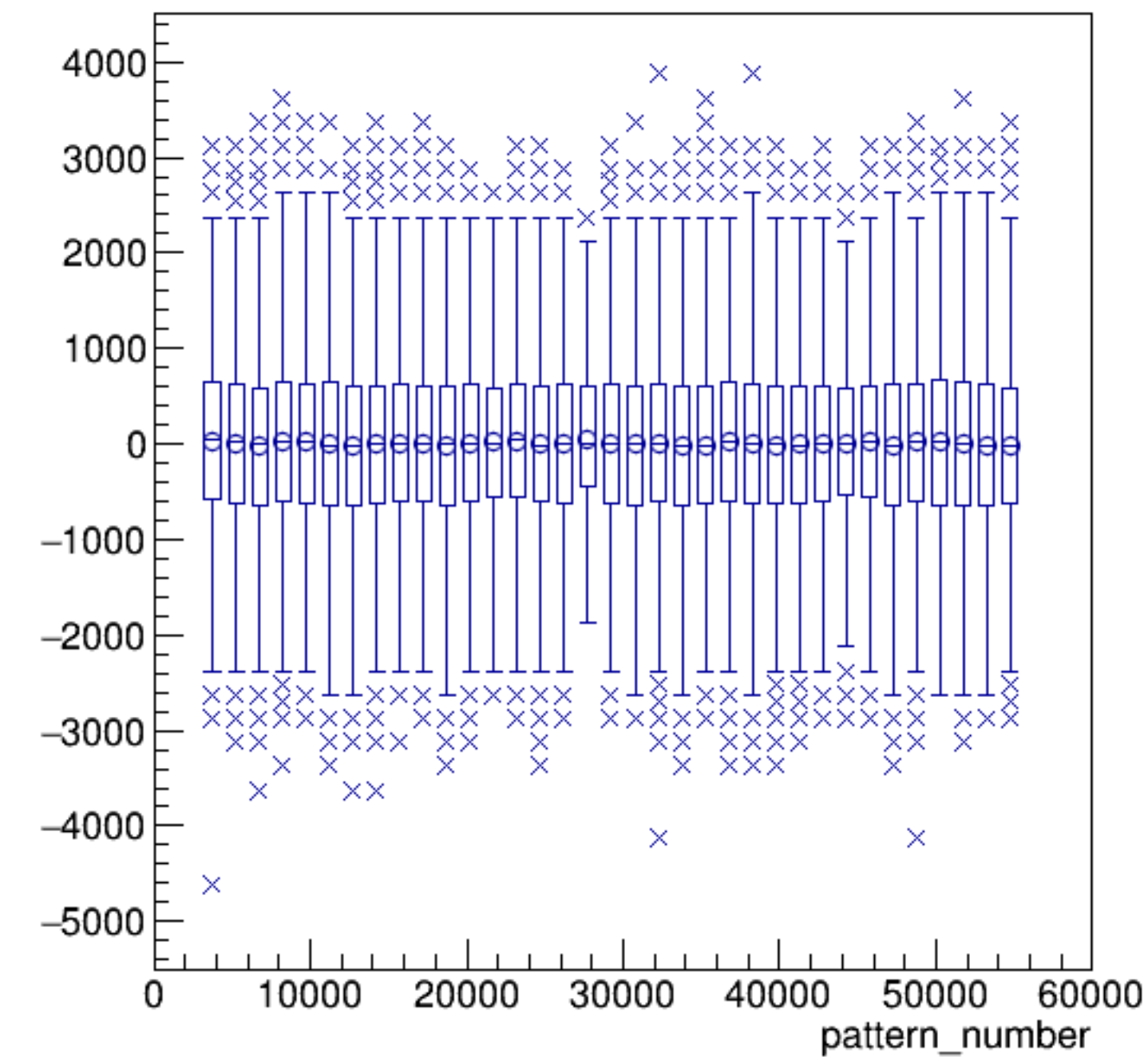


run4586\_000\_pairTree\_dsr.png

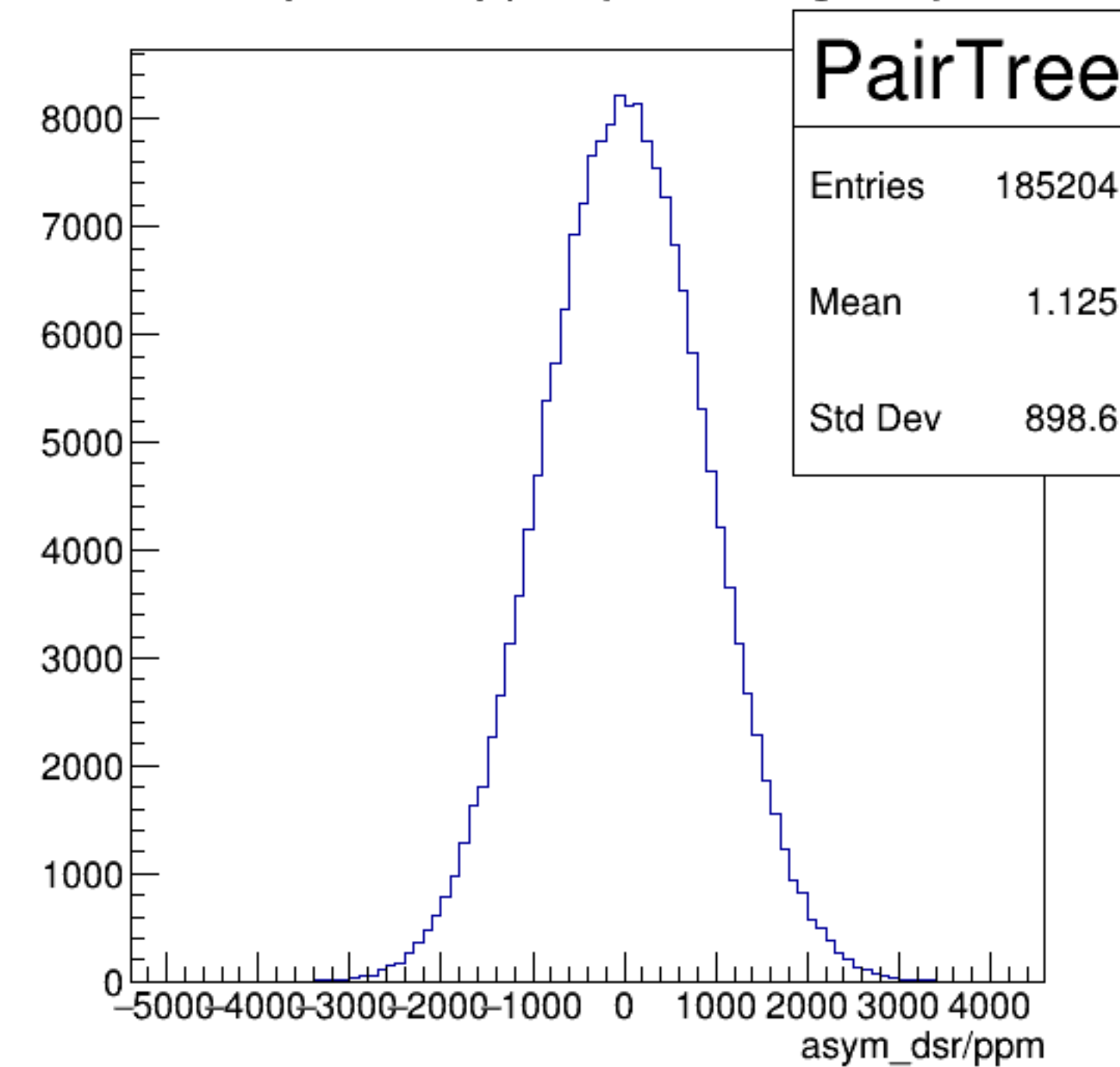
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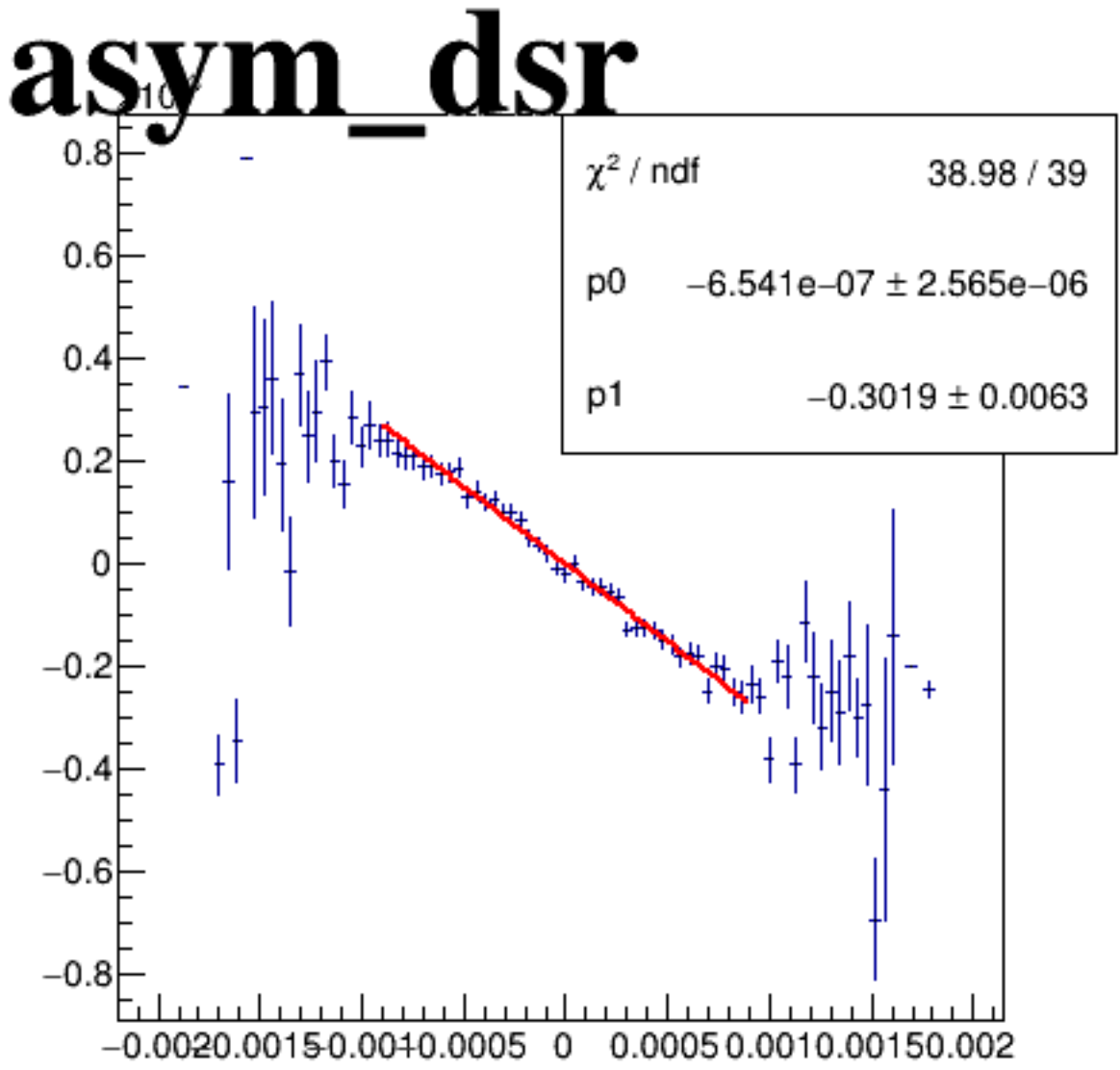
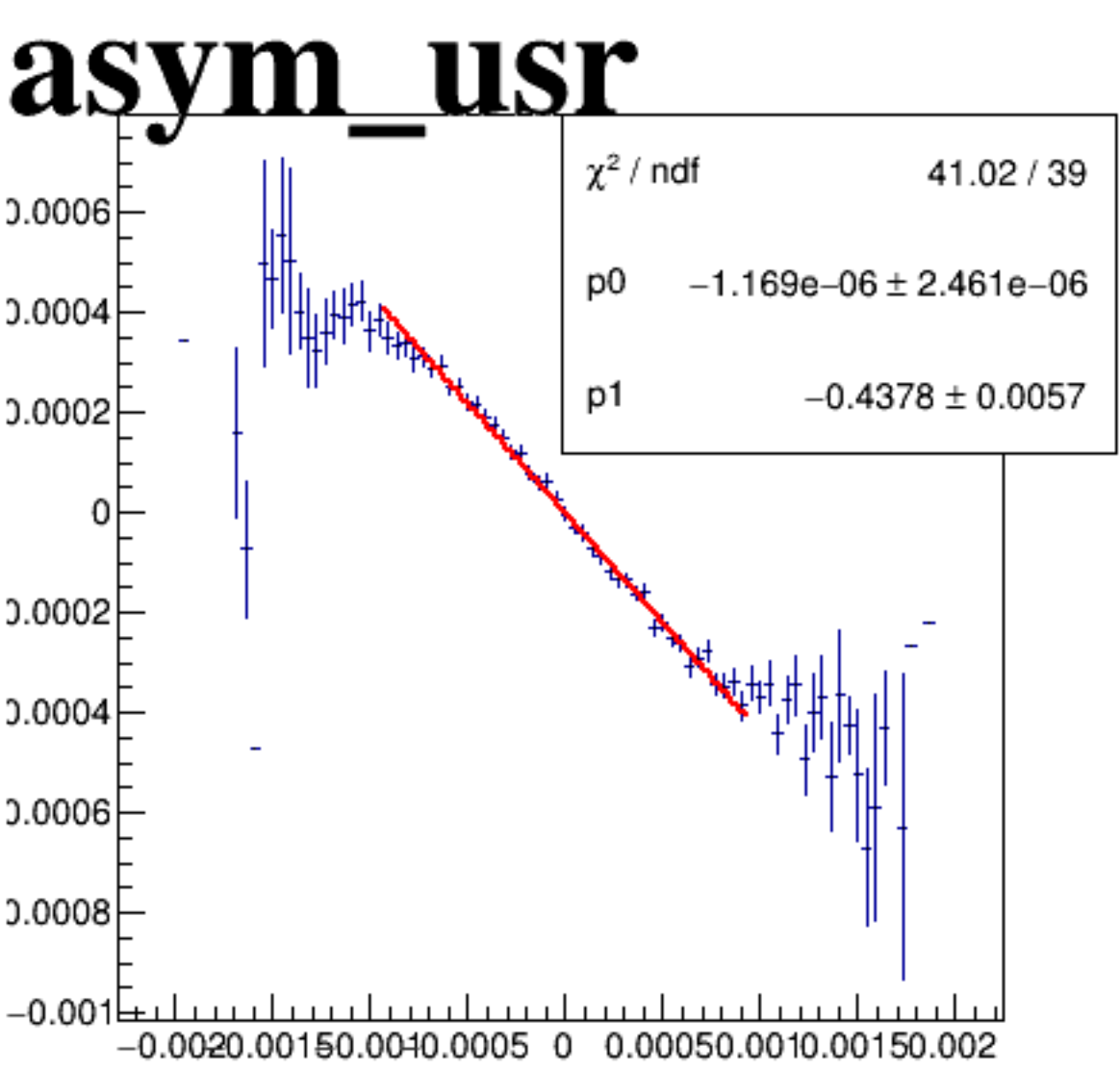
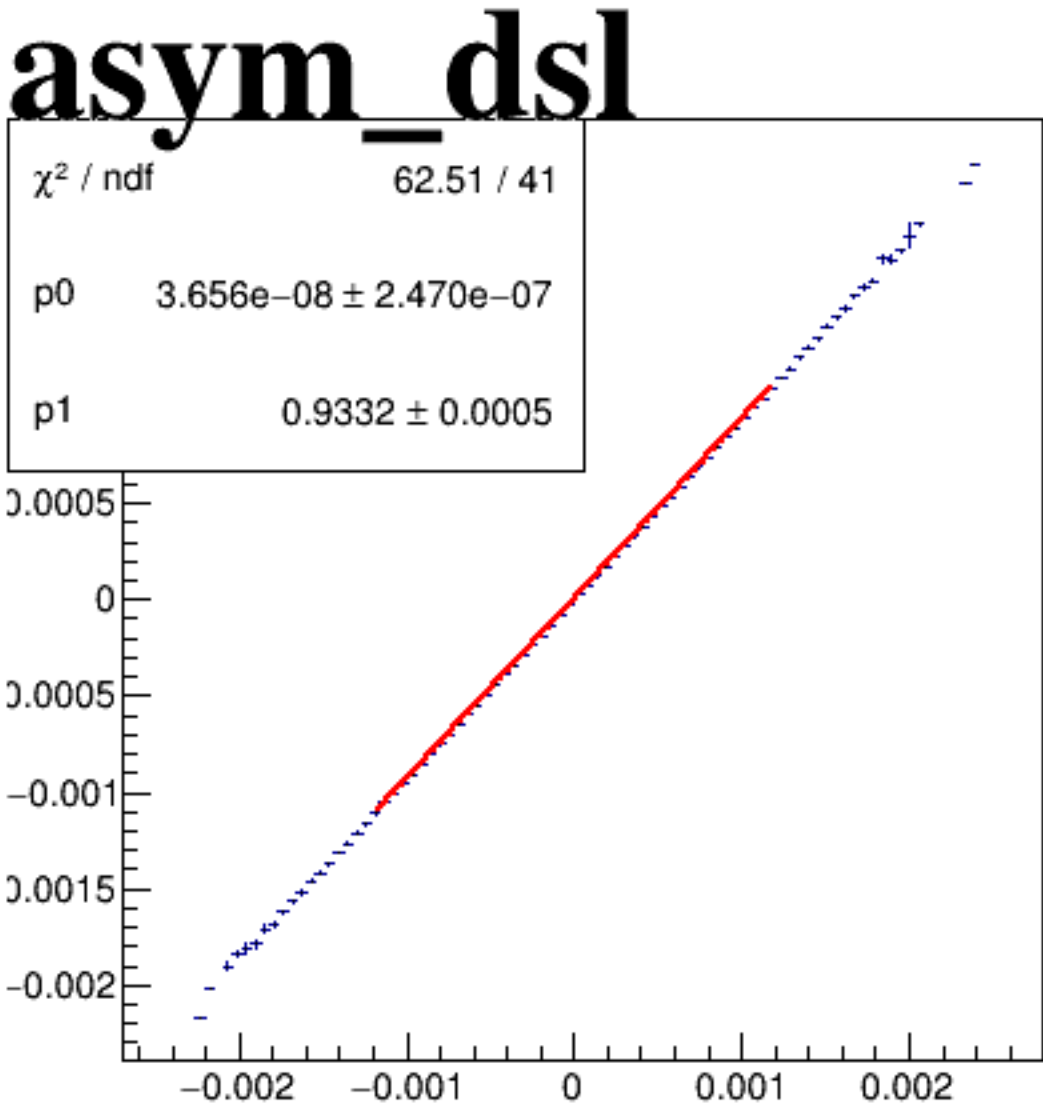
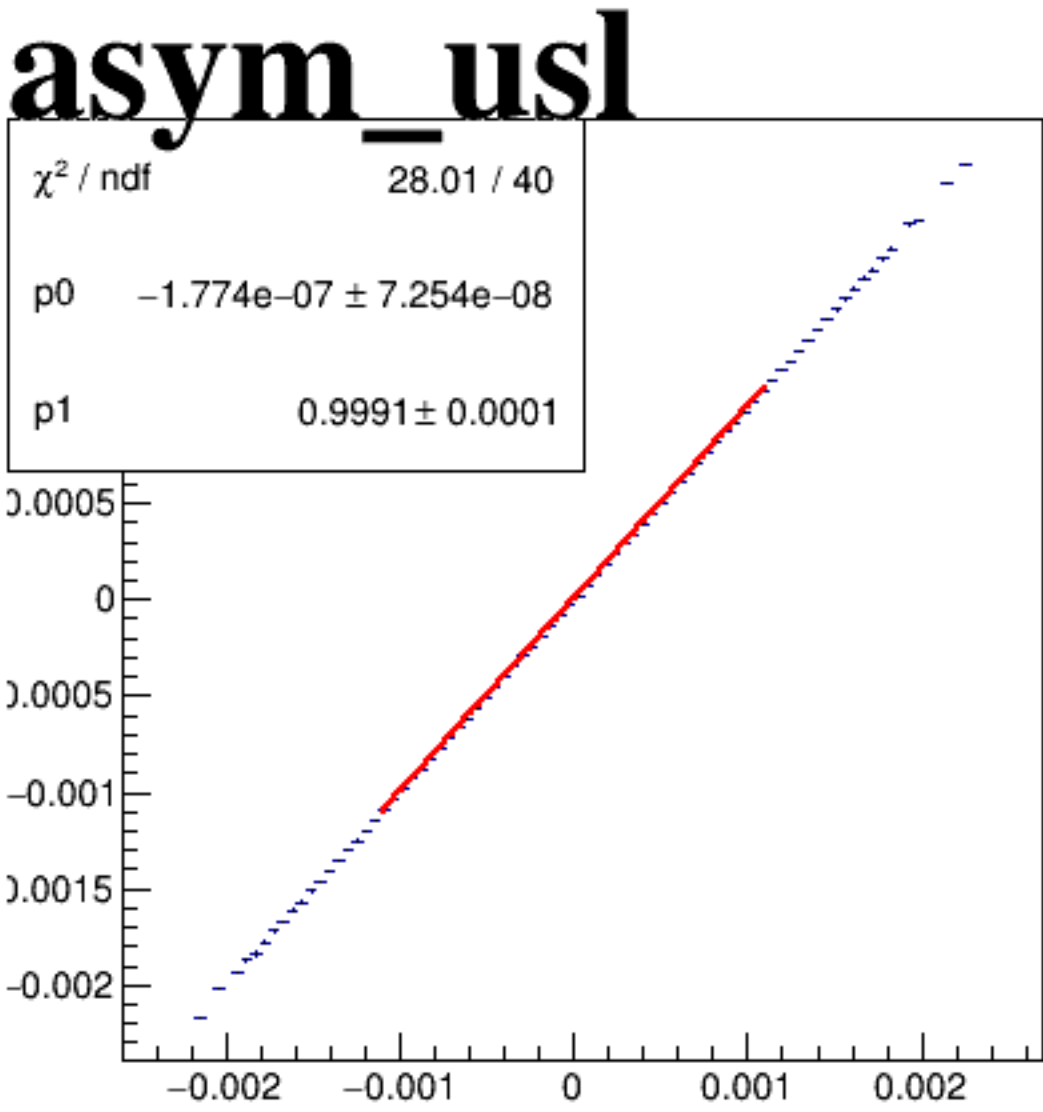




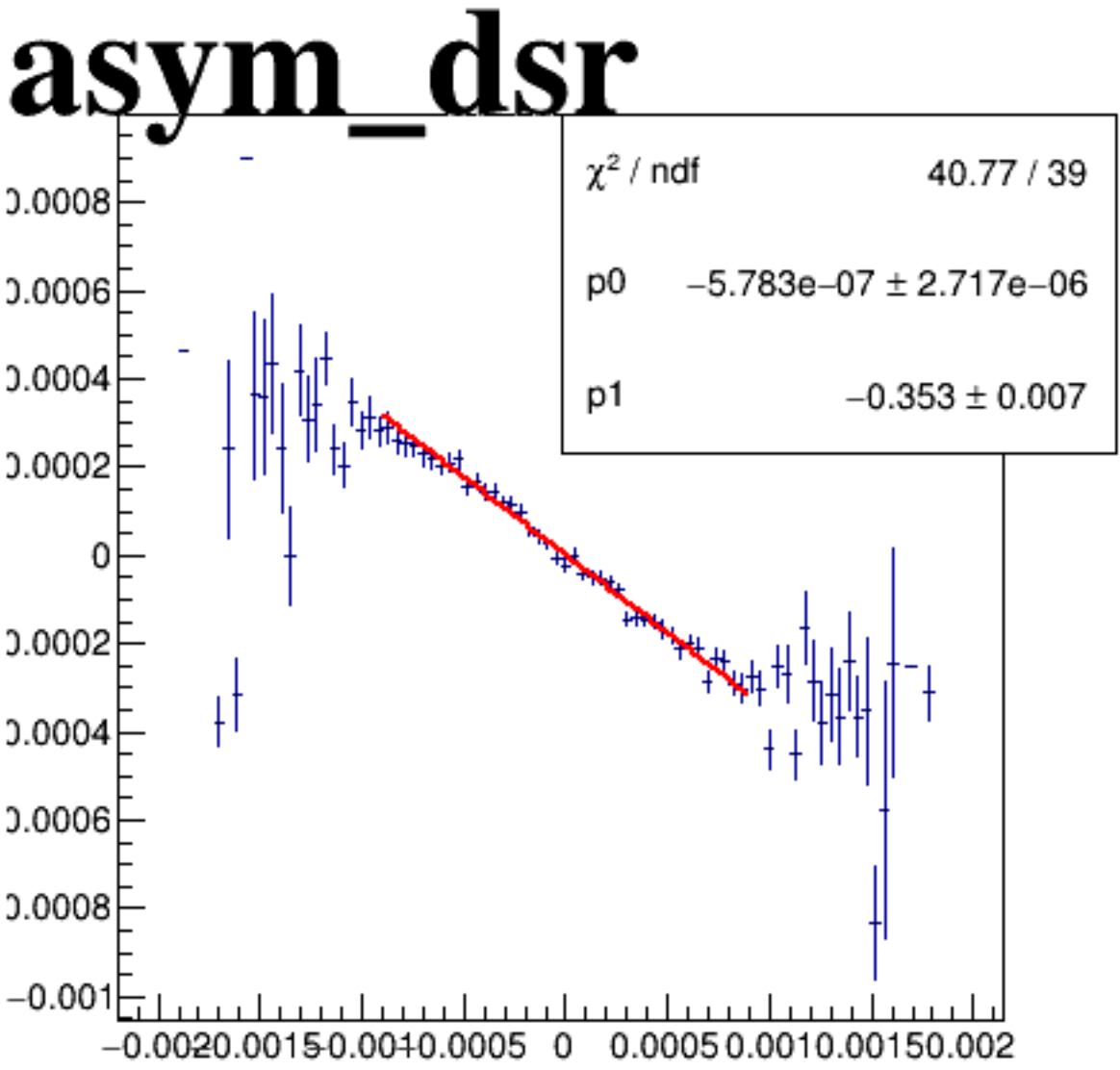
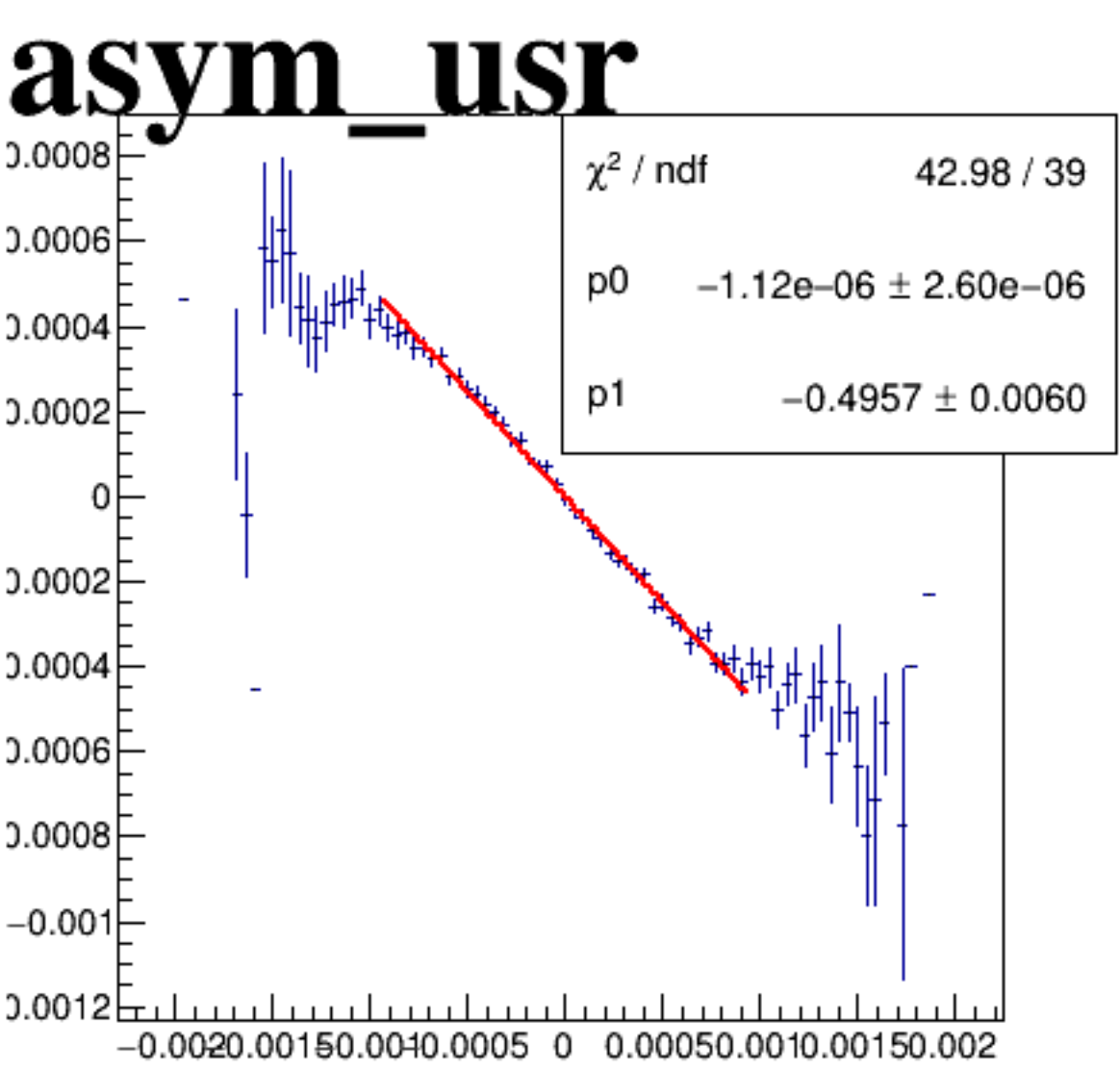
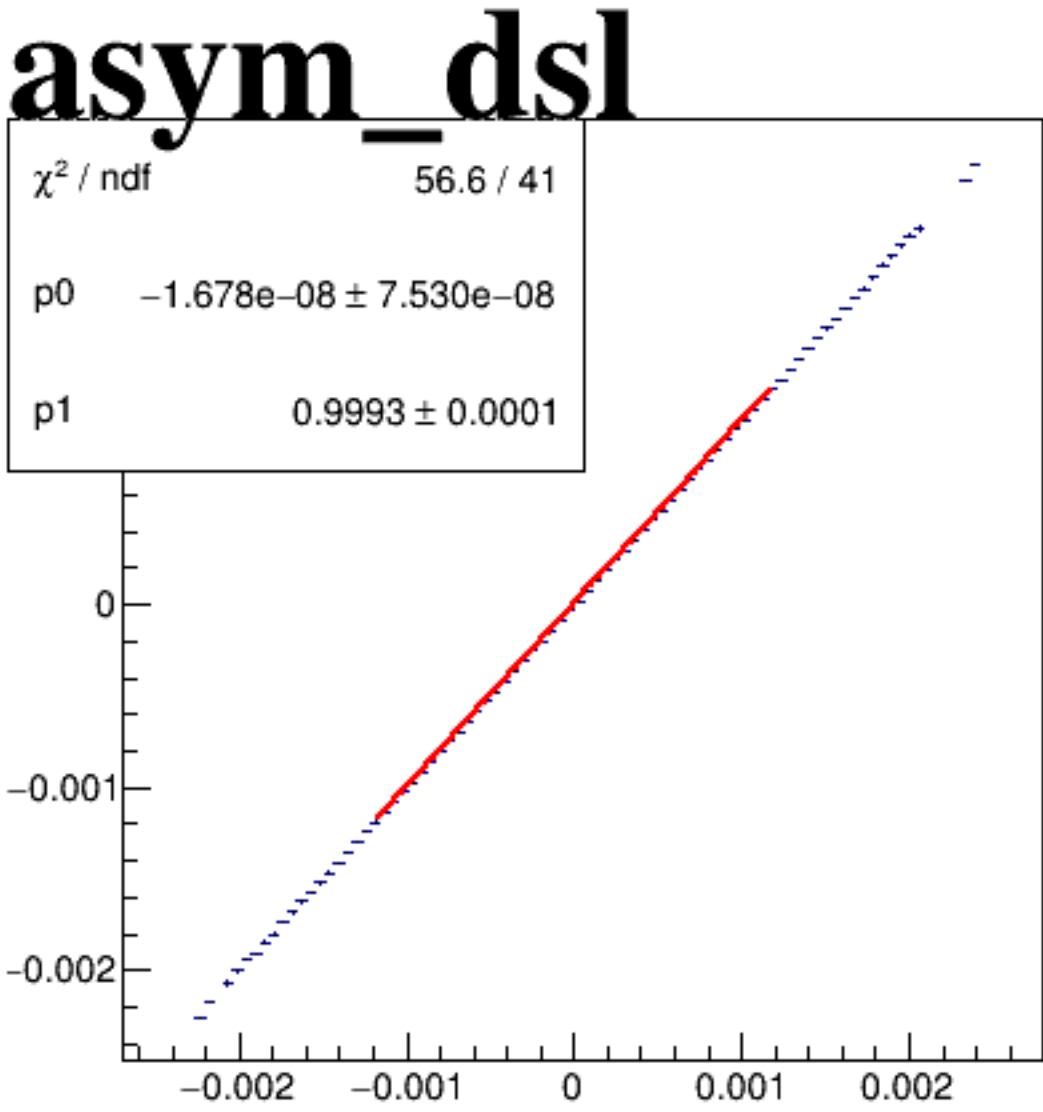
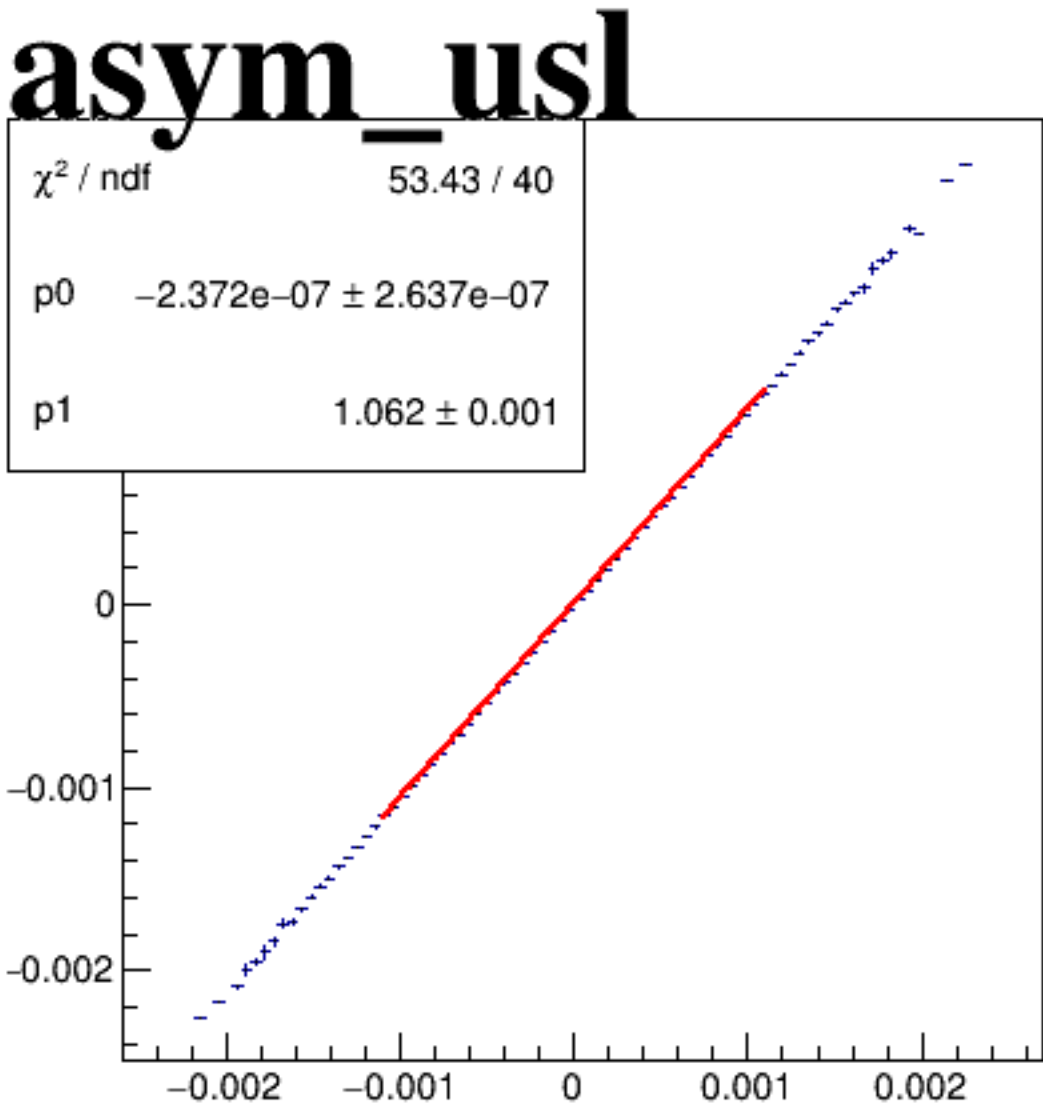




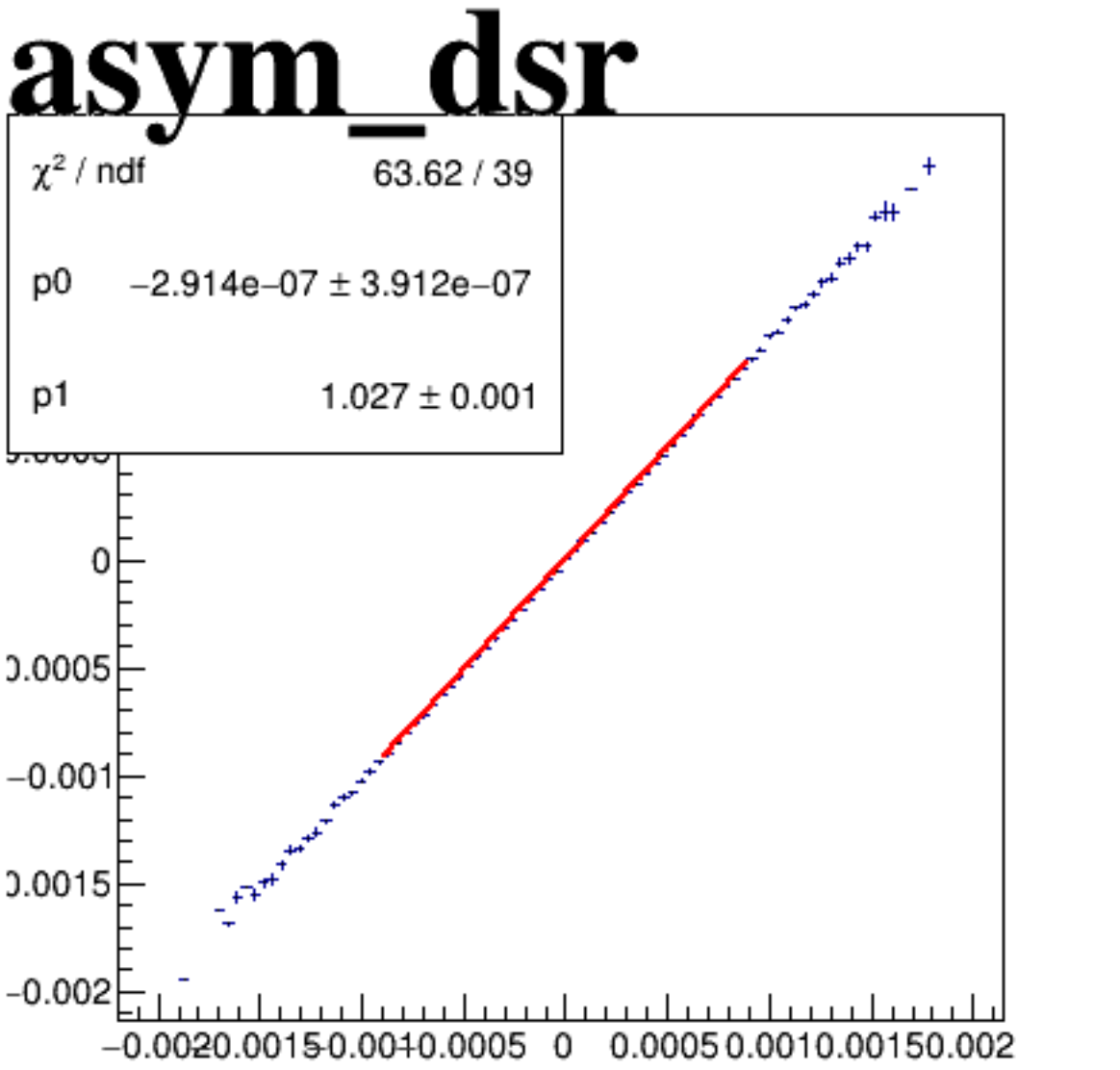
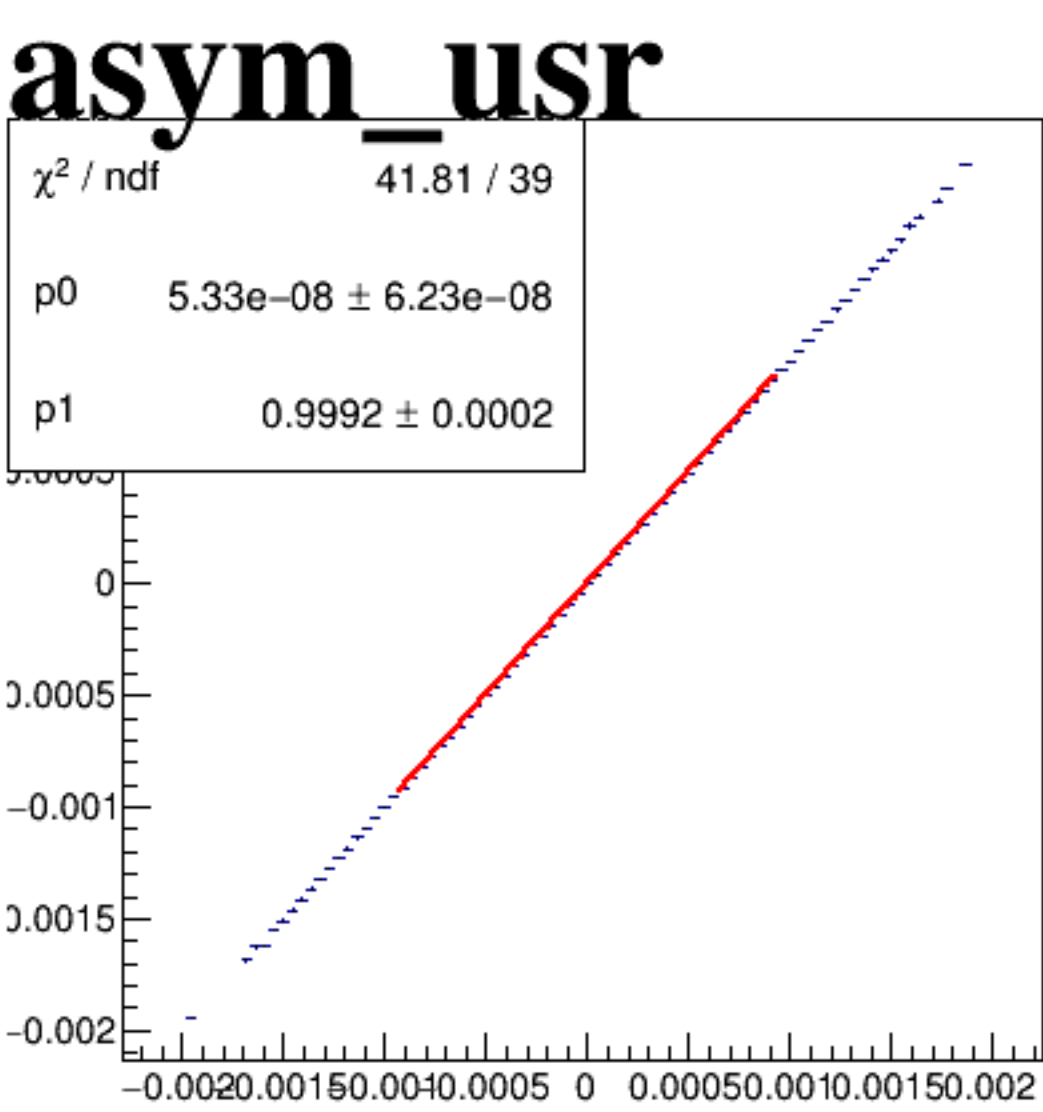
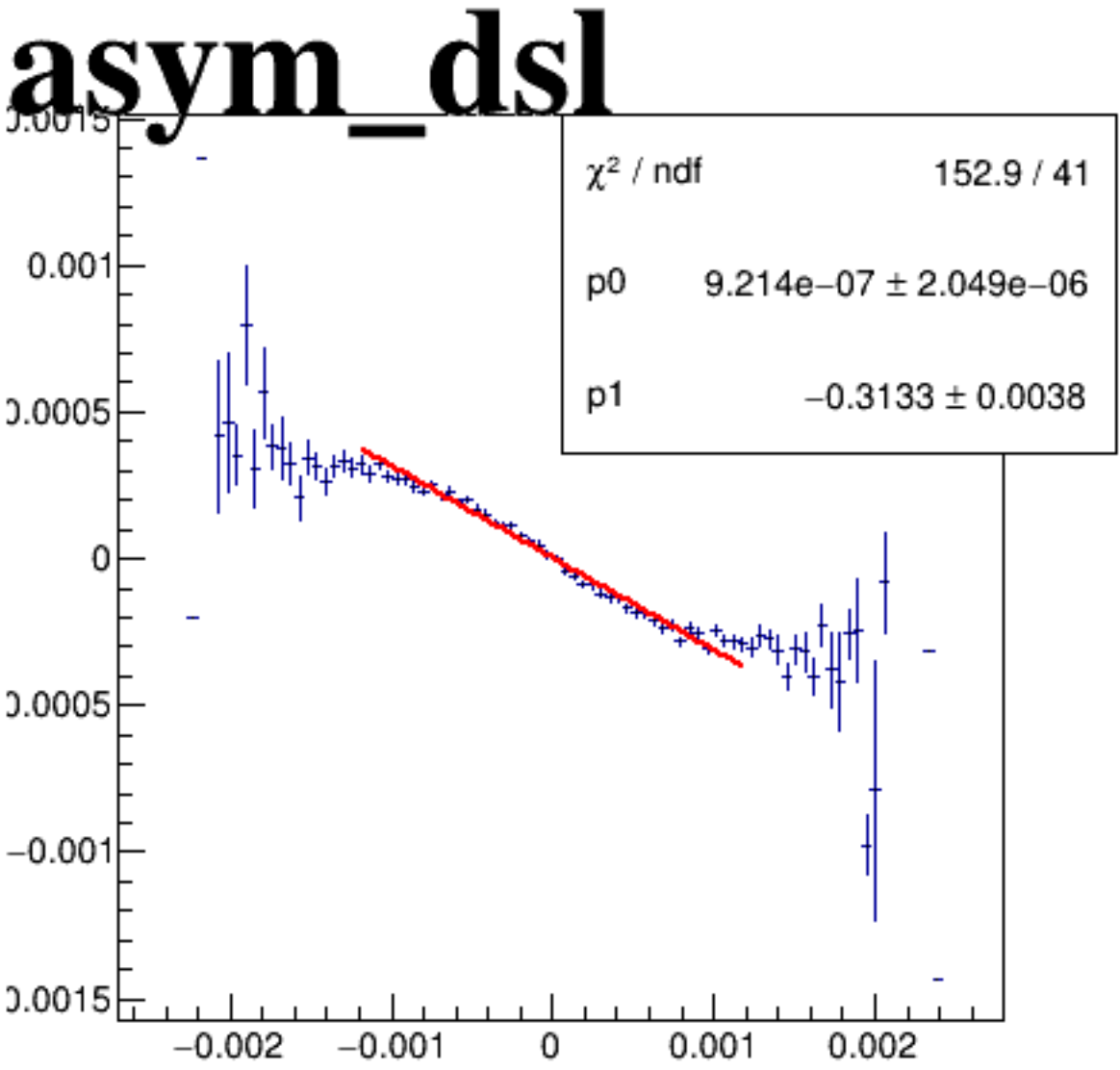
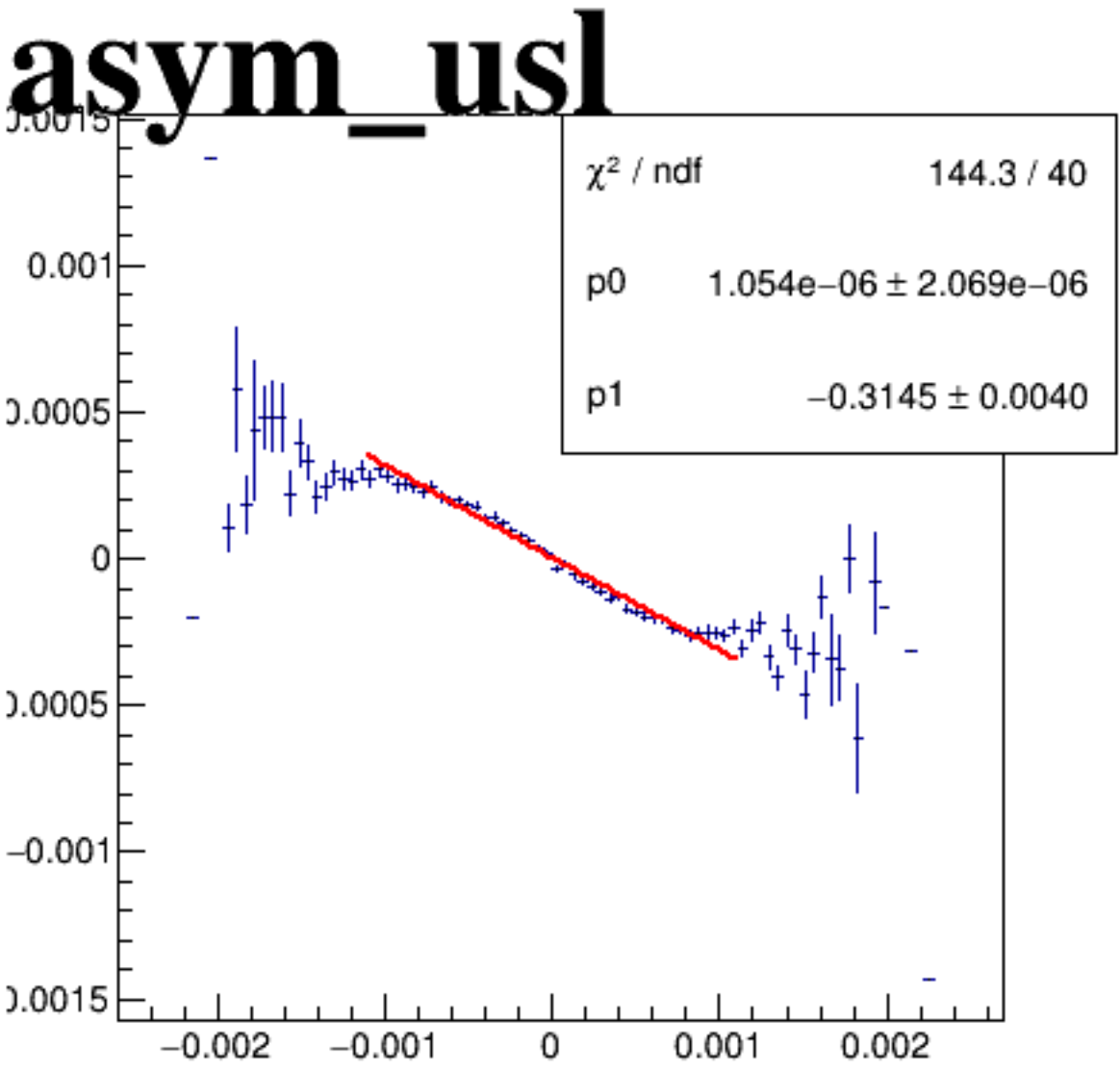
asym\_usl



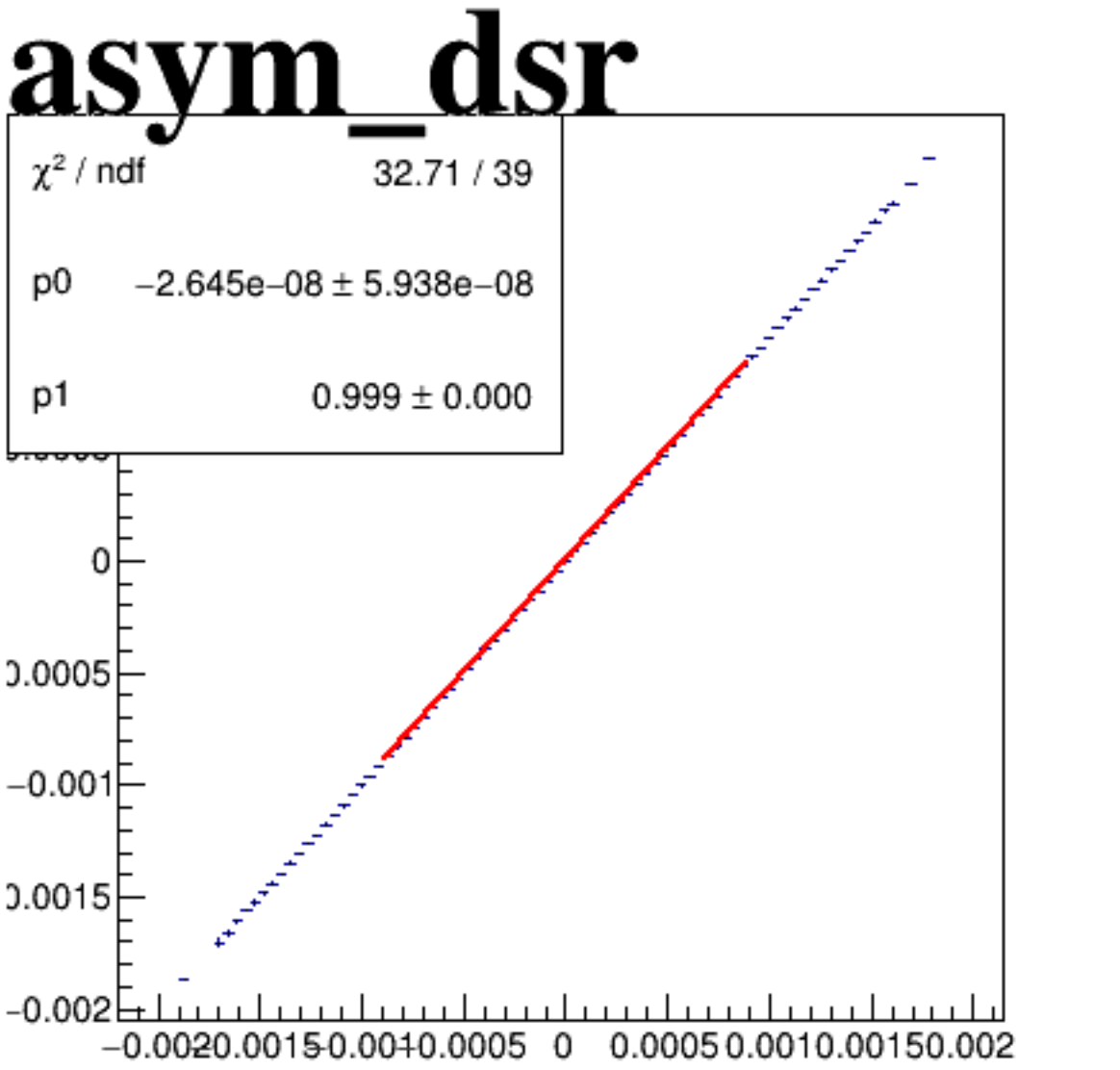
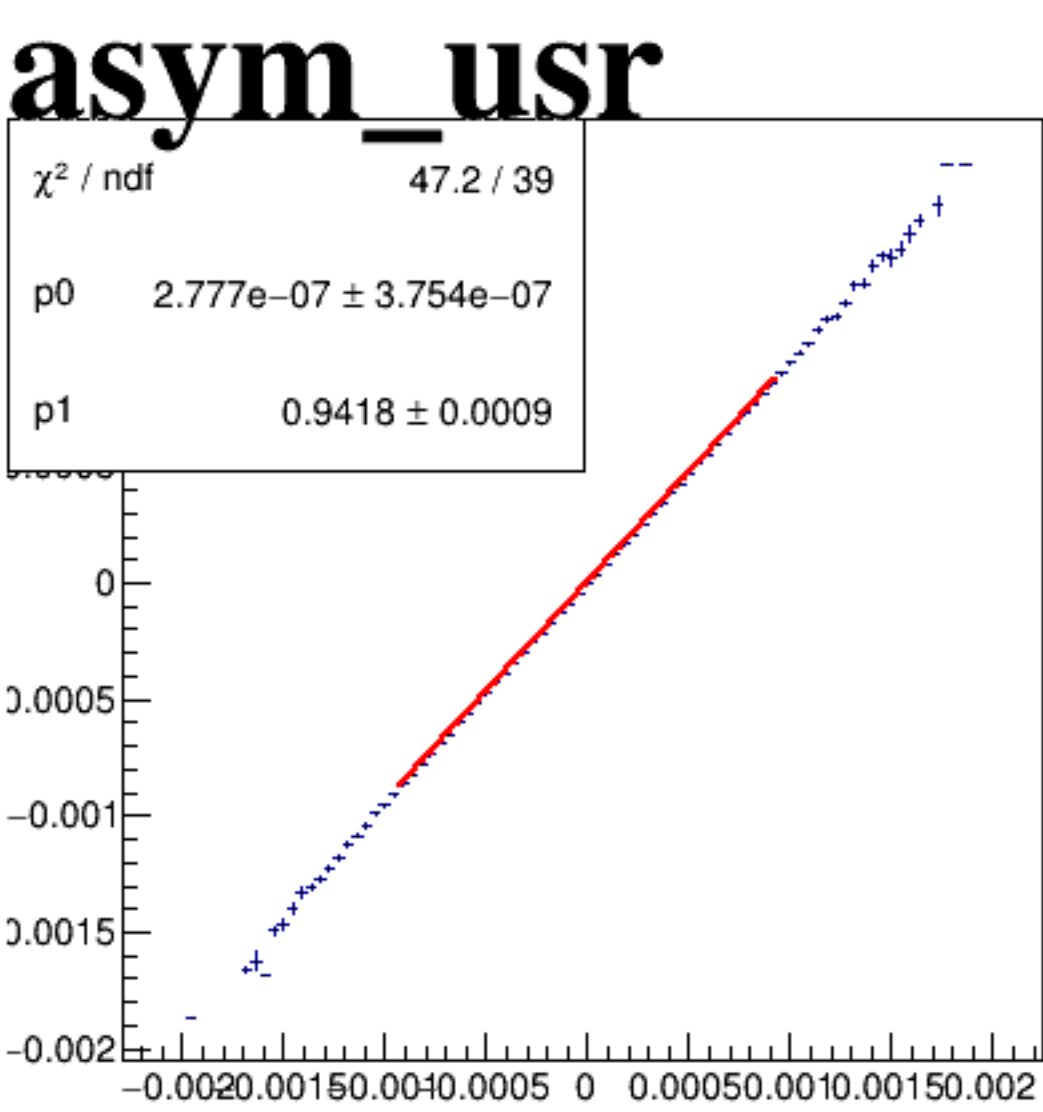
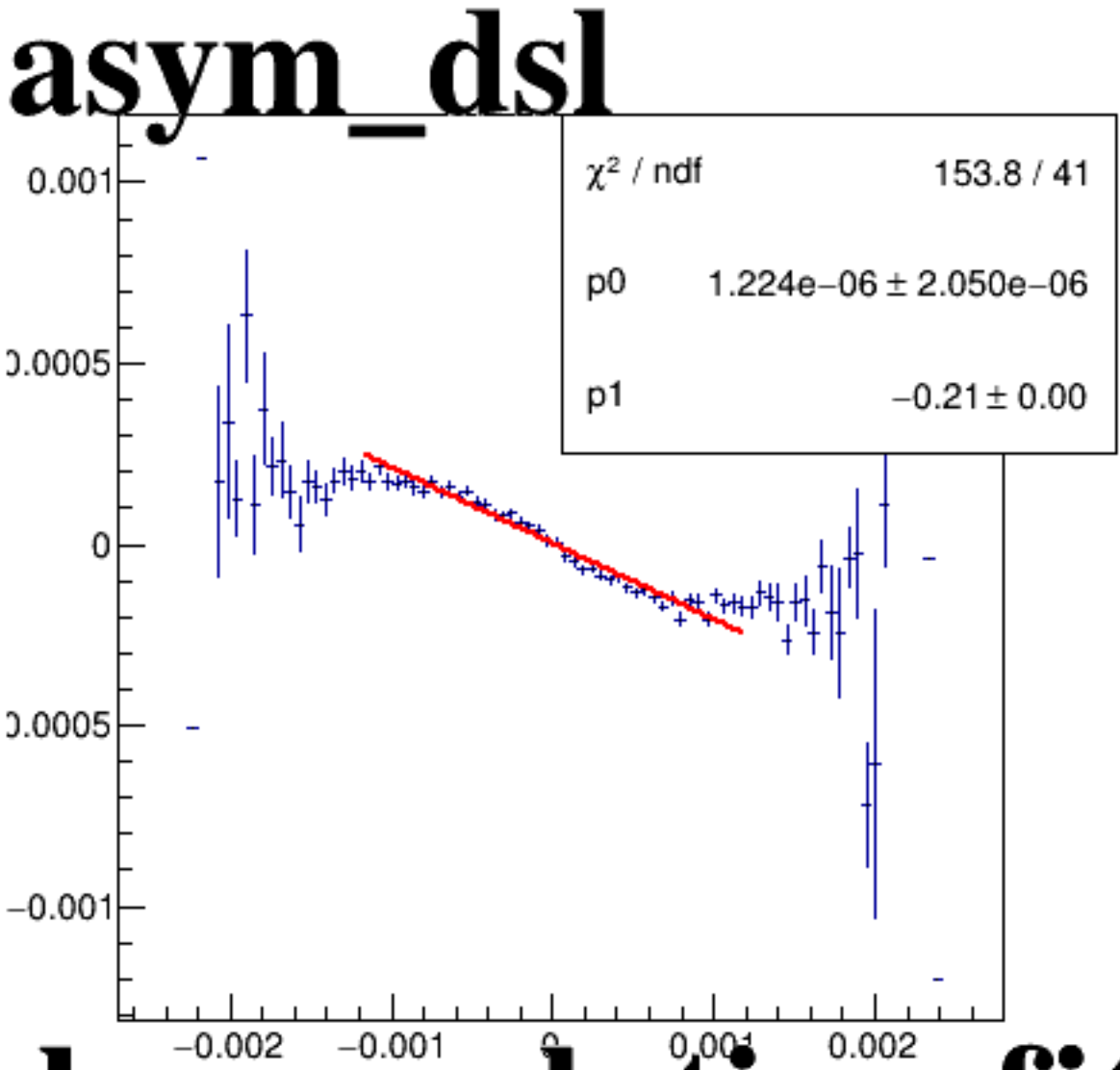
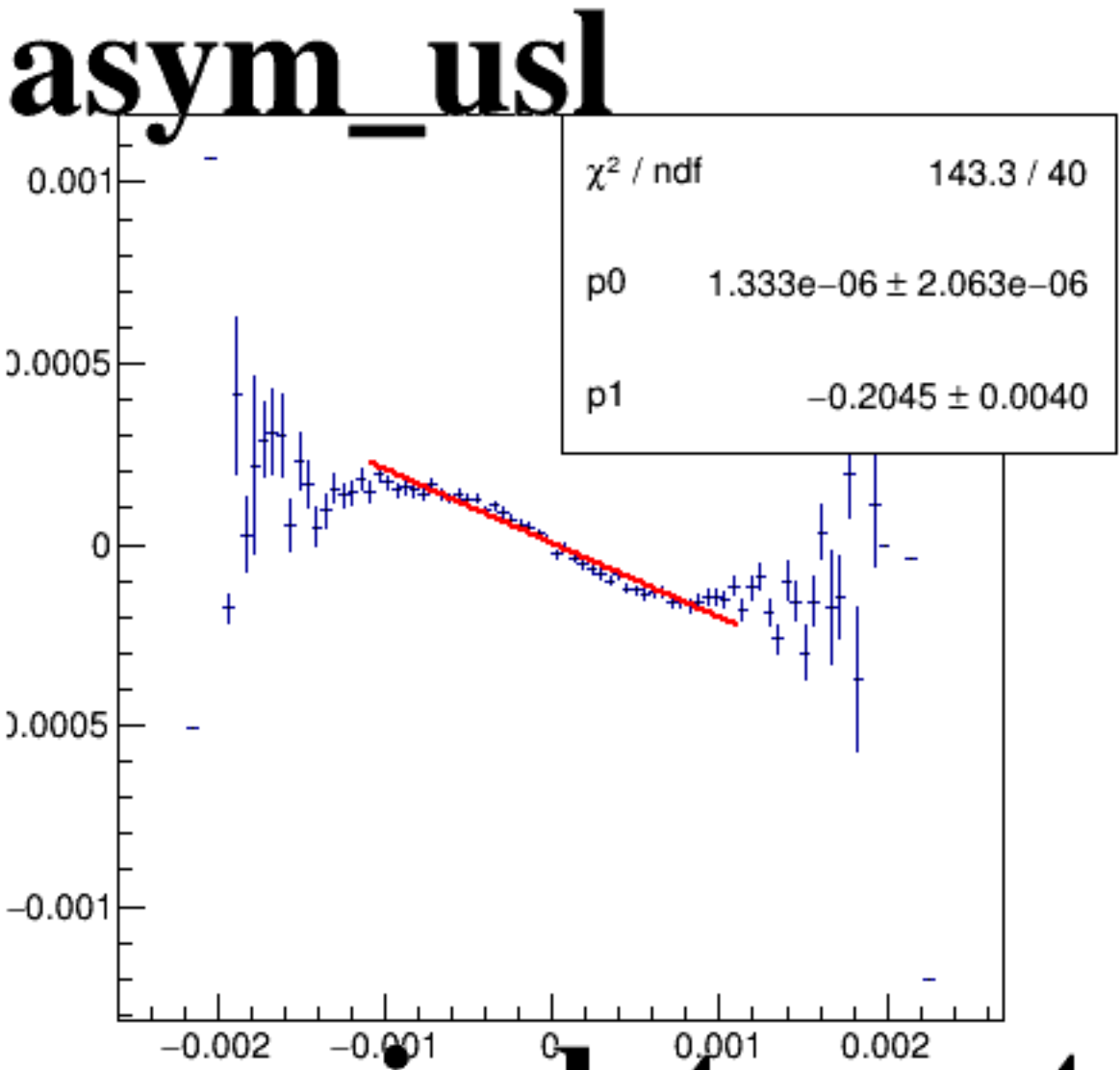
asym\_dsl



asym\_usr

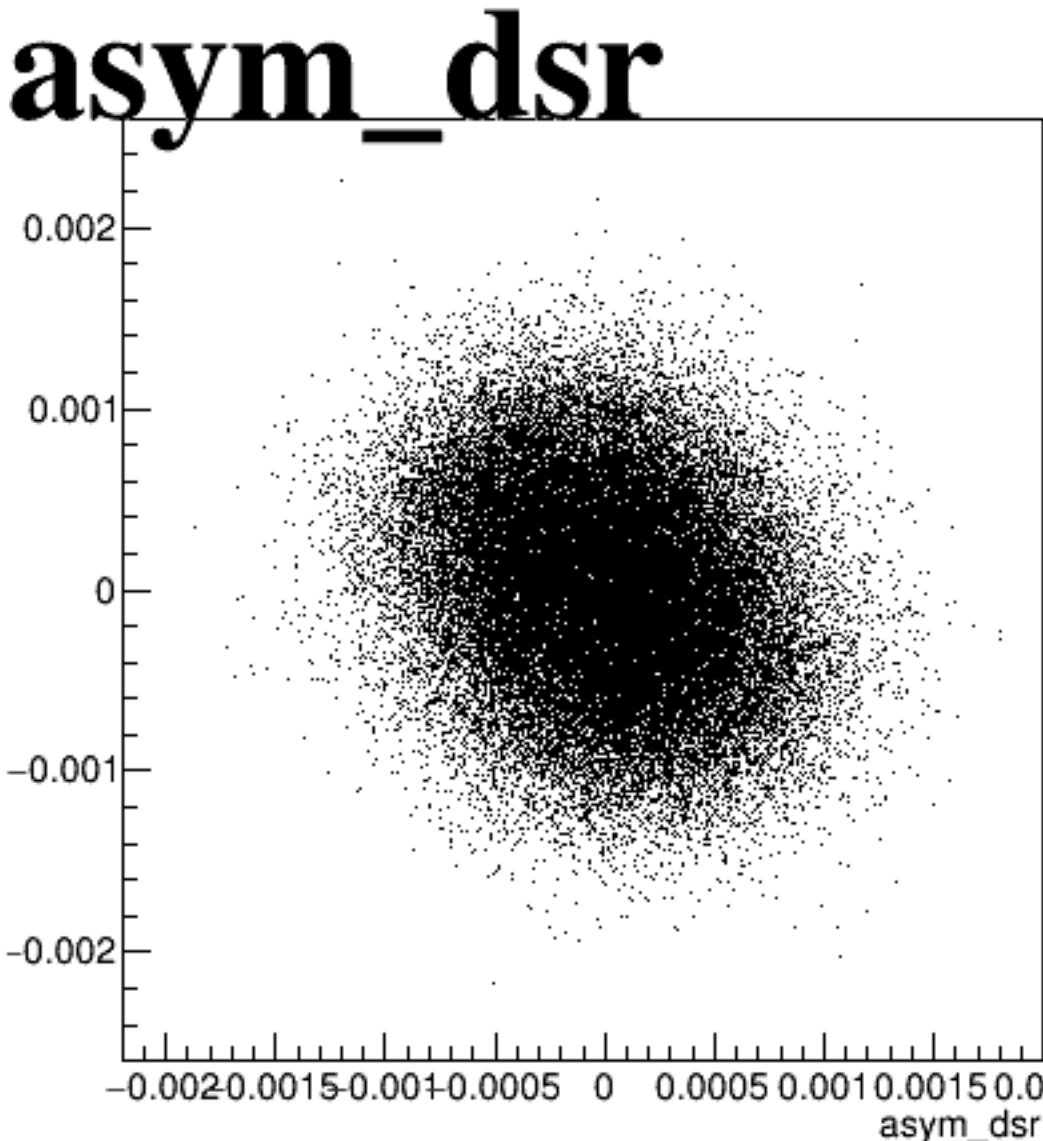
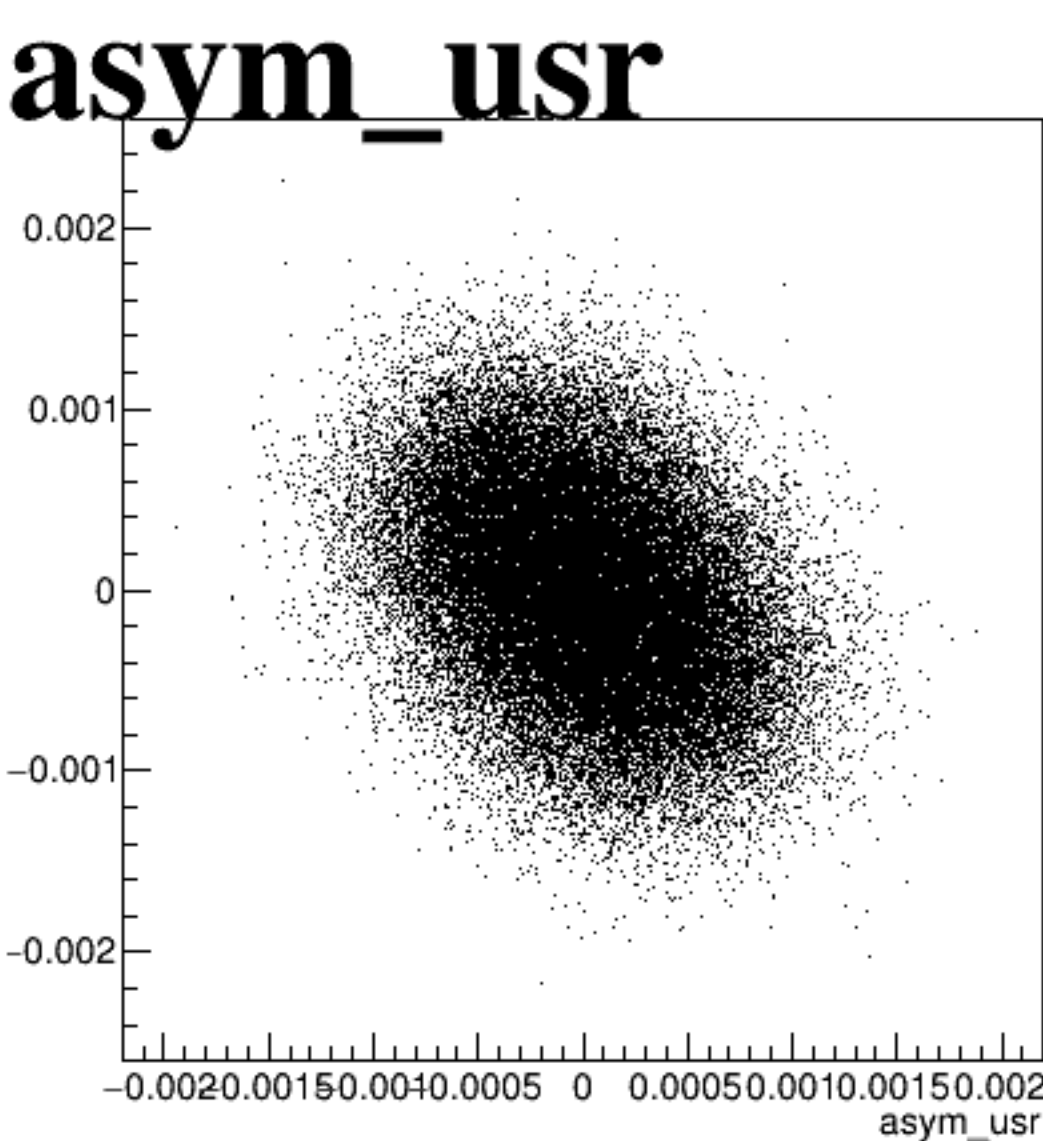
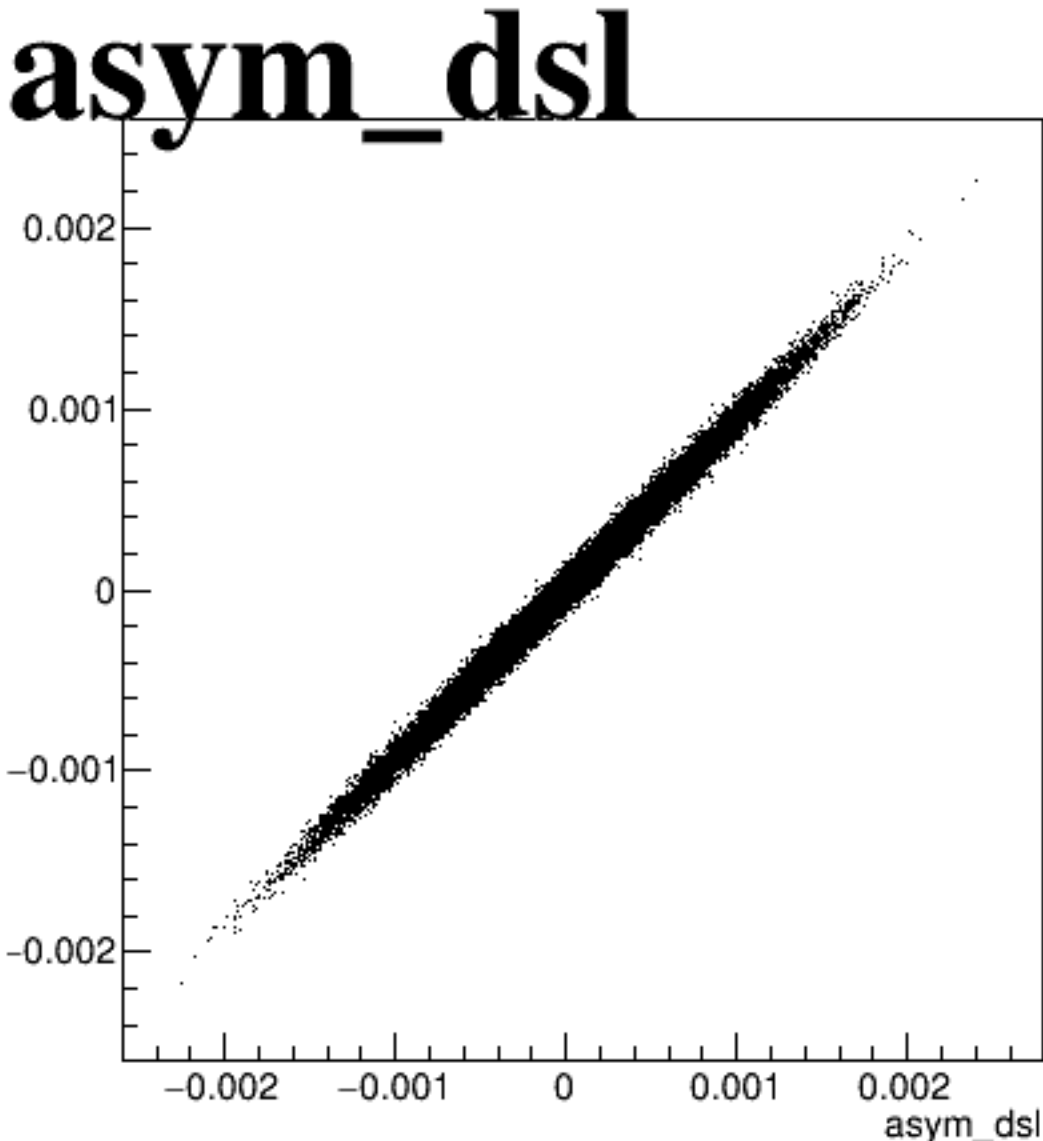
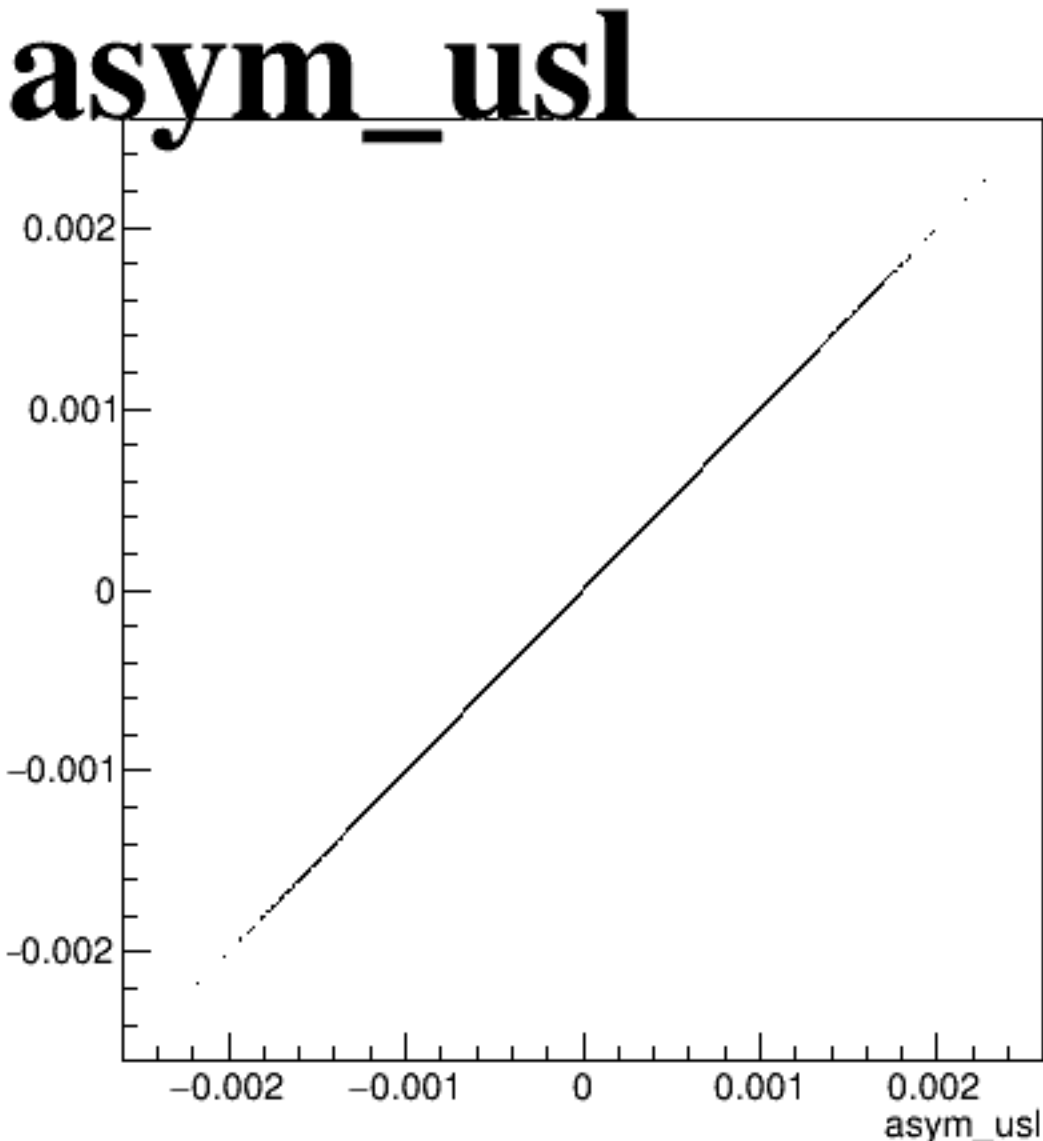


asym\_dsr

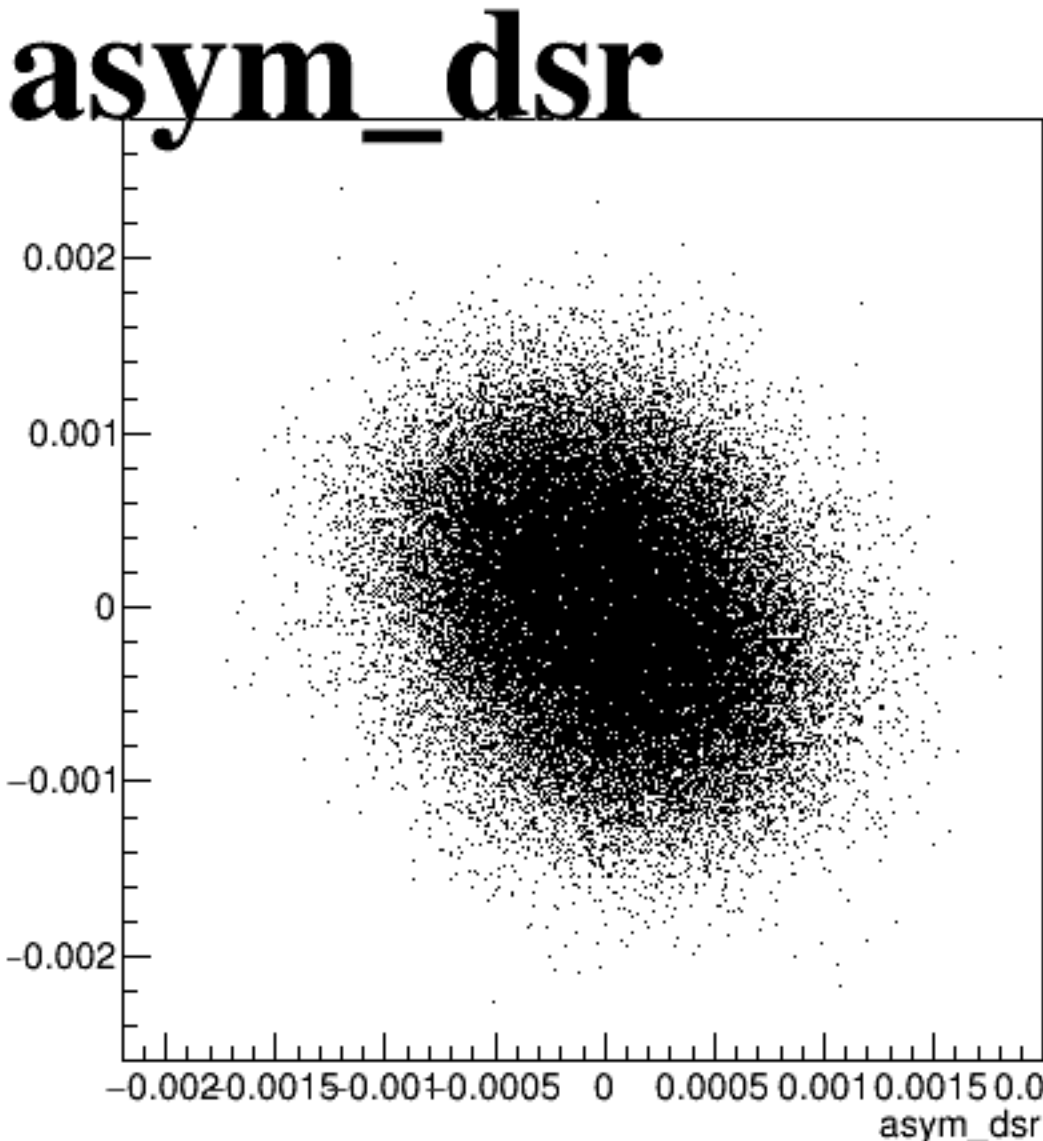
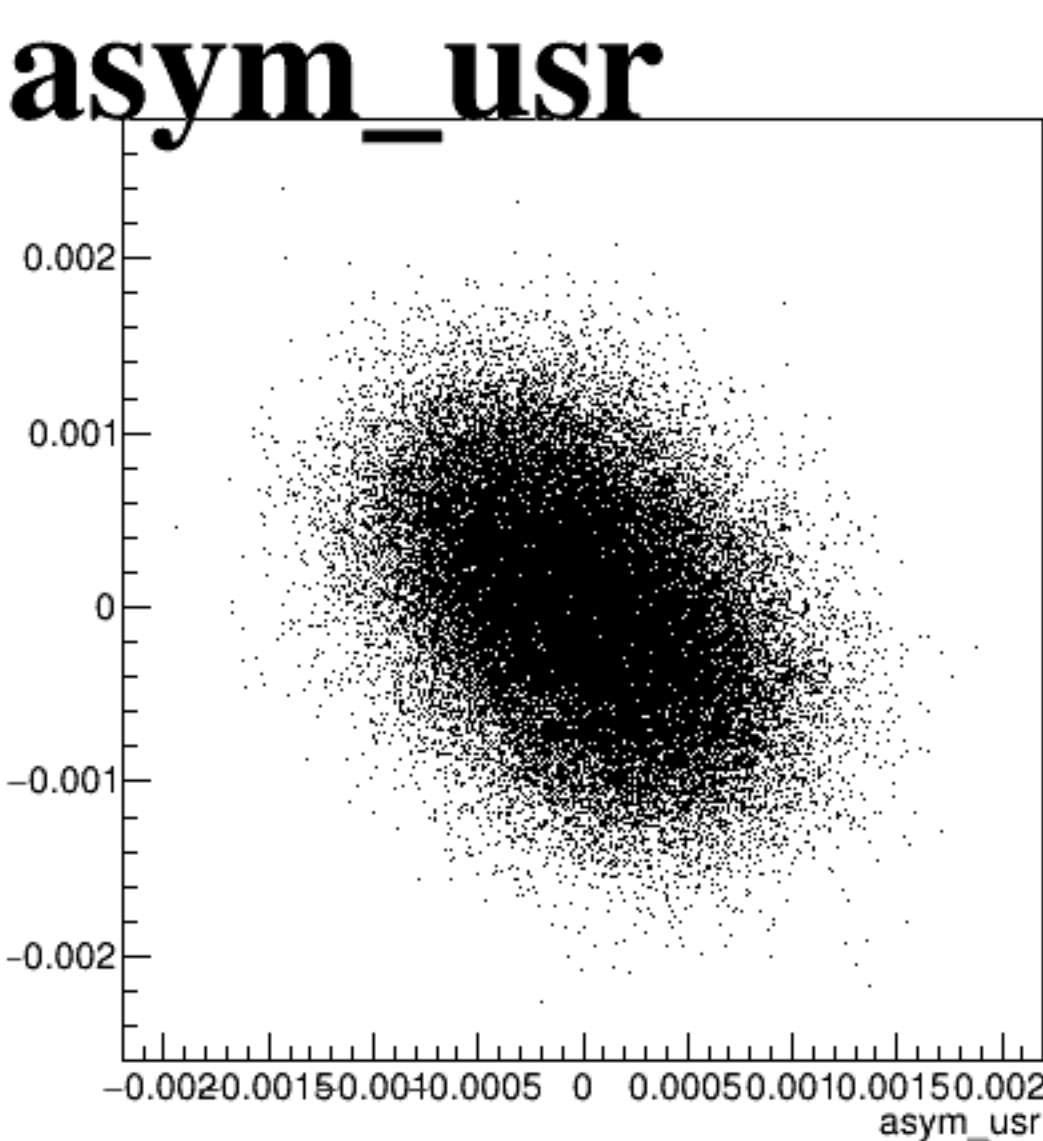
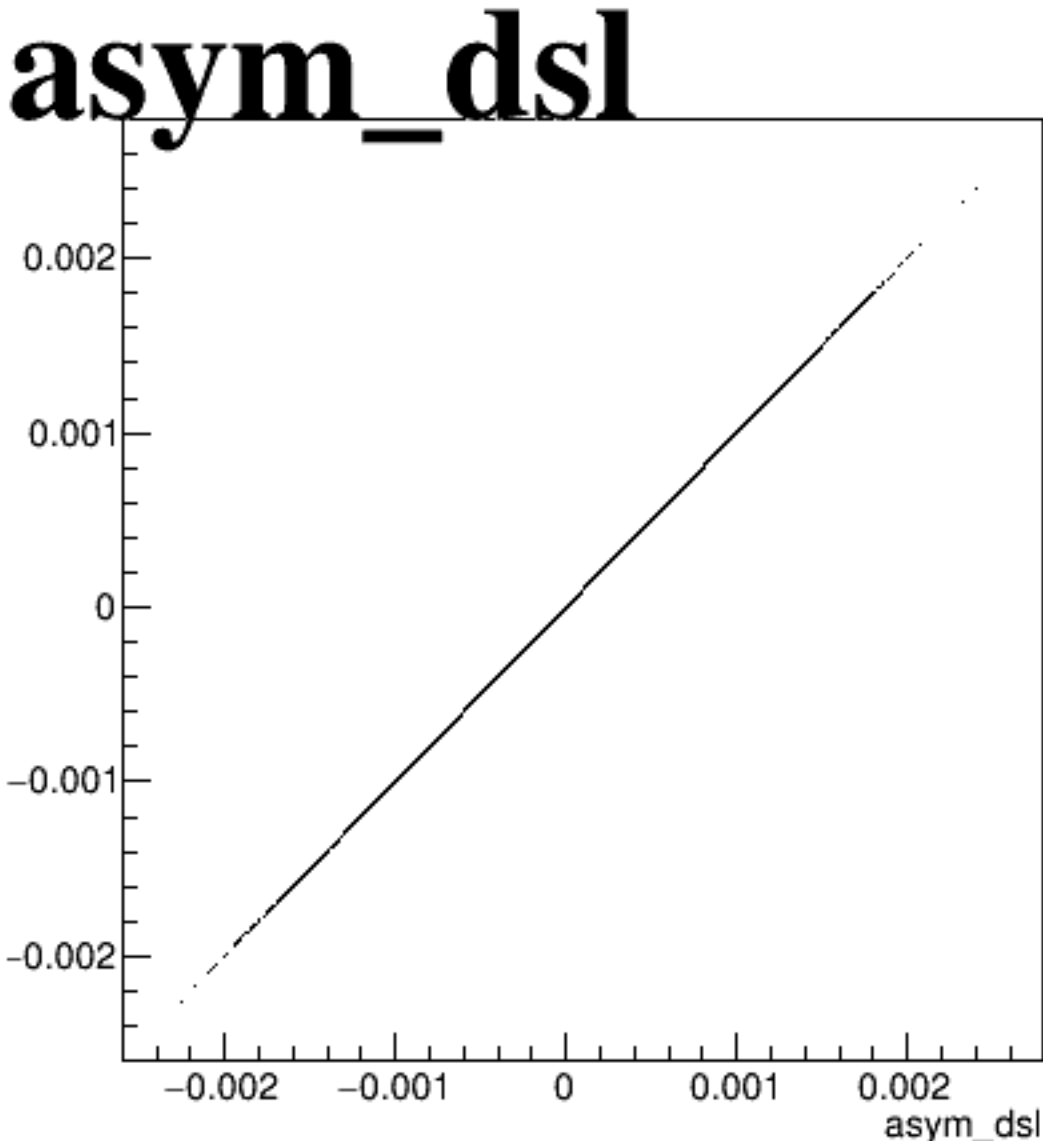
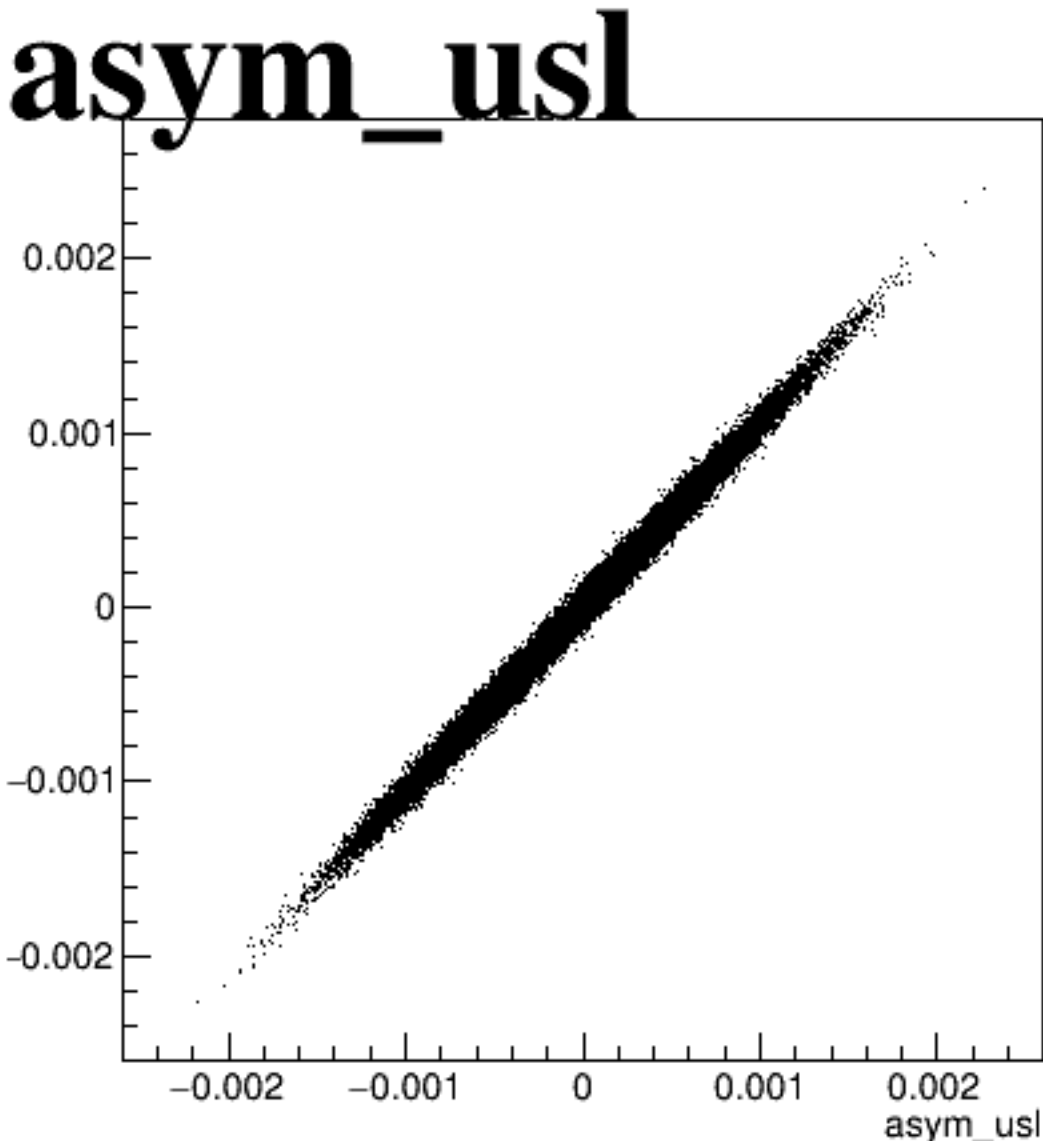




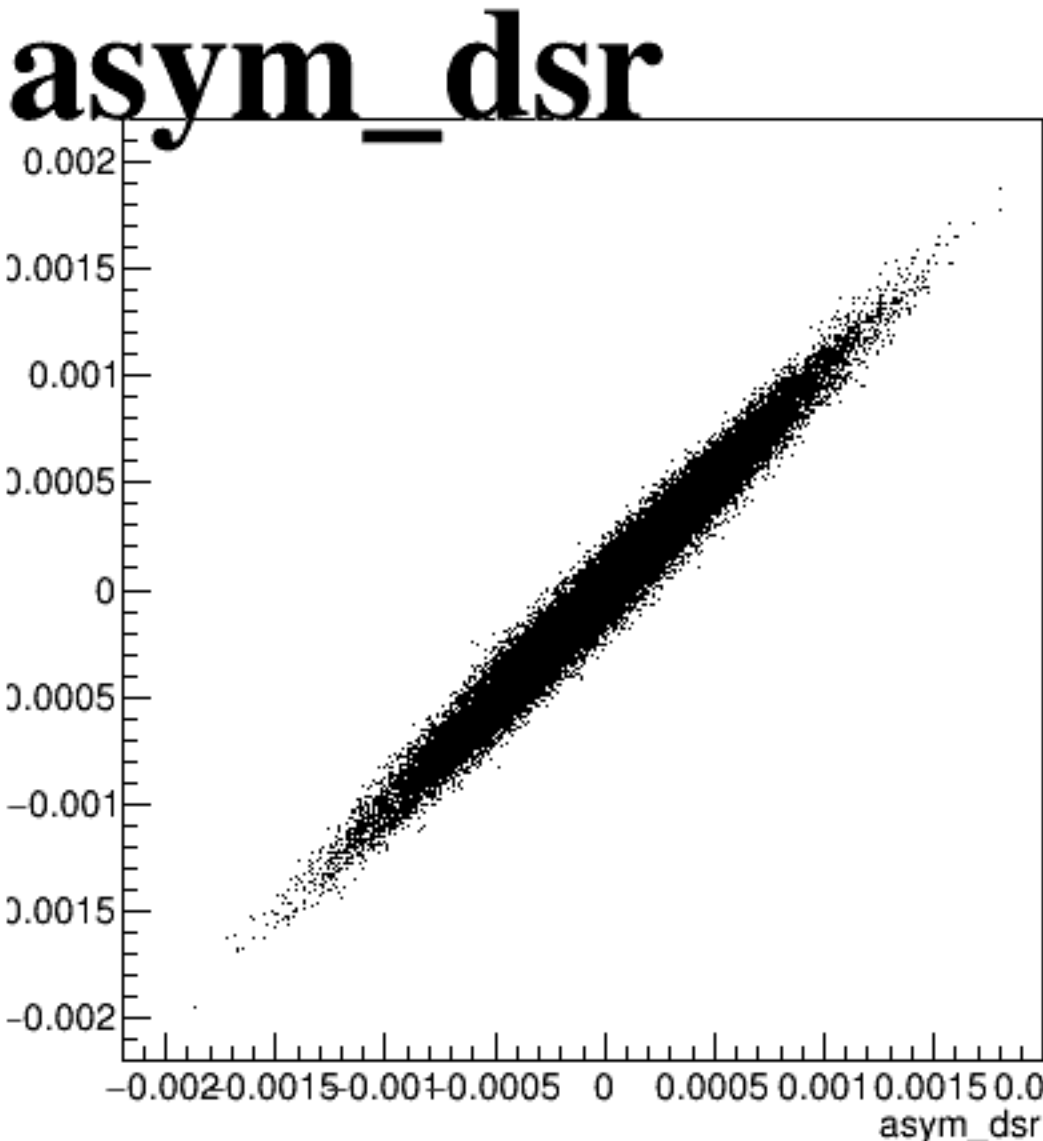
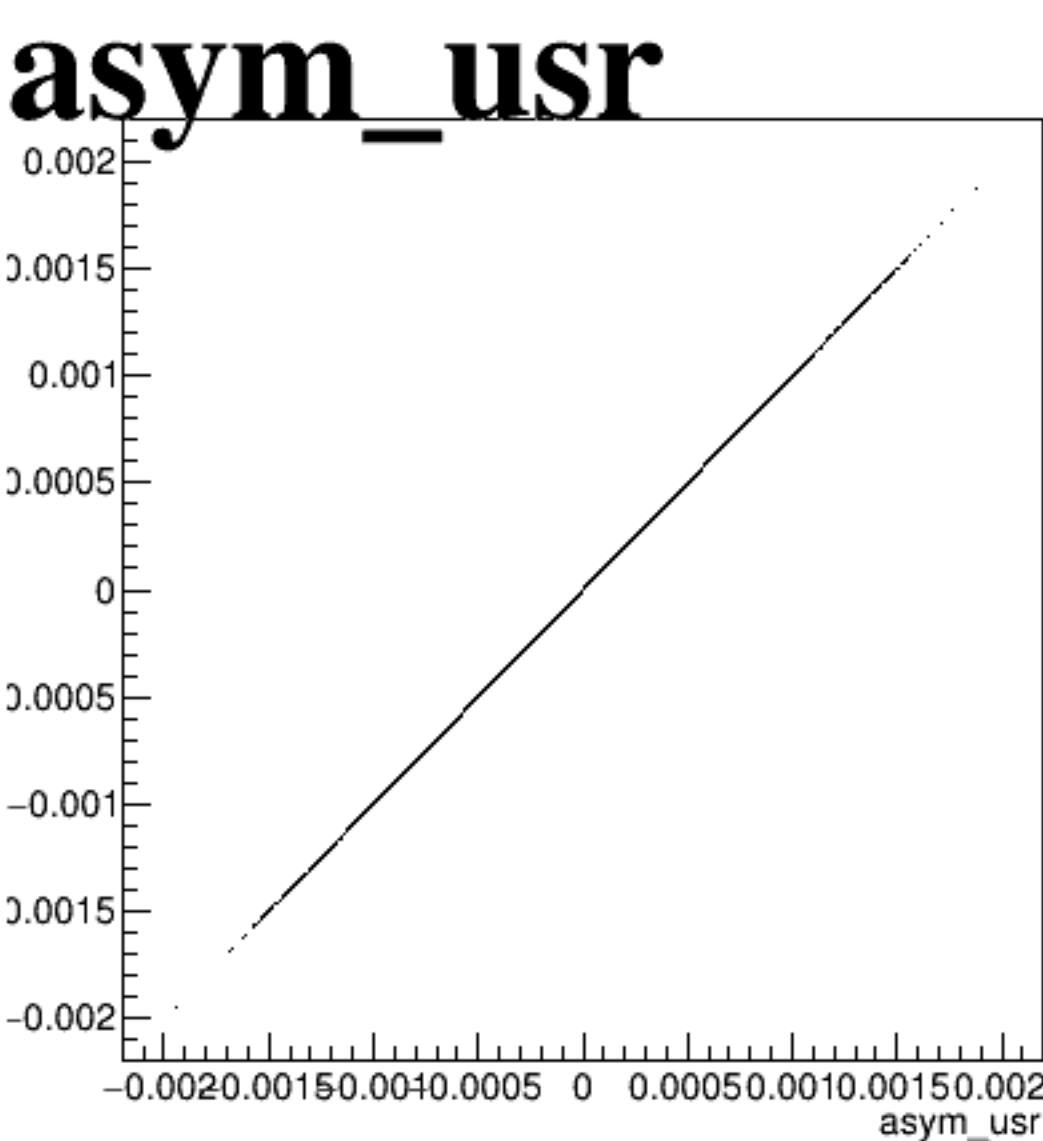
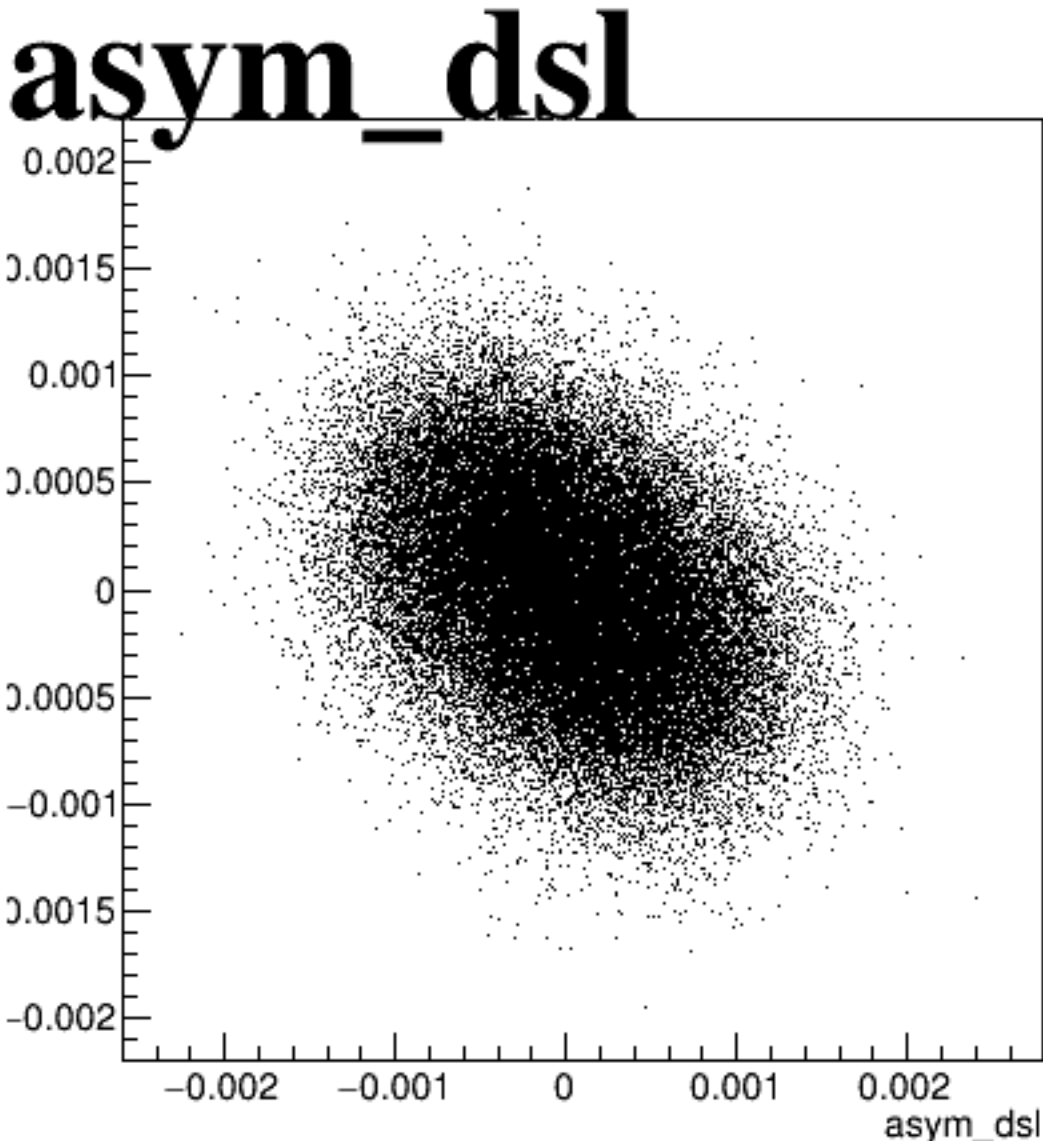
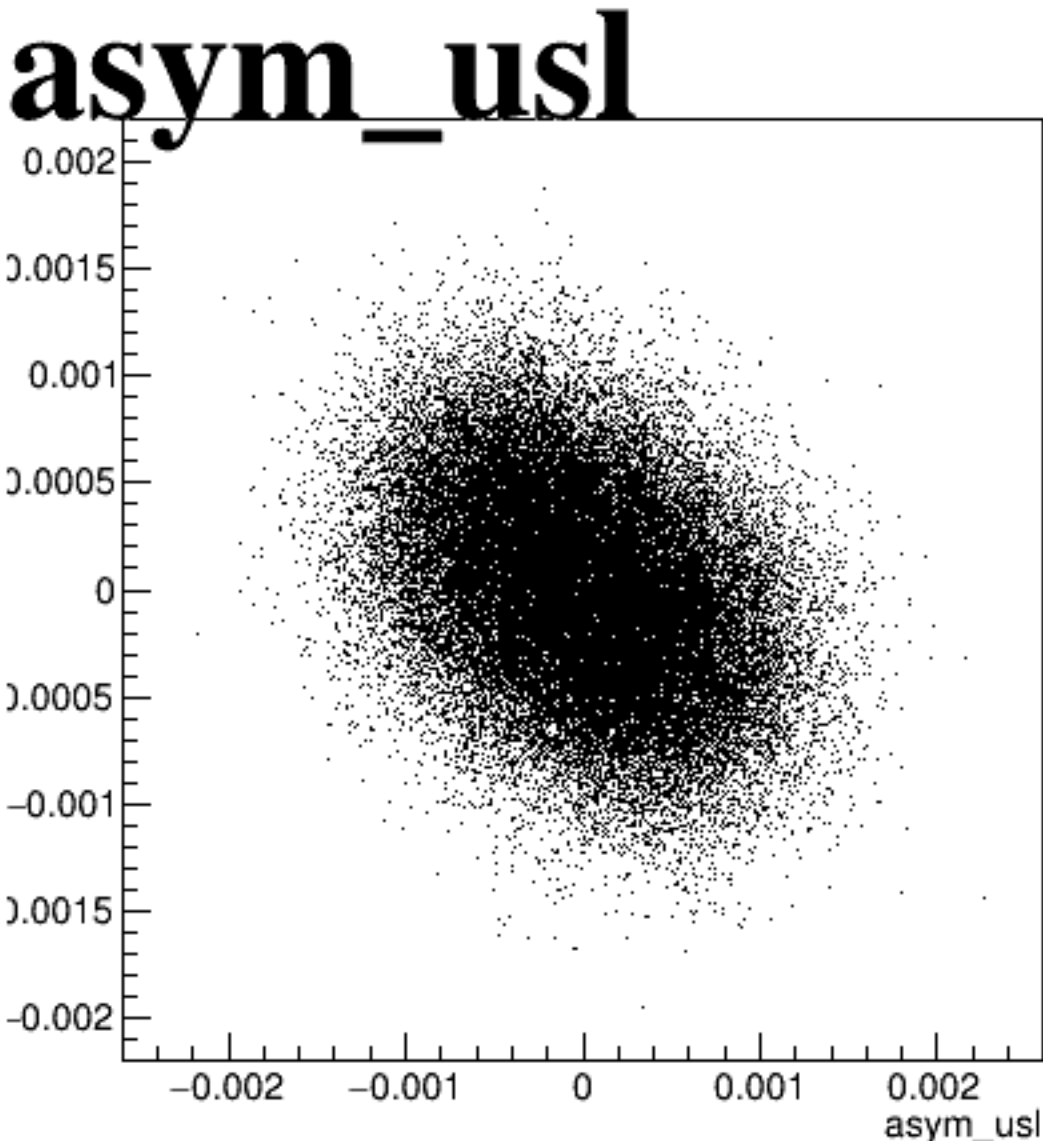
asym\_usl



asym\_dsl



asym\_usr



asym\_dsr

