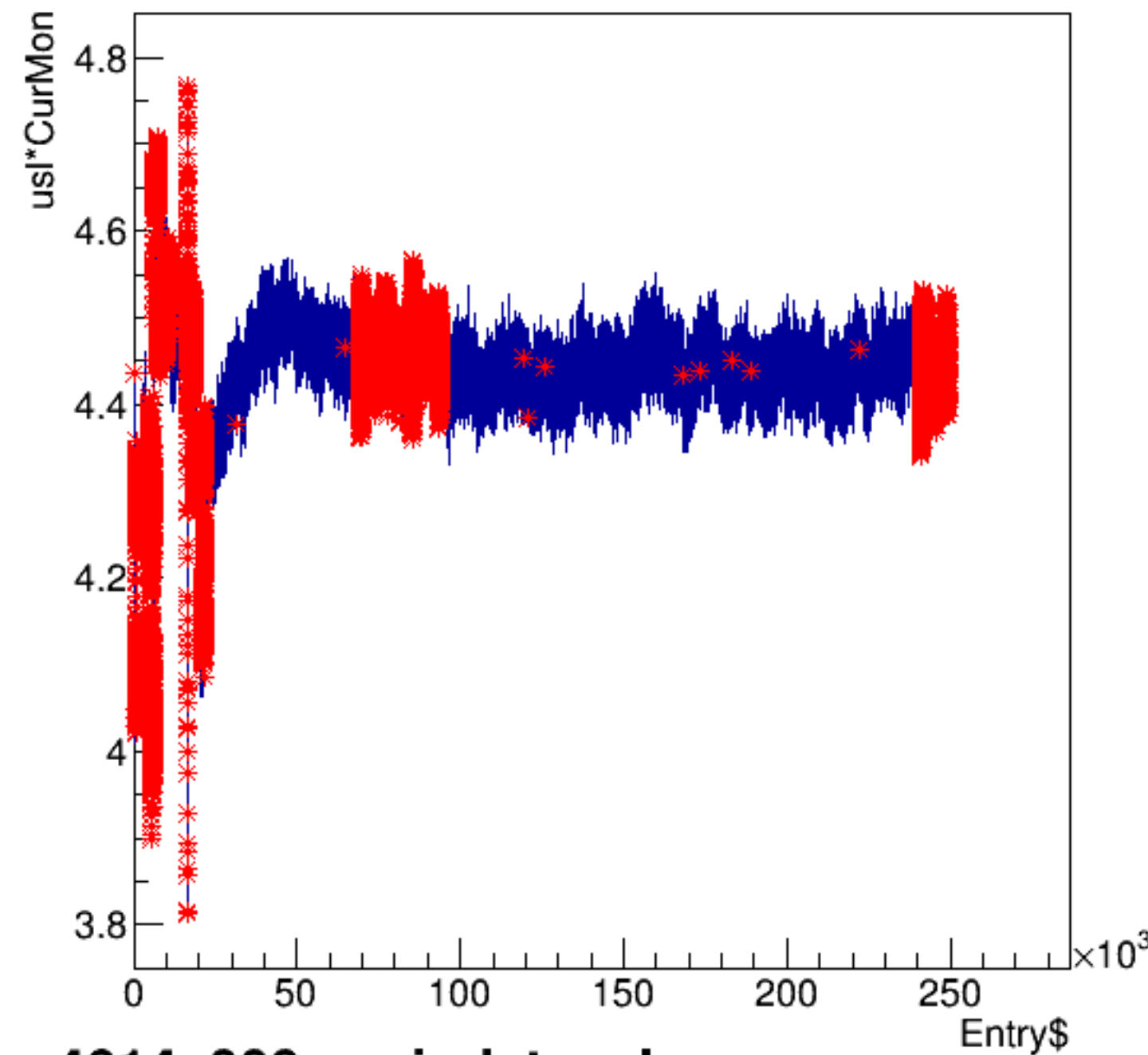
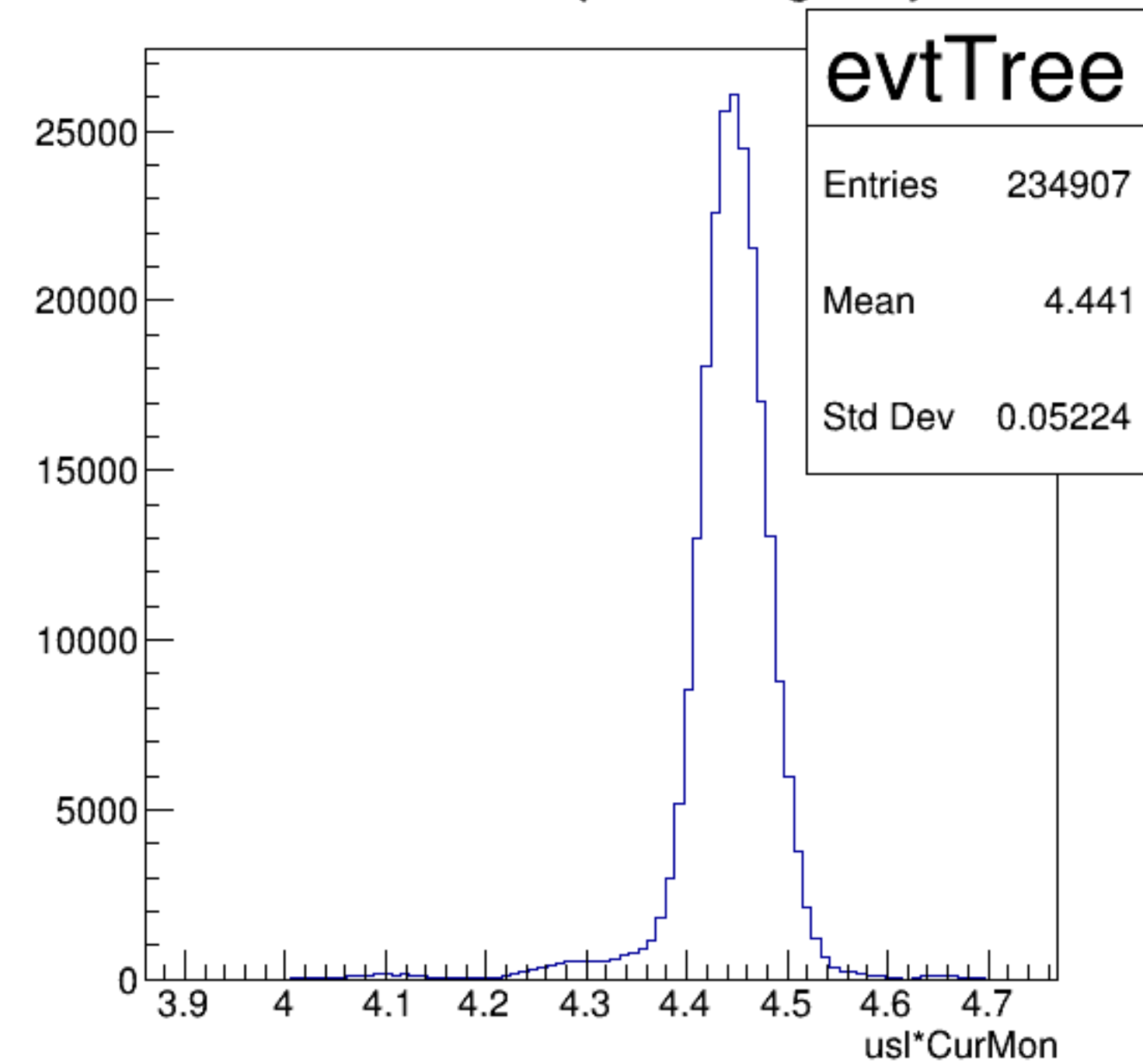


usl*CurMon:Entry\$

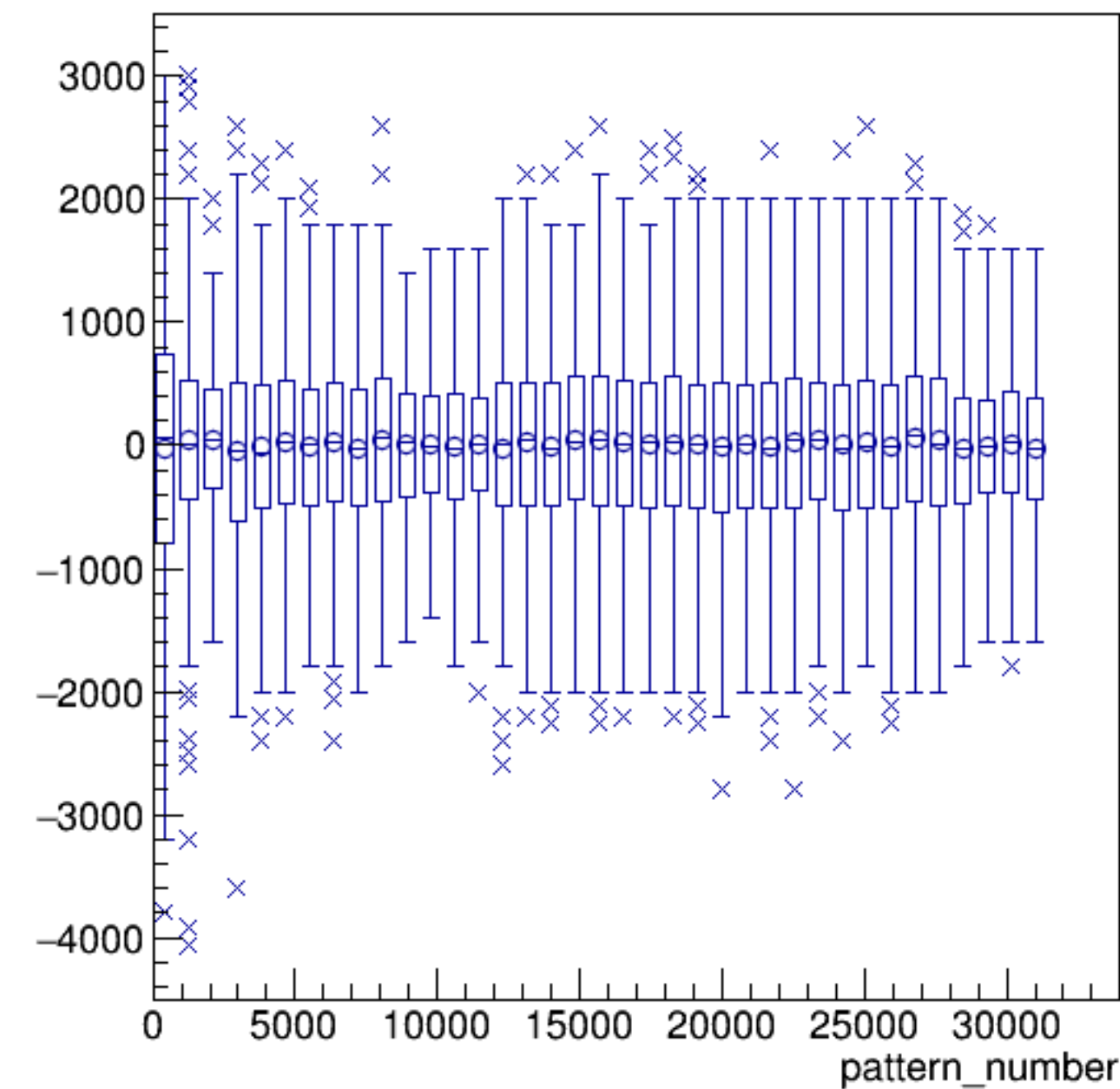


run4614_000_maindet_usl.png

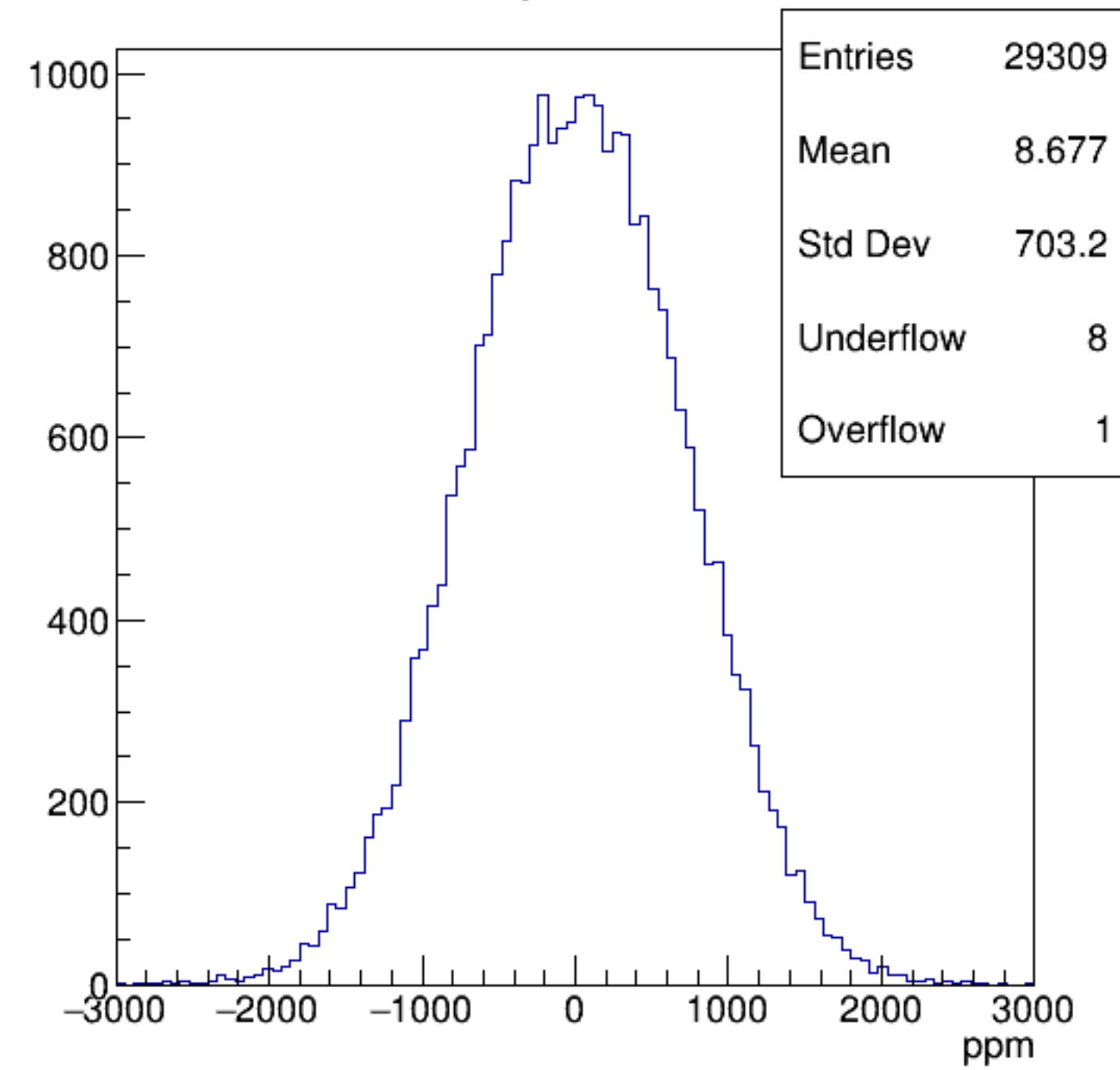
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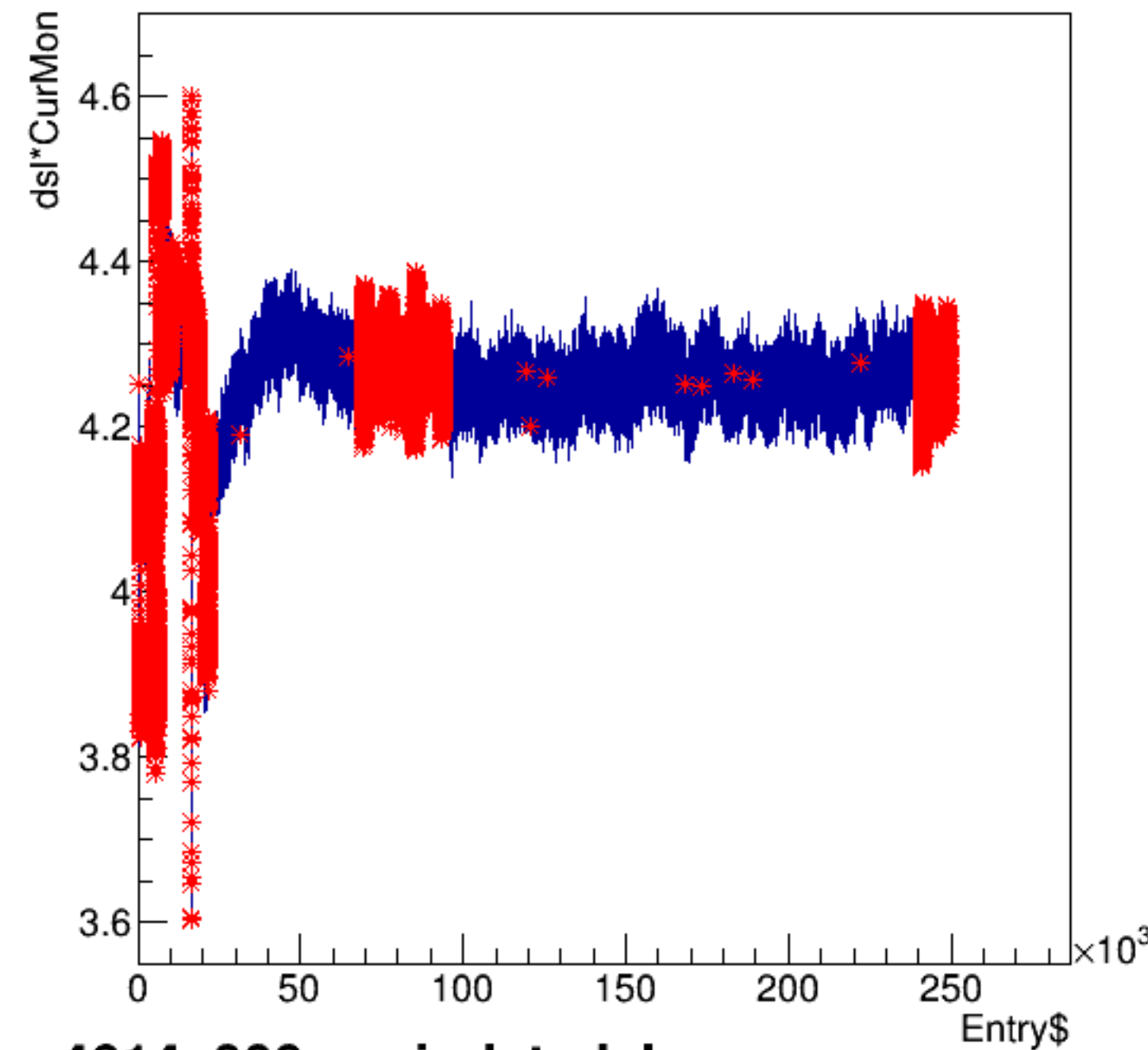
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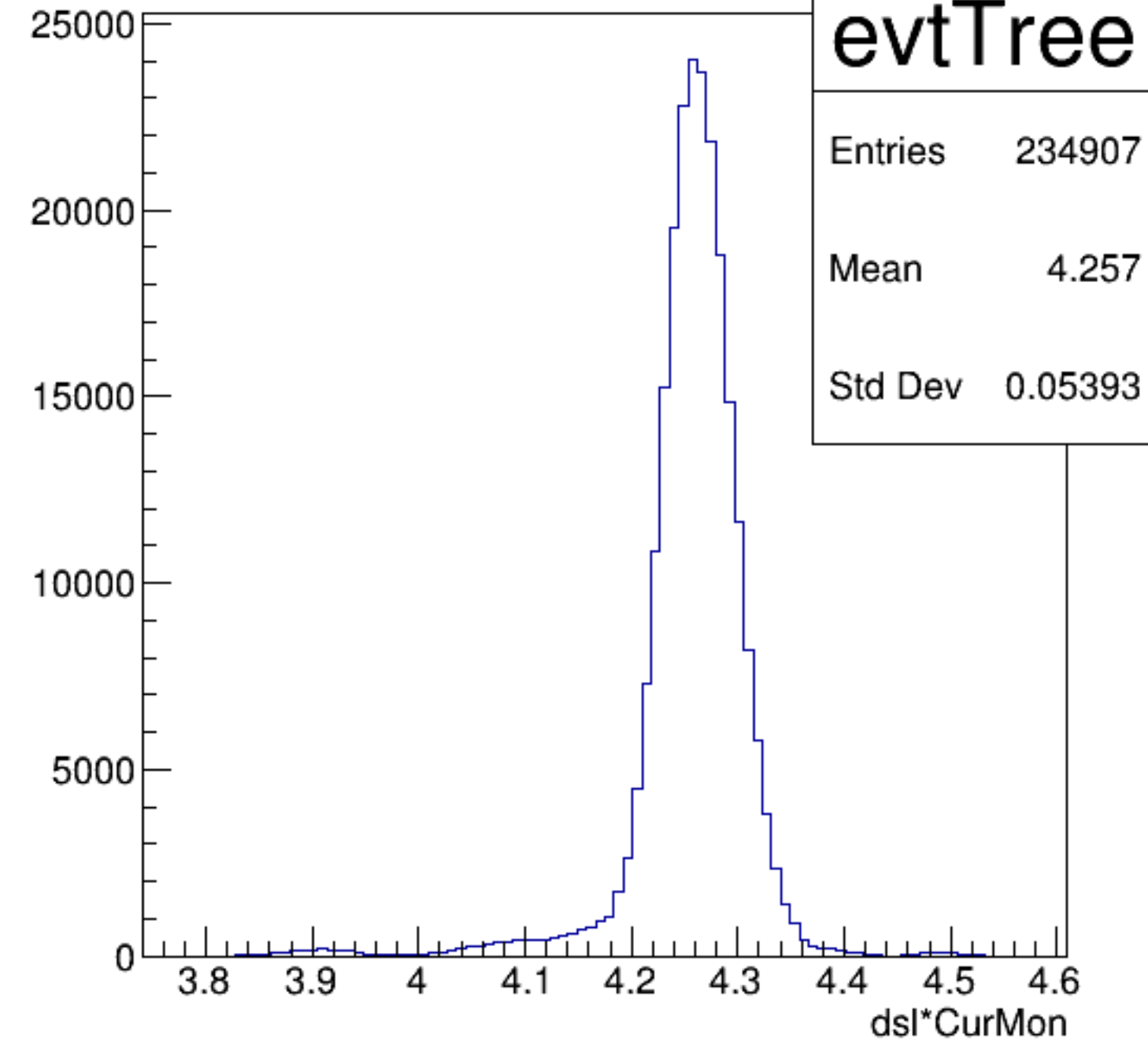
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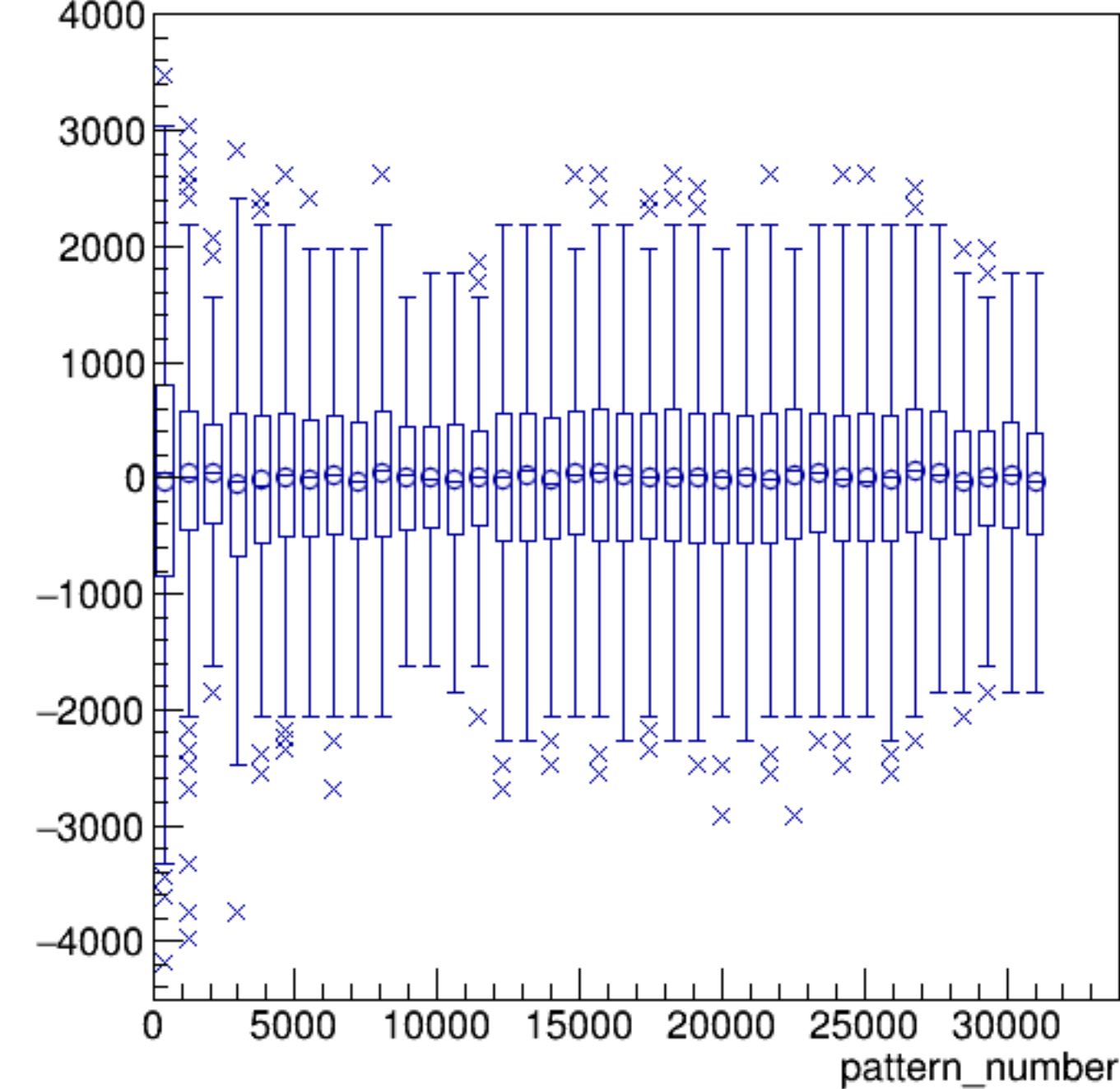
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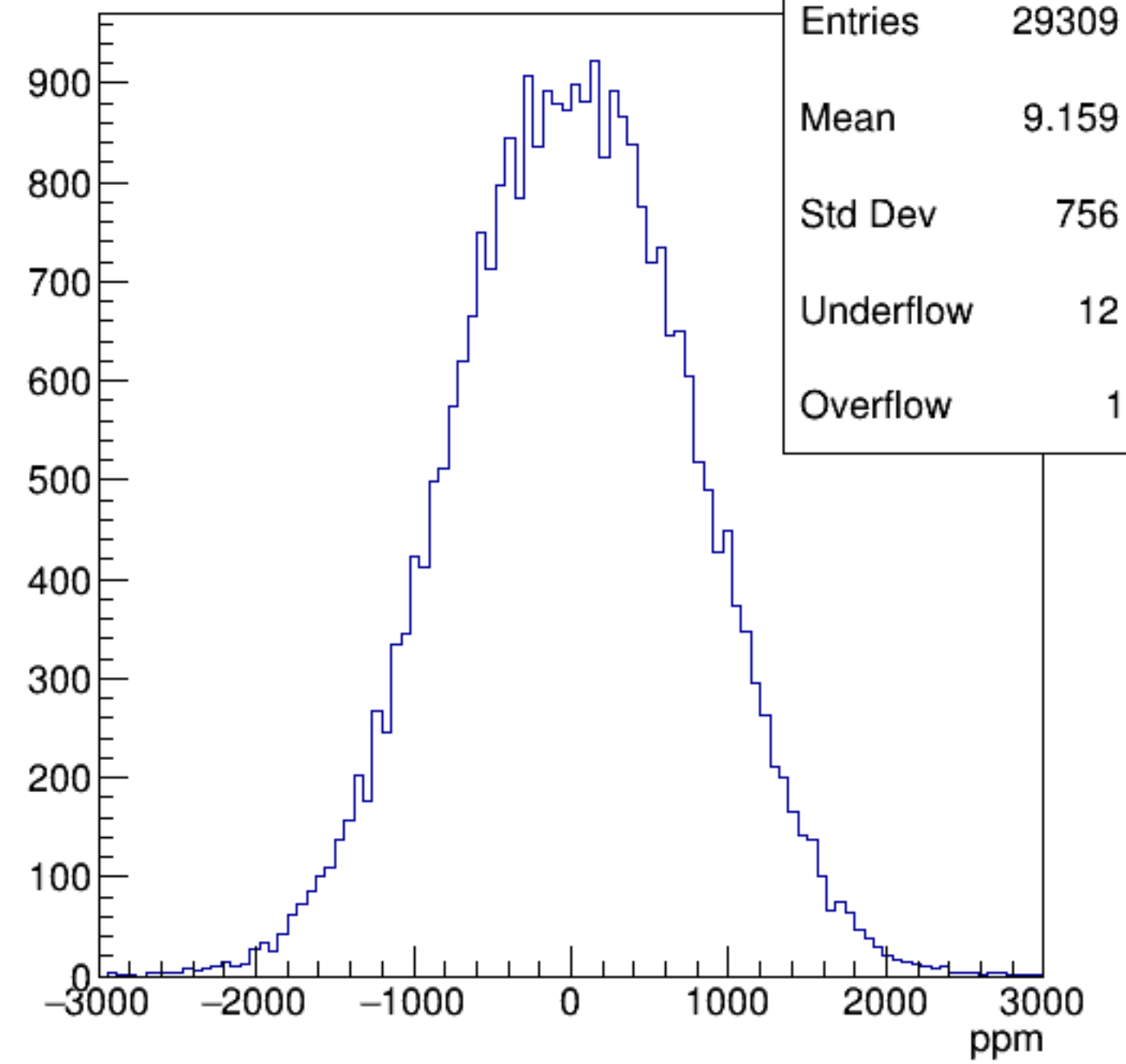
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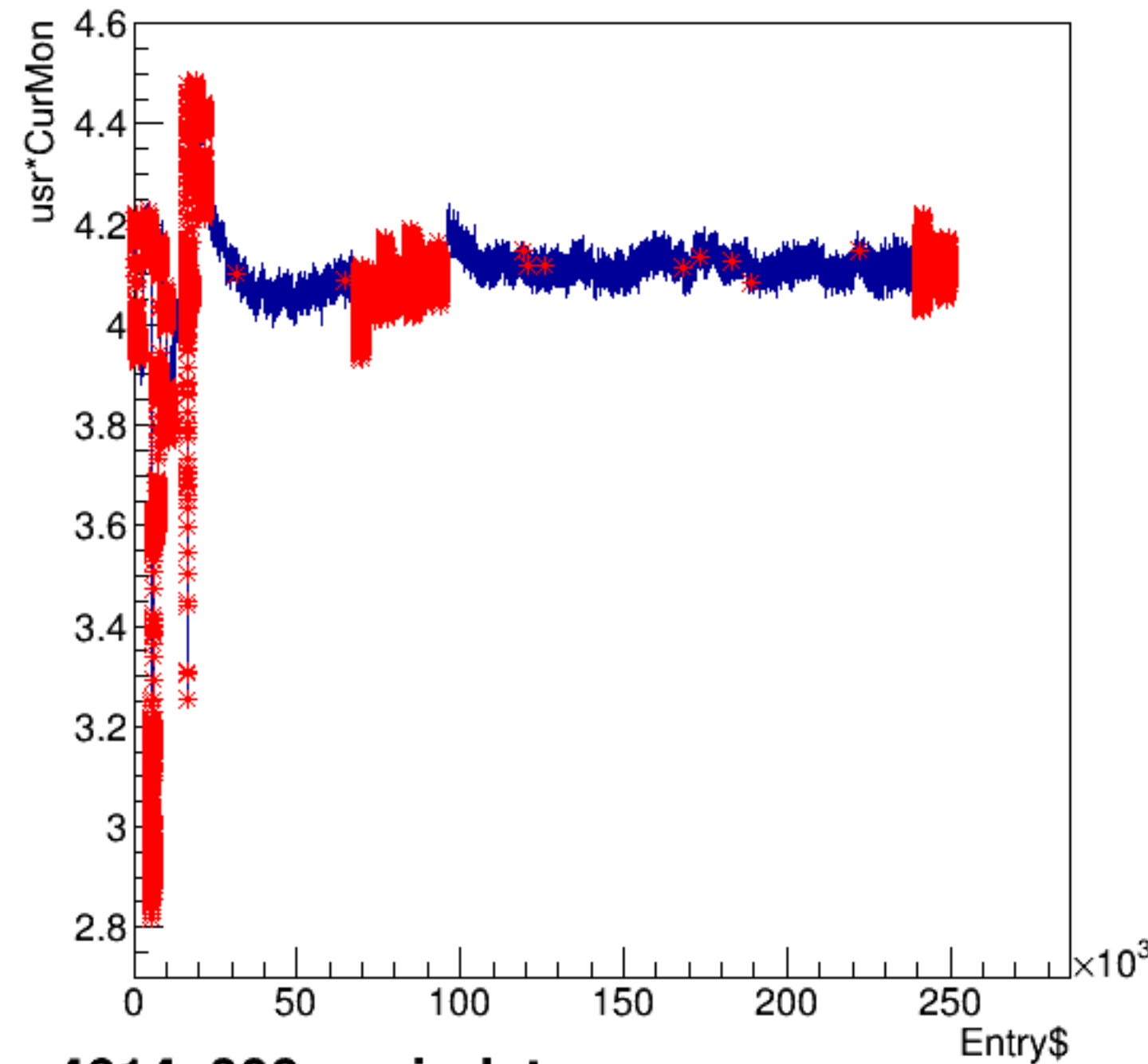
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asym_dsl

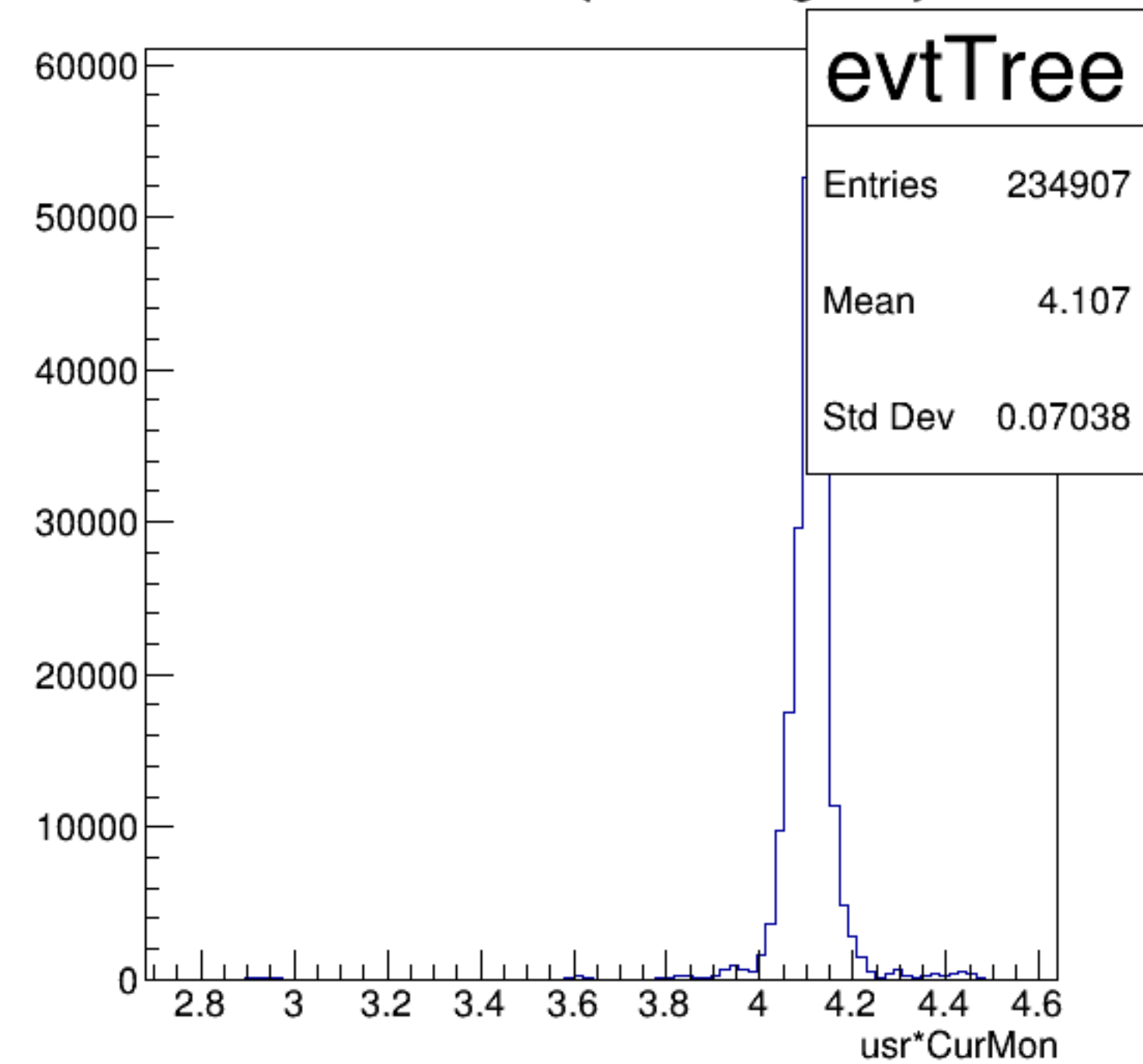


usr*CurMon:Entry\$

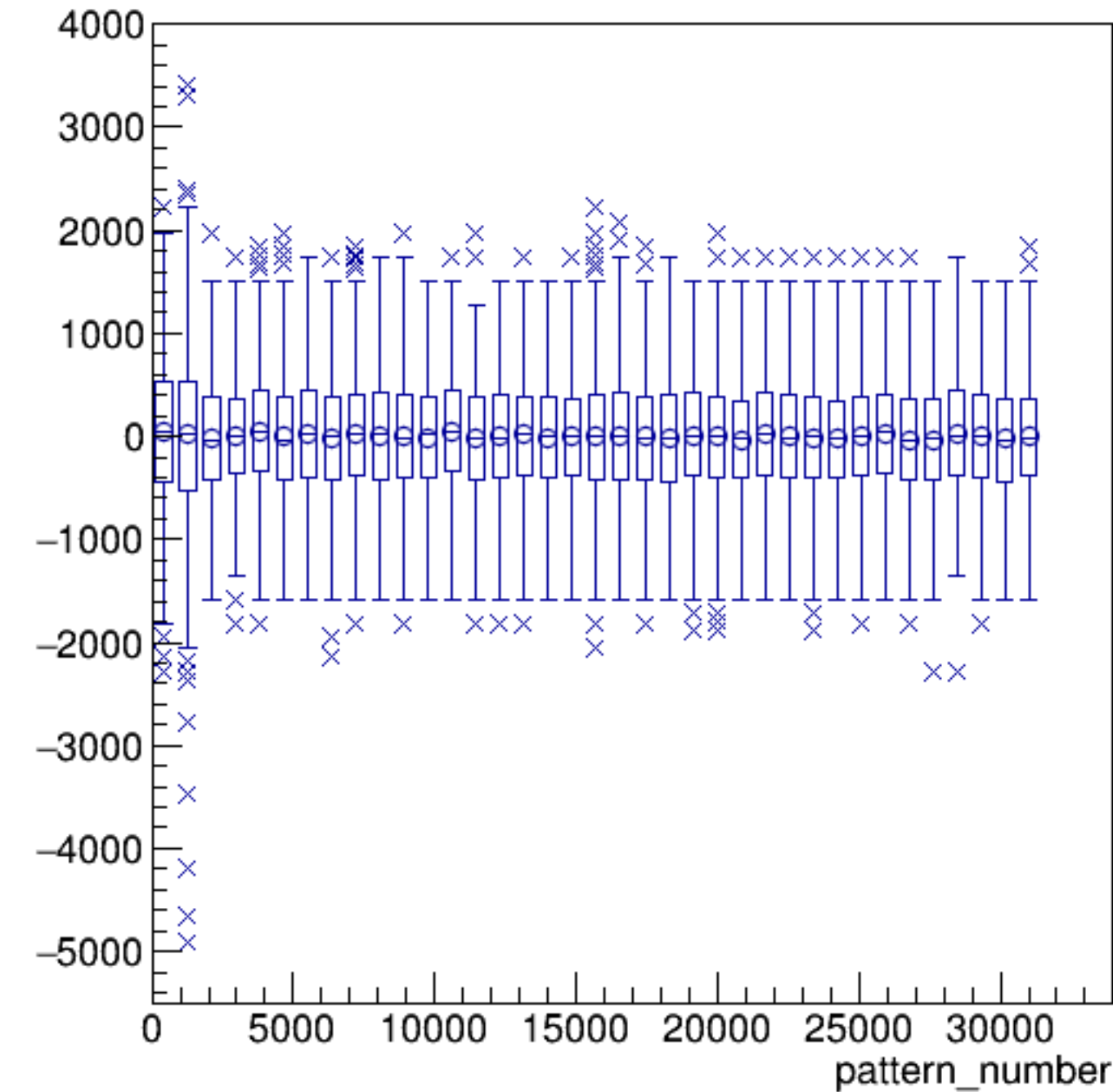


run4614_000_maindet_usr.png

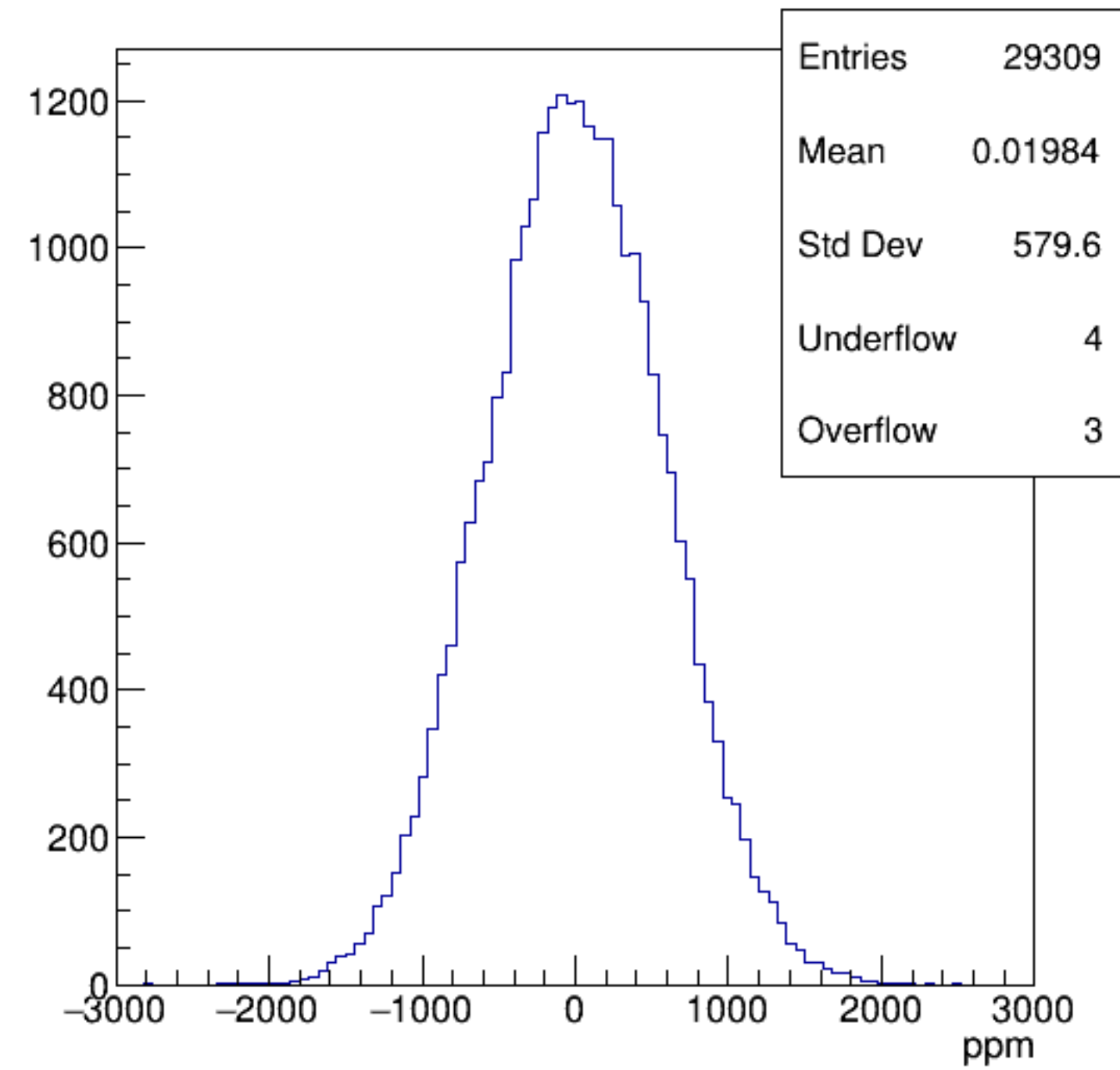
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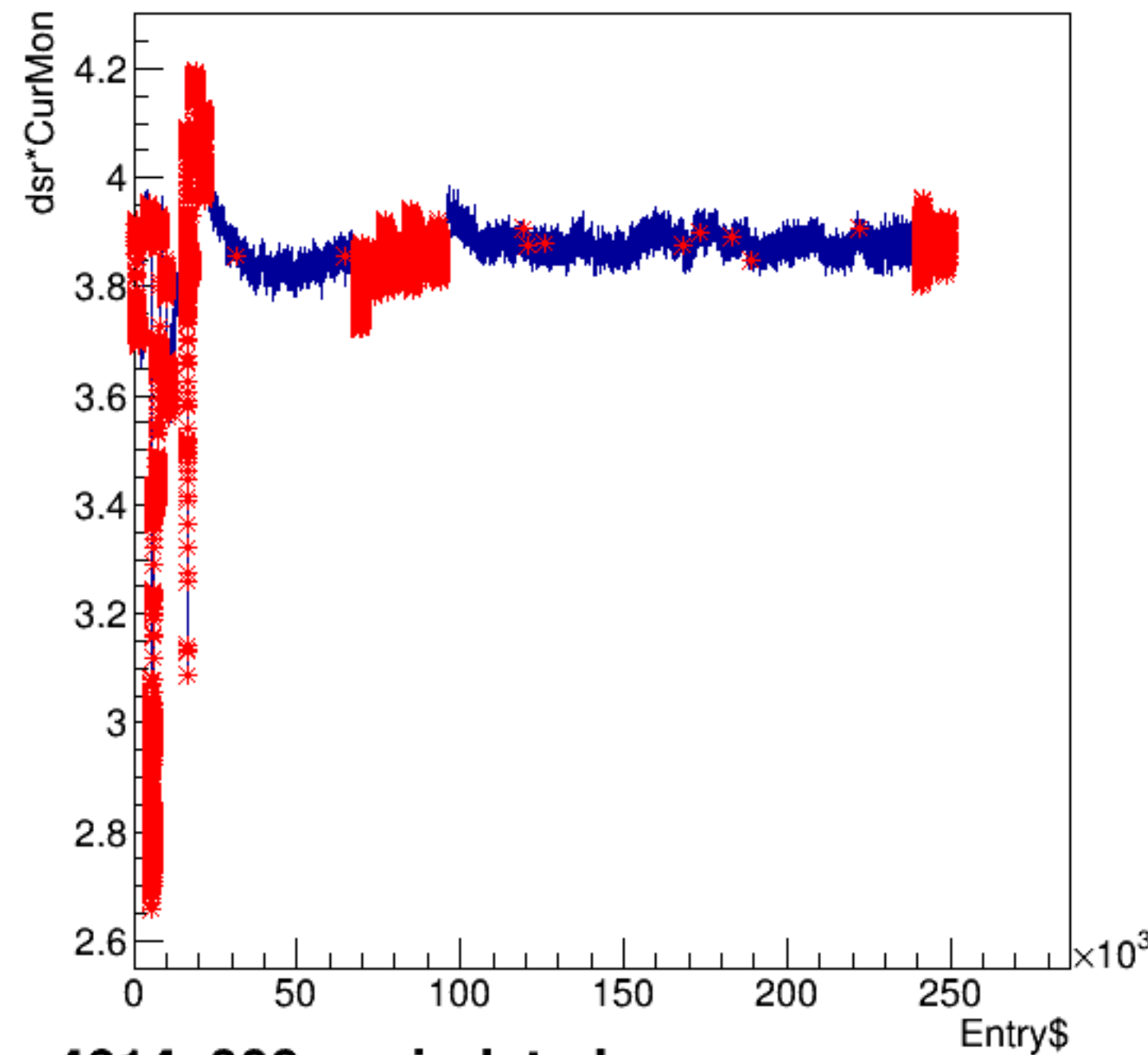
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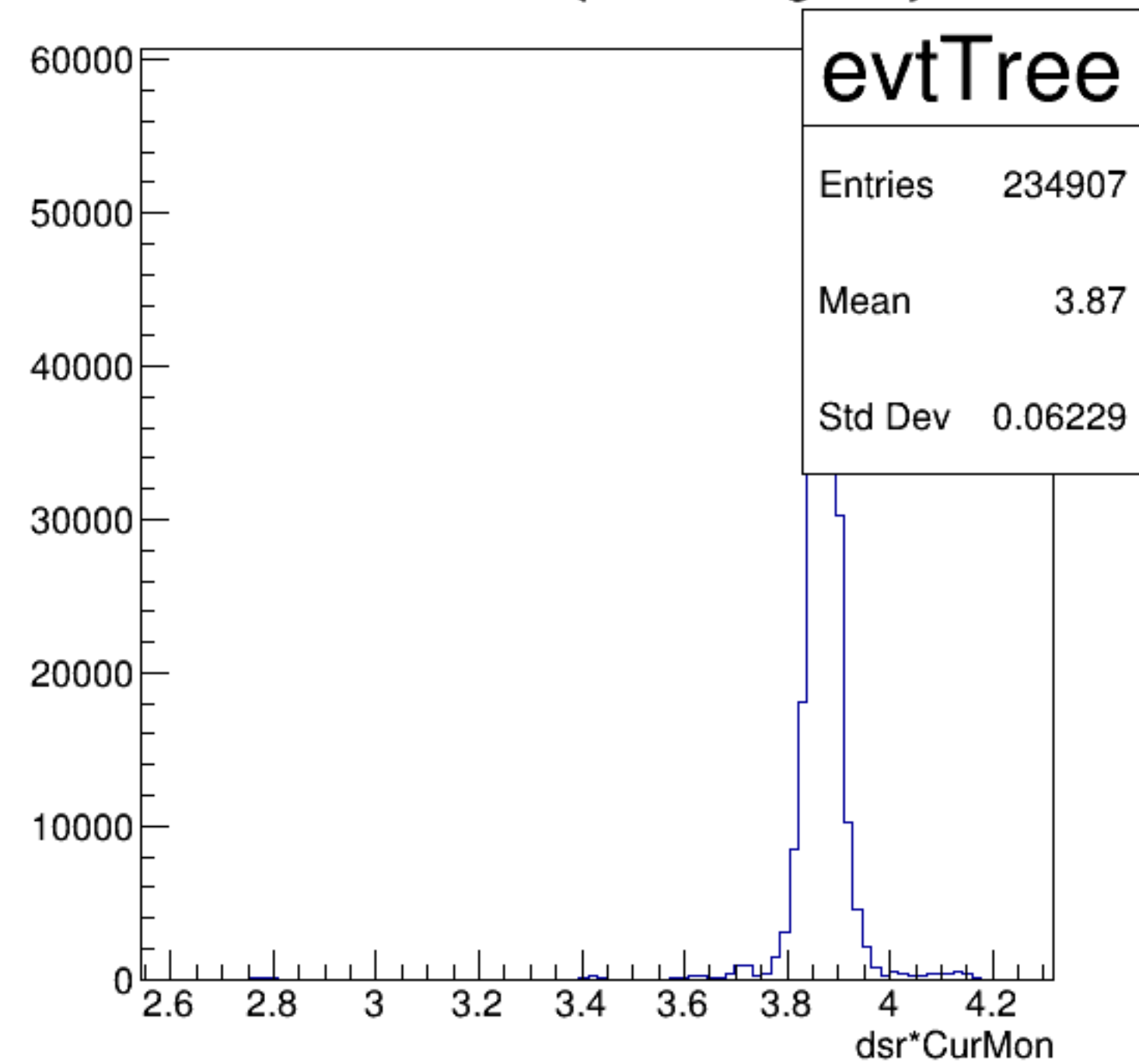
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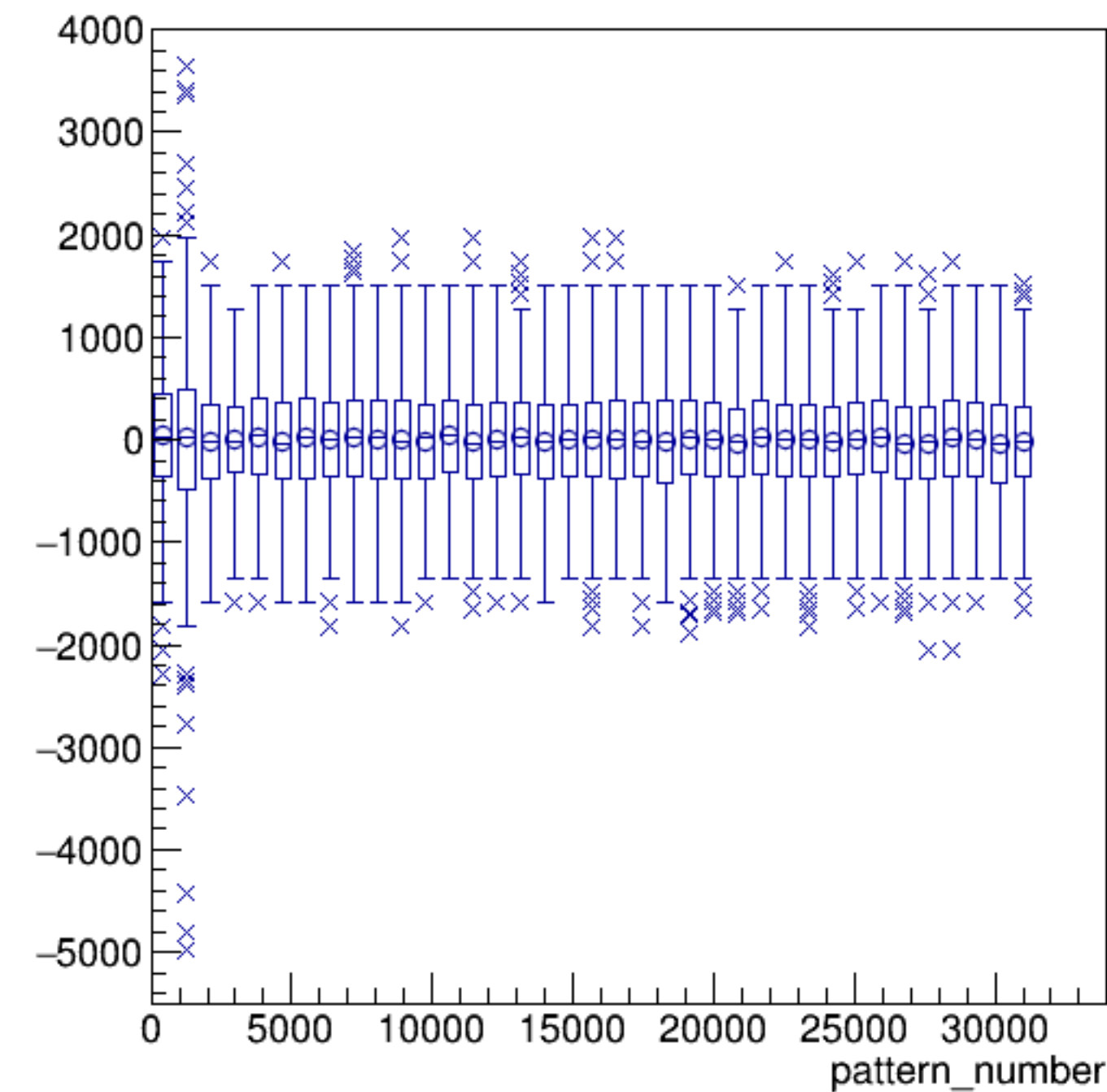
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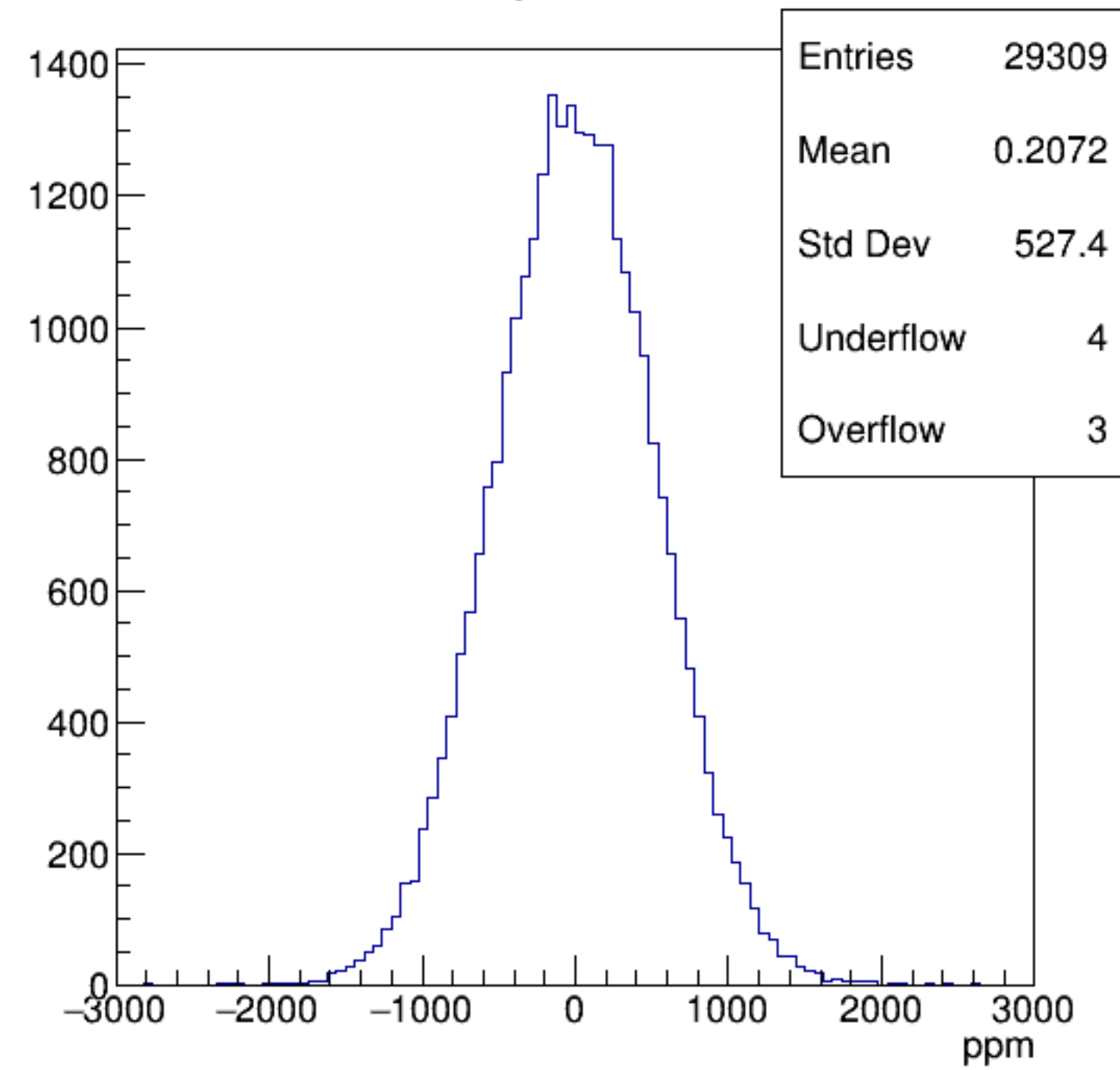
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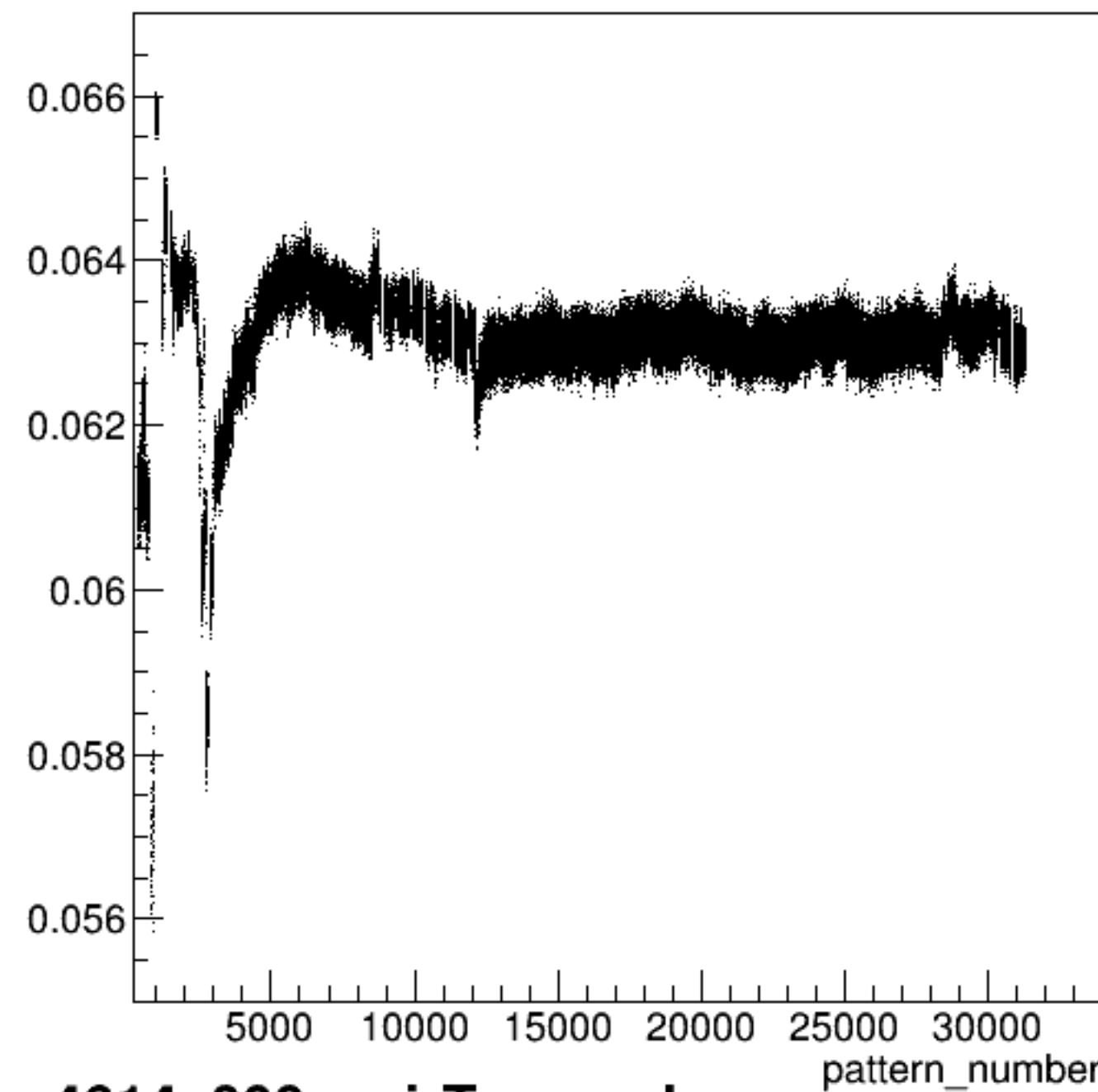
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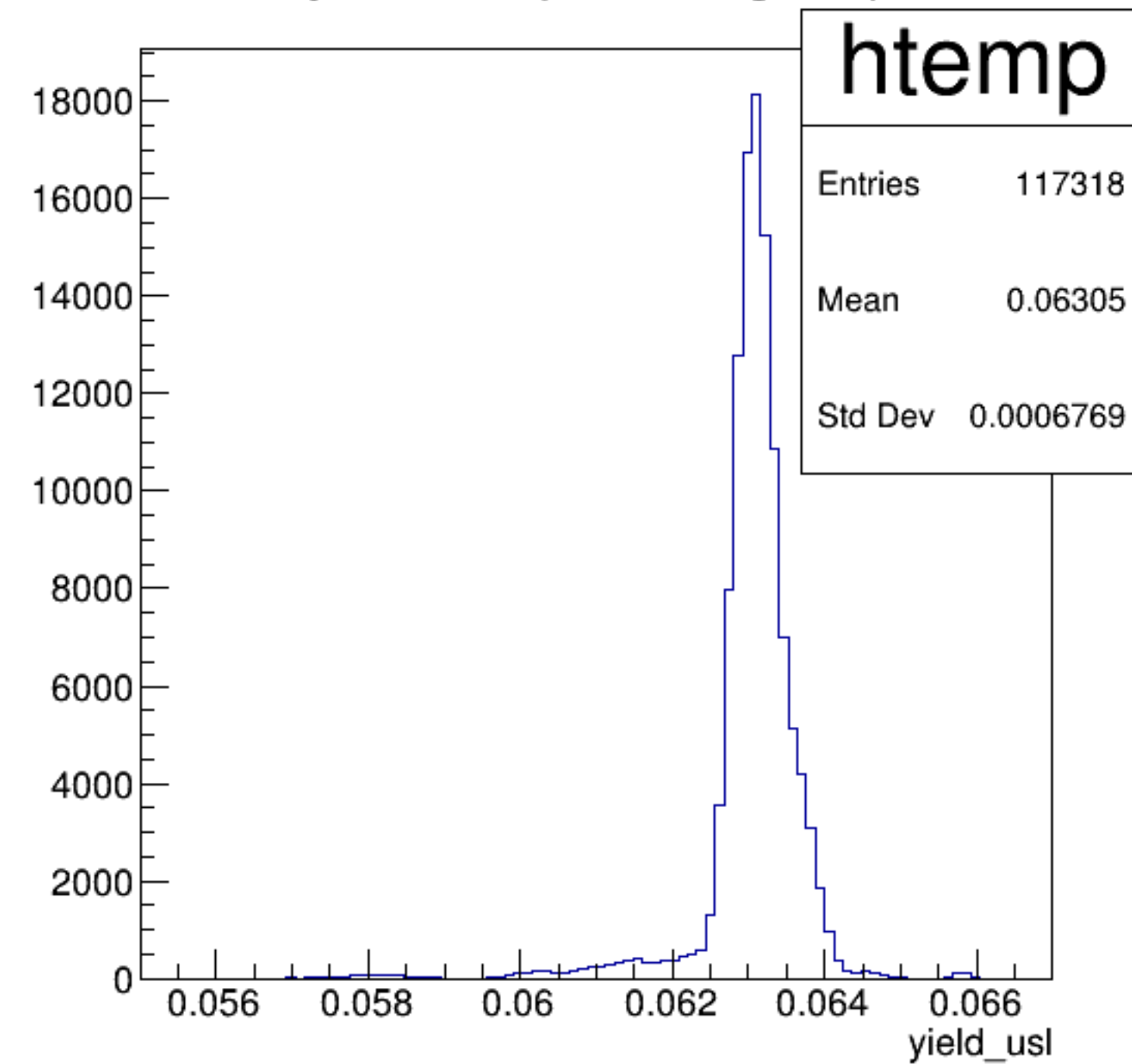
asym_dsr



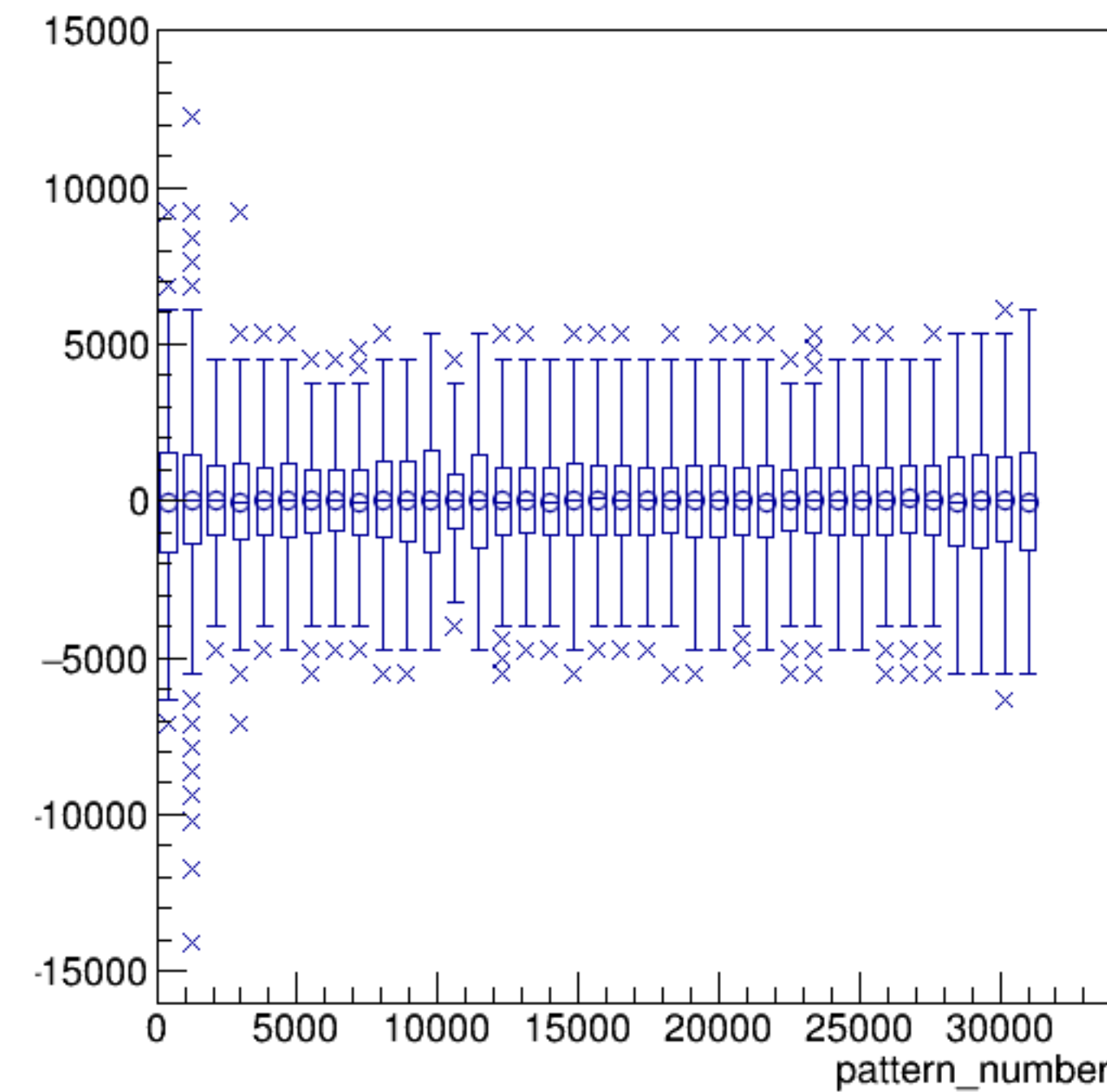
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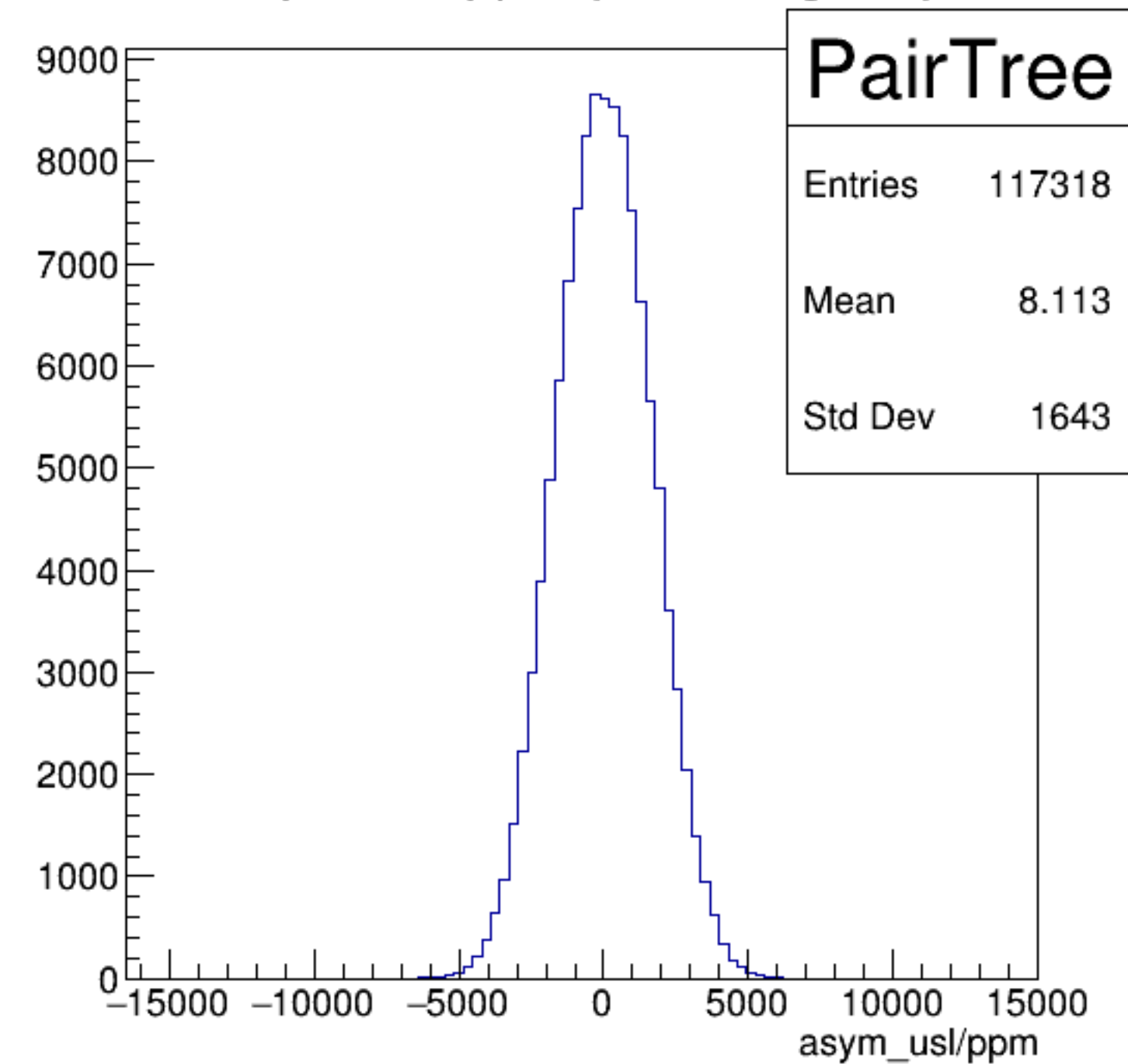
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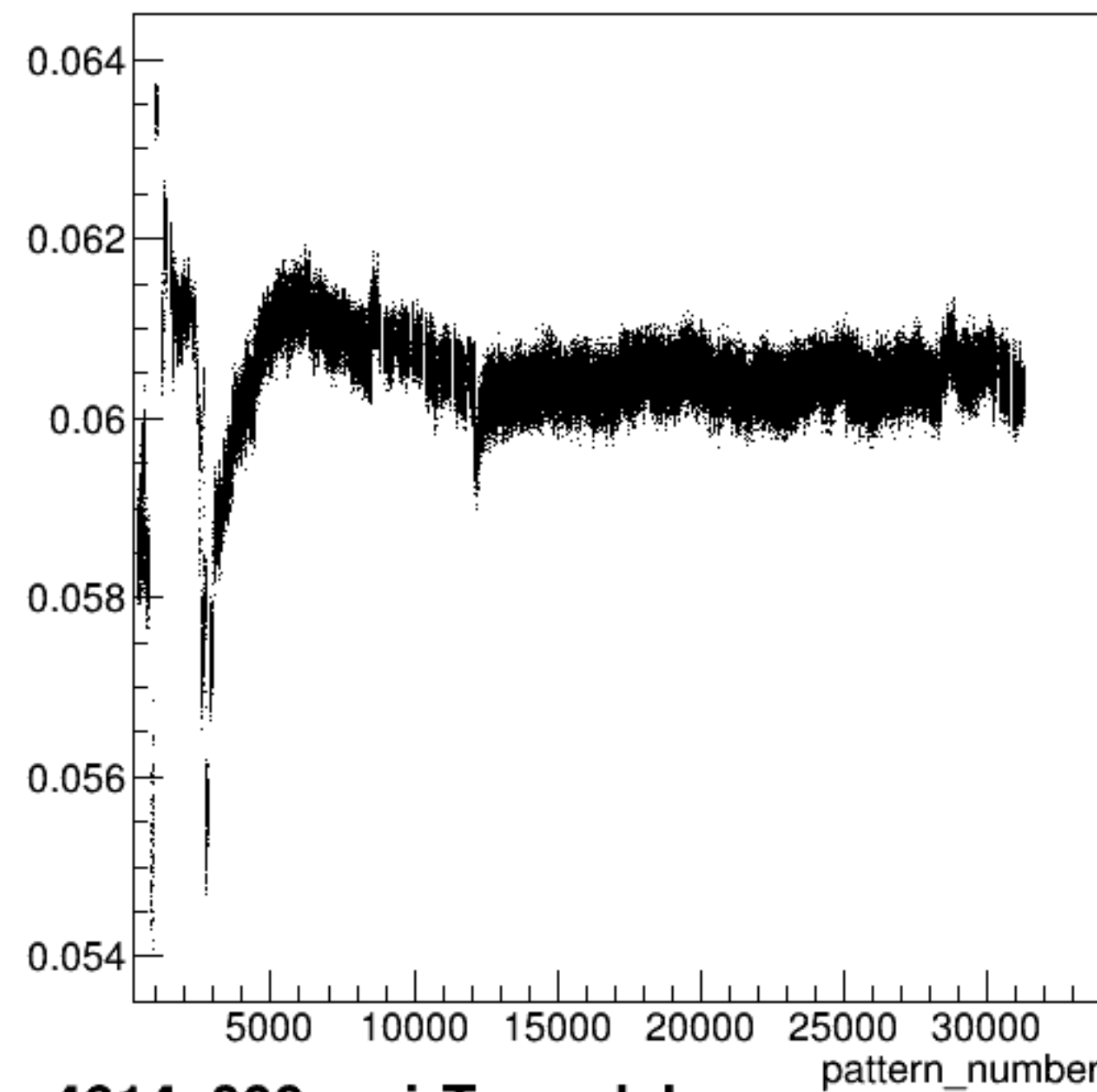
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asym_usl/ppm {ErrorFlag==0}

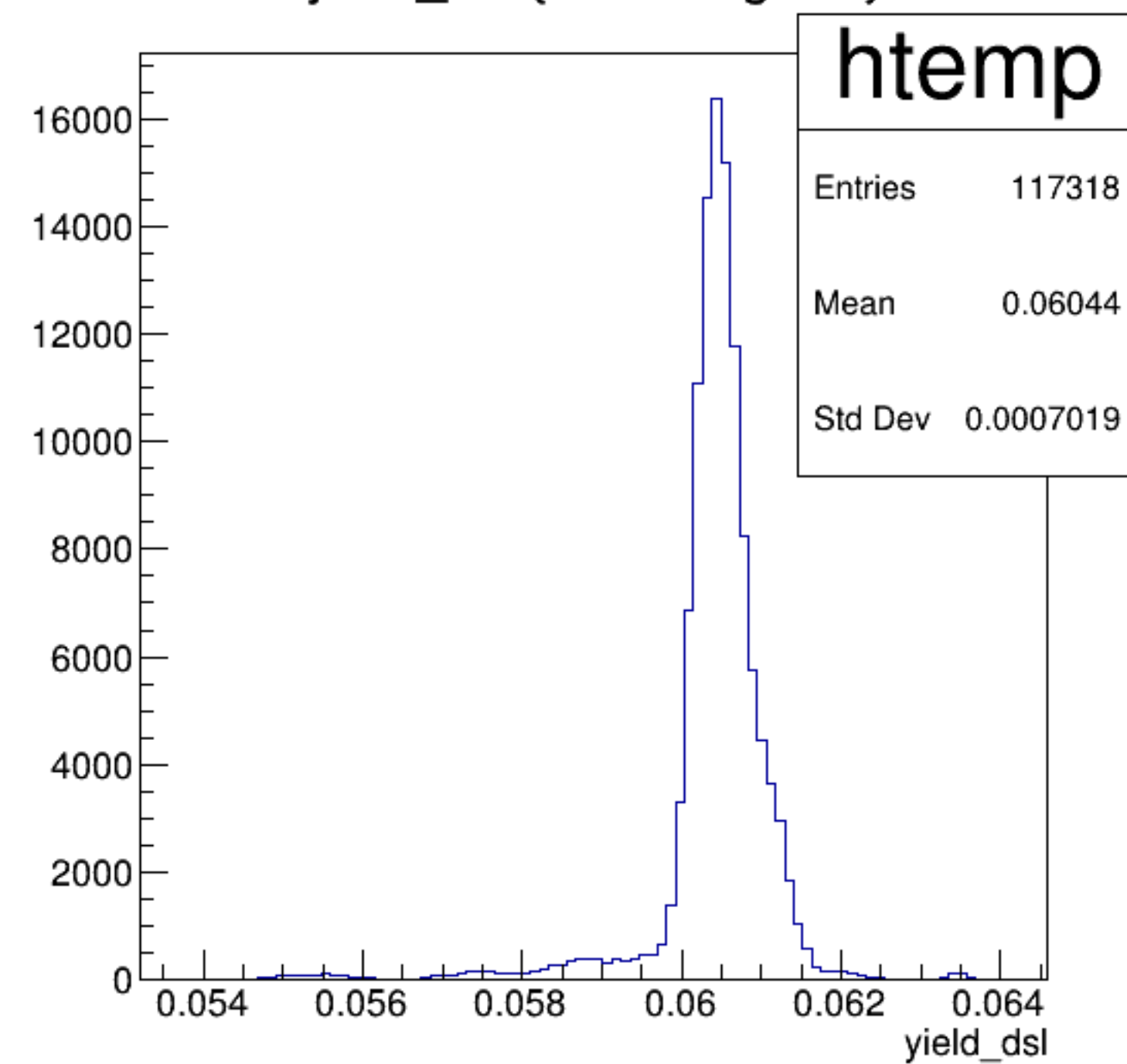


yield_dsl:pattern_number {ErrorFlag==0}

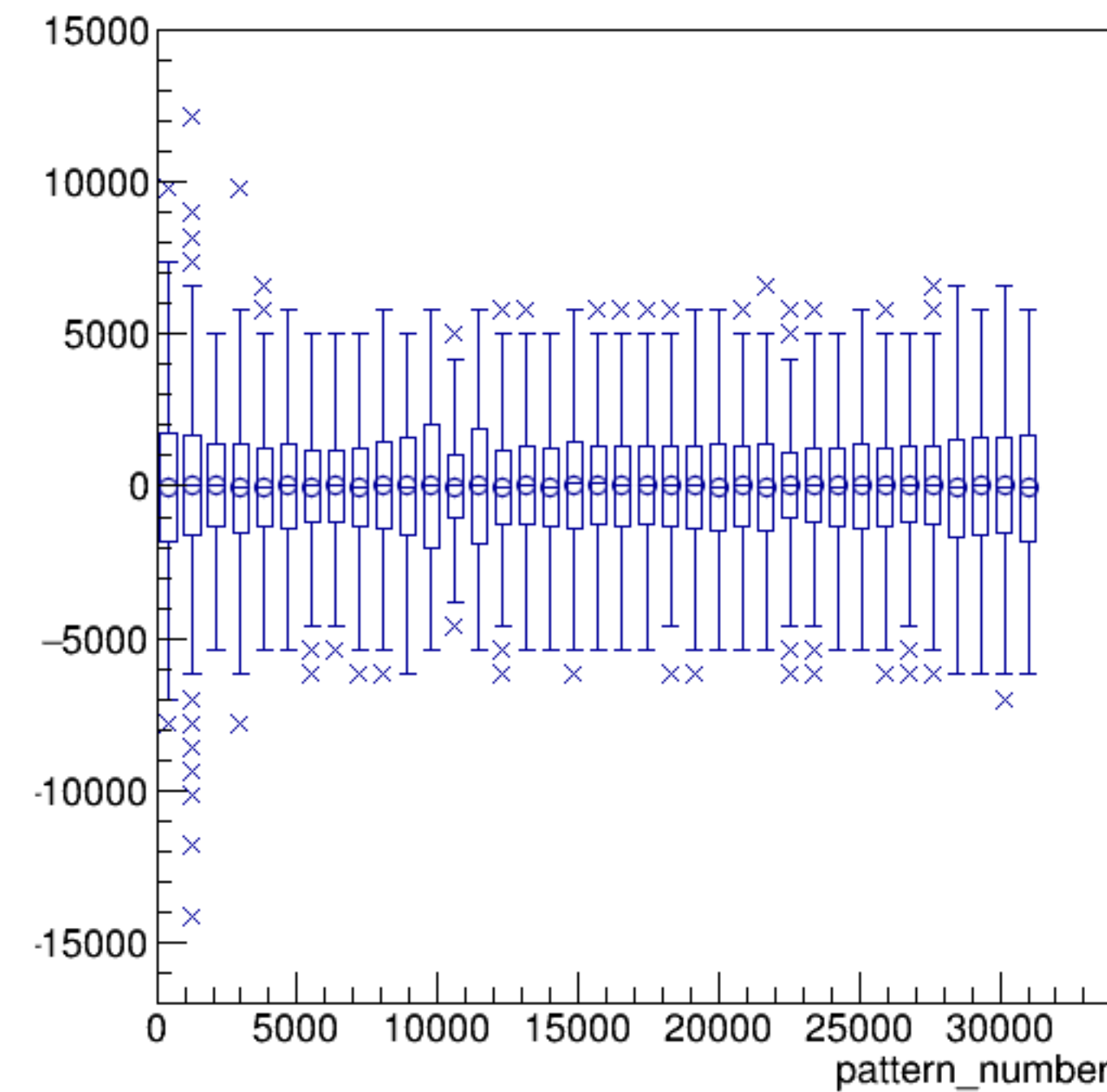


run4614_000_pairTree_dsl.png

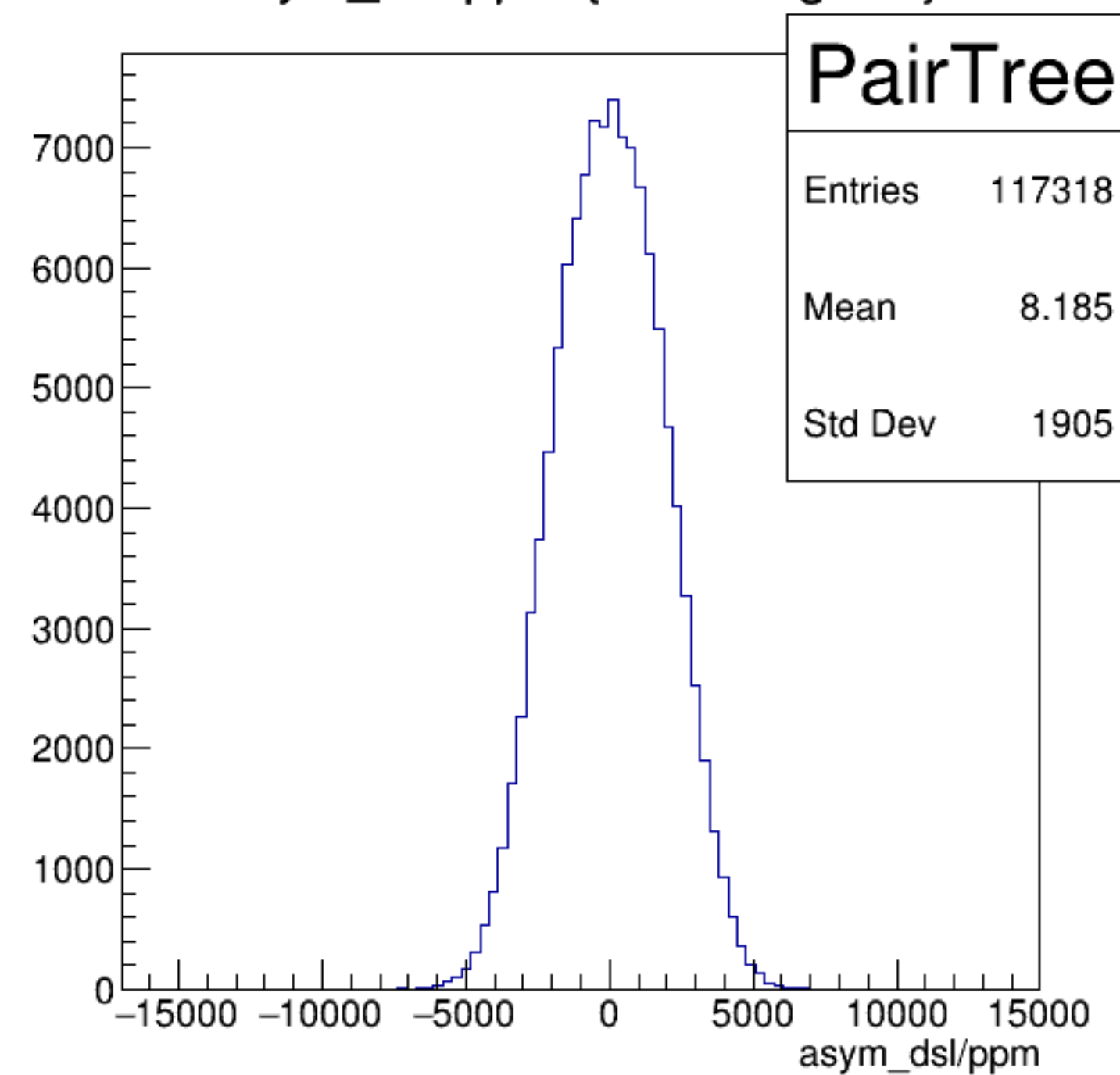
yield_dsl {ErrorFlag==0}



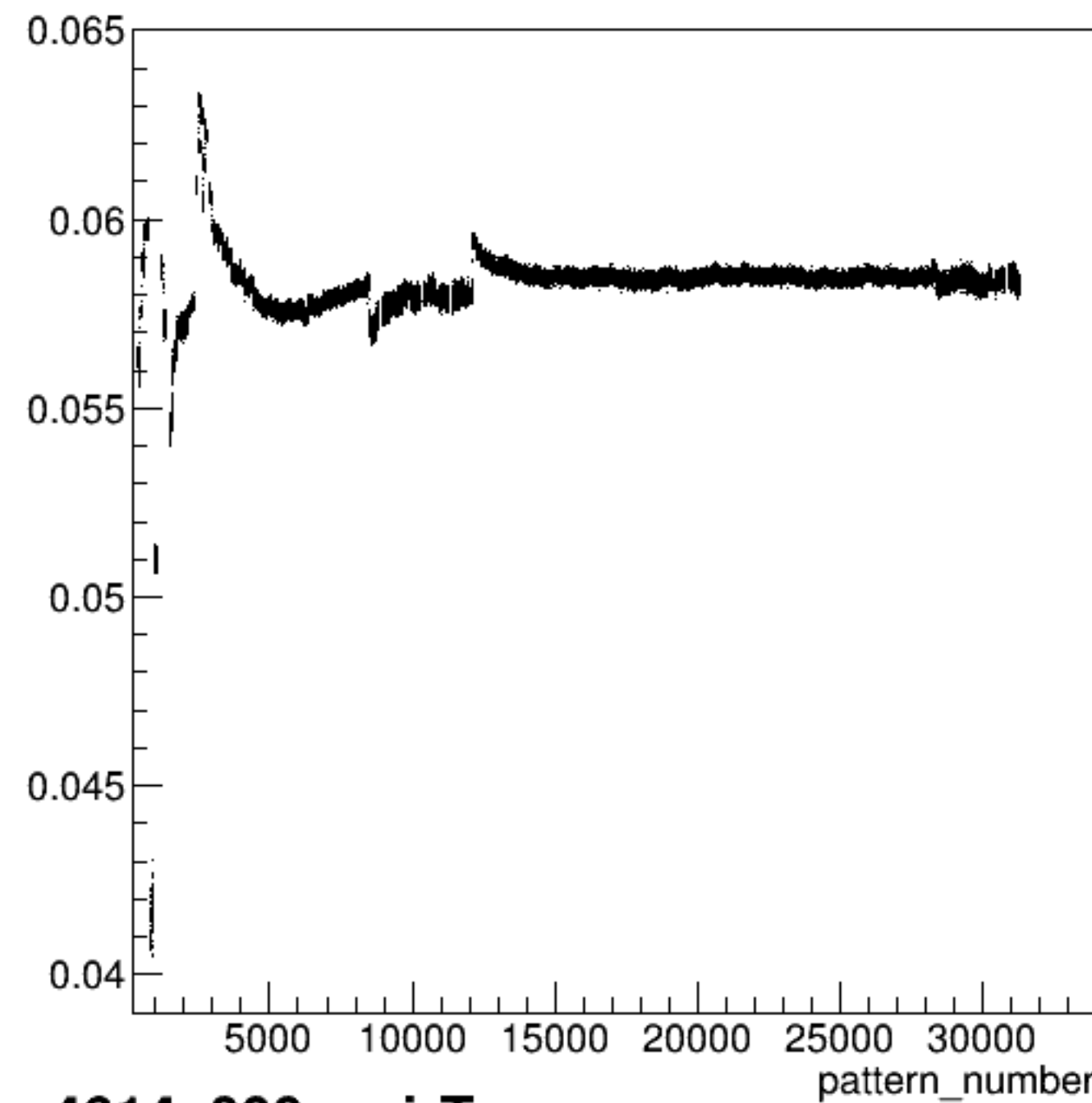
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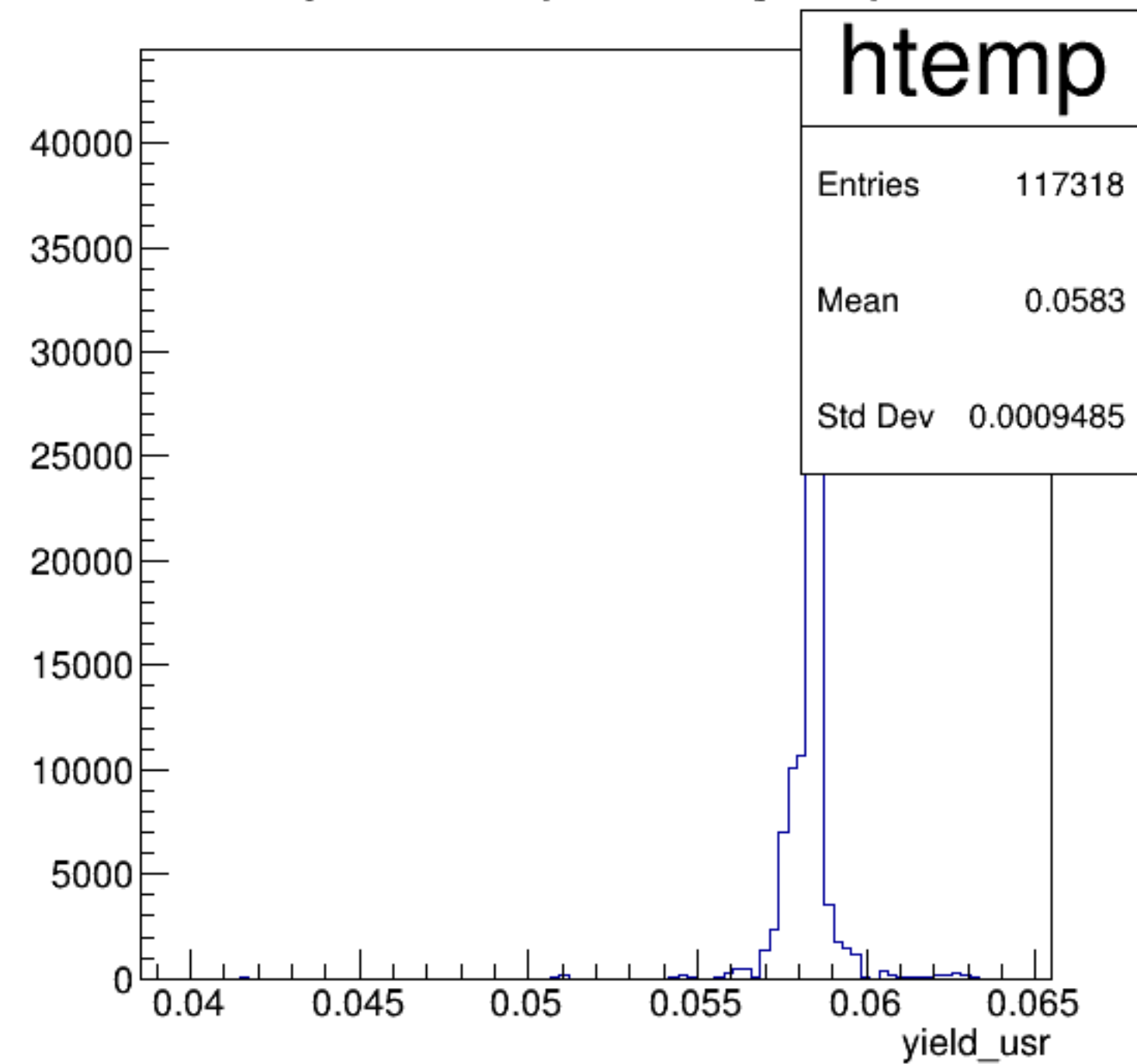
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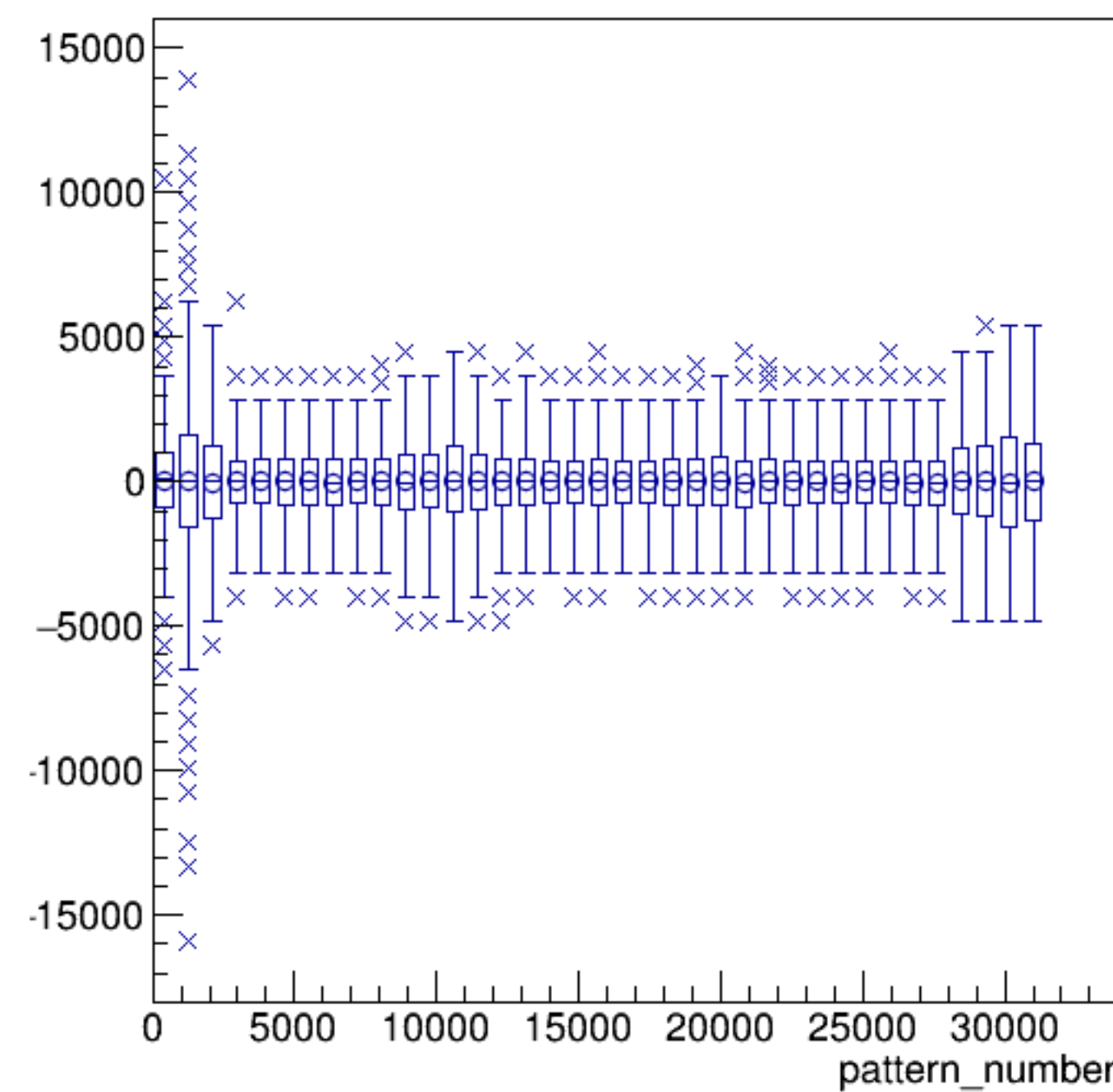
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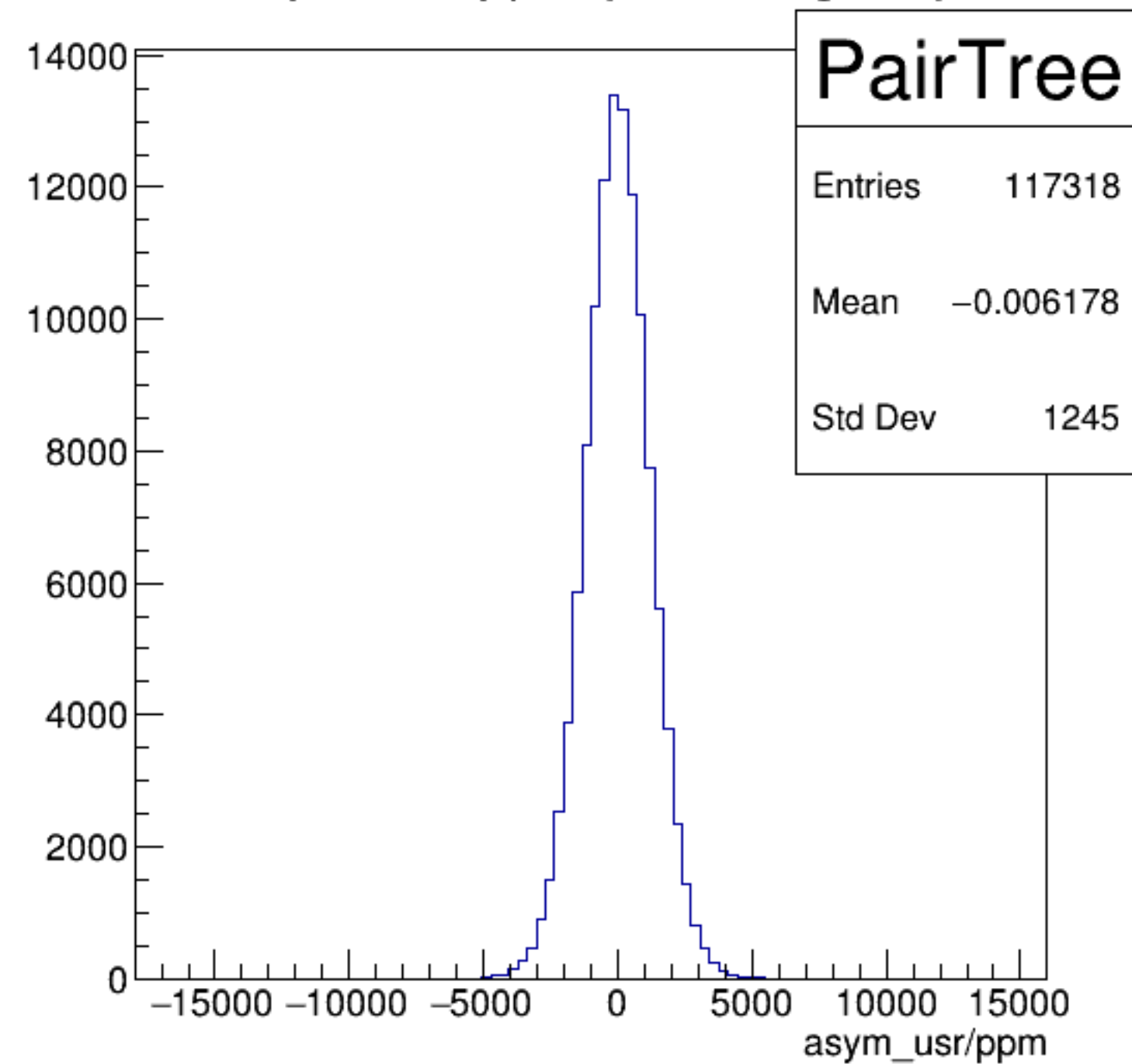
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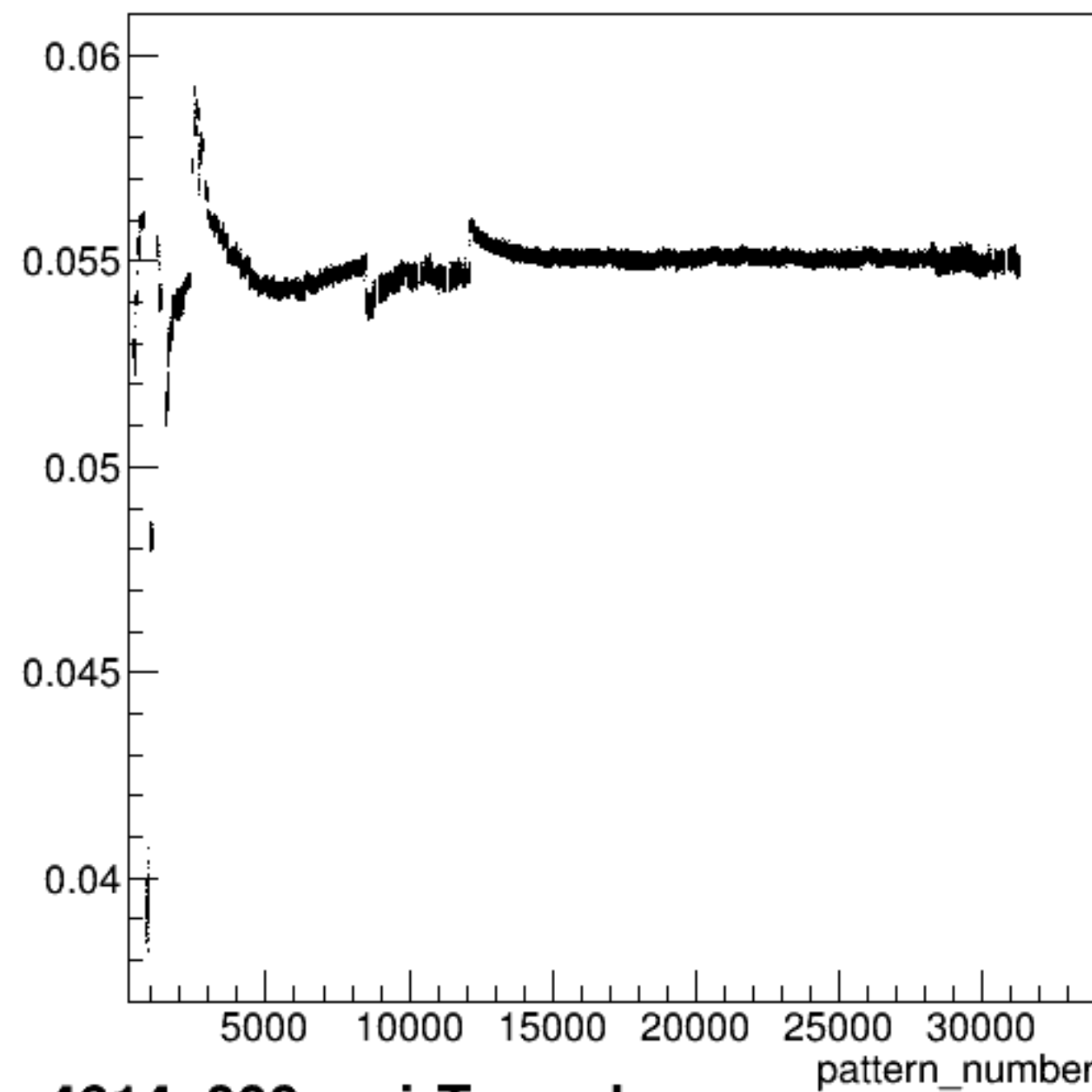
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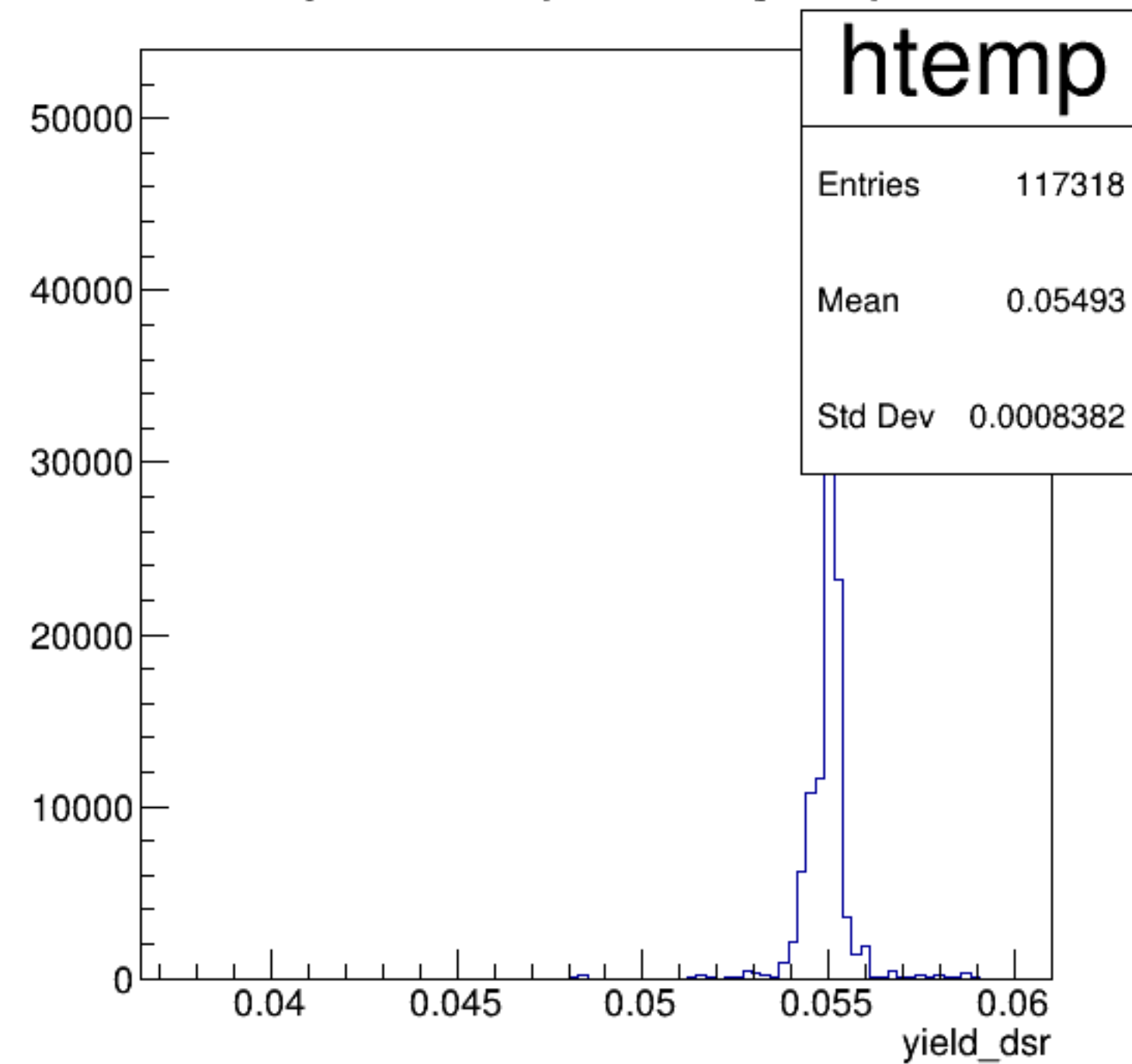
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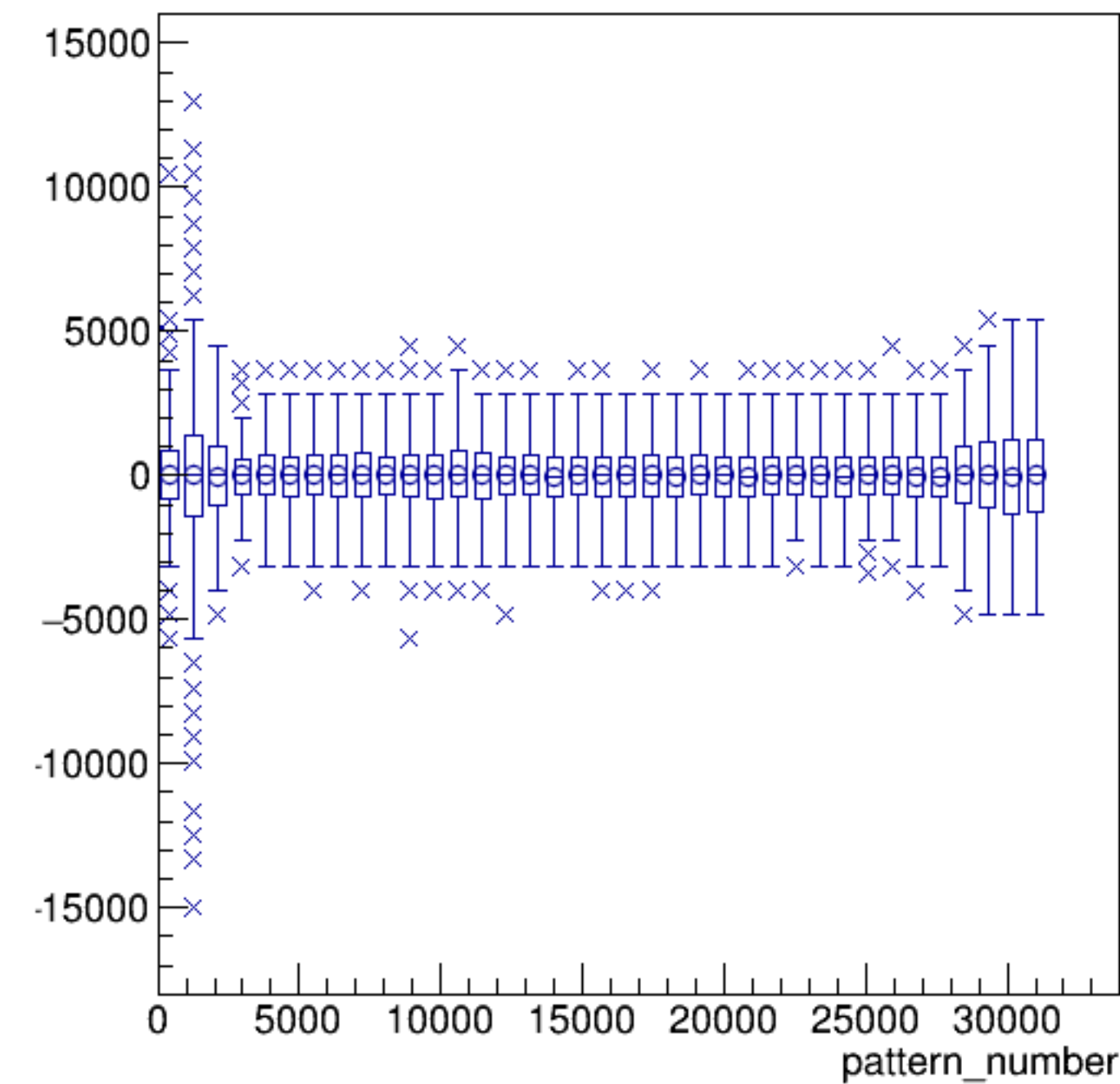
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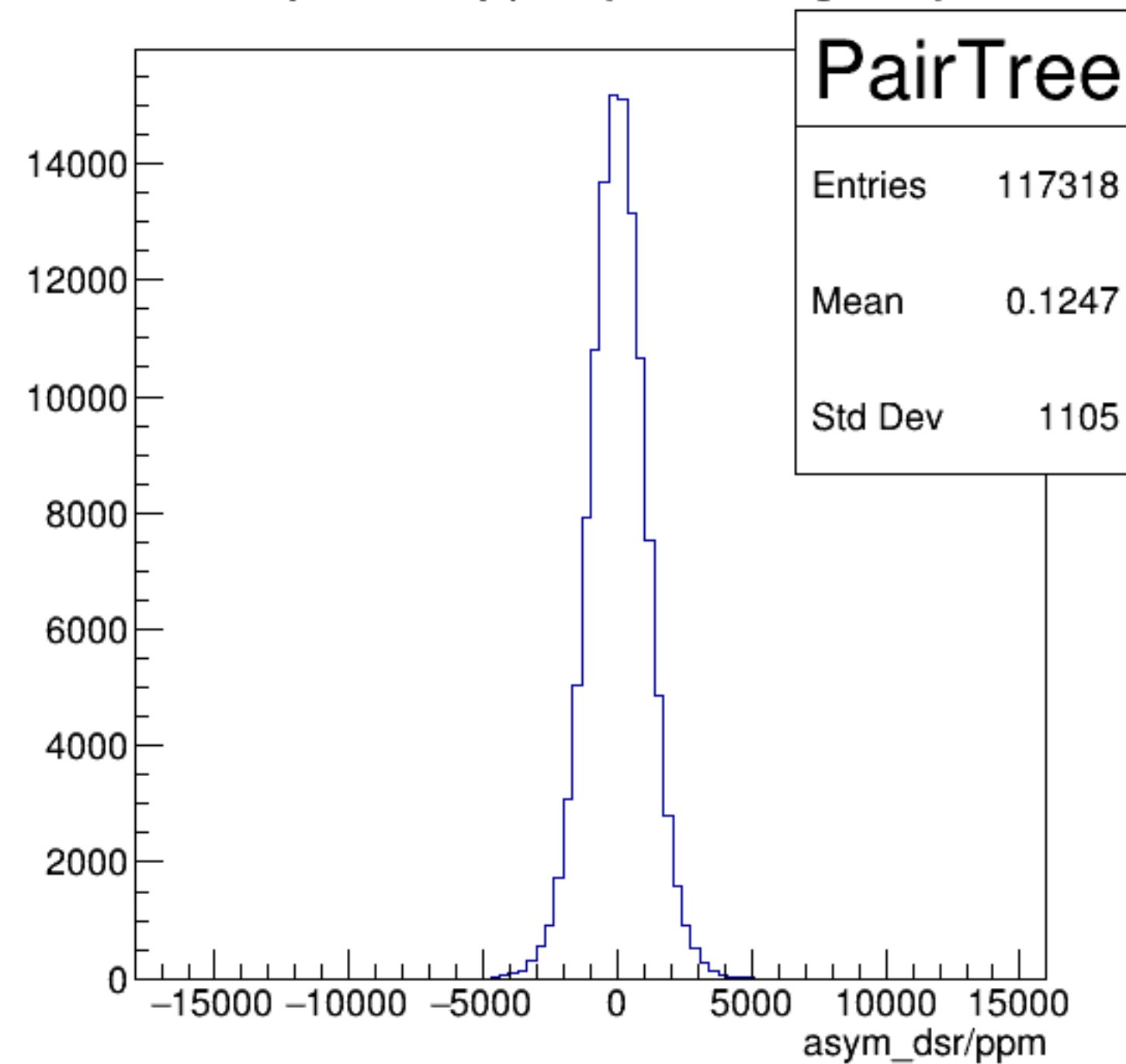
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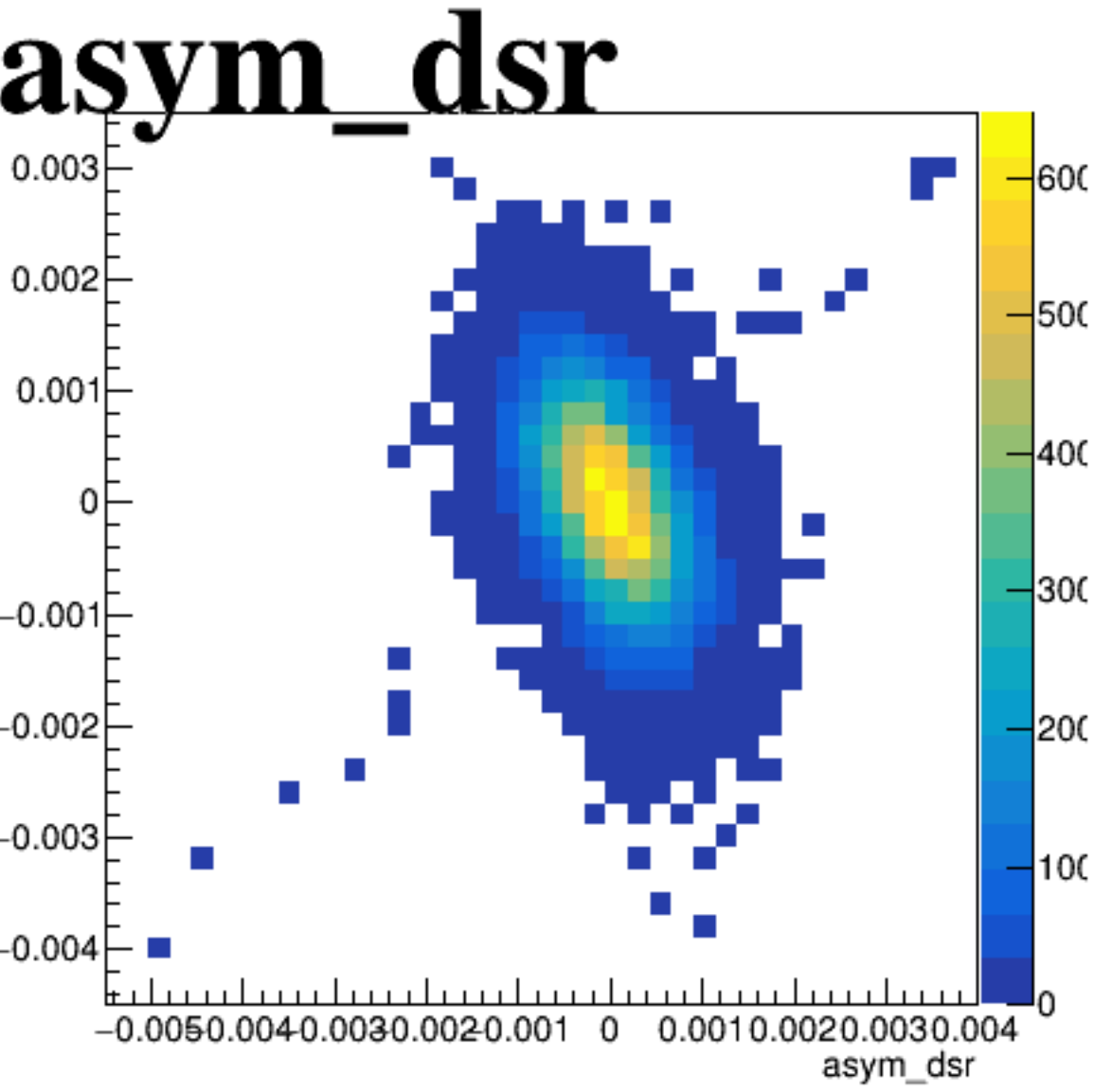
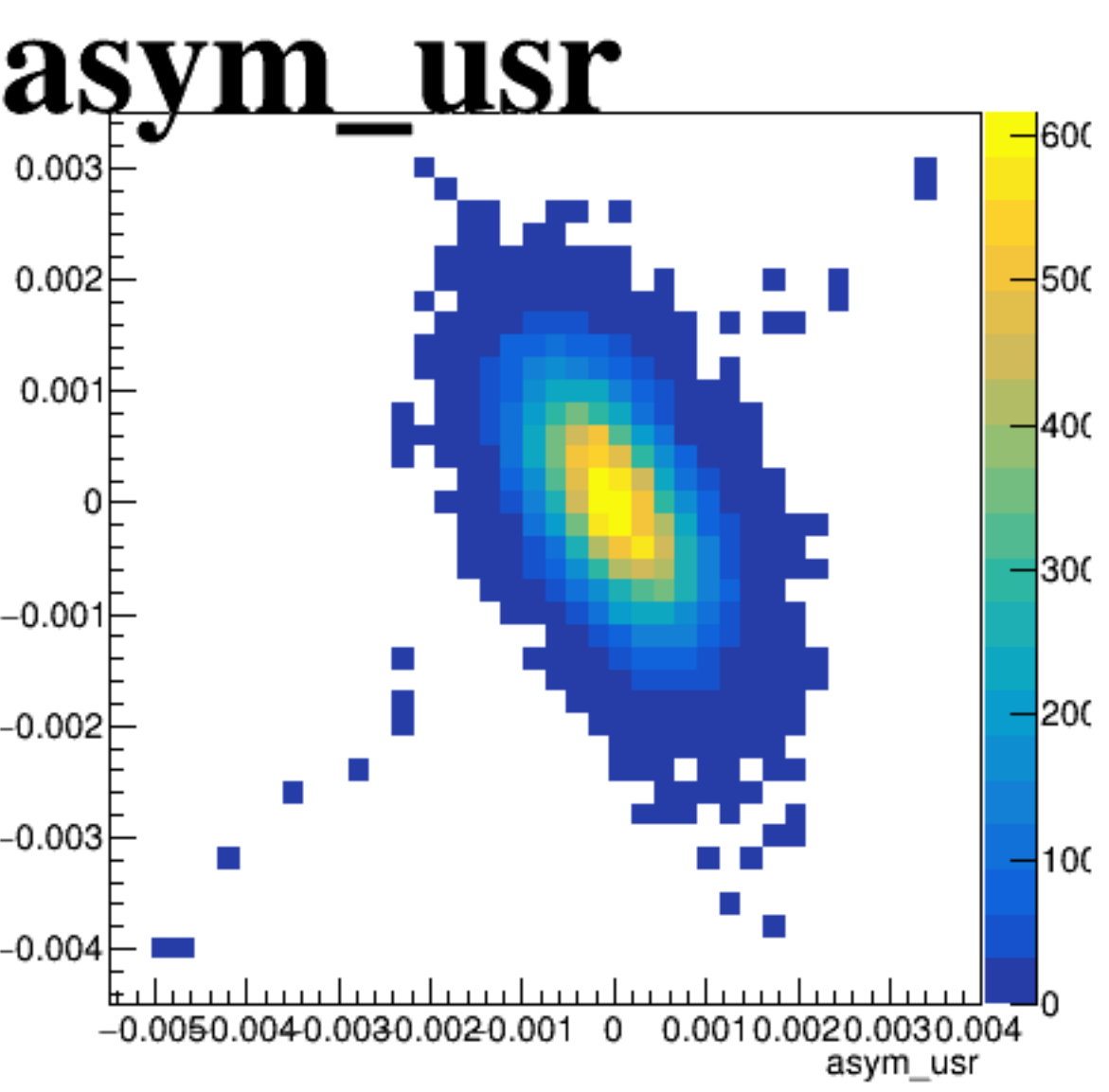
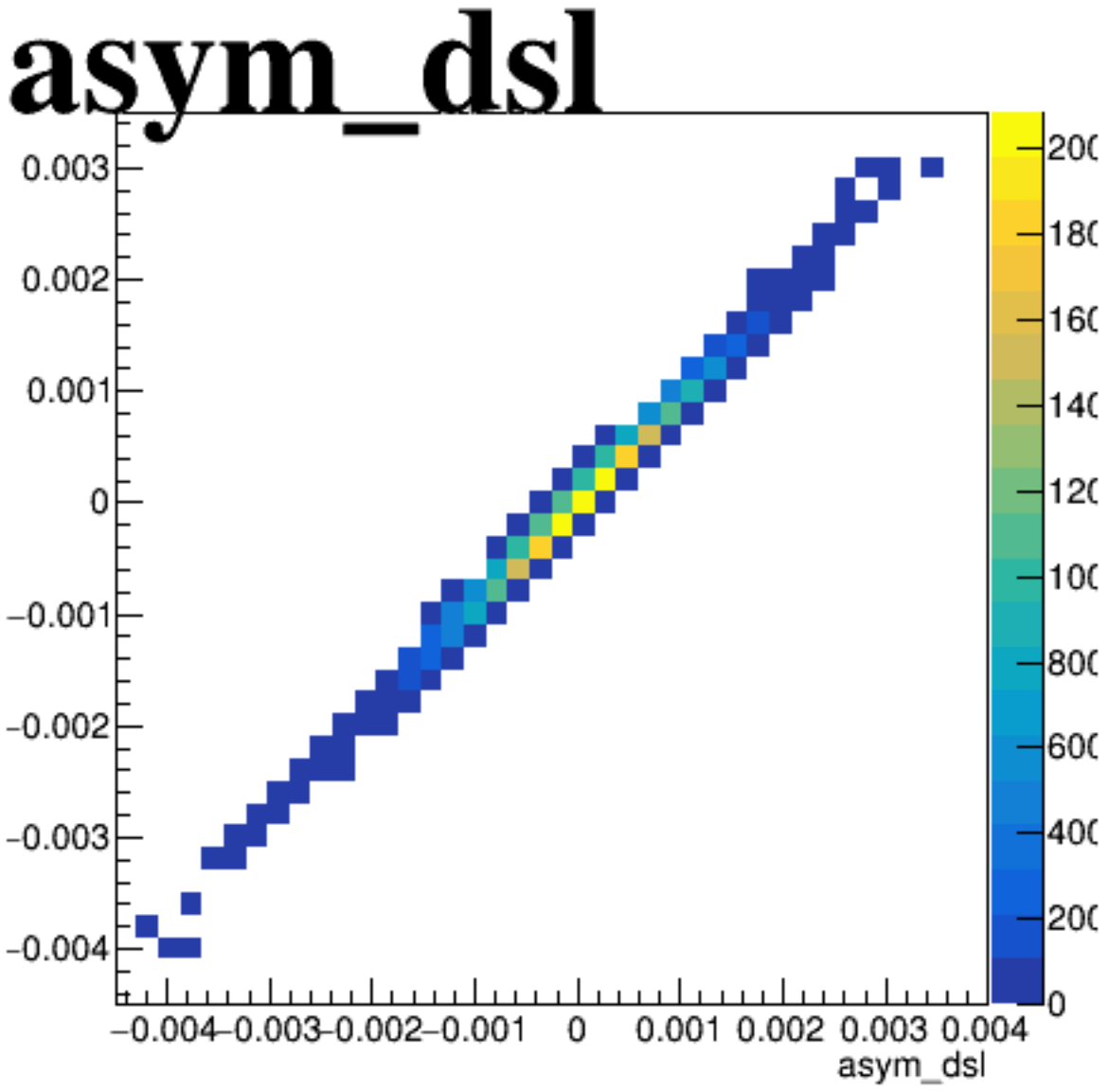
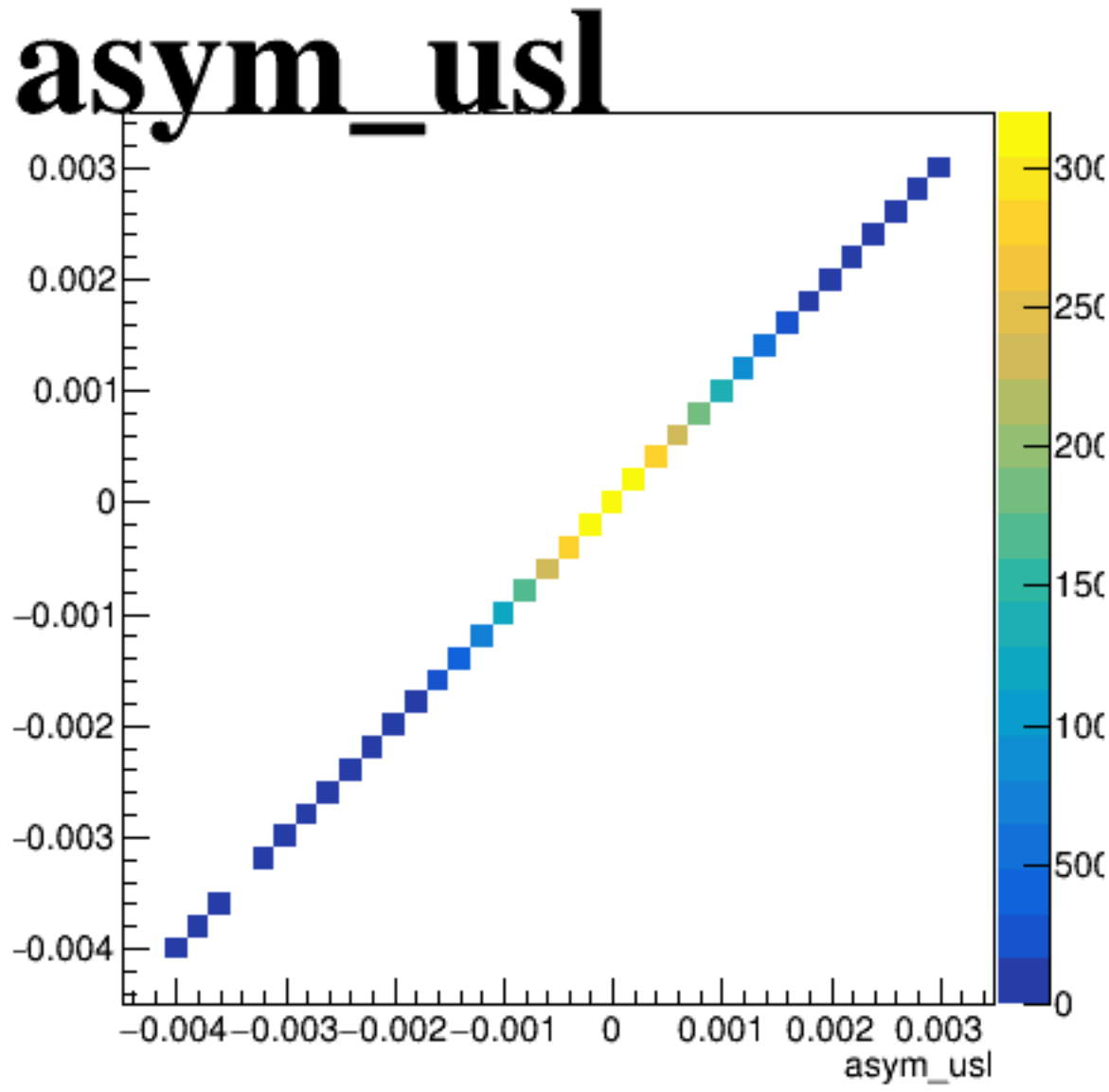
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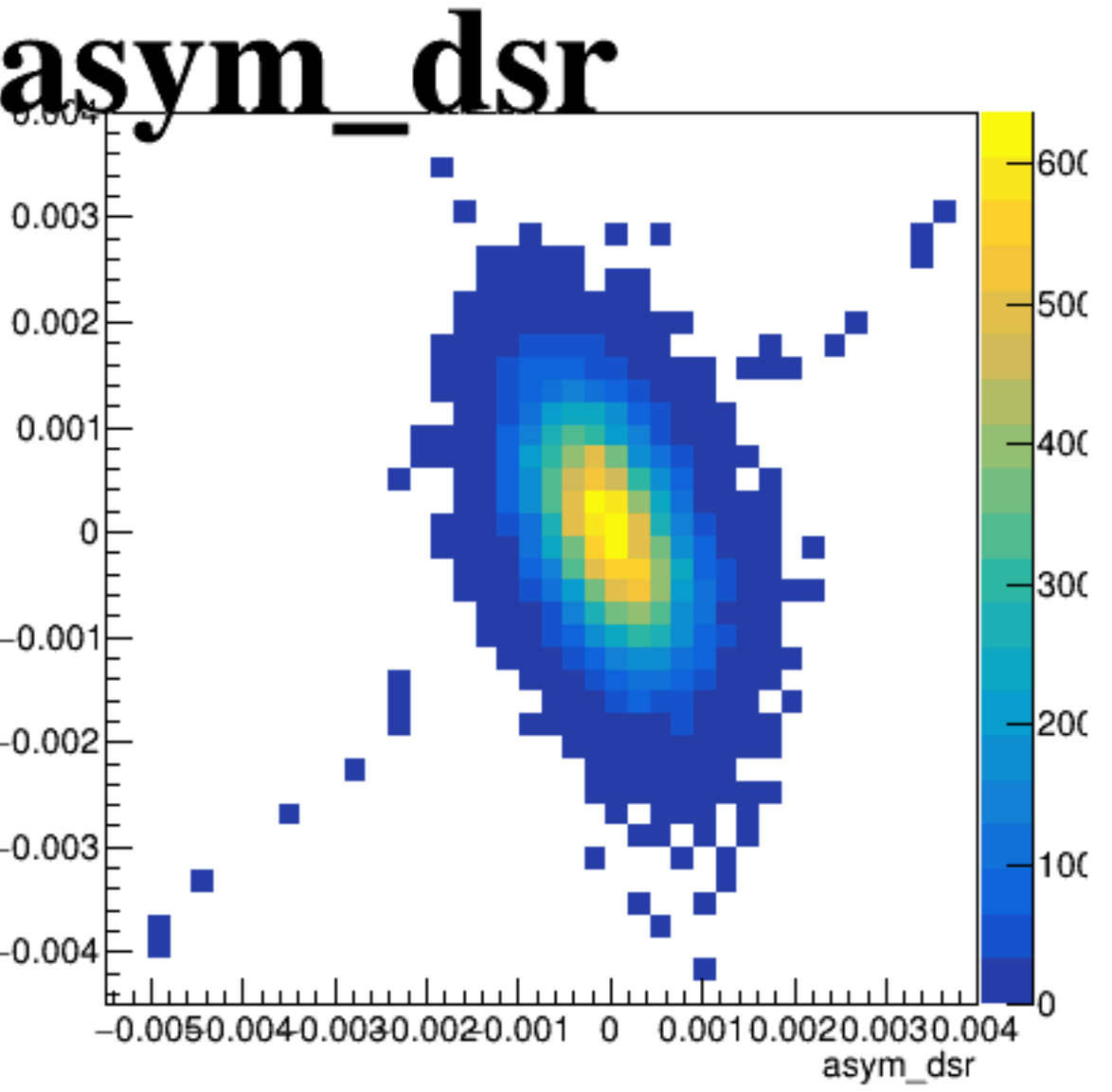
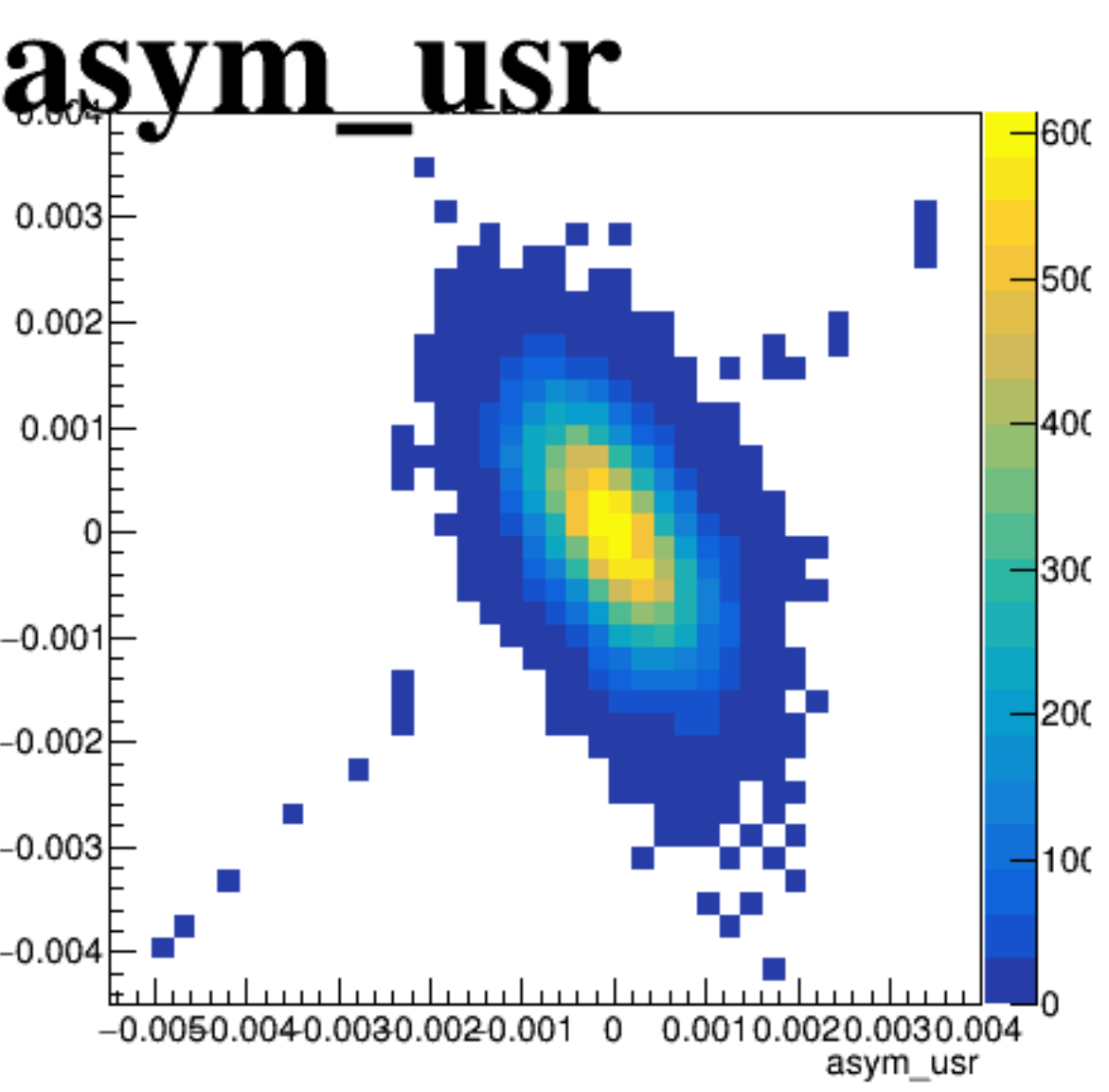
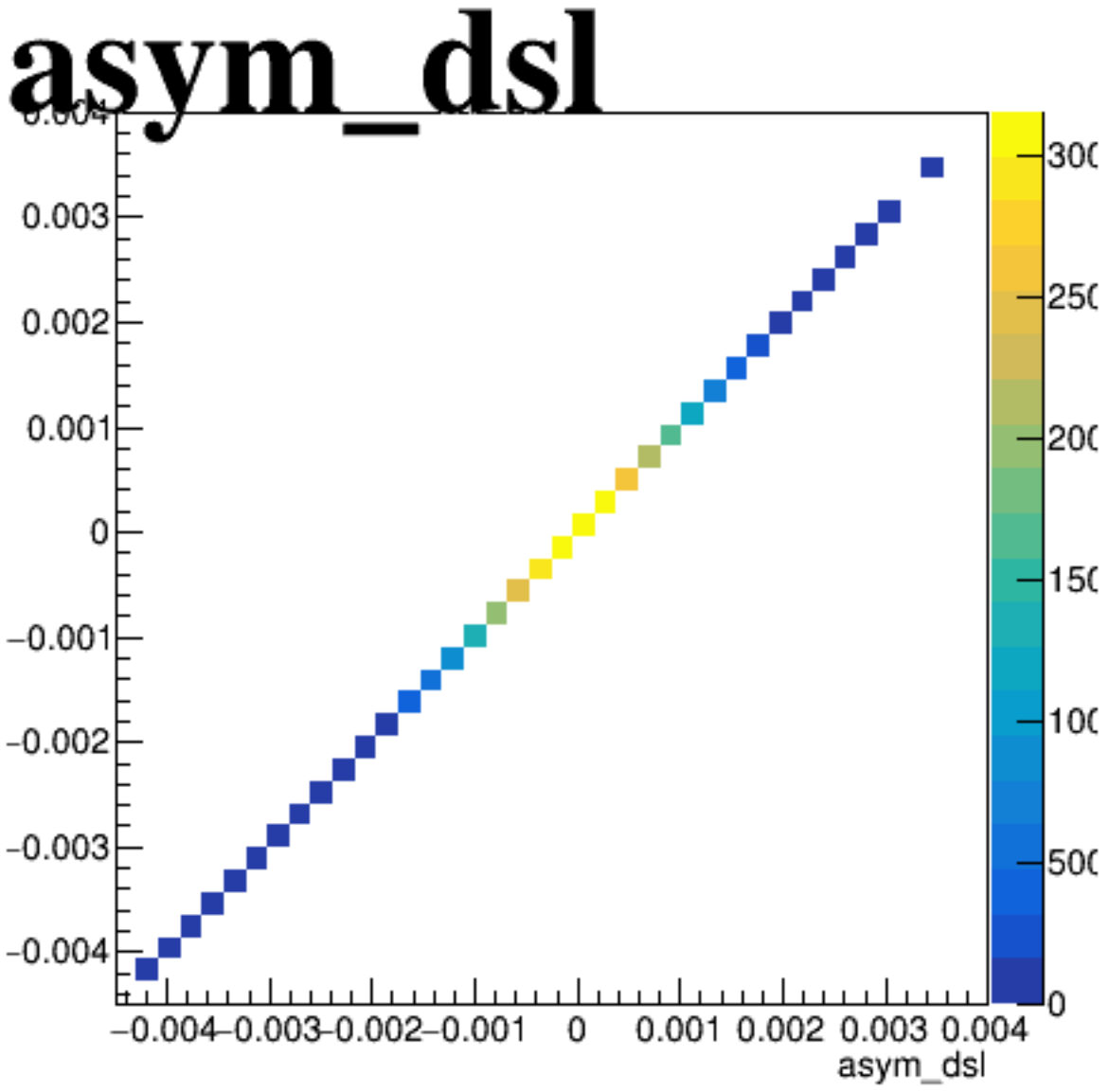
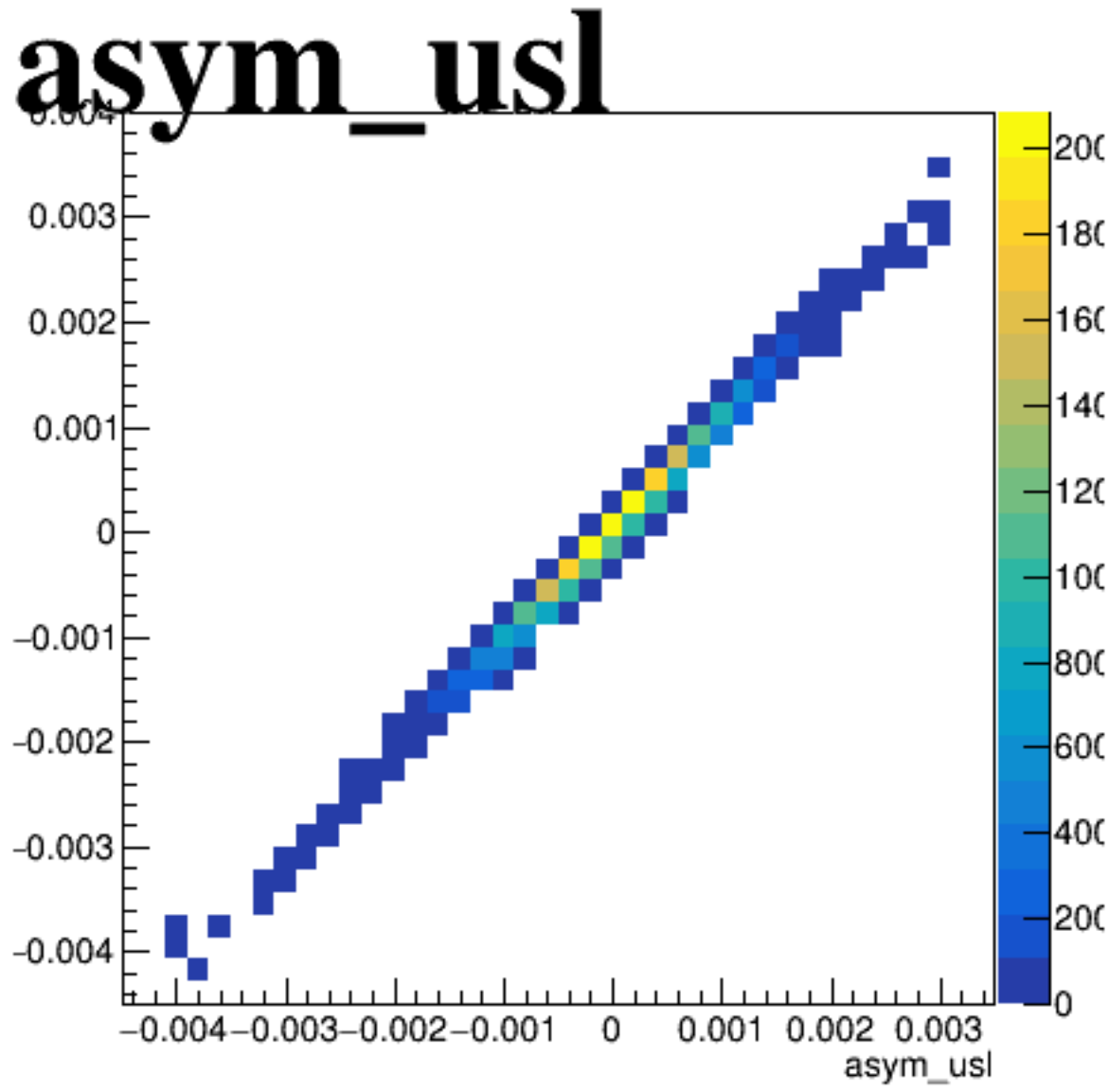
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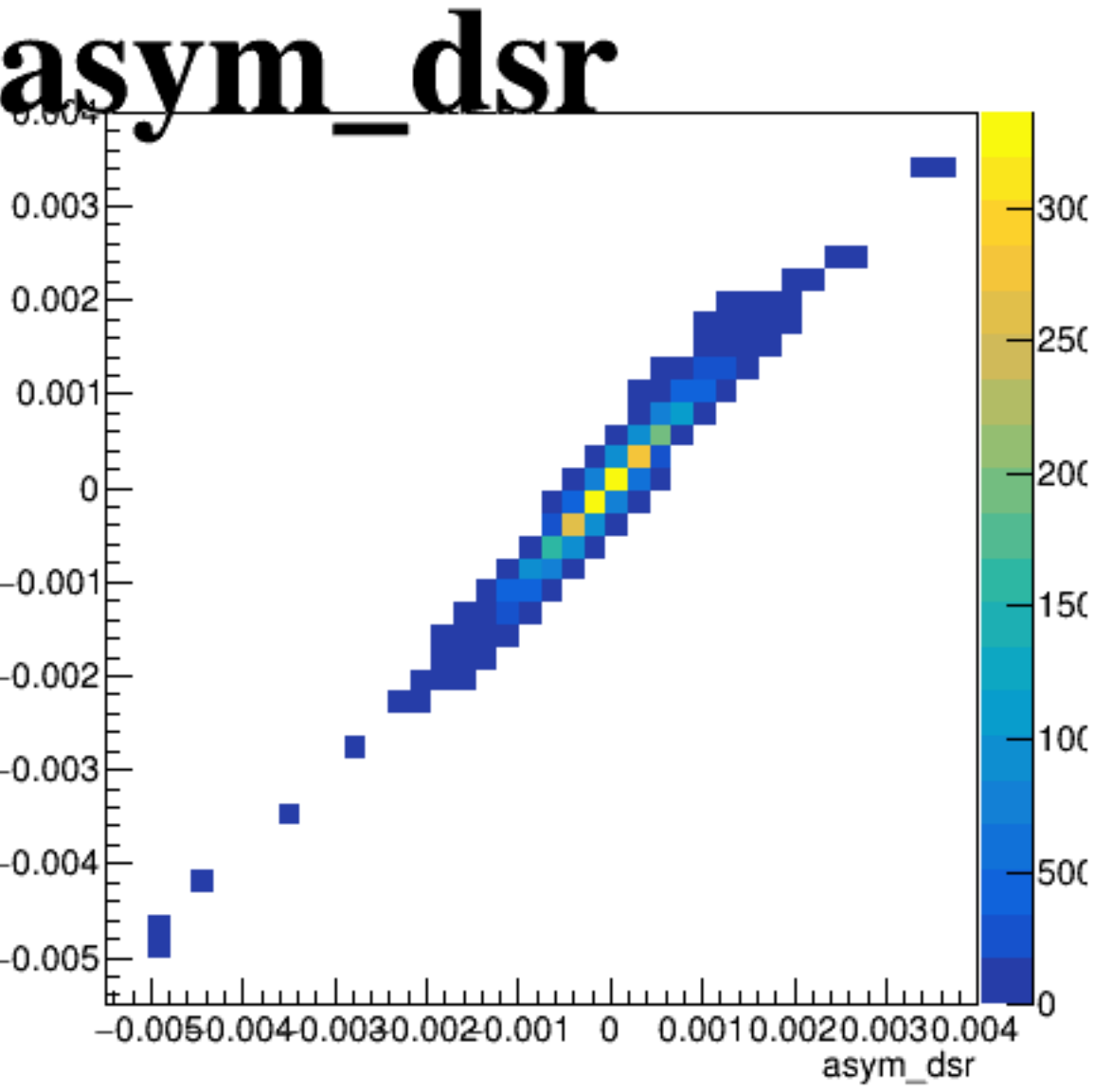
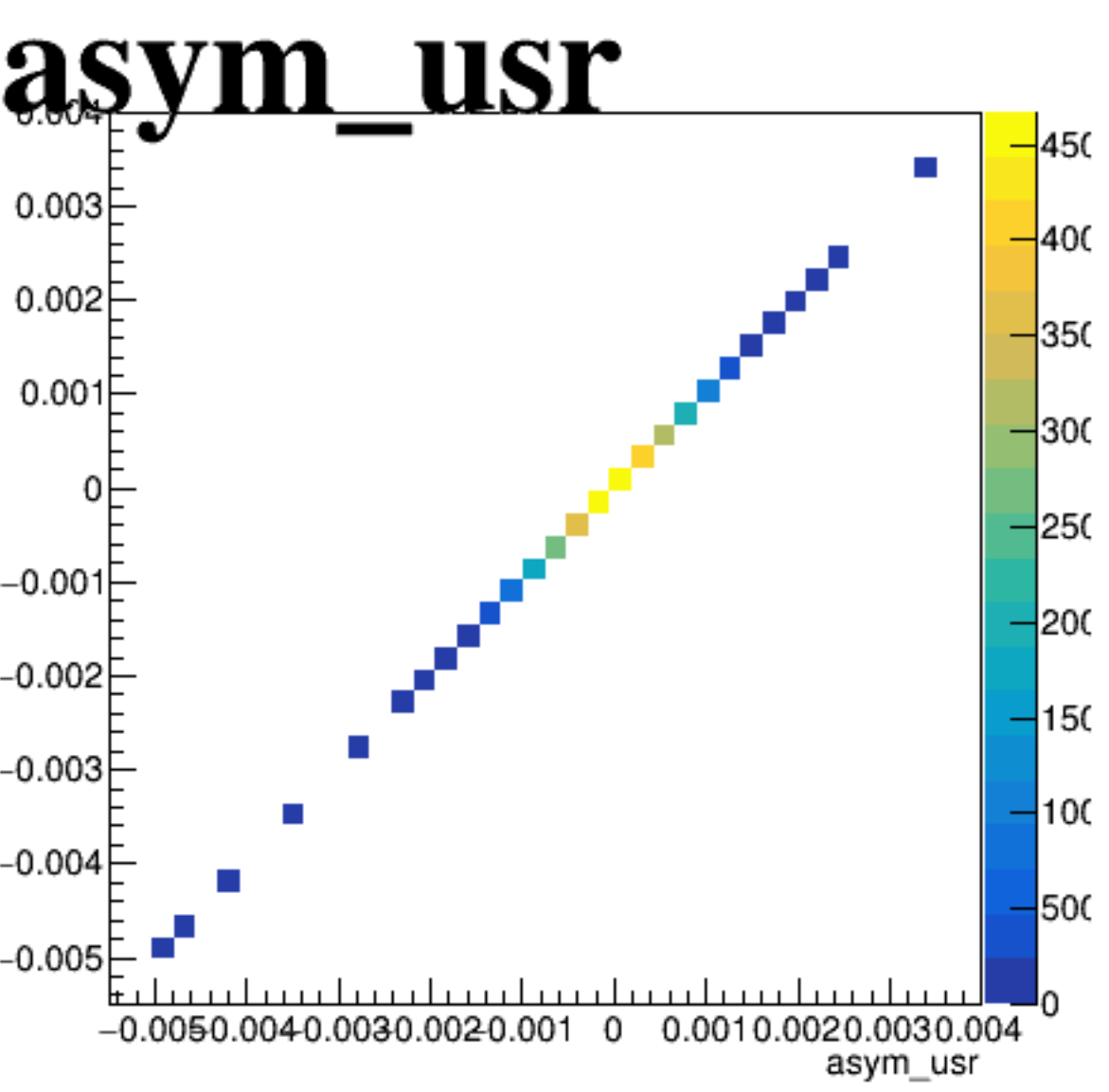
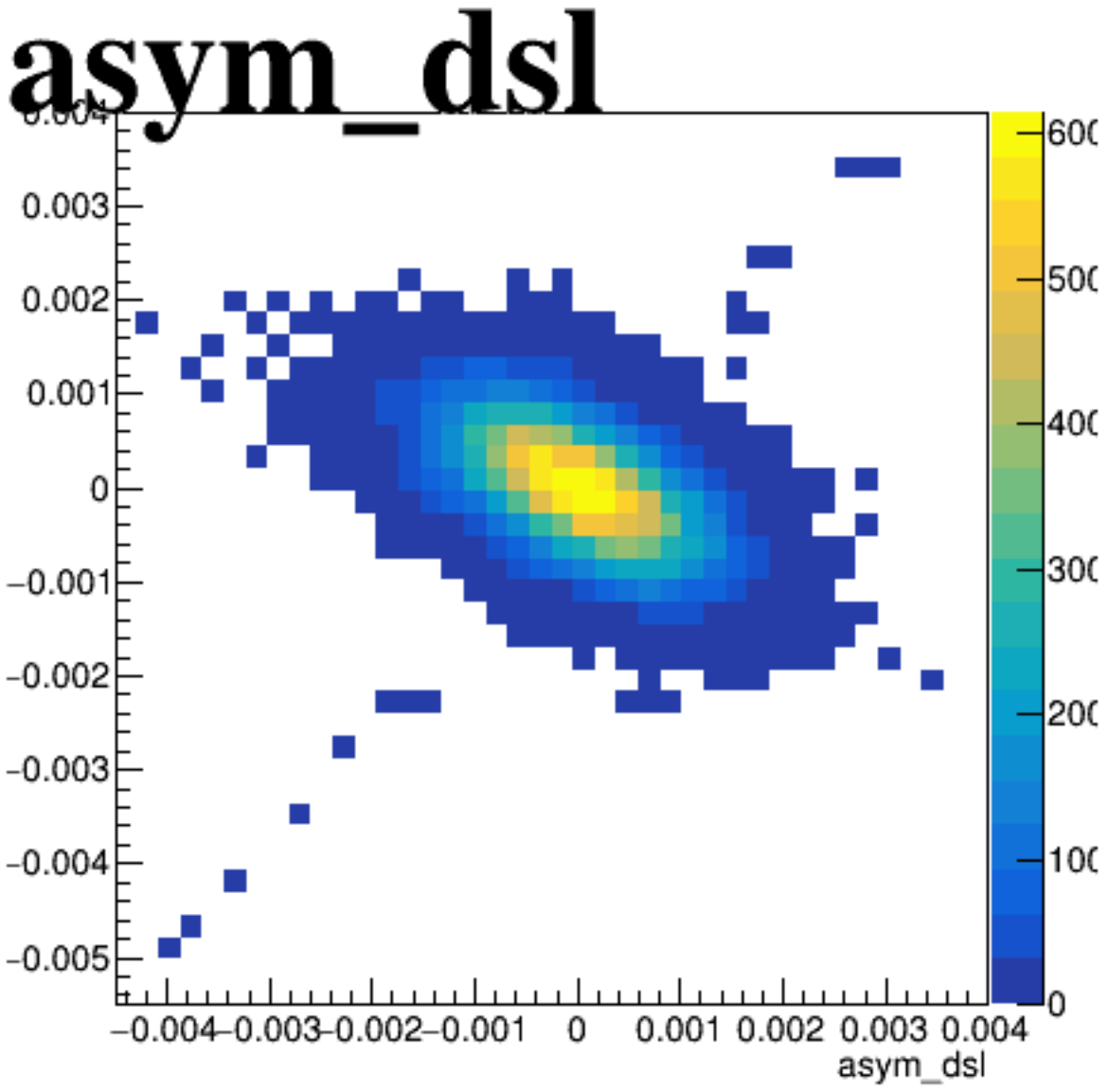
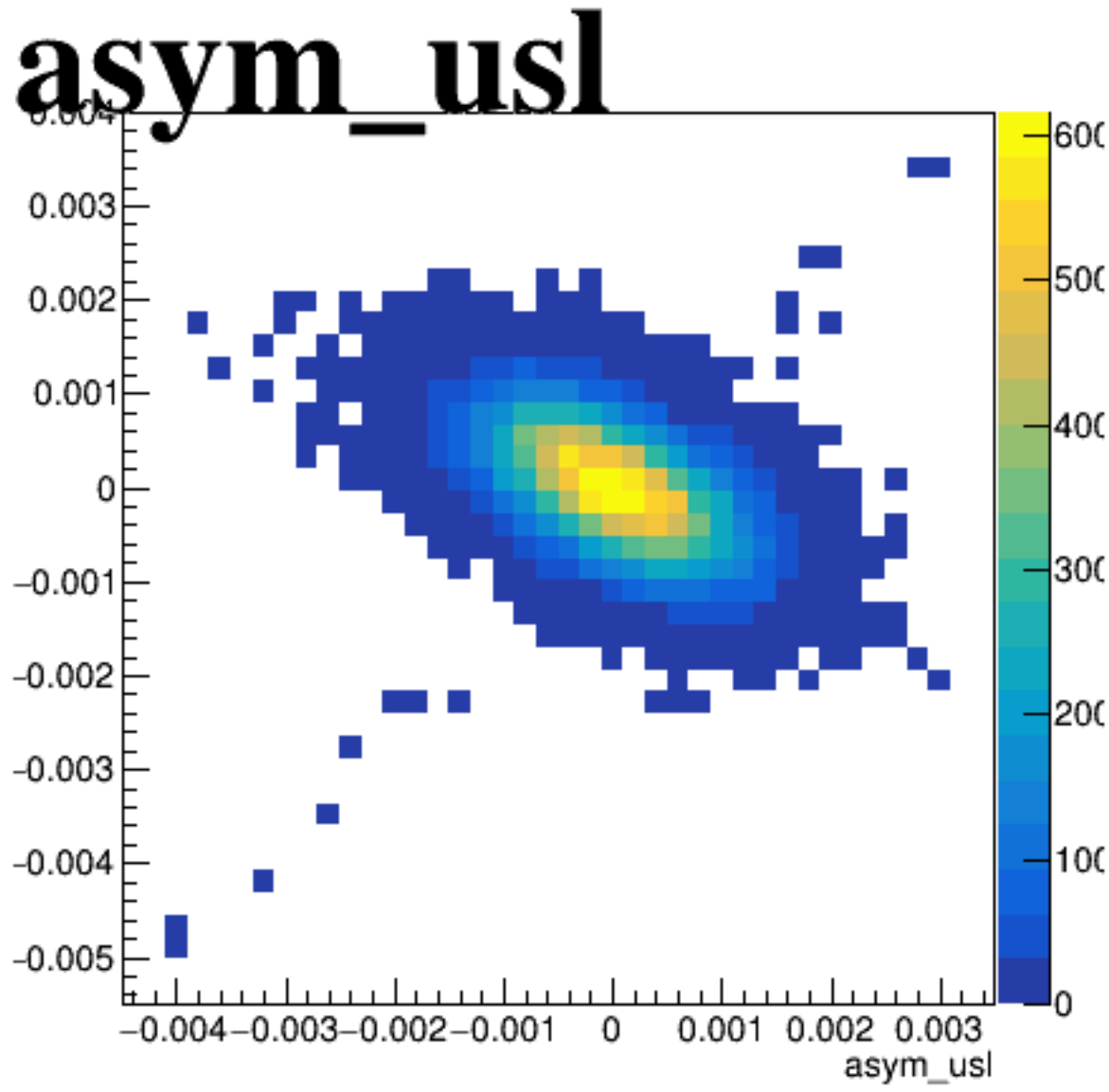
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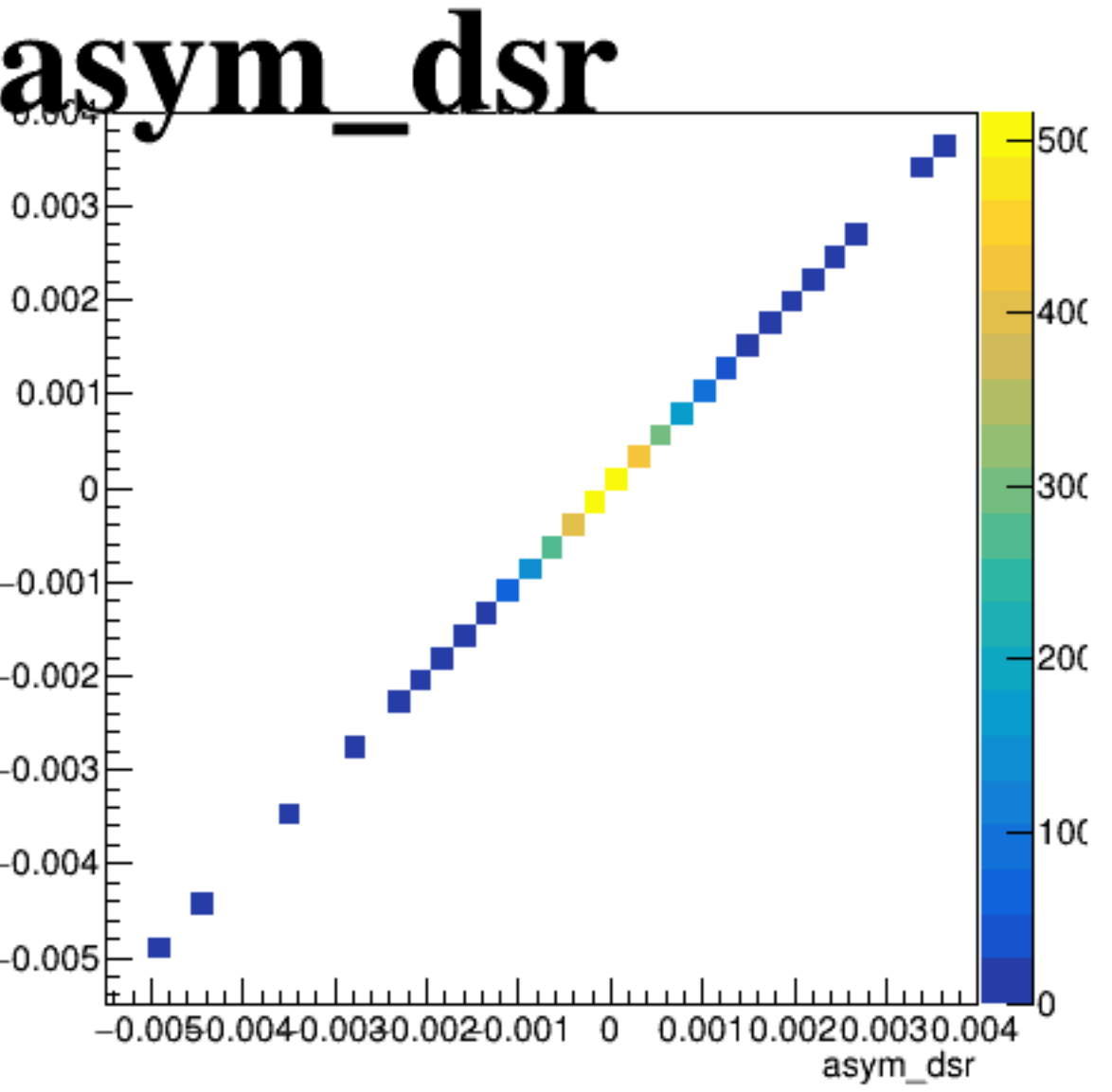
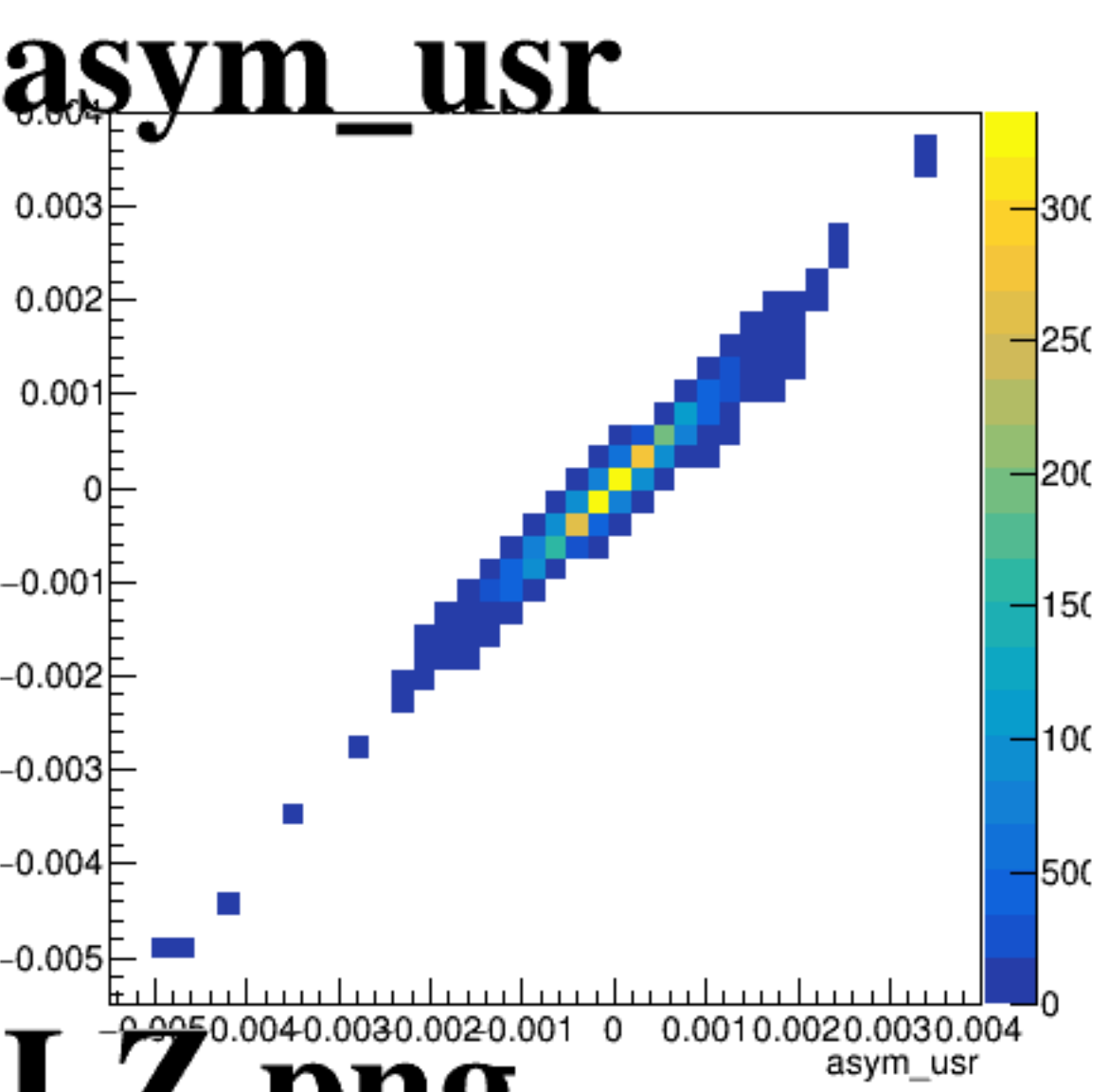
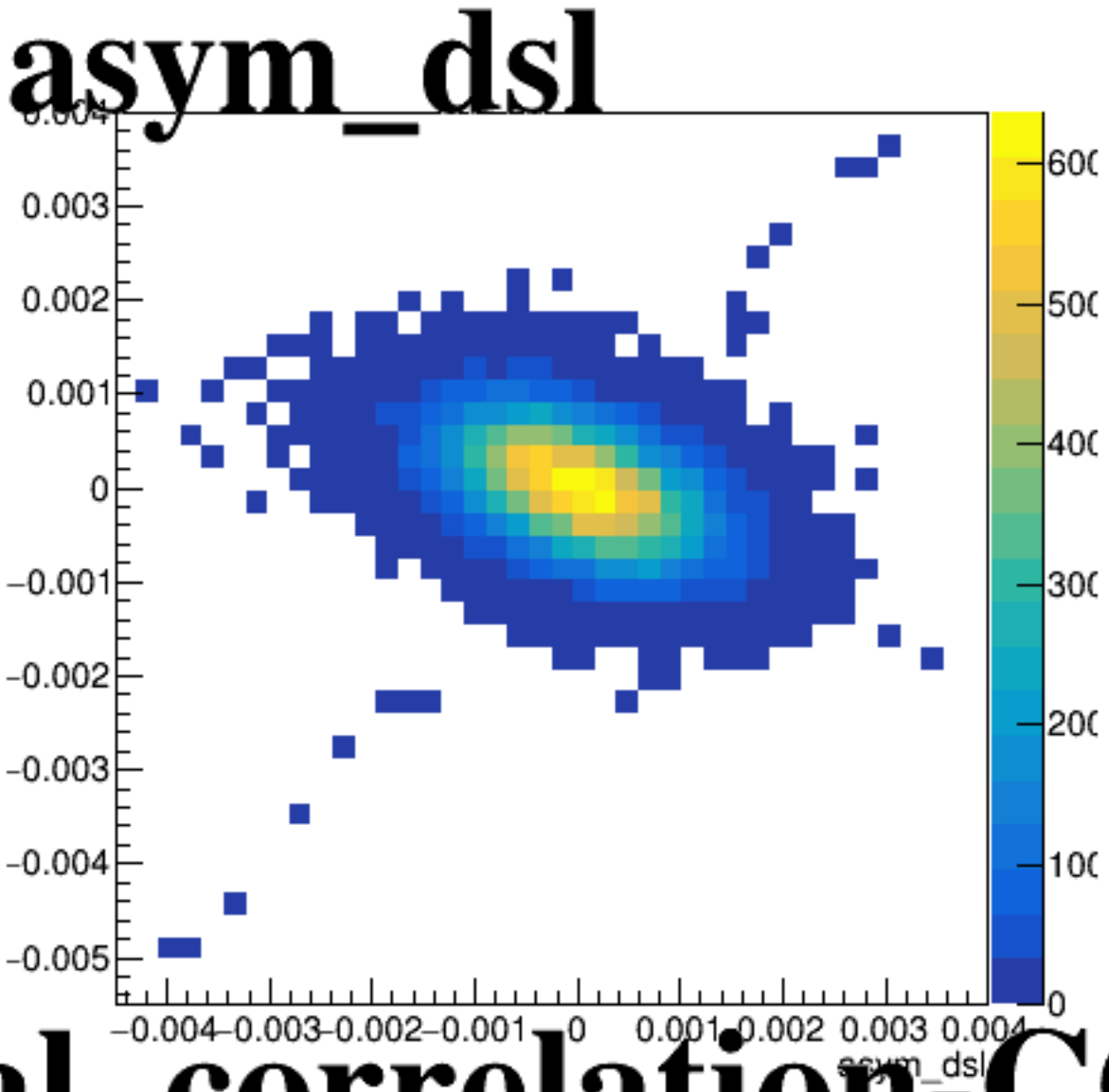
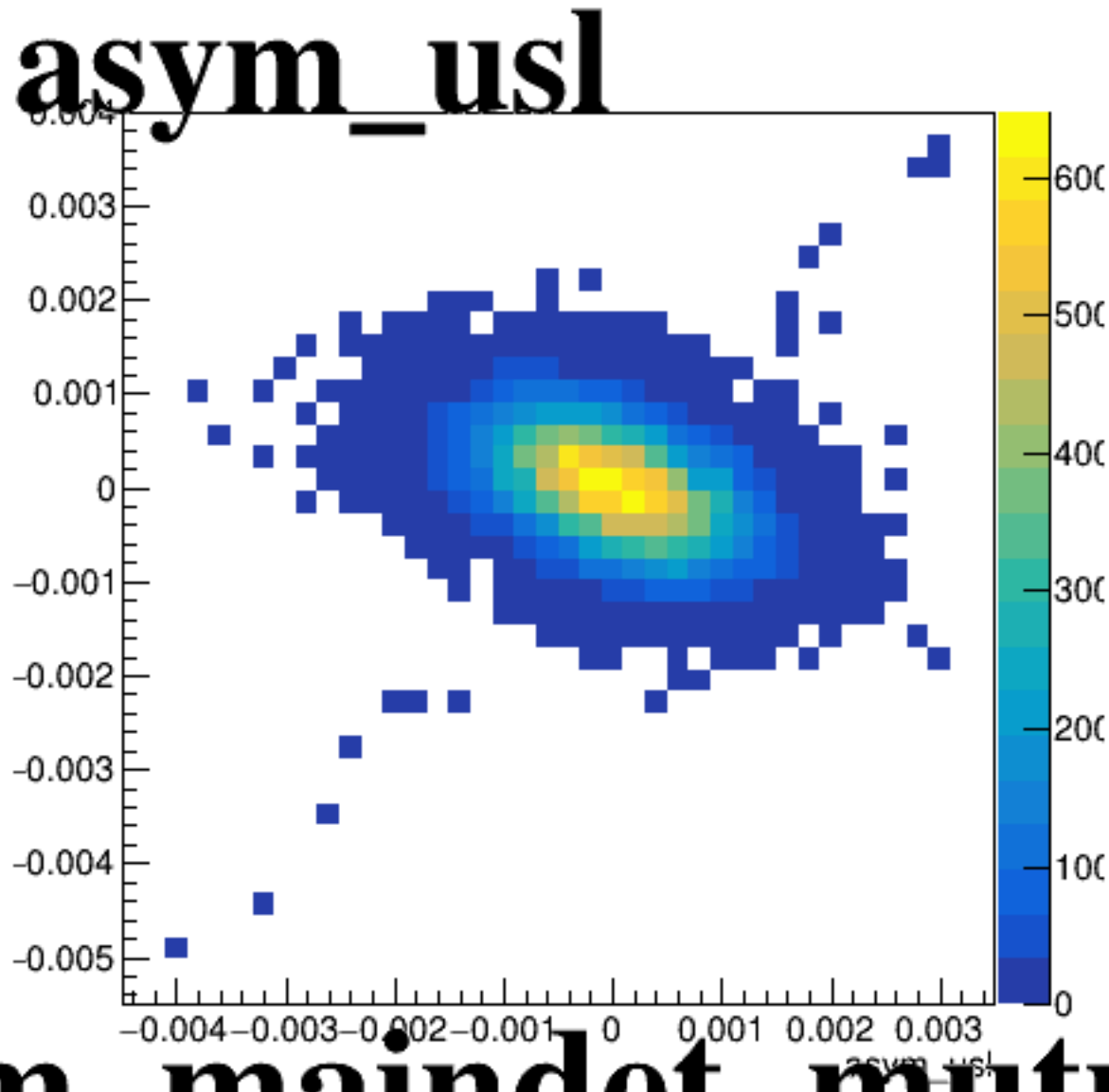
asym_dsl



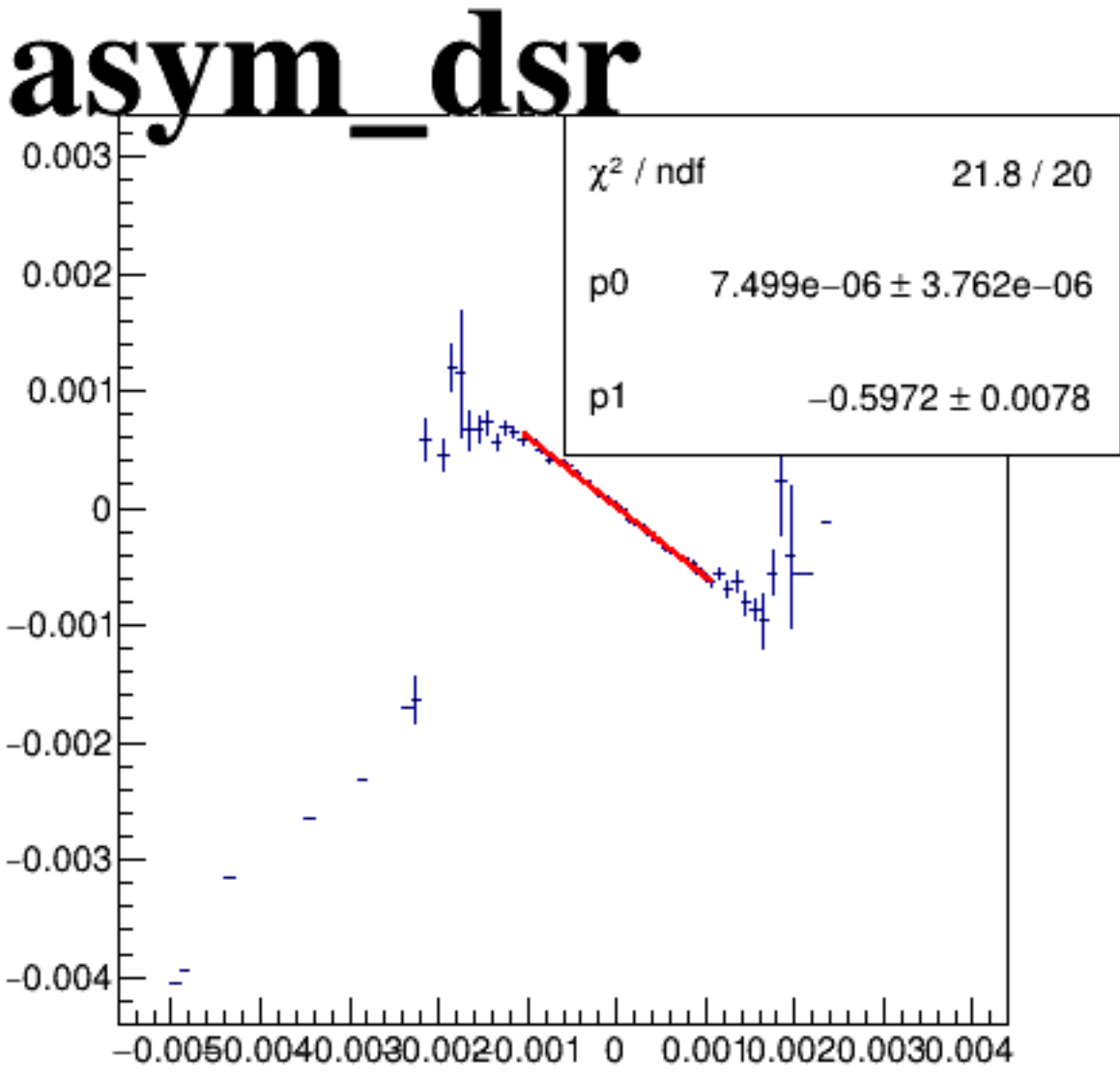
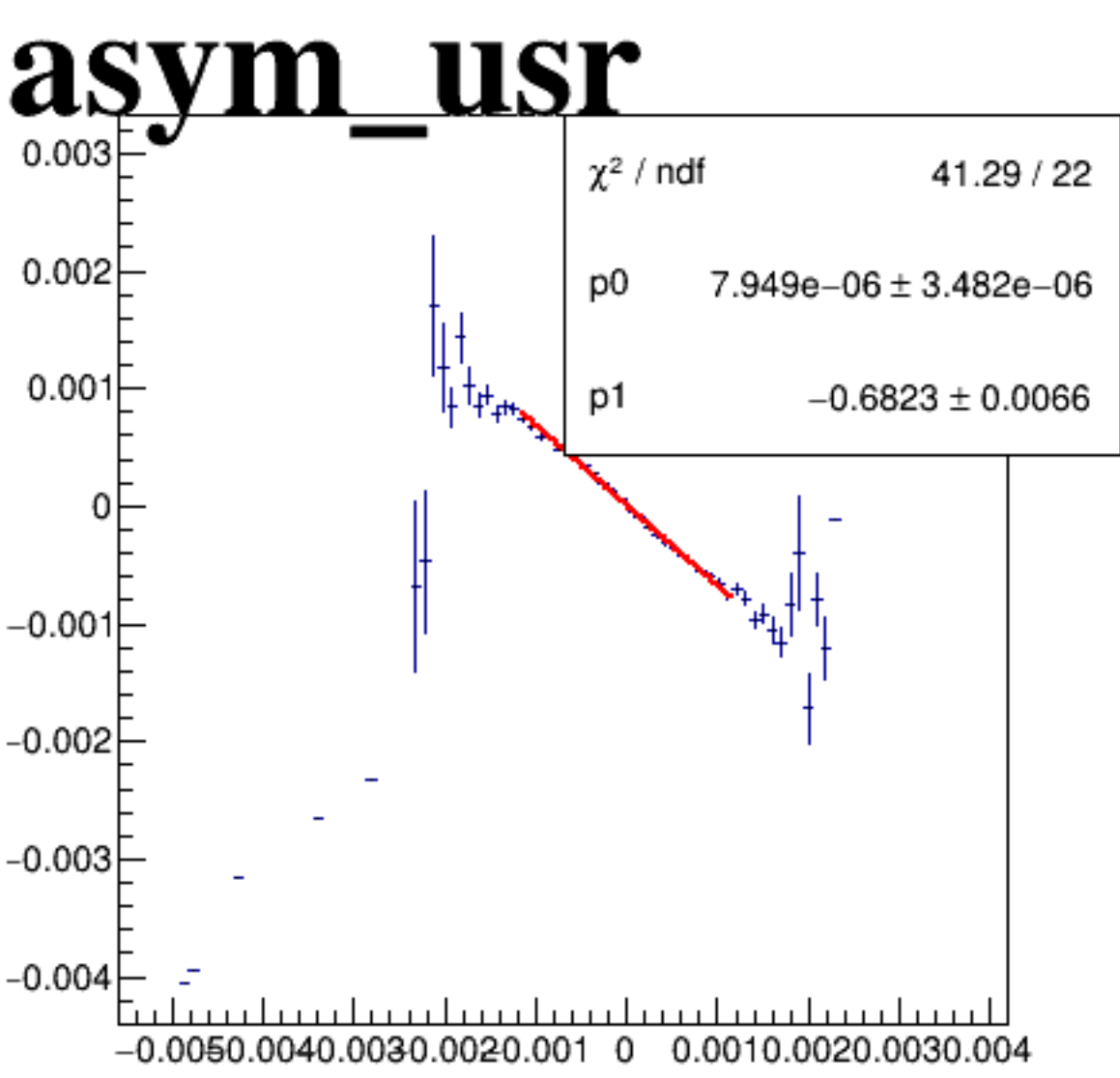
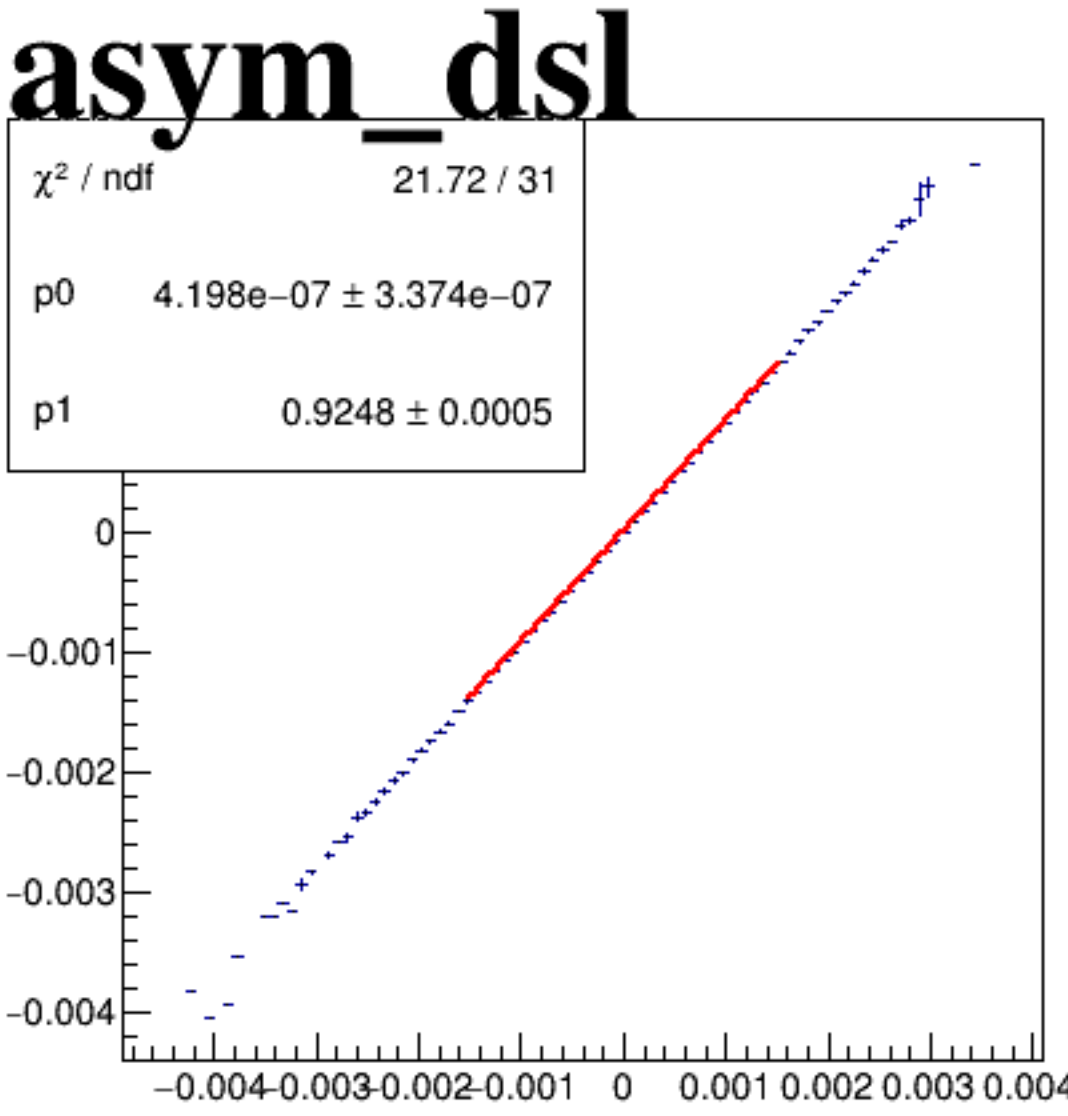
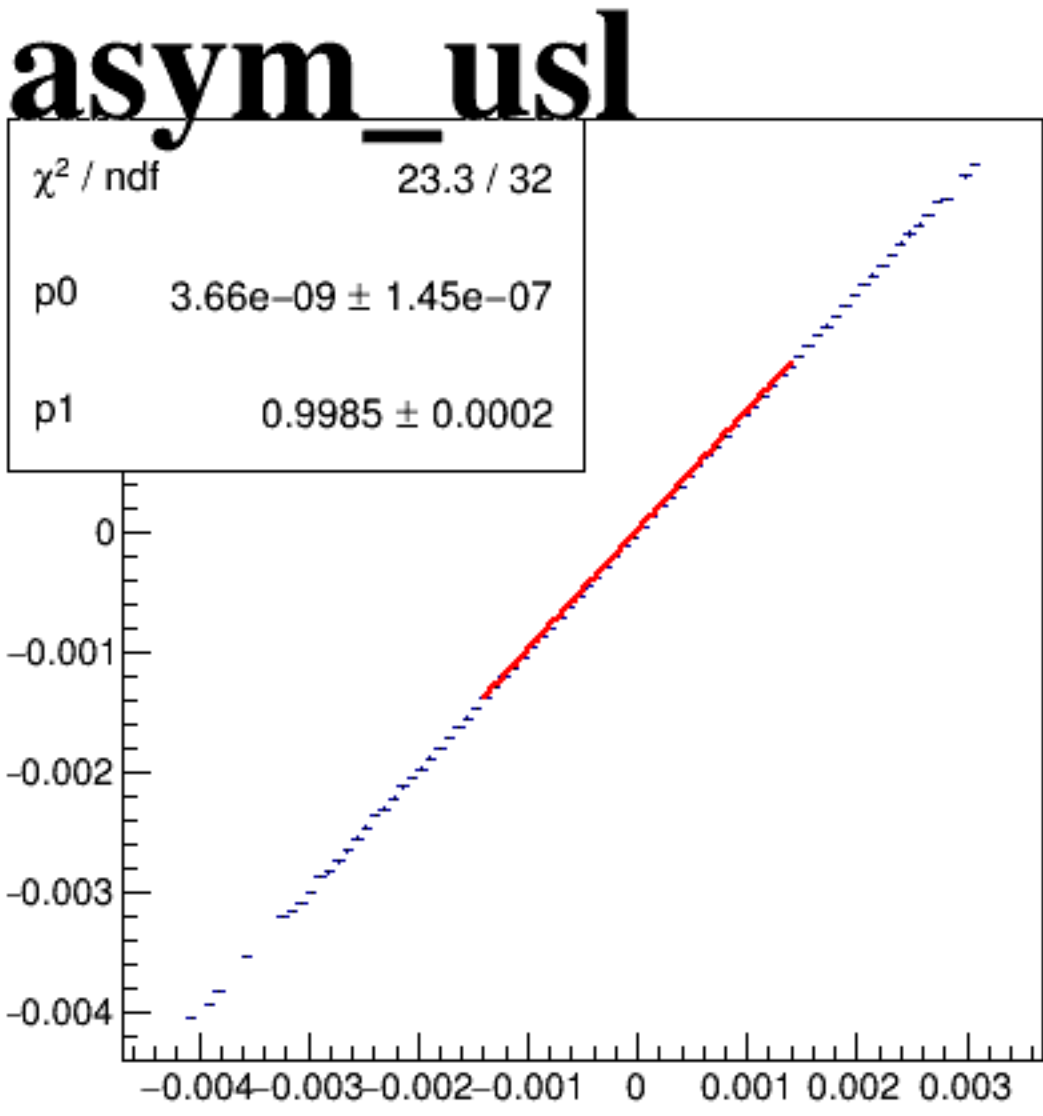
asym_usr



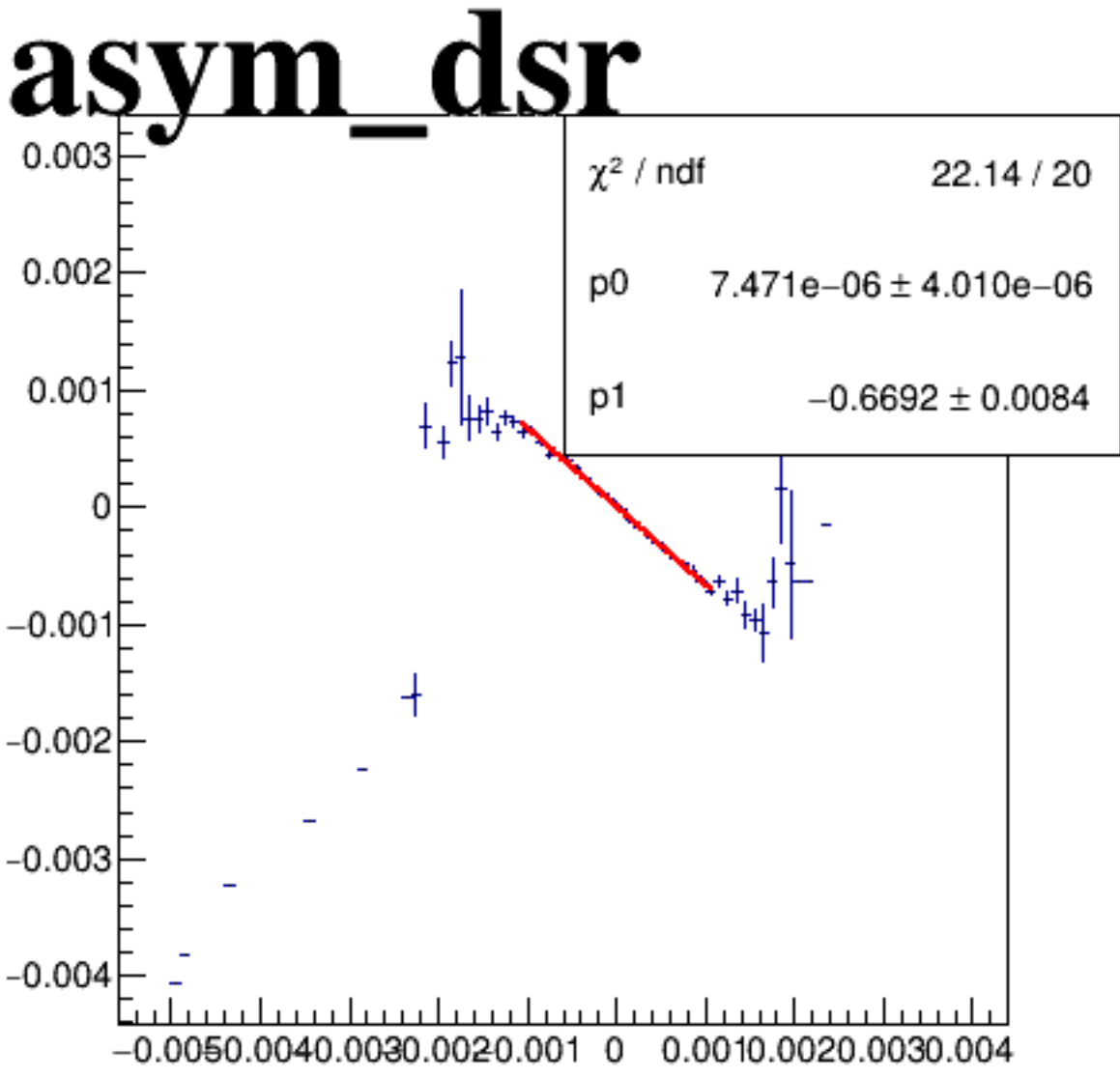
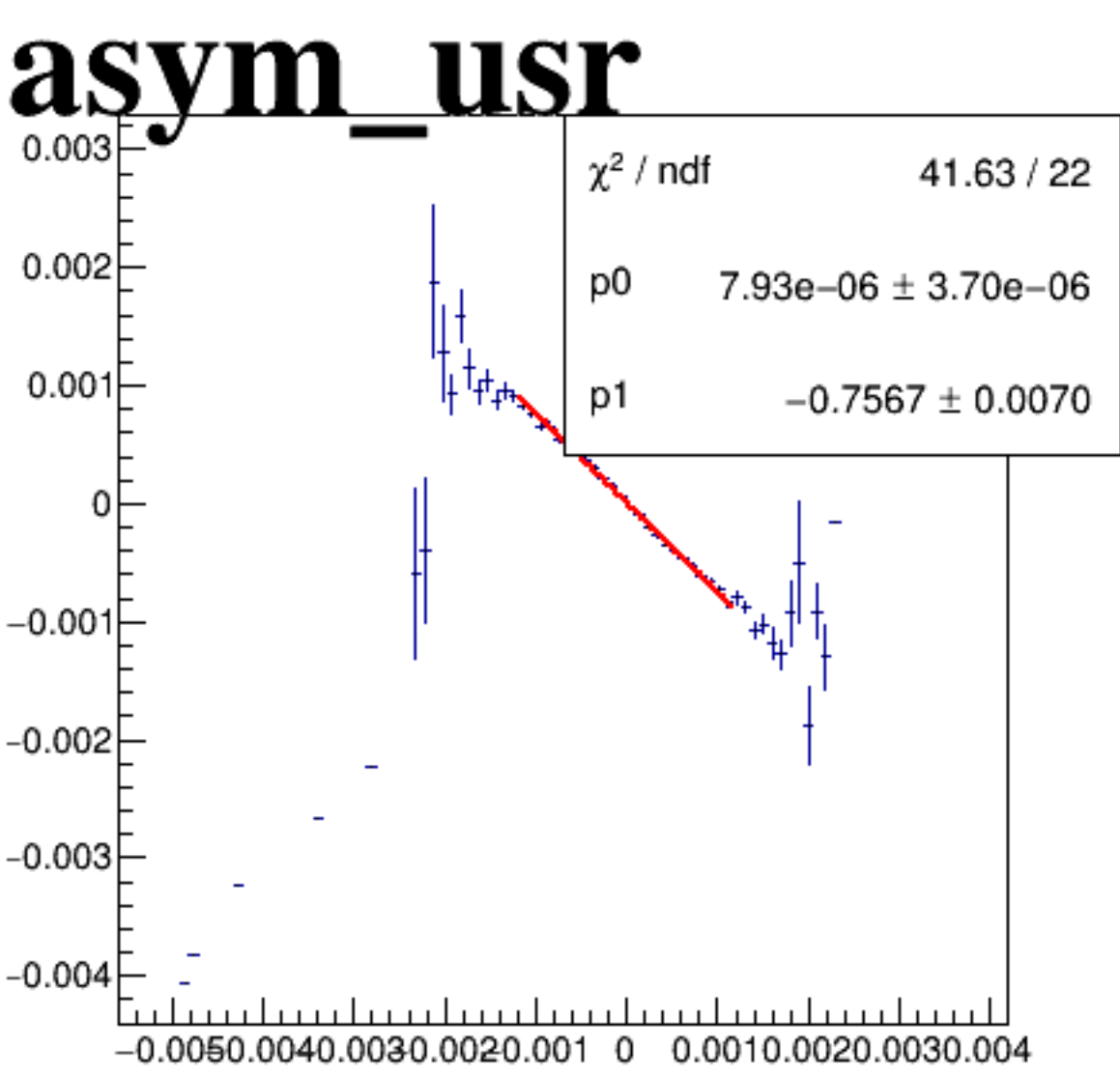
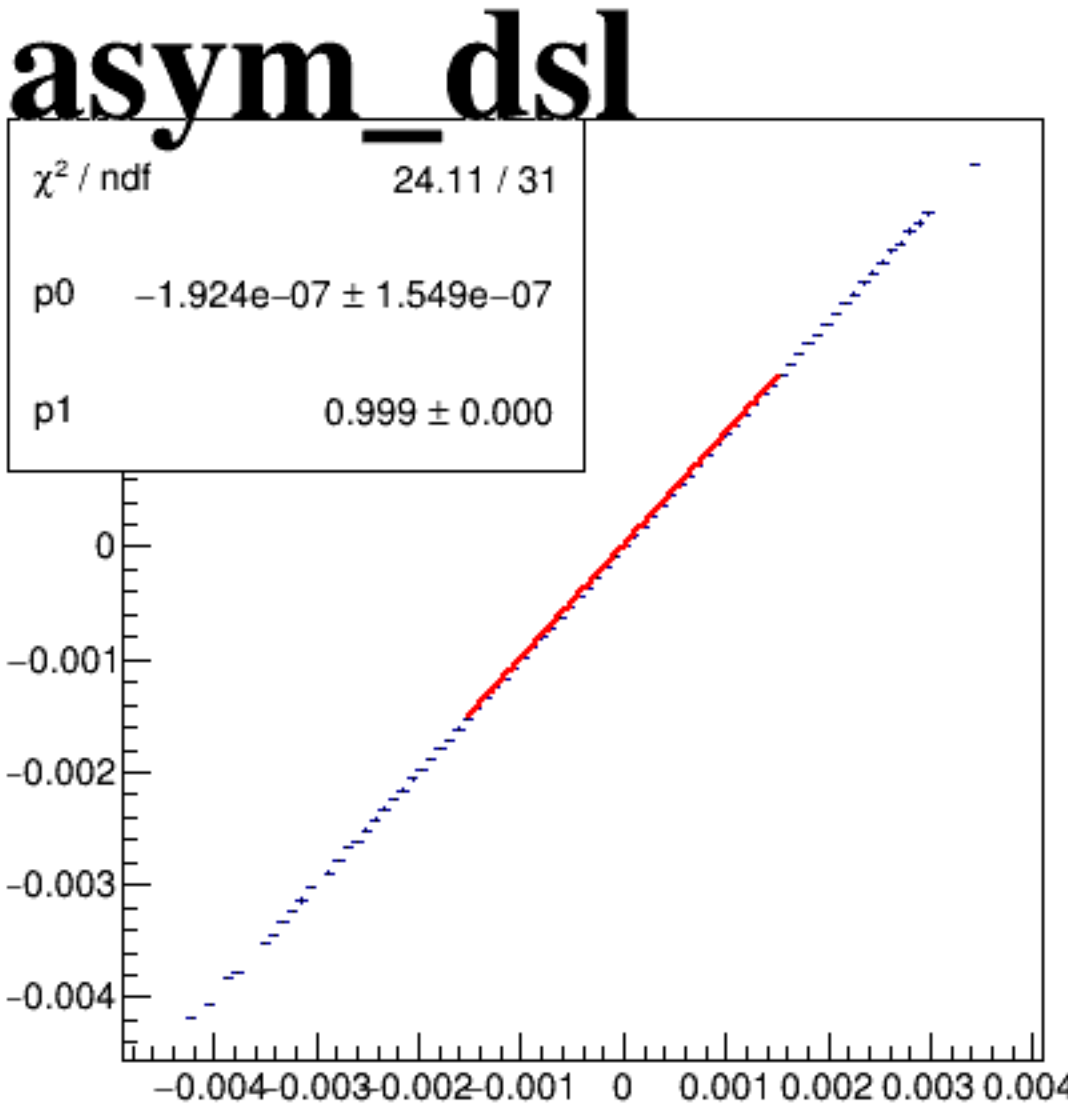
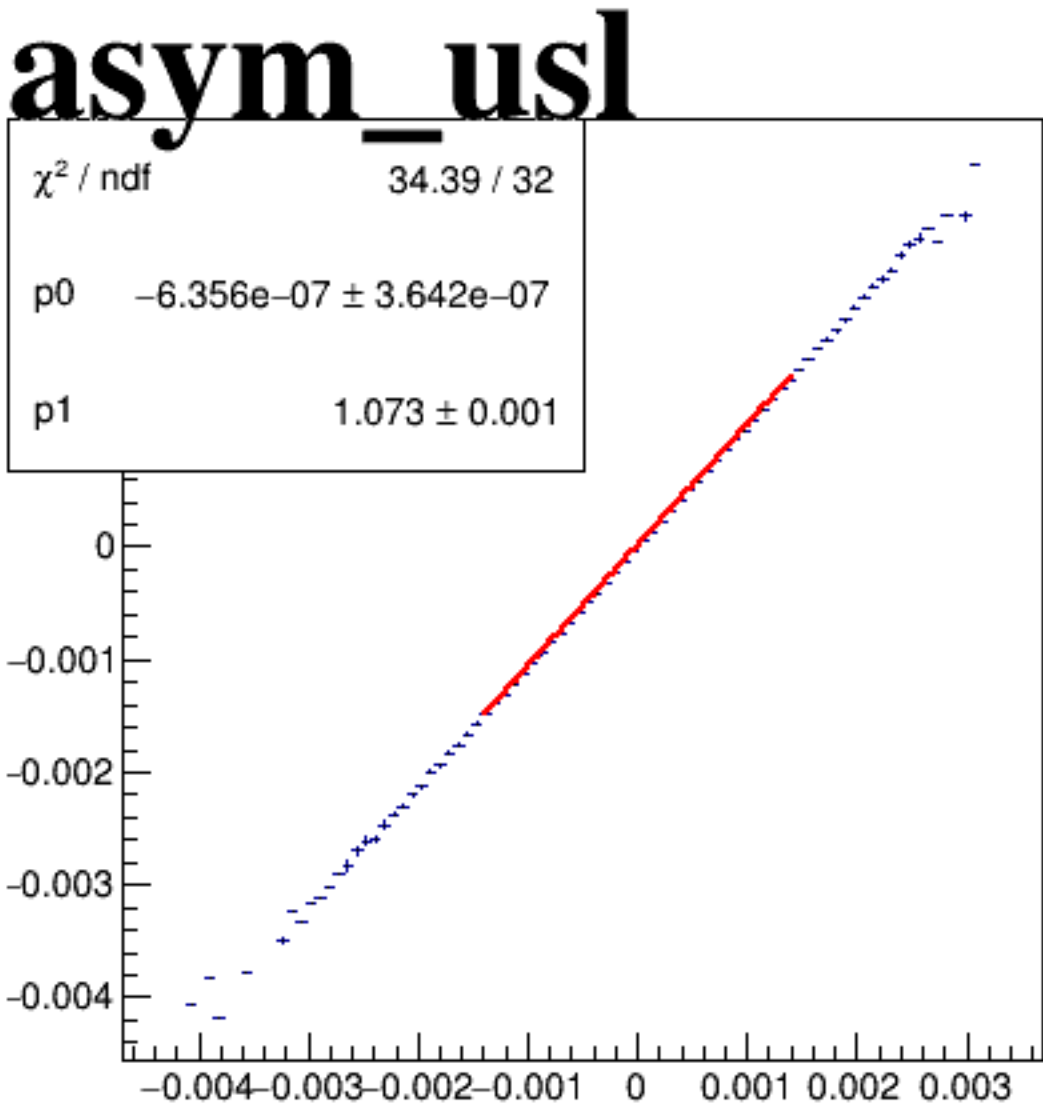
asym_dsr



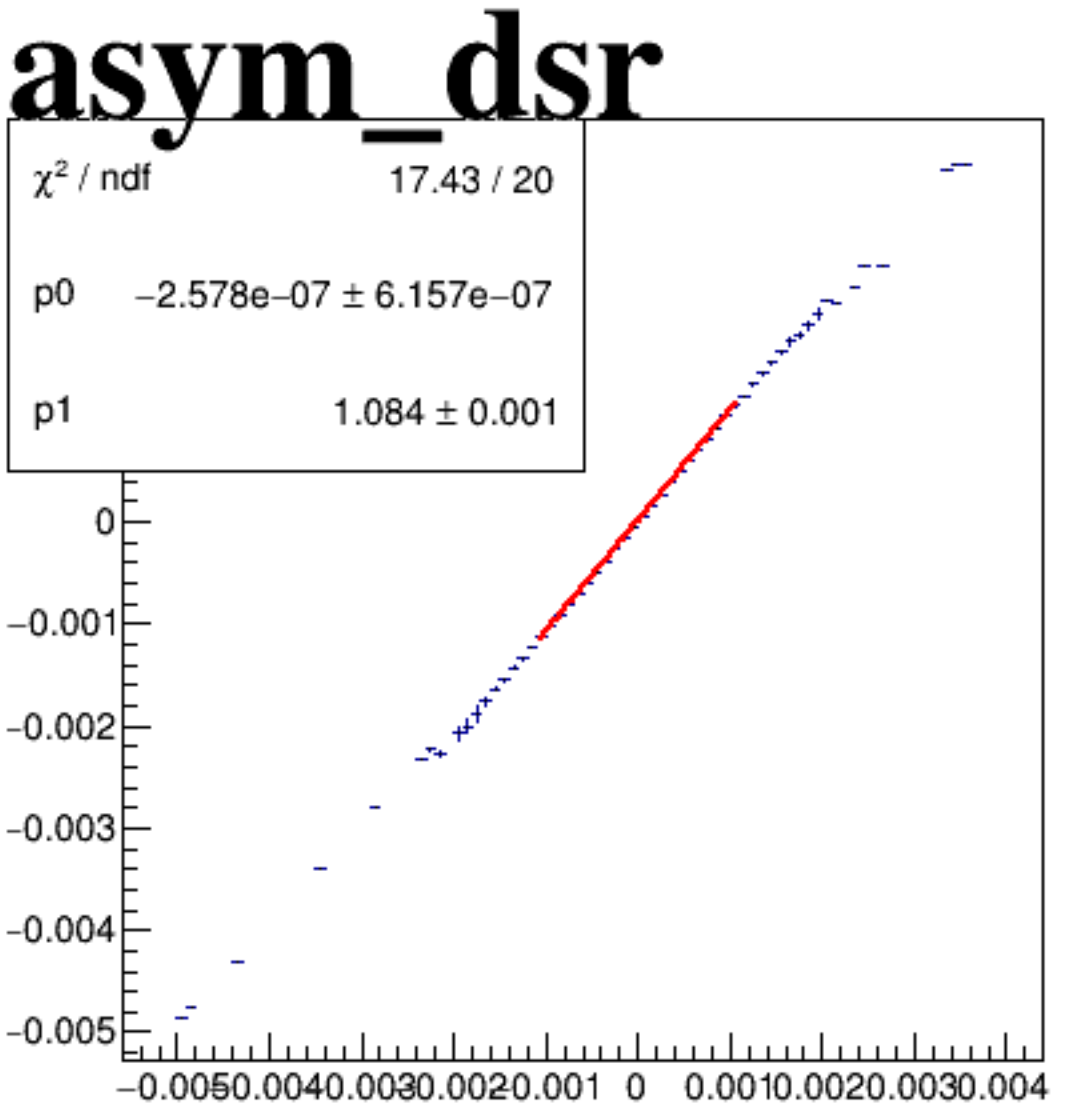
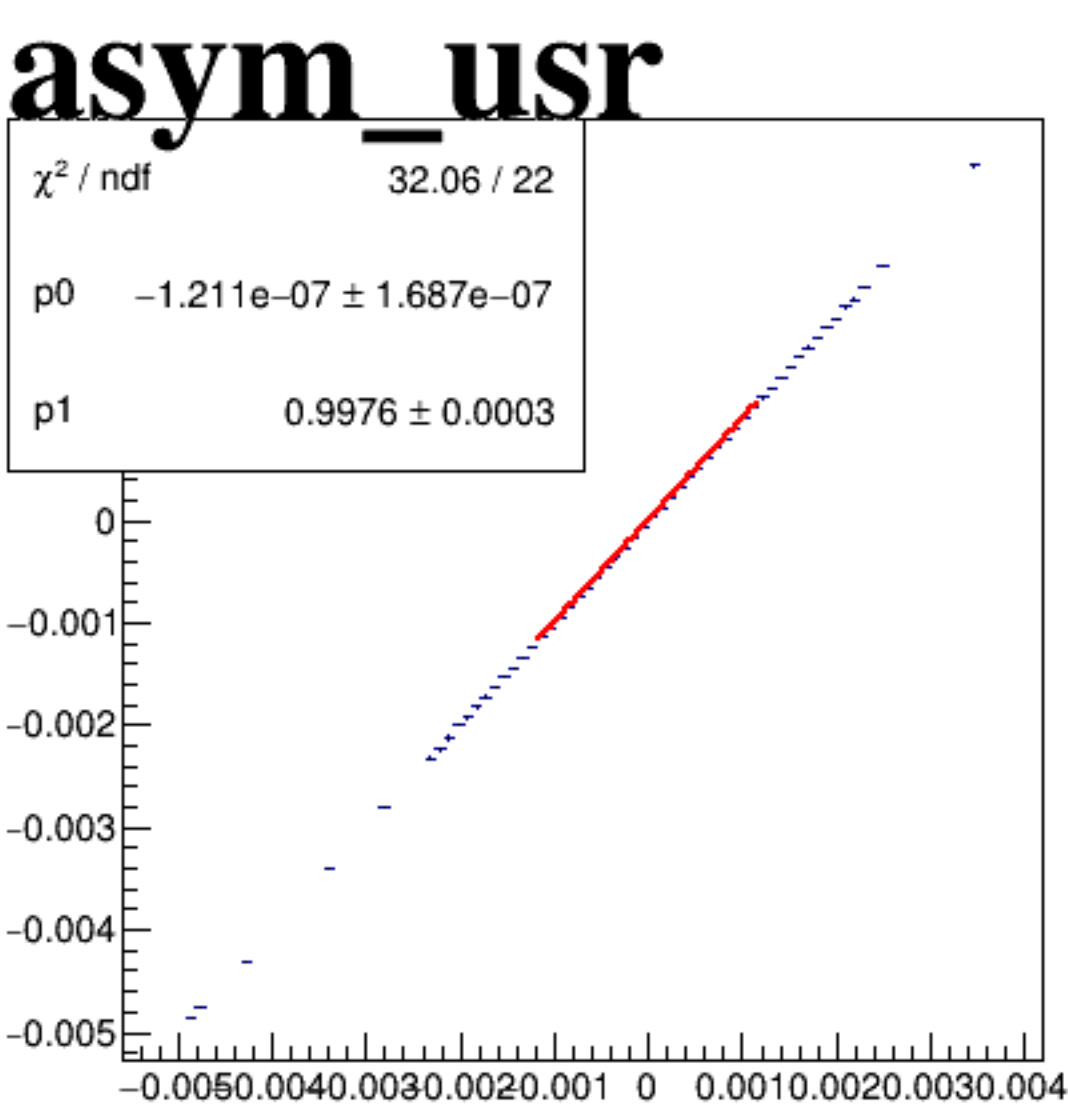
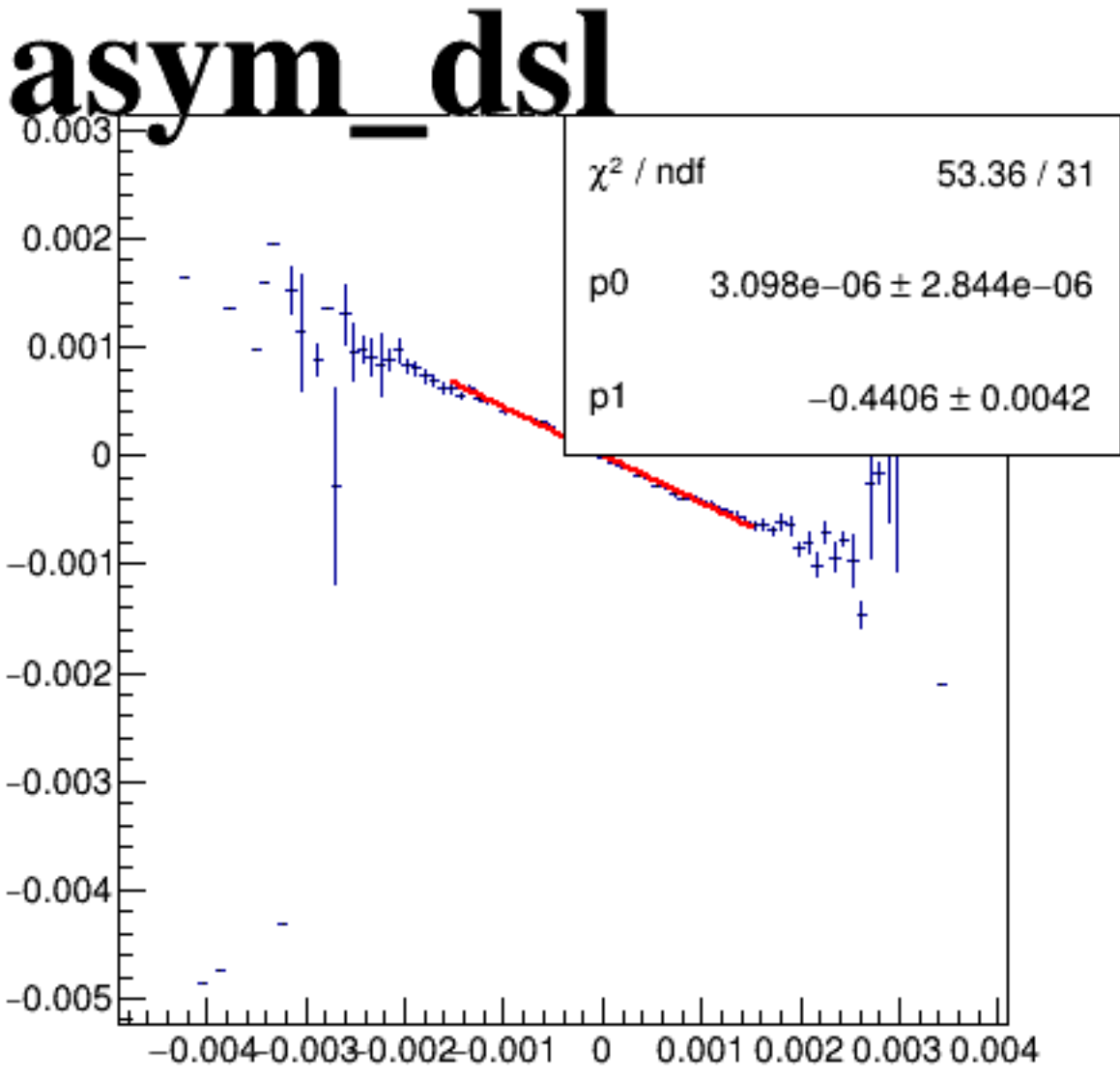
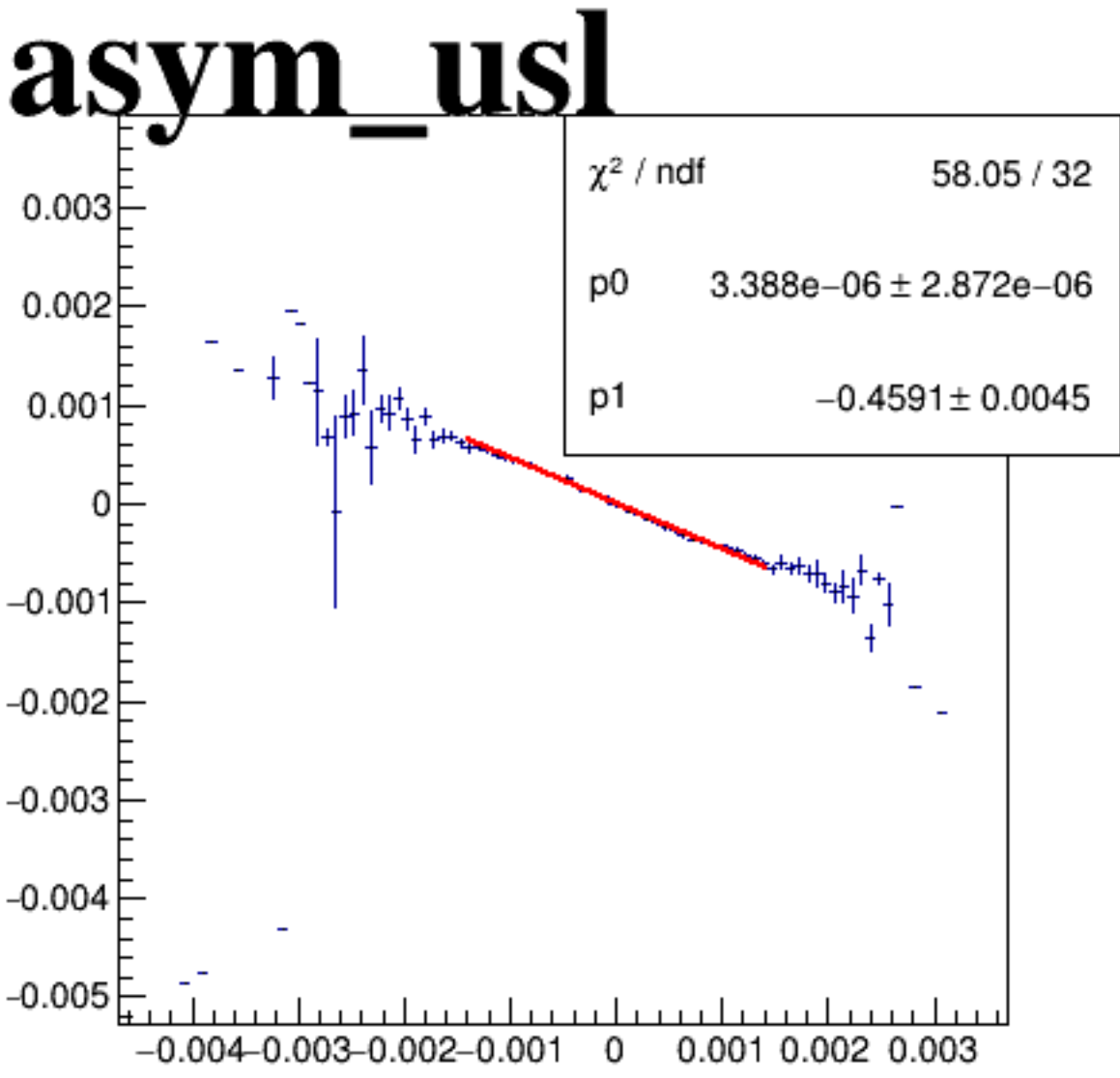
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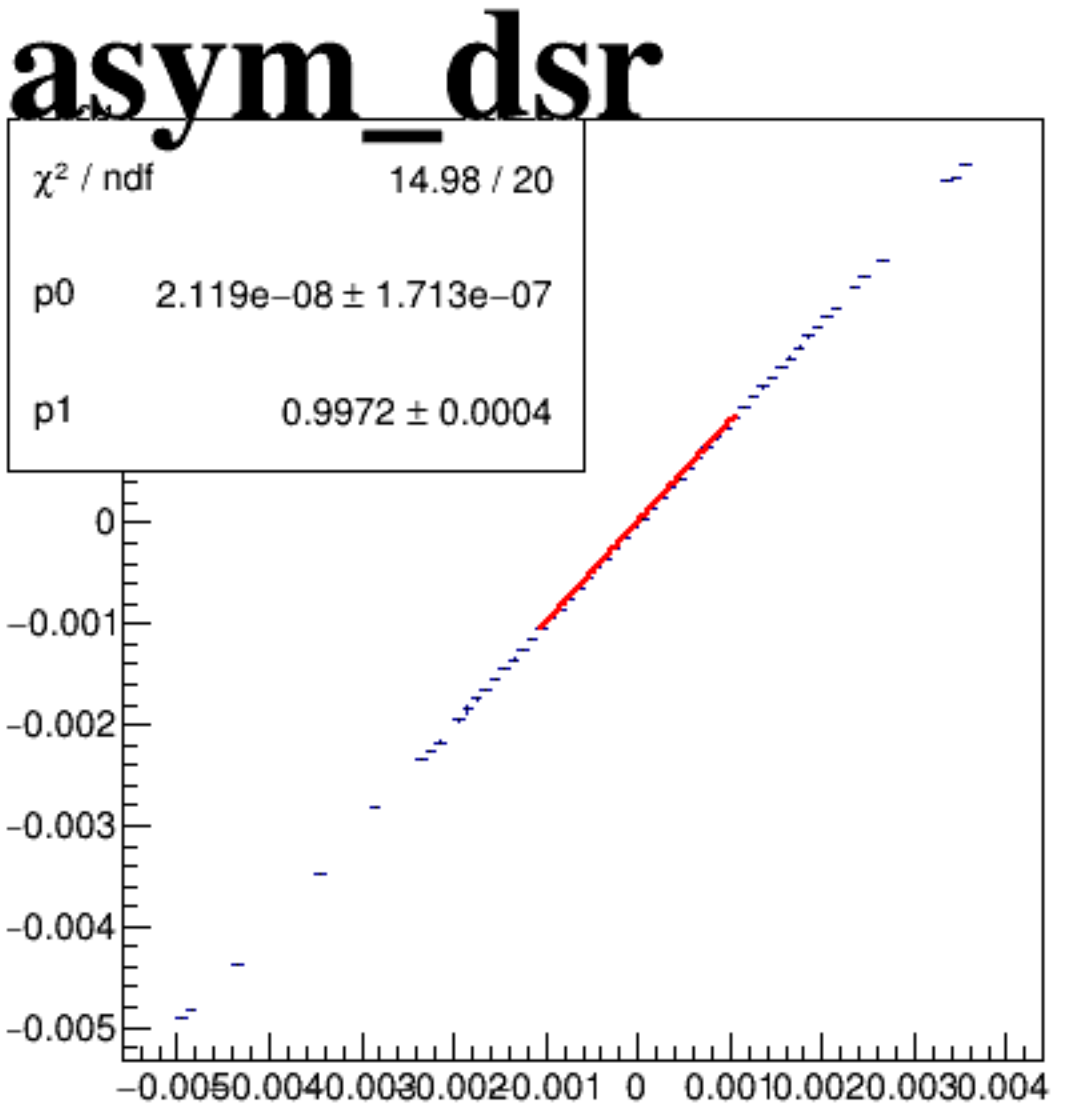
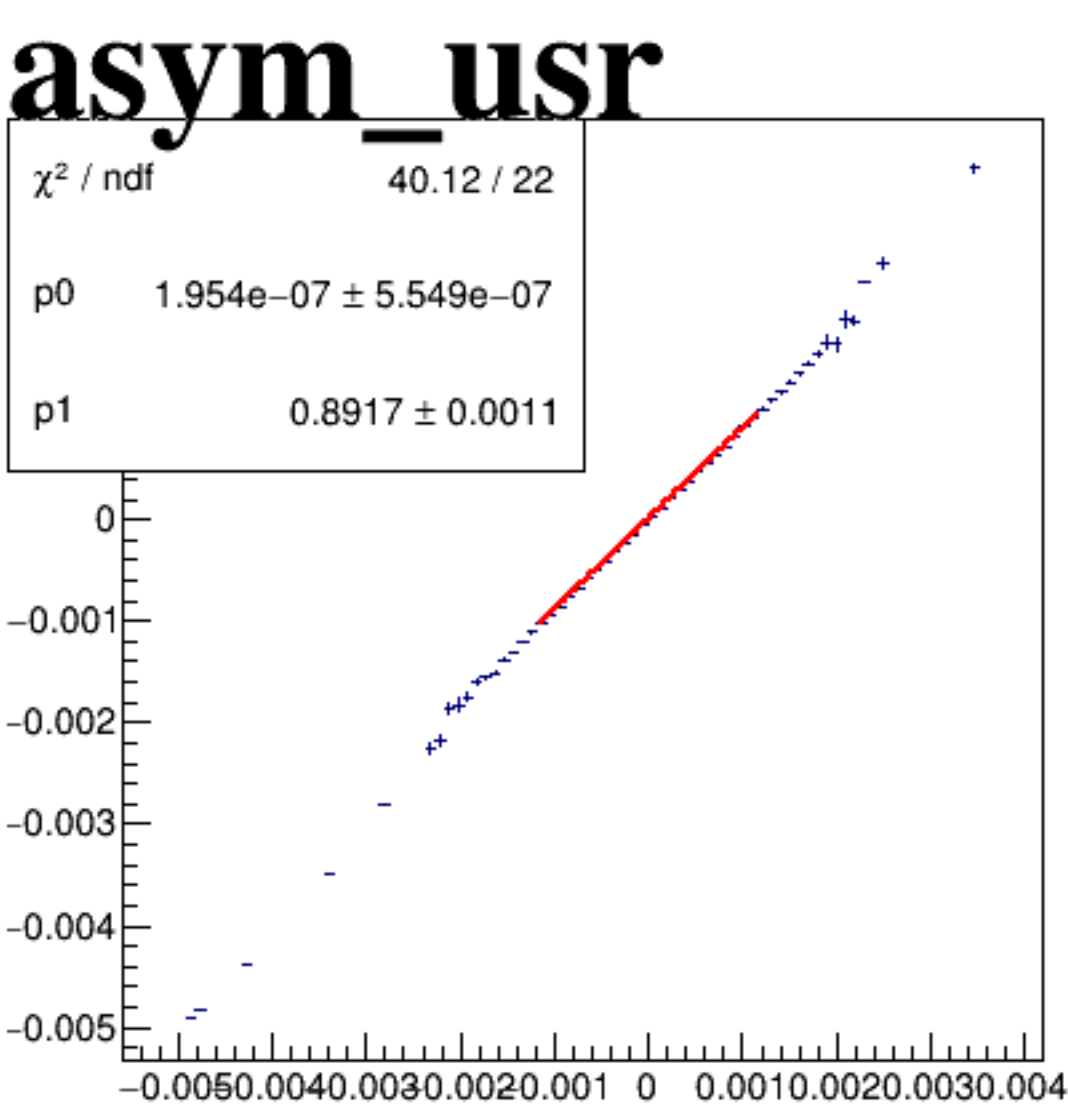
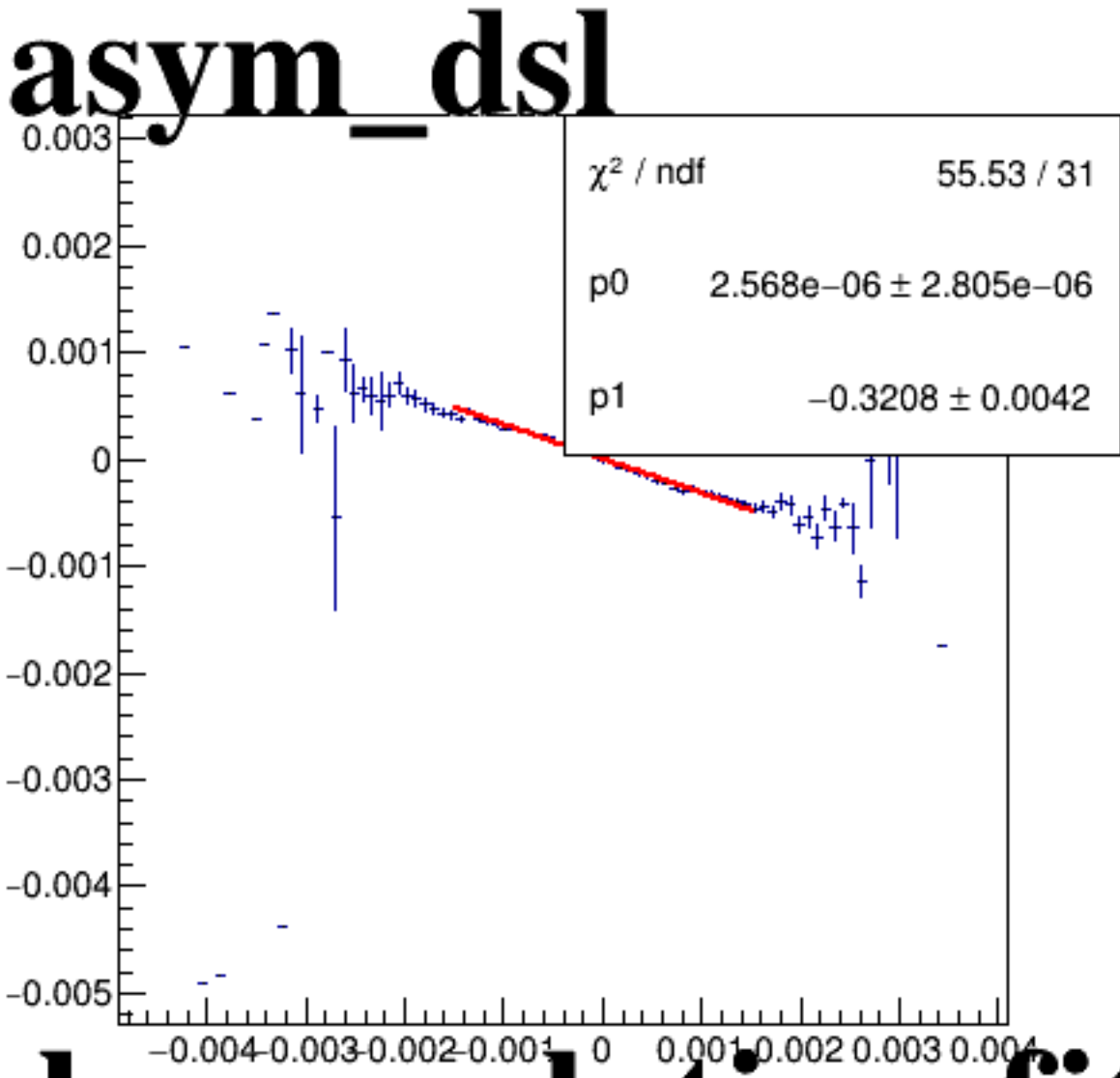
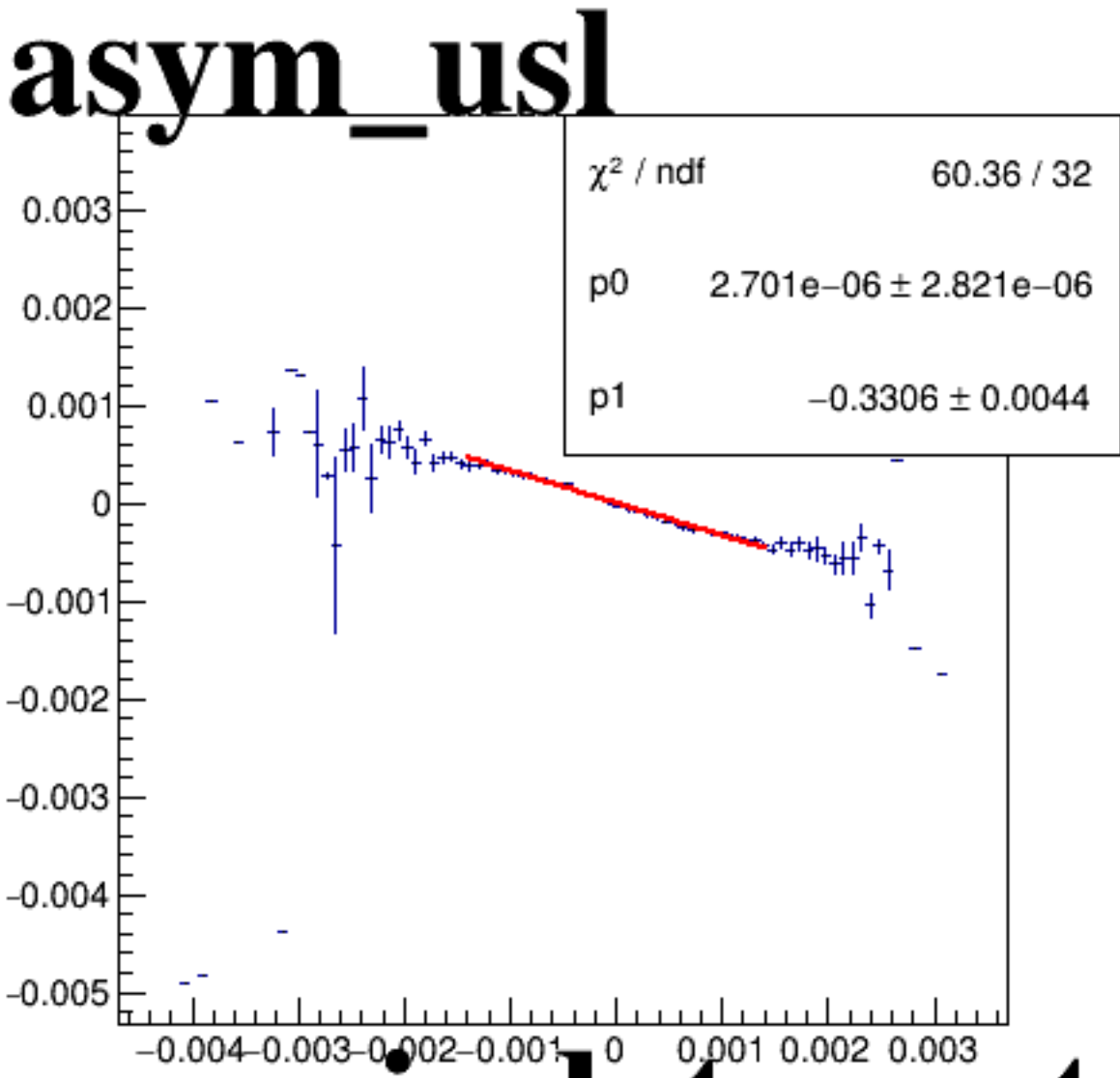
asym_dsl



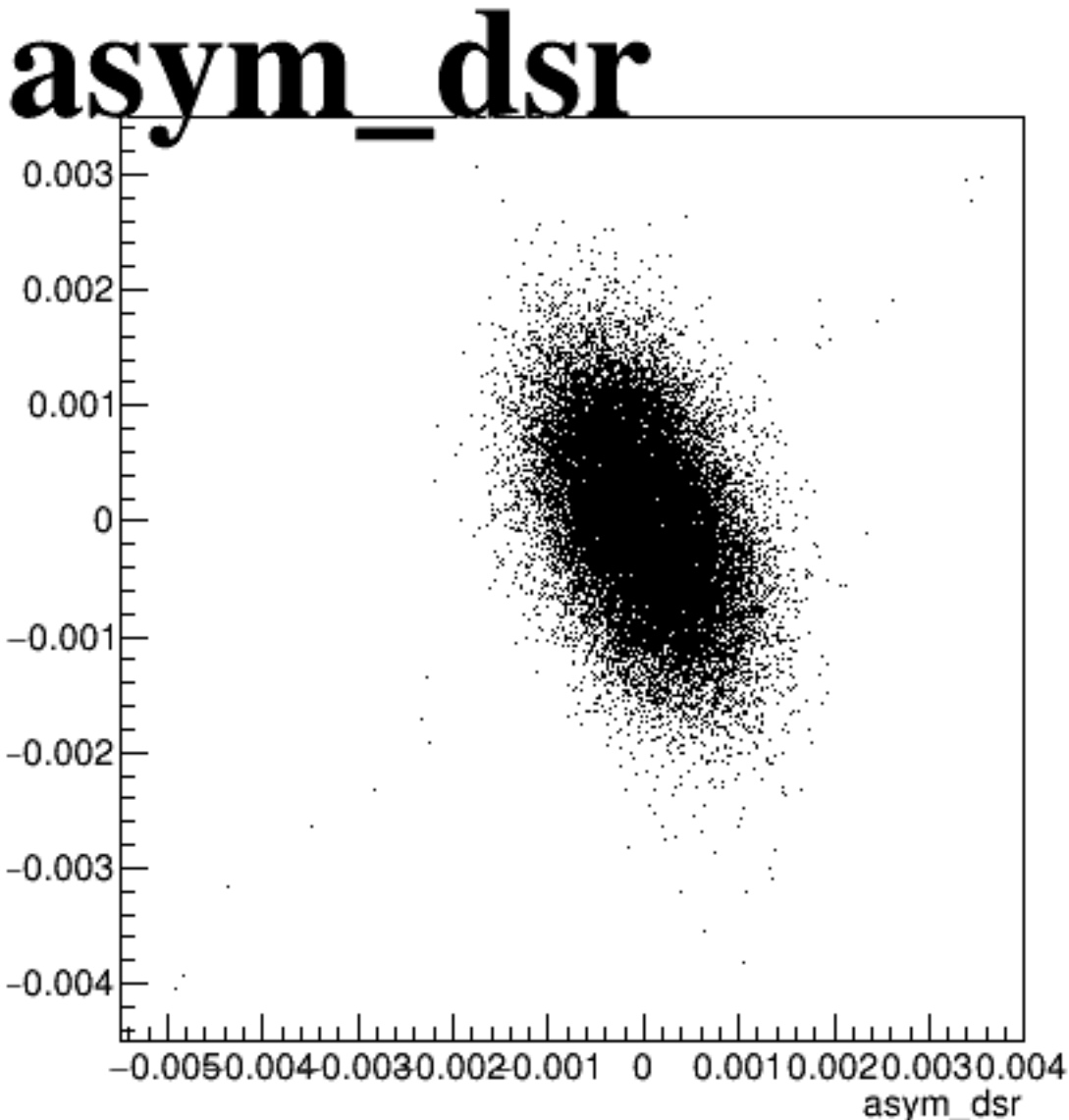
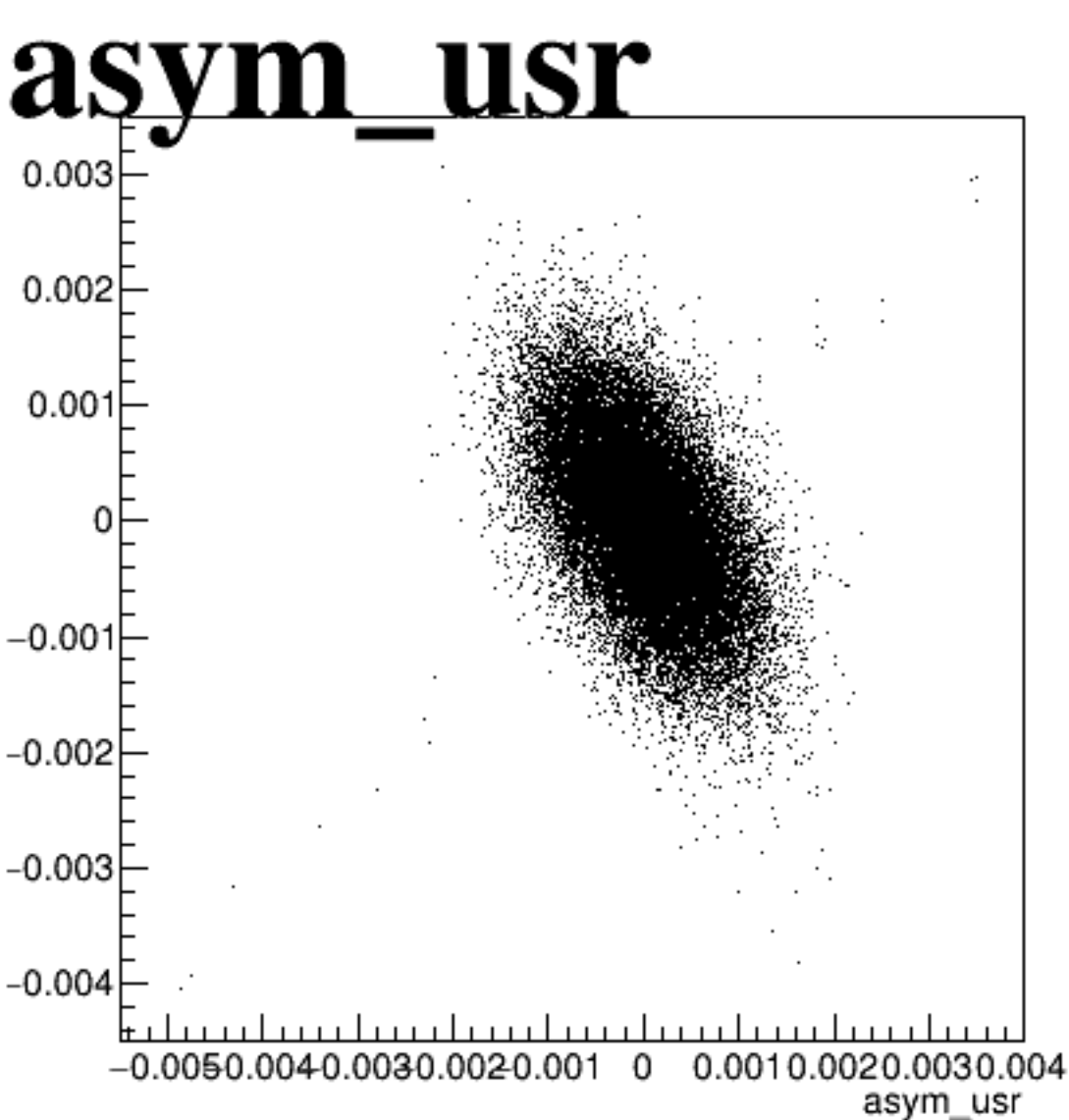
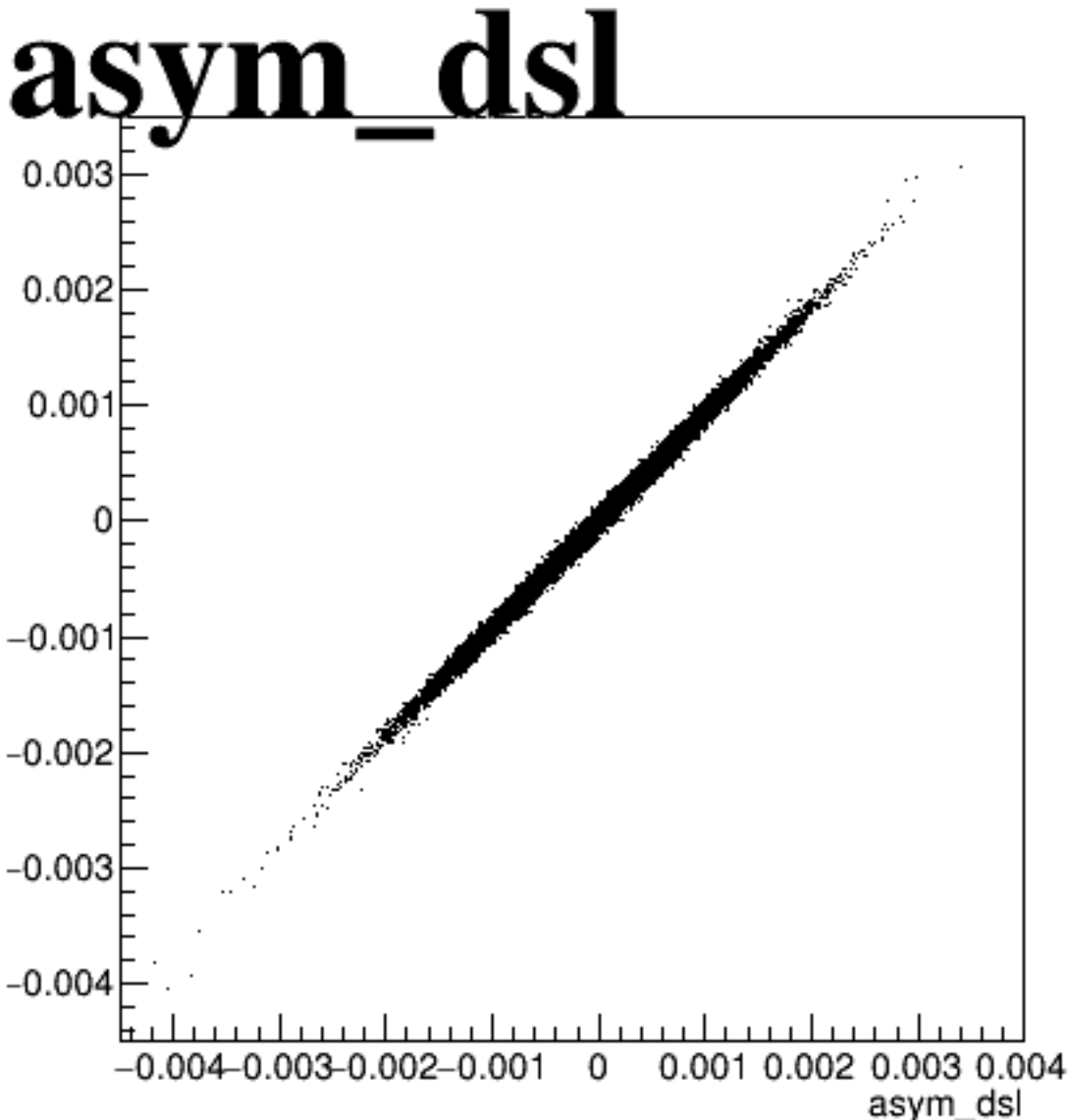
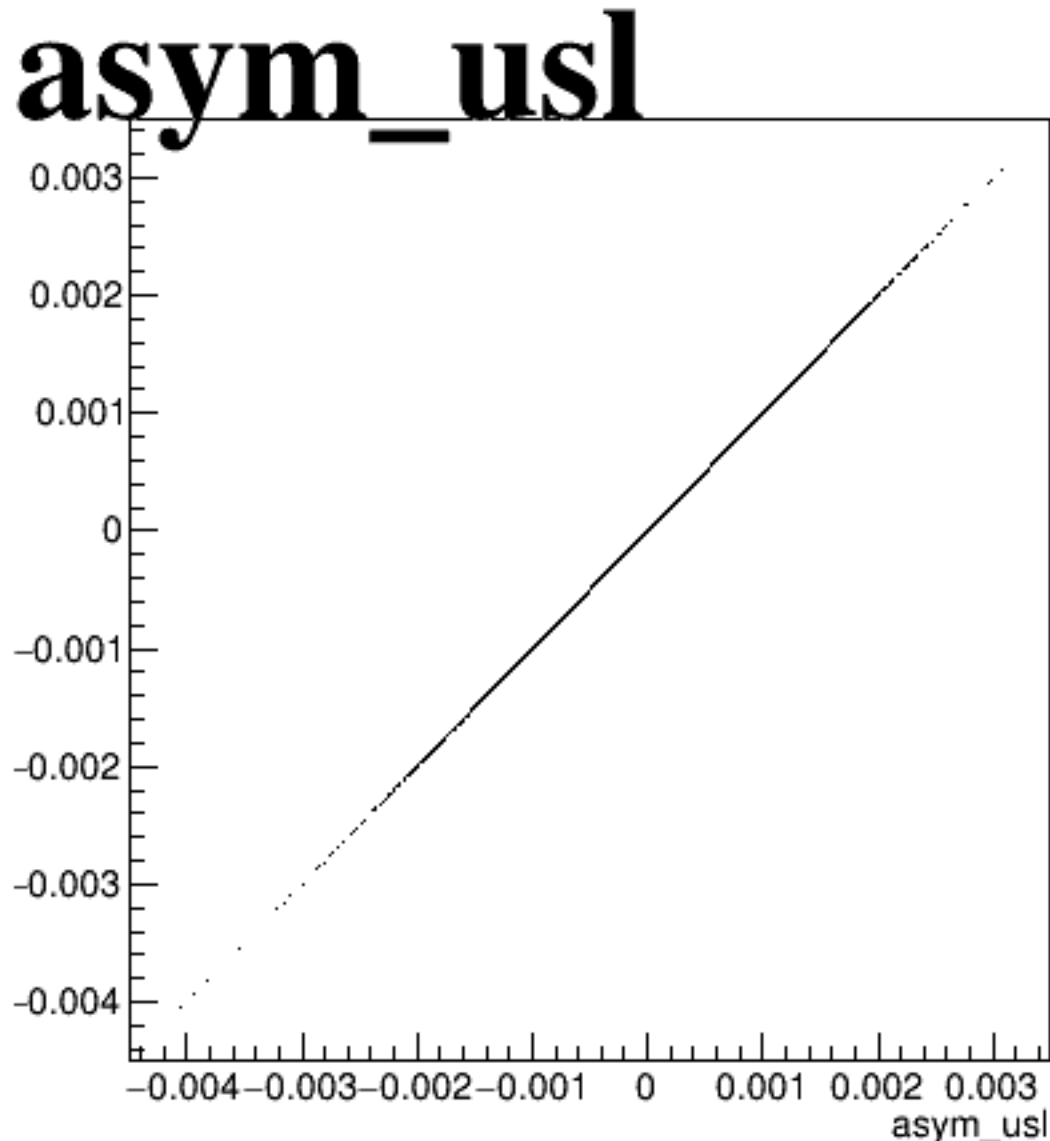
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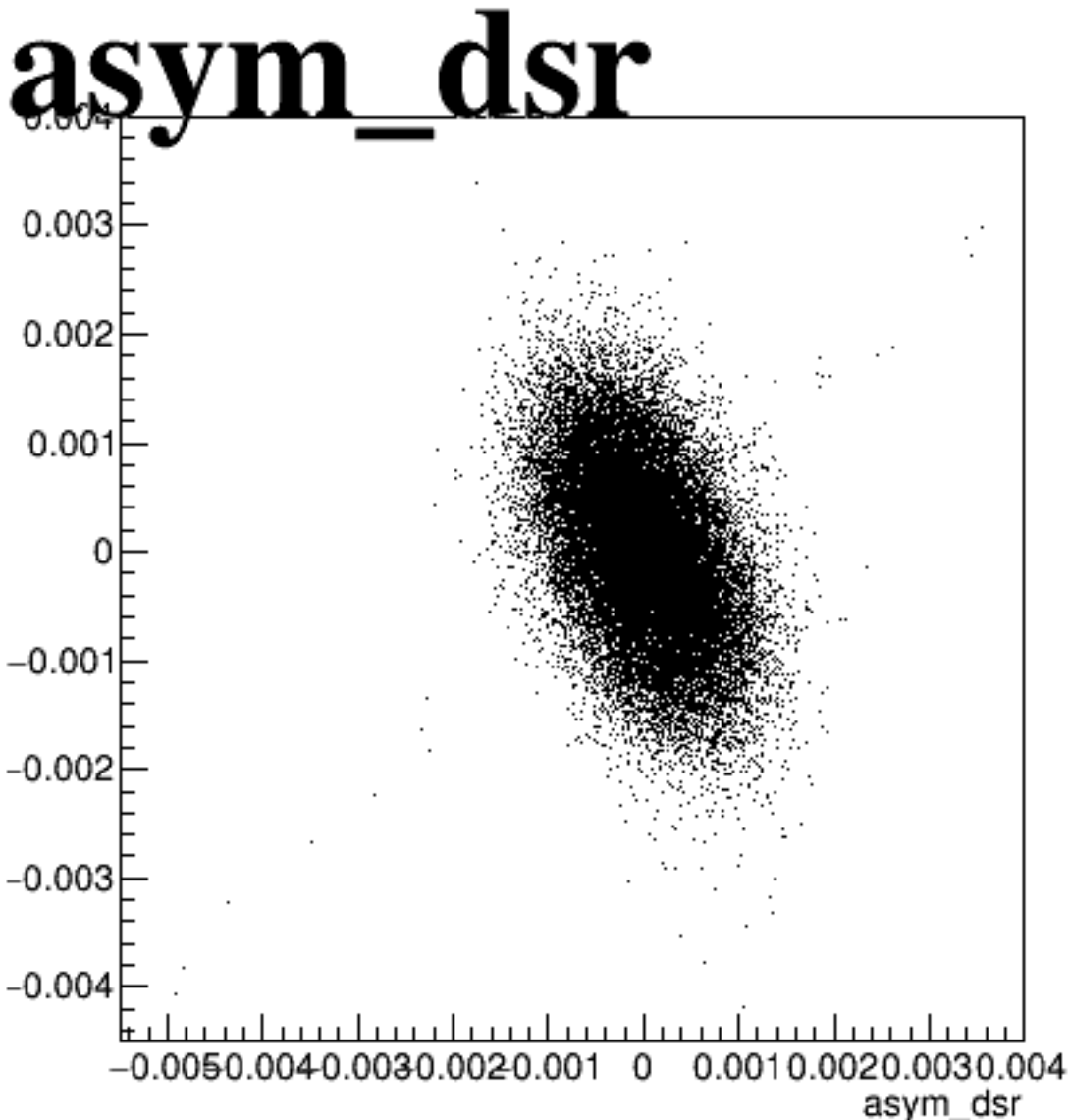
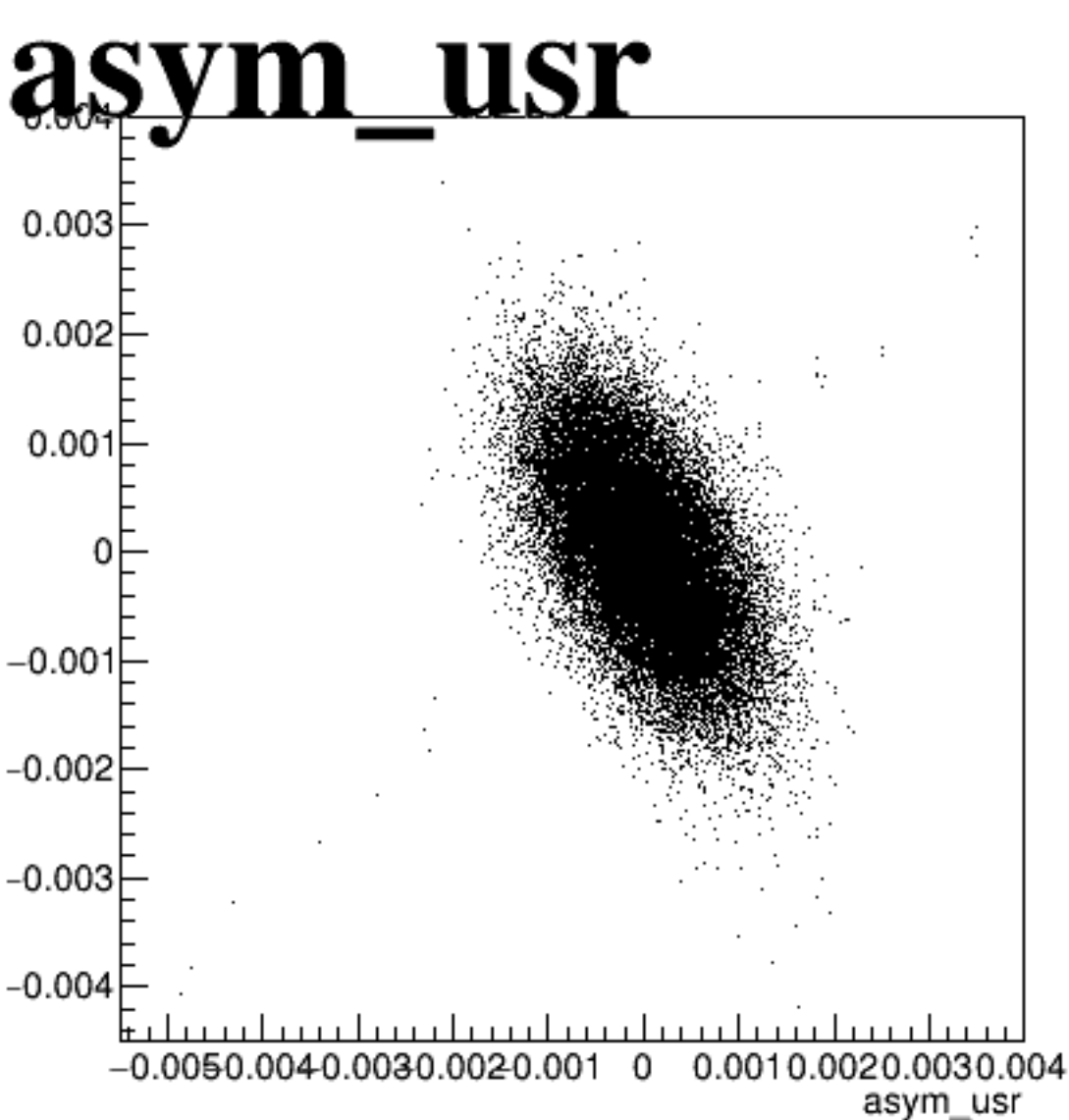
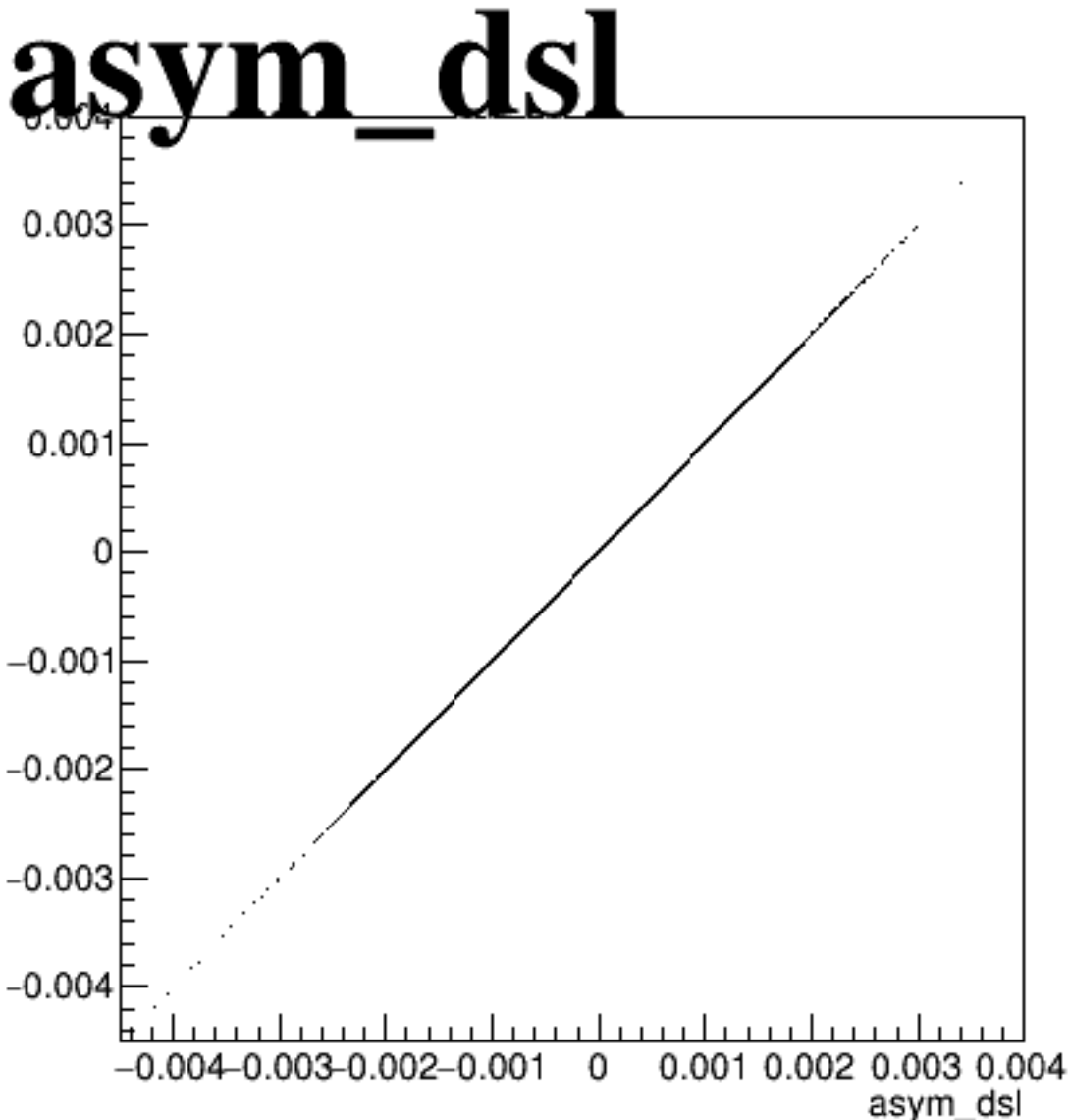
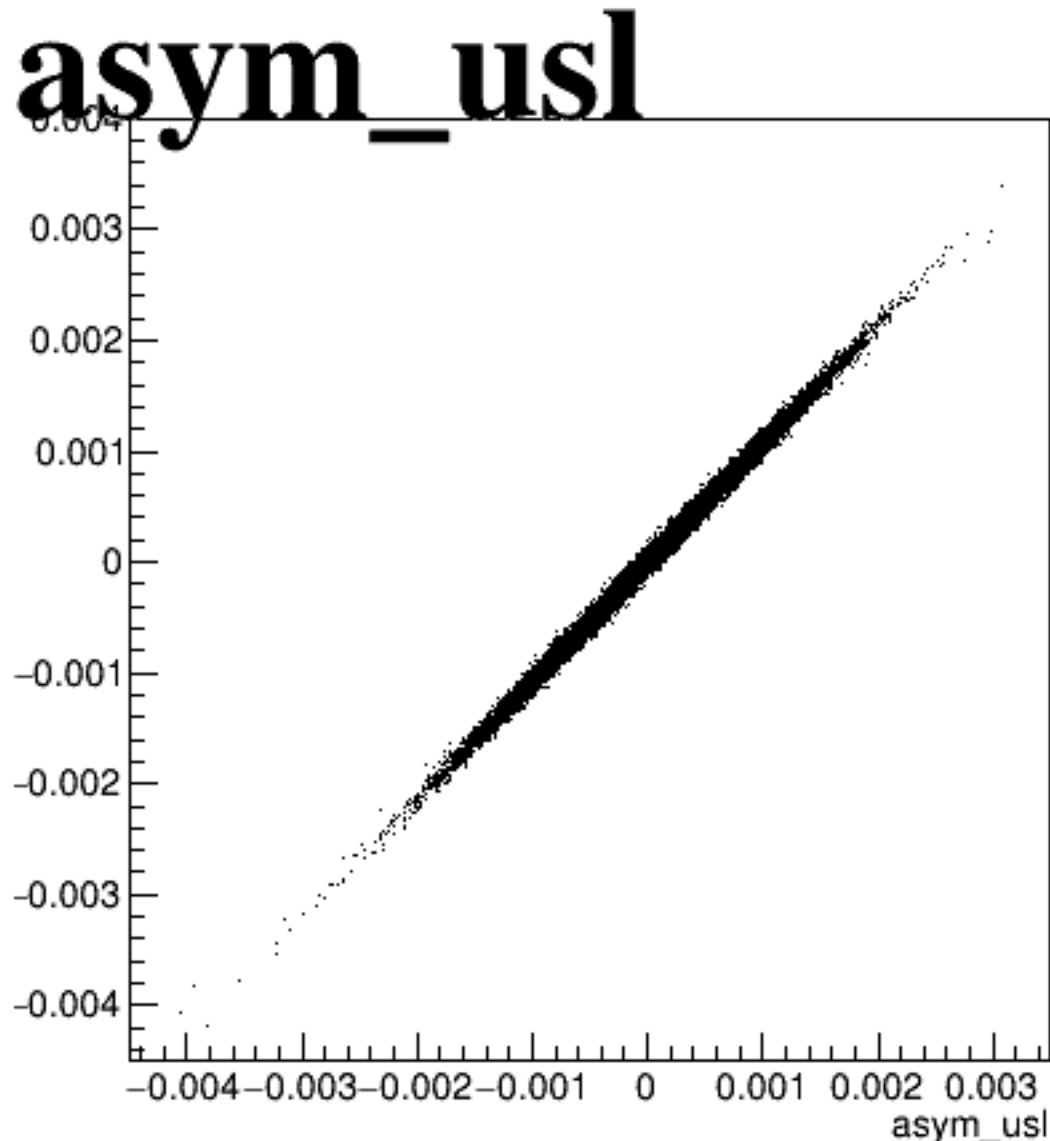
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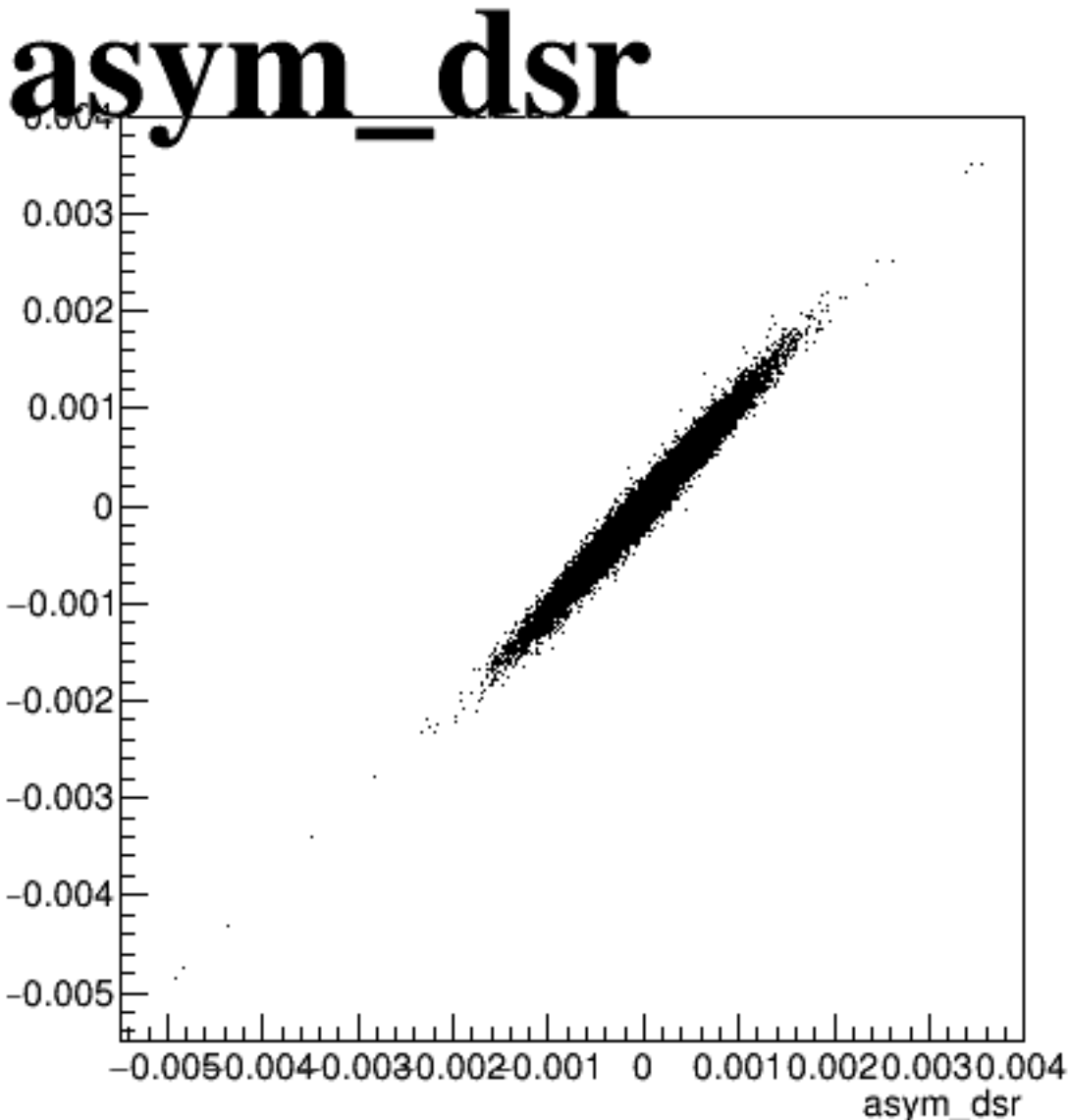
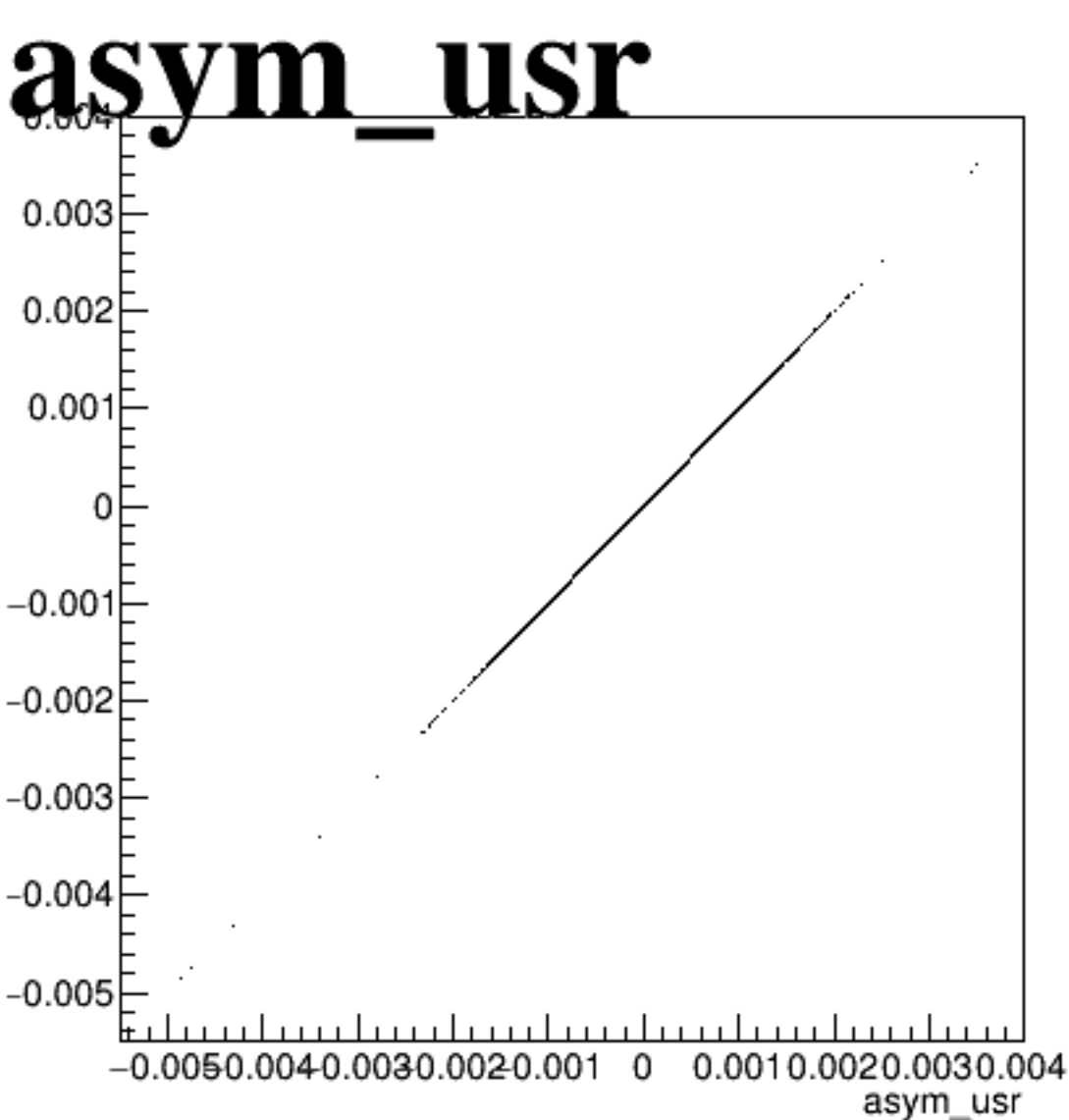
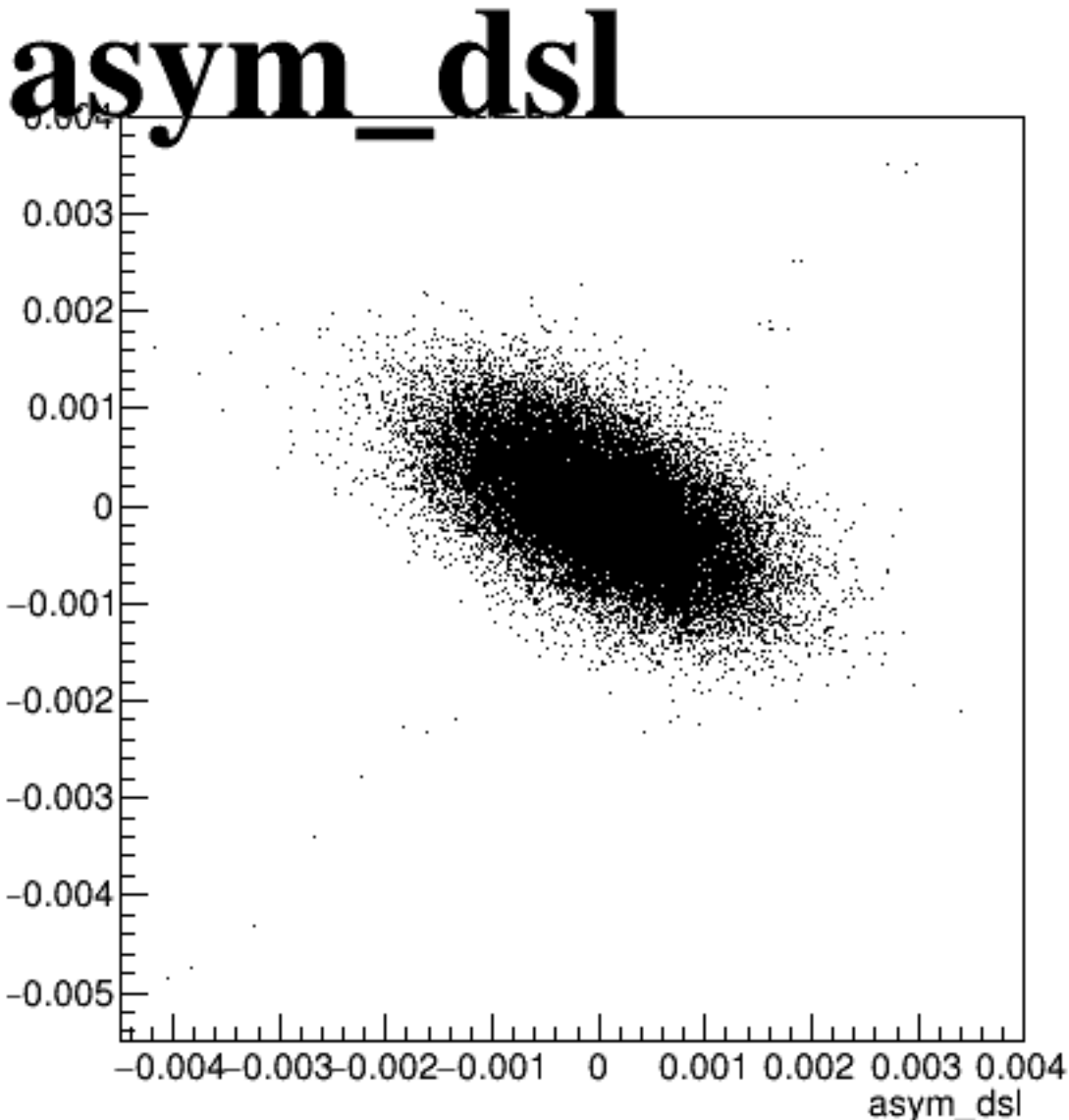
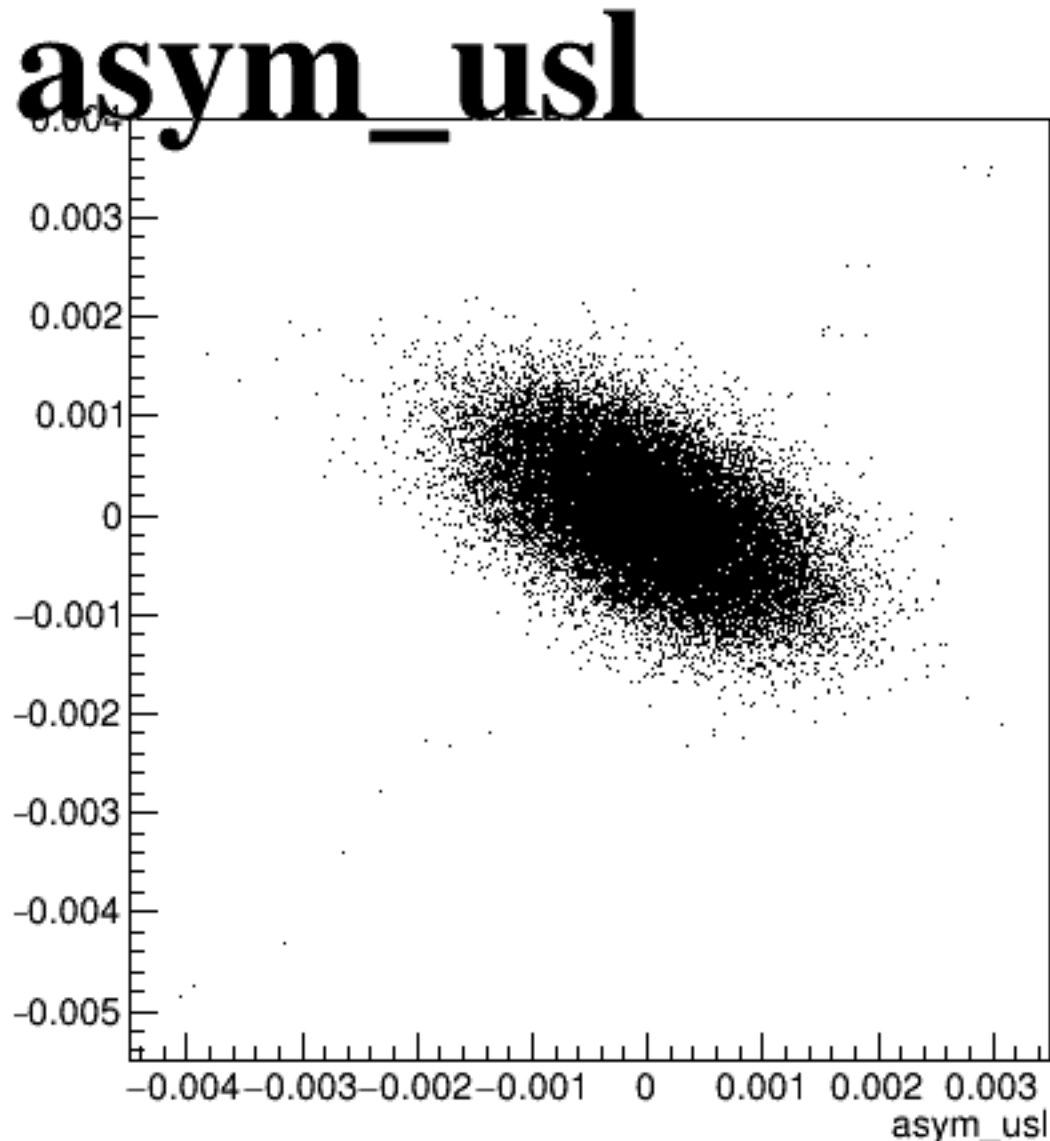
asym_usl



asym_dsl



asym_usr



asym_dsr

