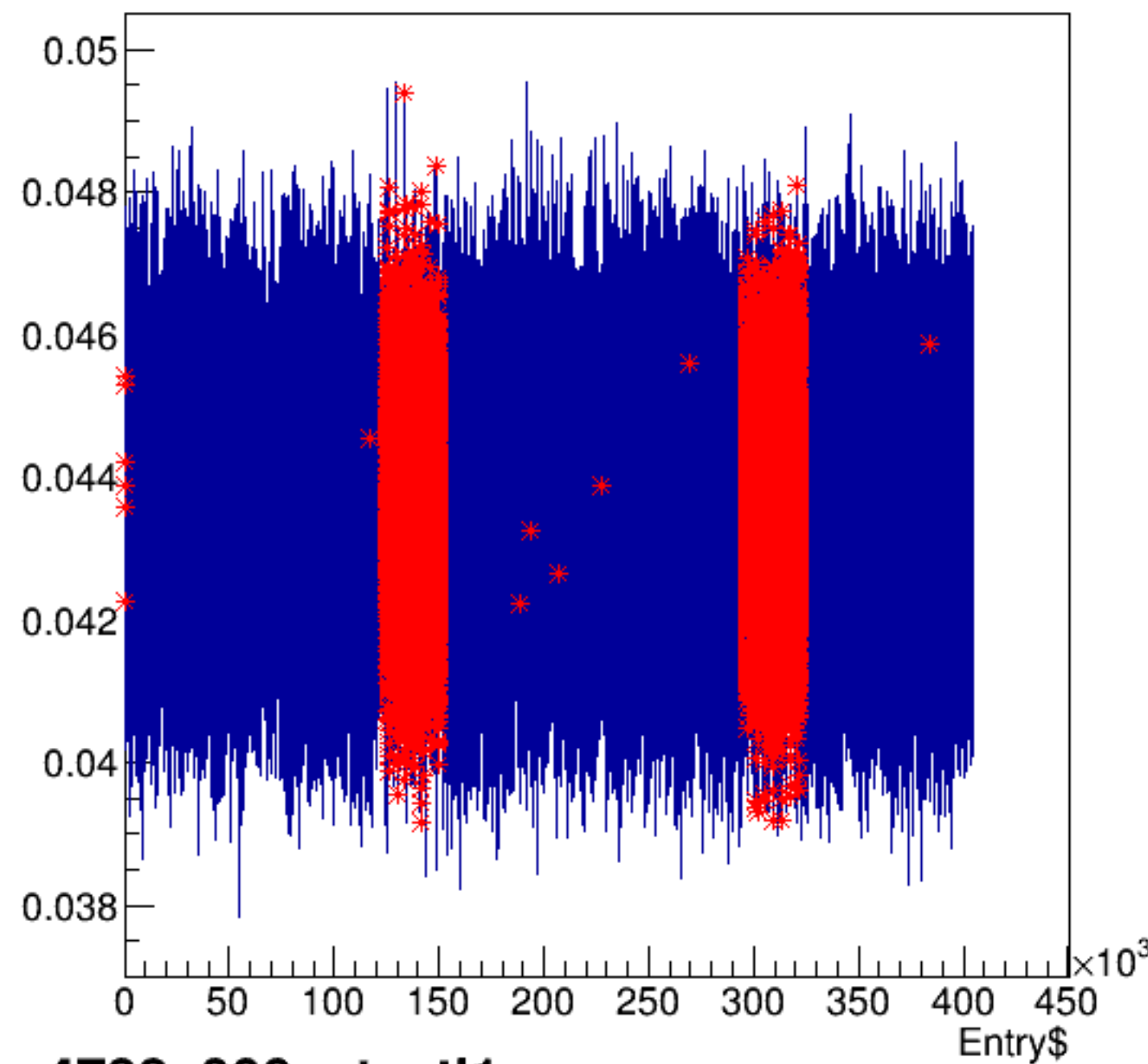
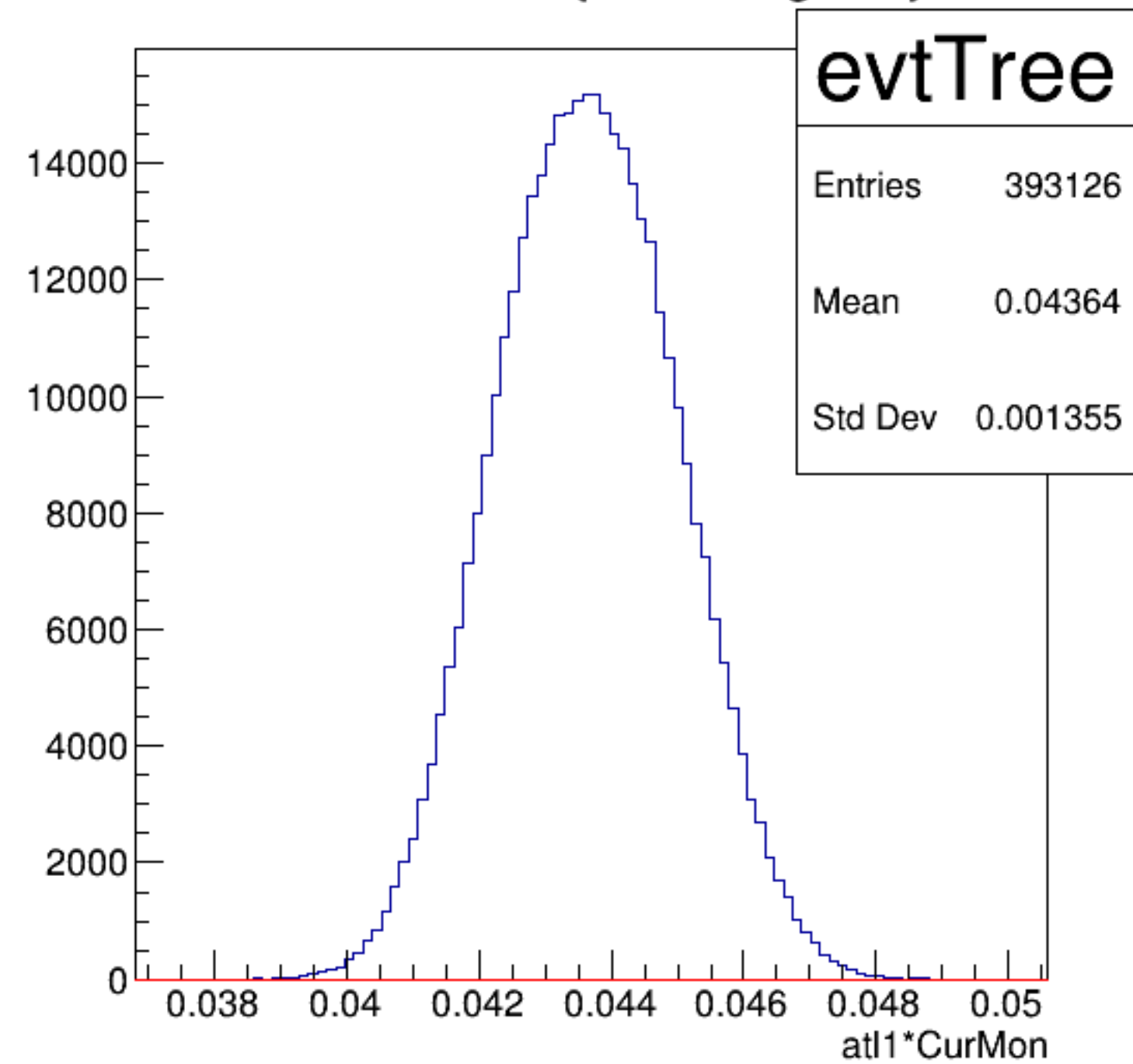


at1\*CurMon:Entry\$

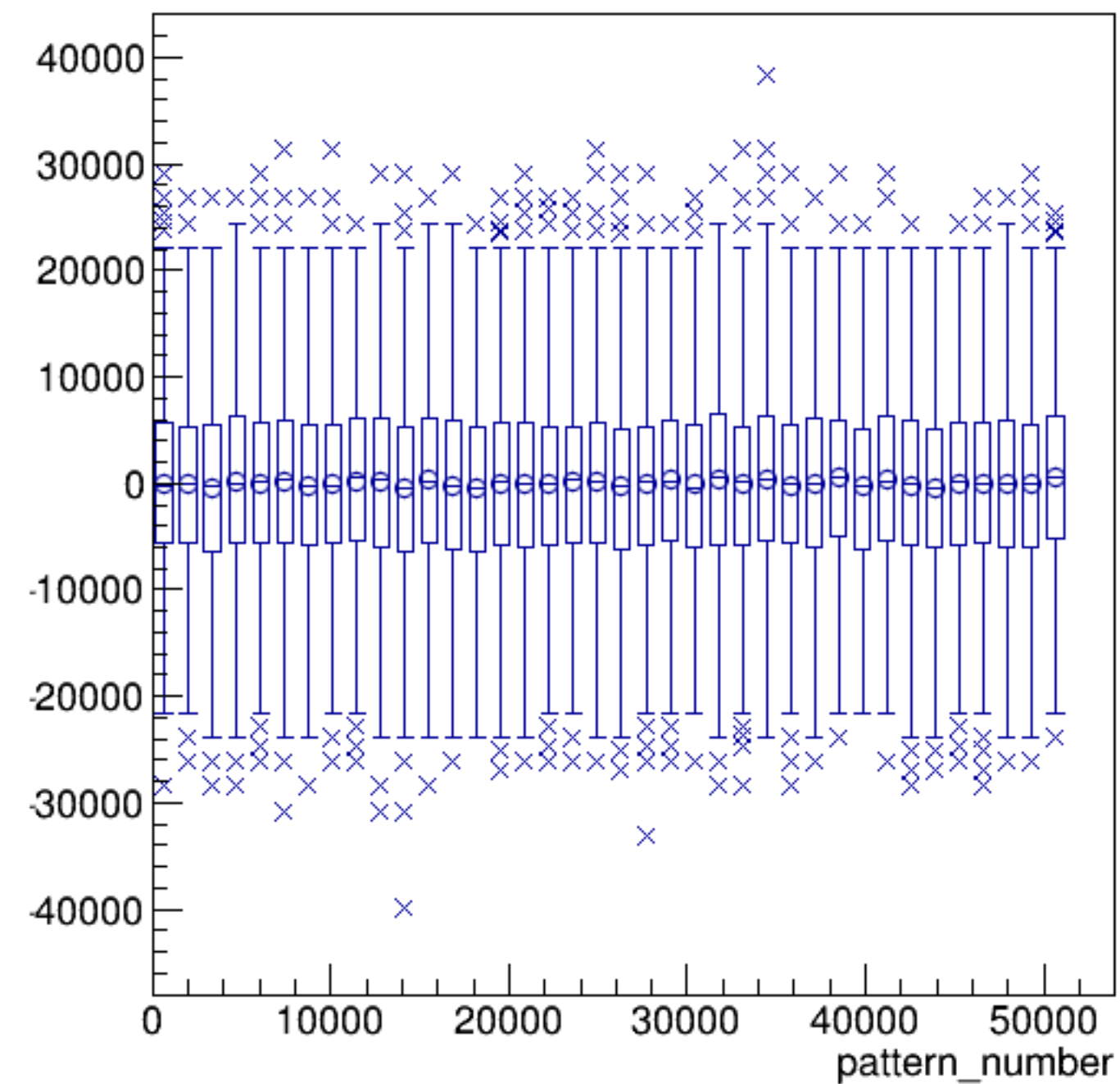


run4732\_000\_at\_atl1.png

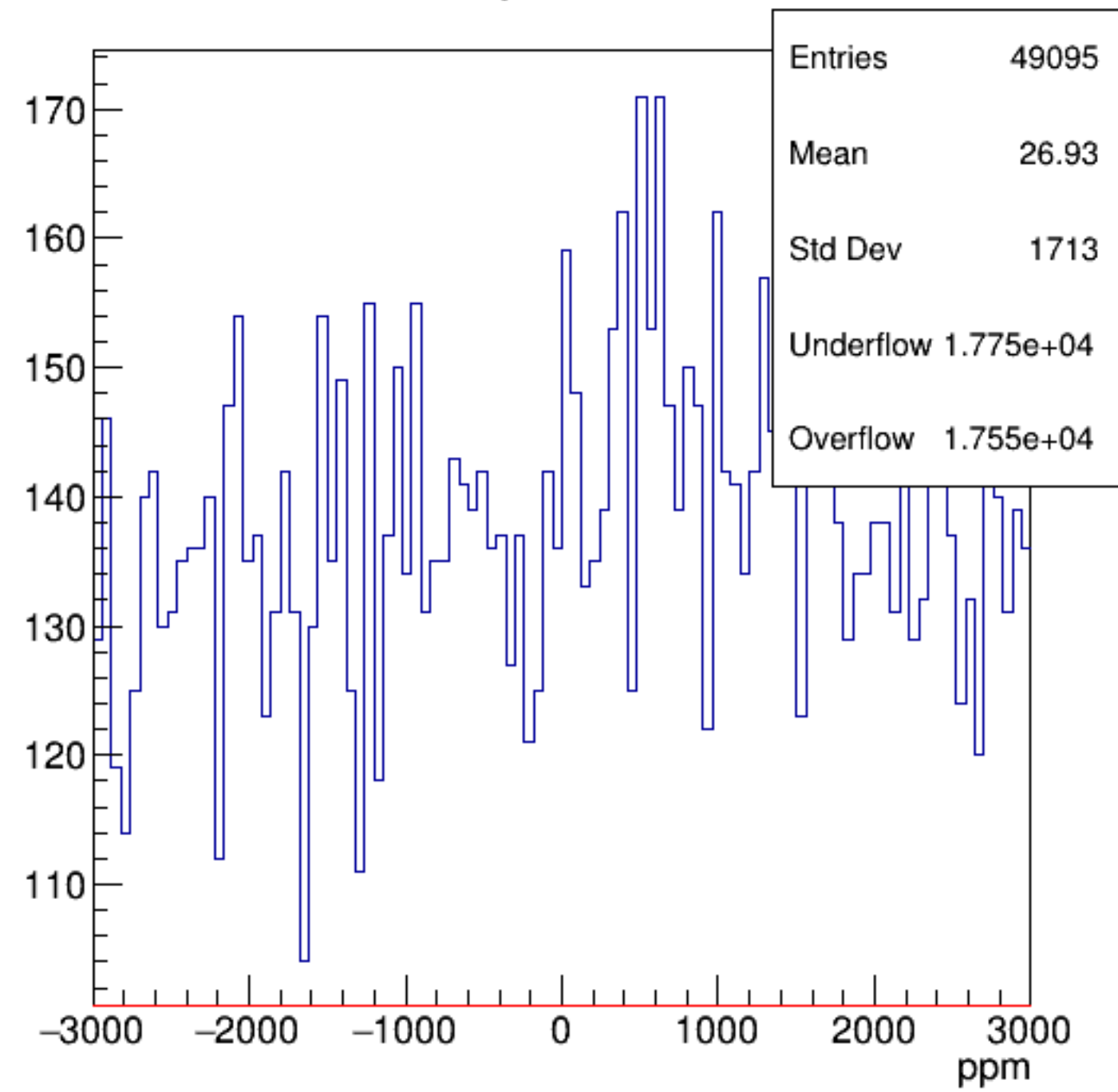
at1\*CurMon {ErrorFlag==0}



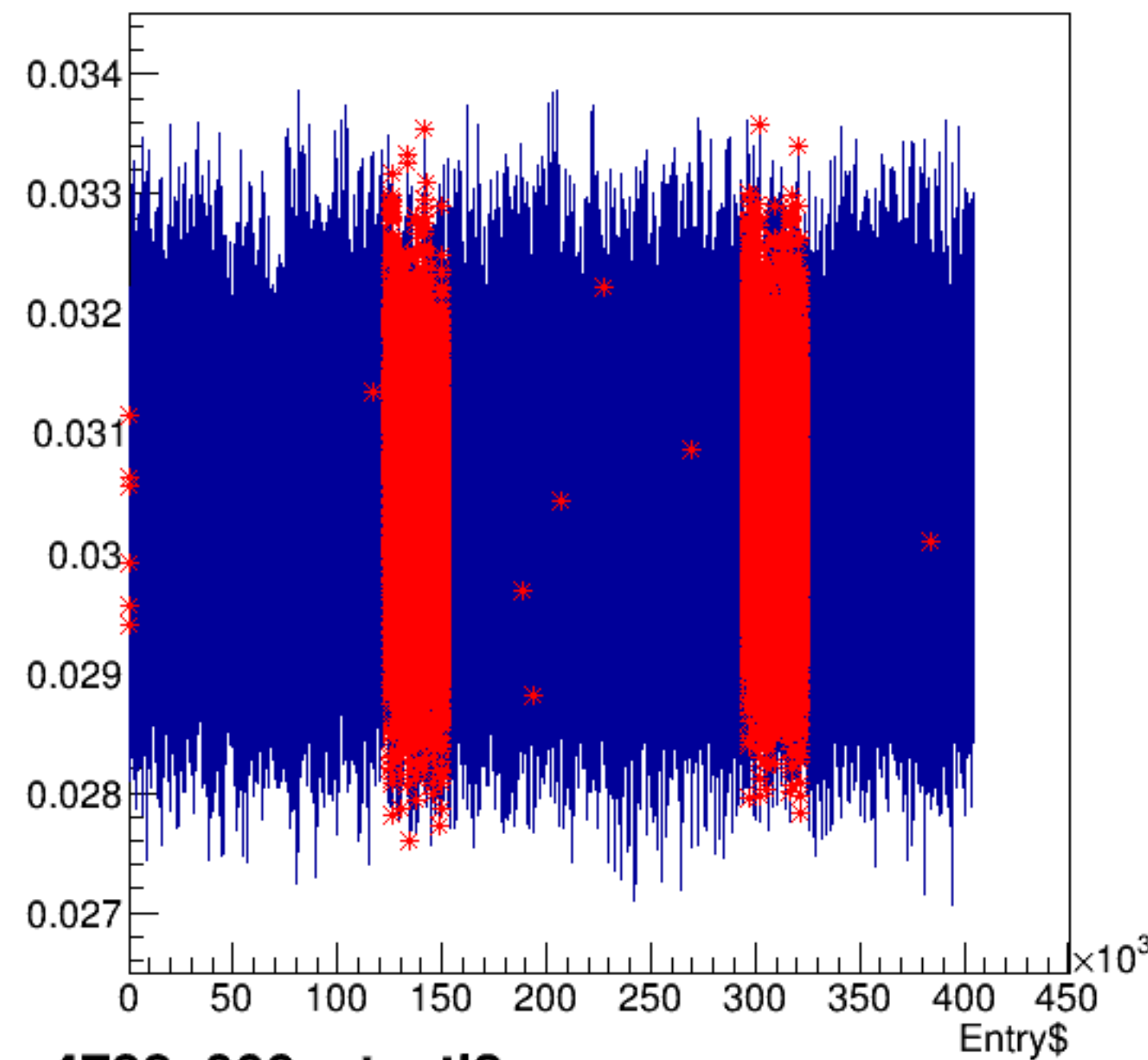
asym\_atl1/ppm:pattern\_number {ErrorFlag==0}



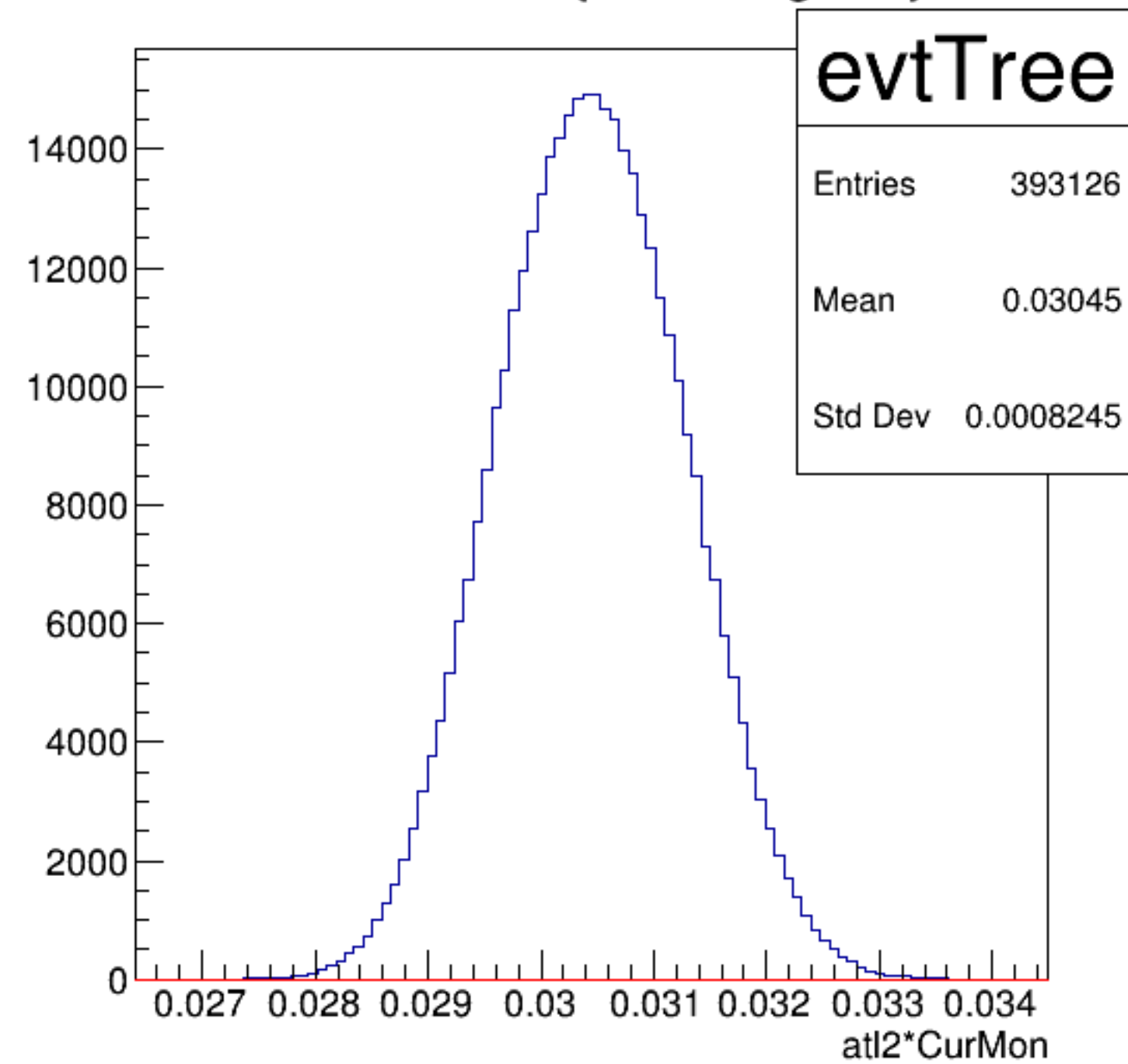
asym\_atl1



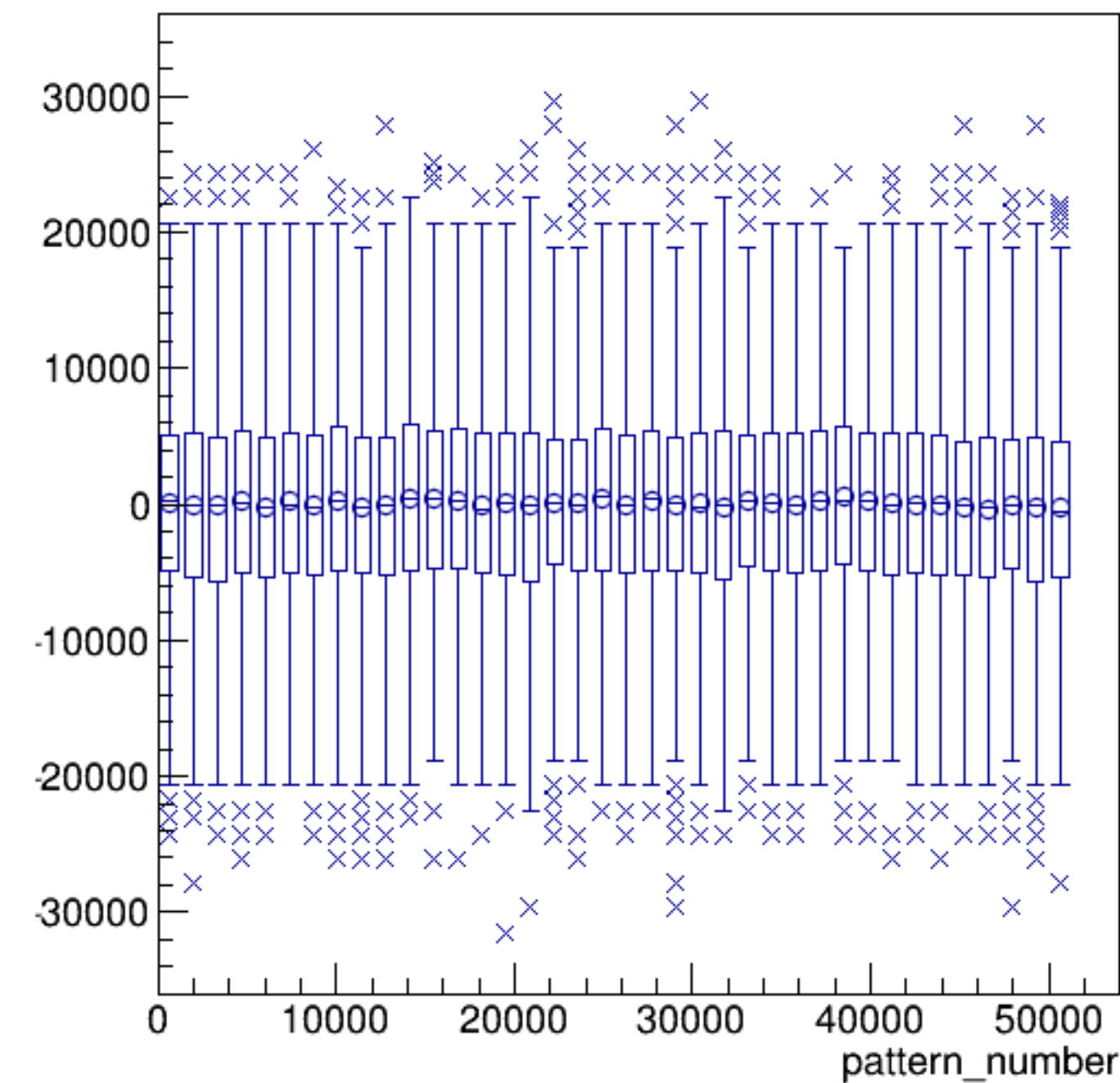
atl2\*CurMon:Entry\$



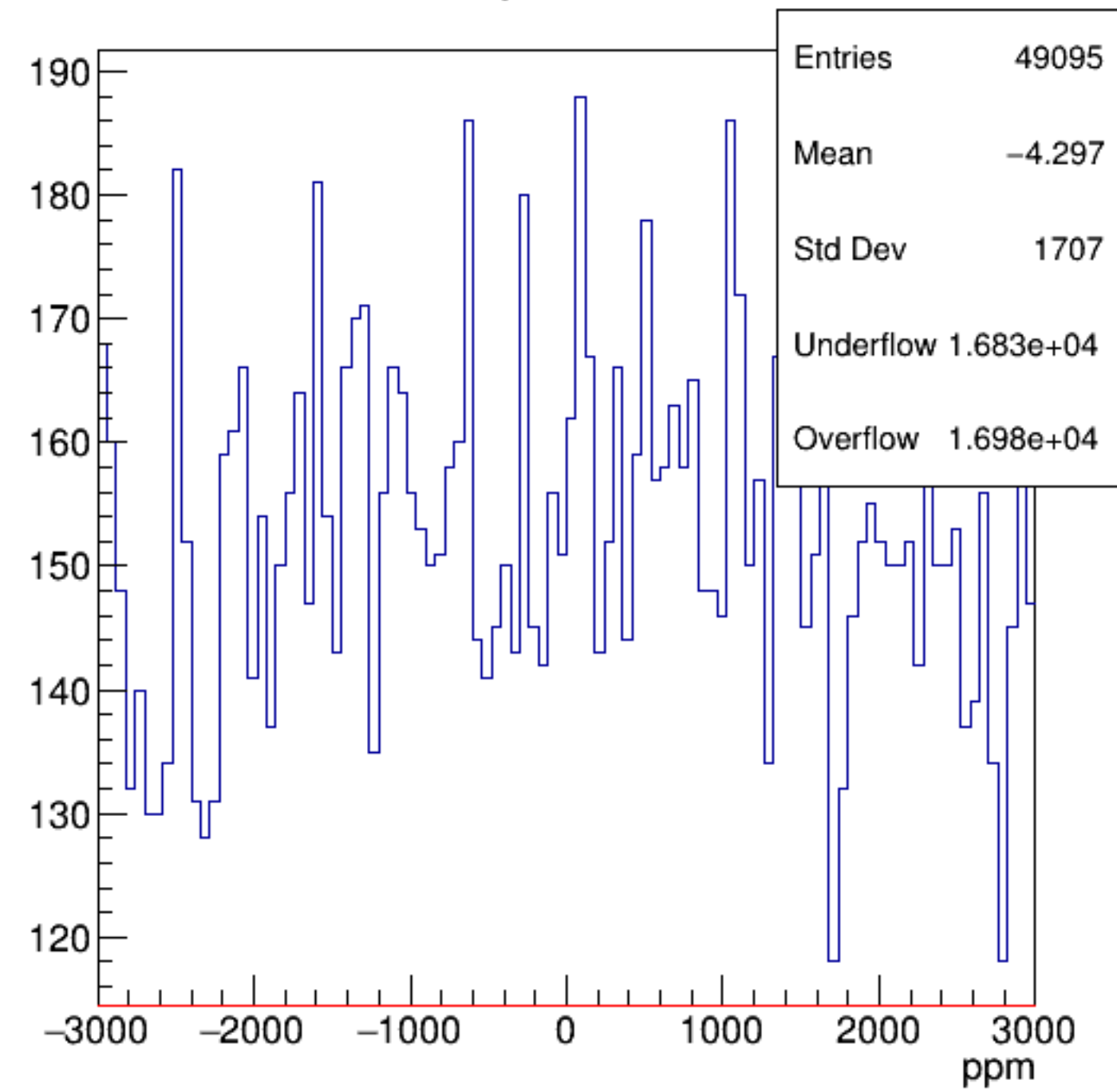
atl2\*CurMon {ErrorFlag==0}



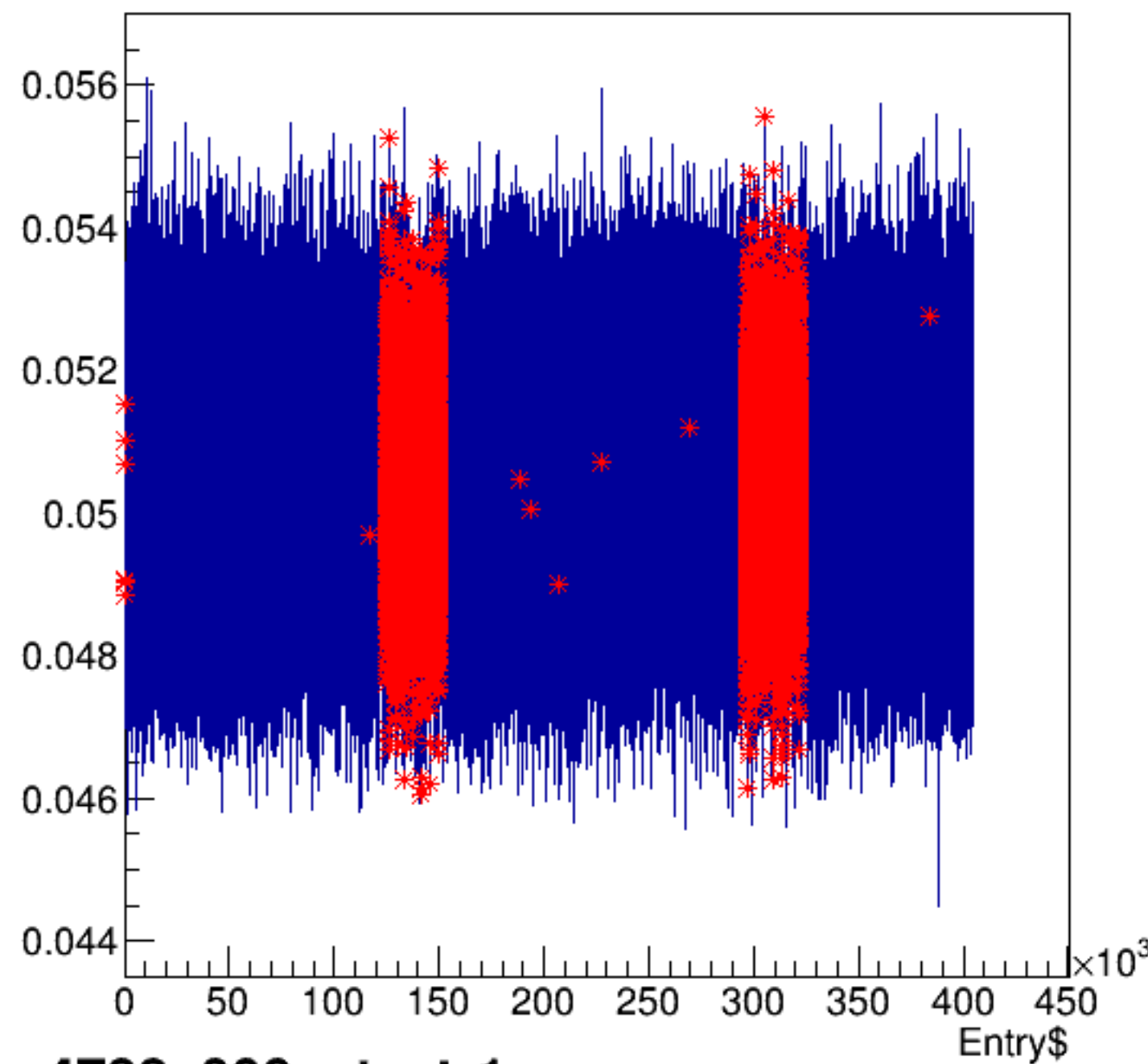
asym\_atl2/ppm:pattern\_number {ErrorFlag==0}



asym\_atl2

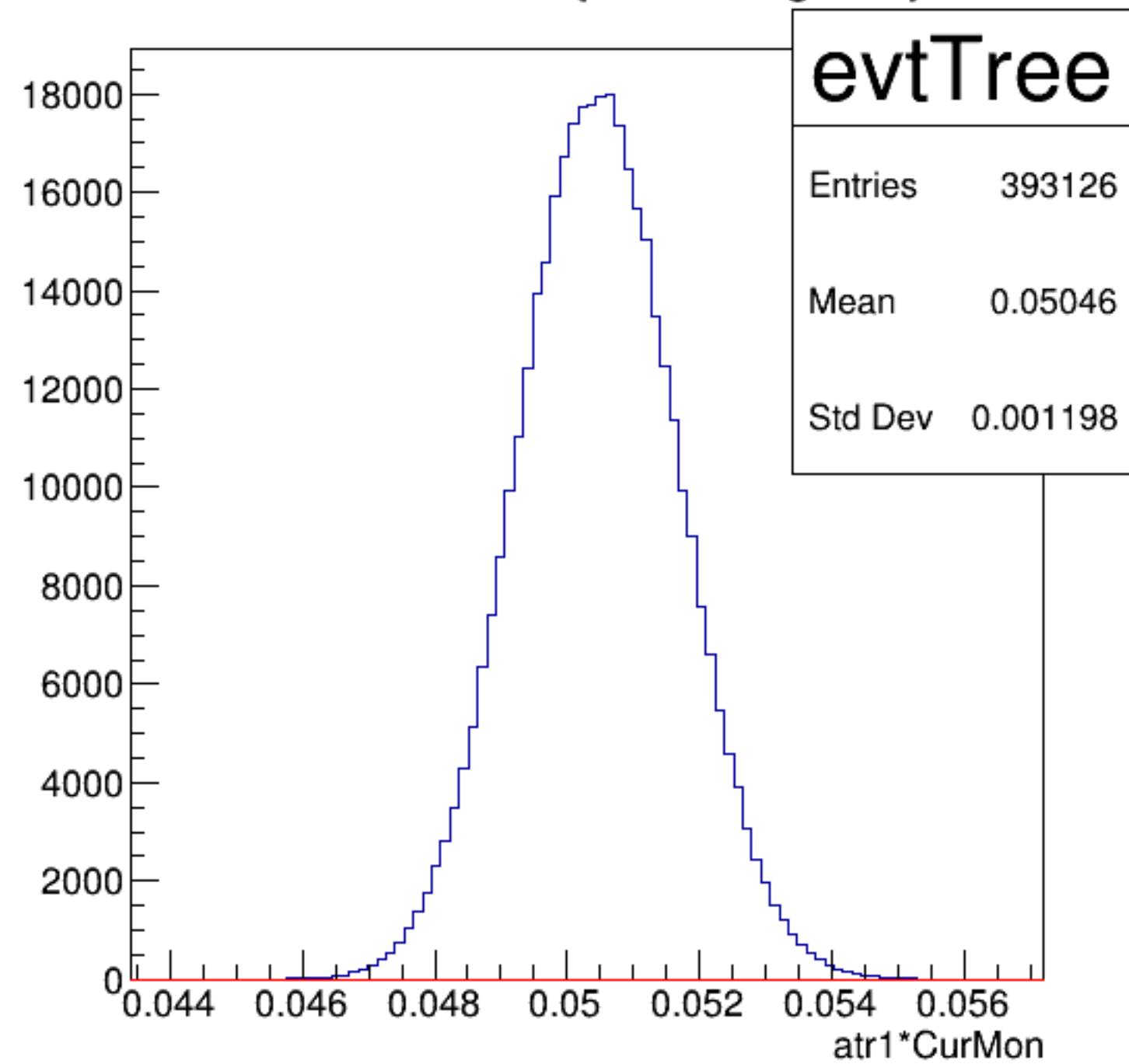


atr1\*CurMon:Entry\$

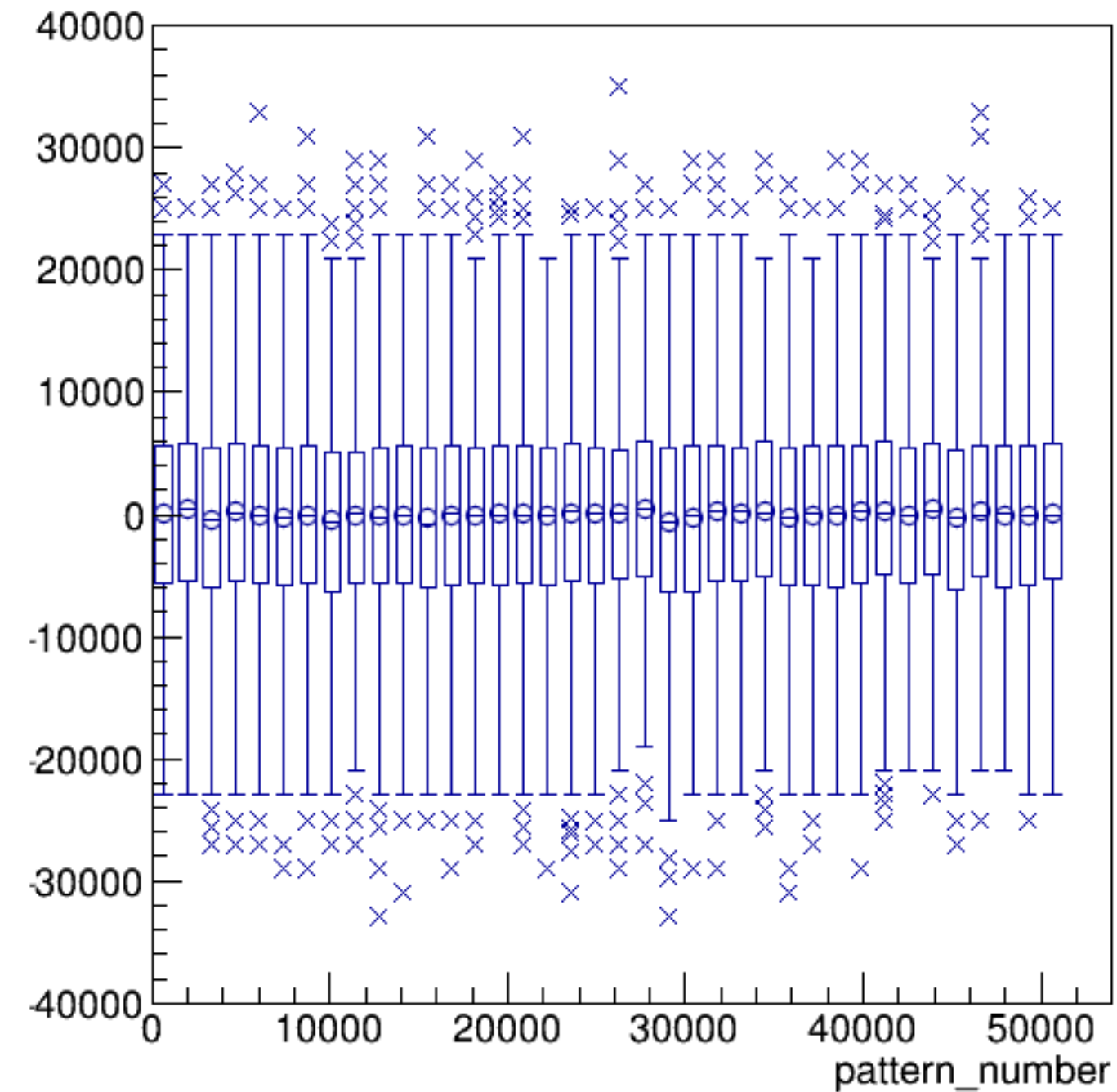


run4732\_000\_at atr1.png

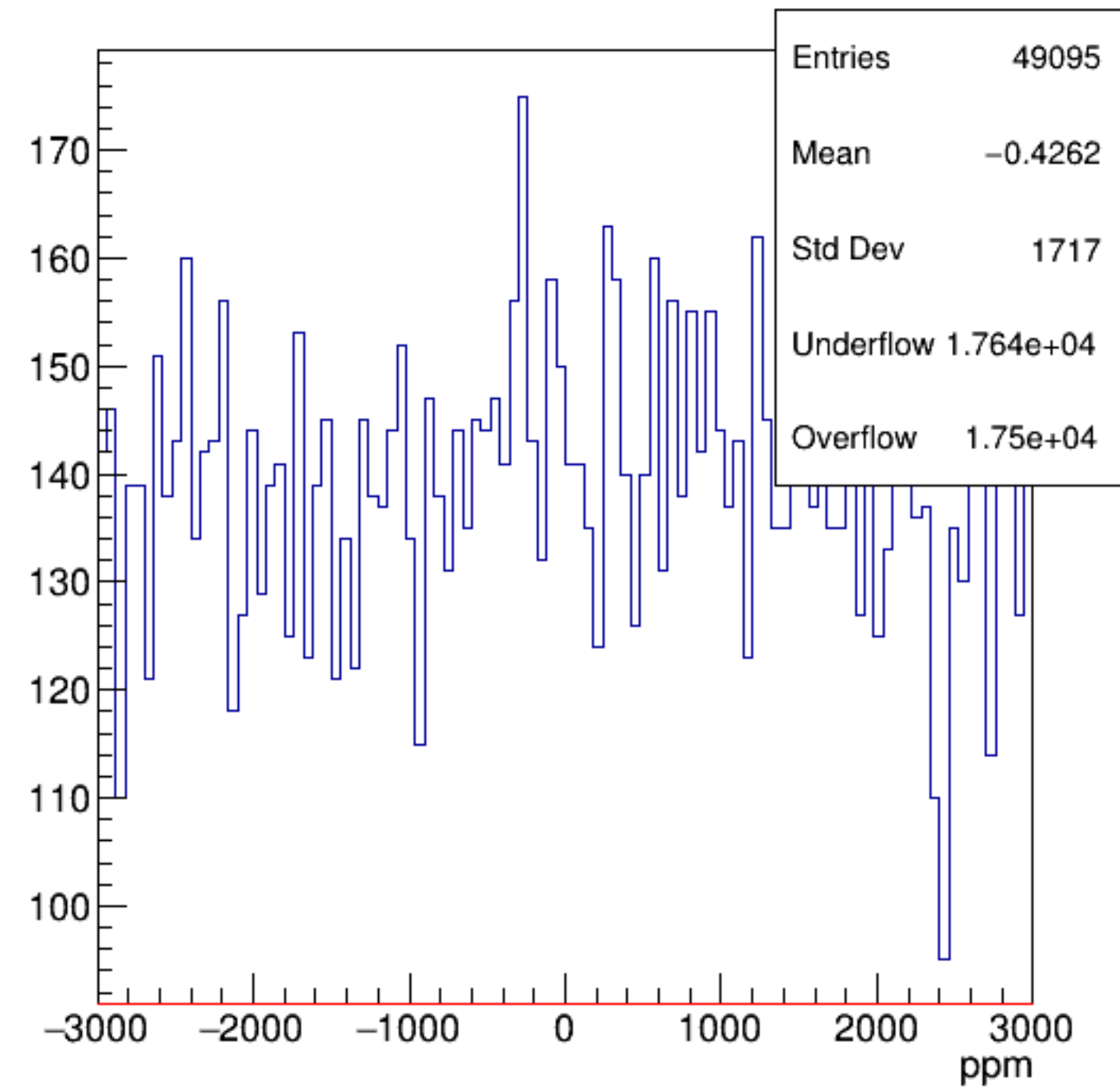
atr1\*CurMon {ErrorFlag==0}



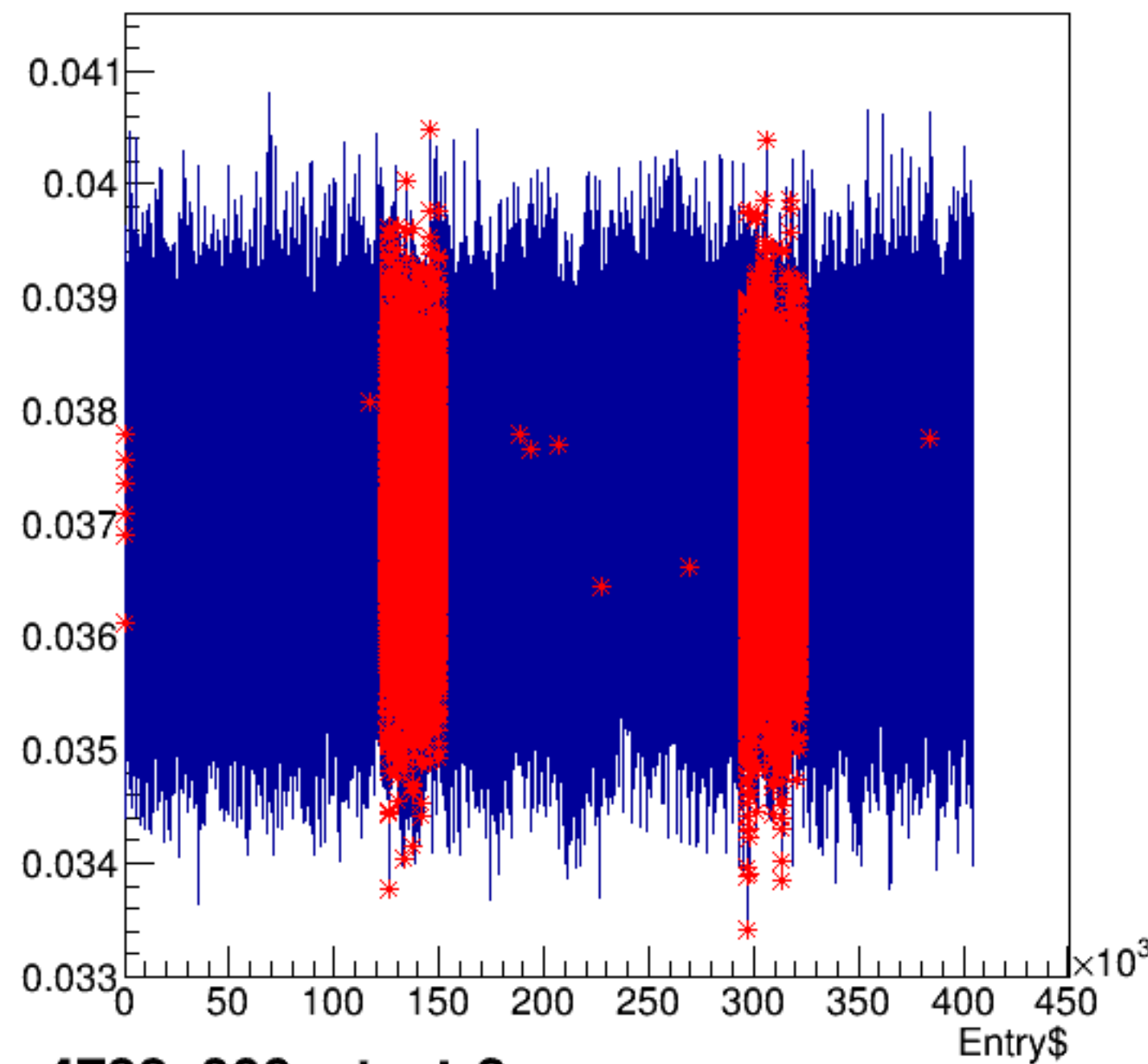
asym\_atr1/ppm:pattern\_number {ErrorFlag==0}



asym\_atr1

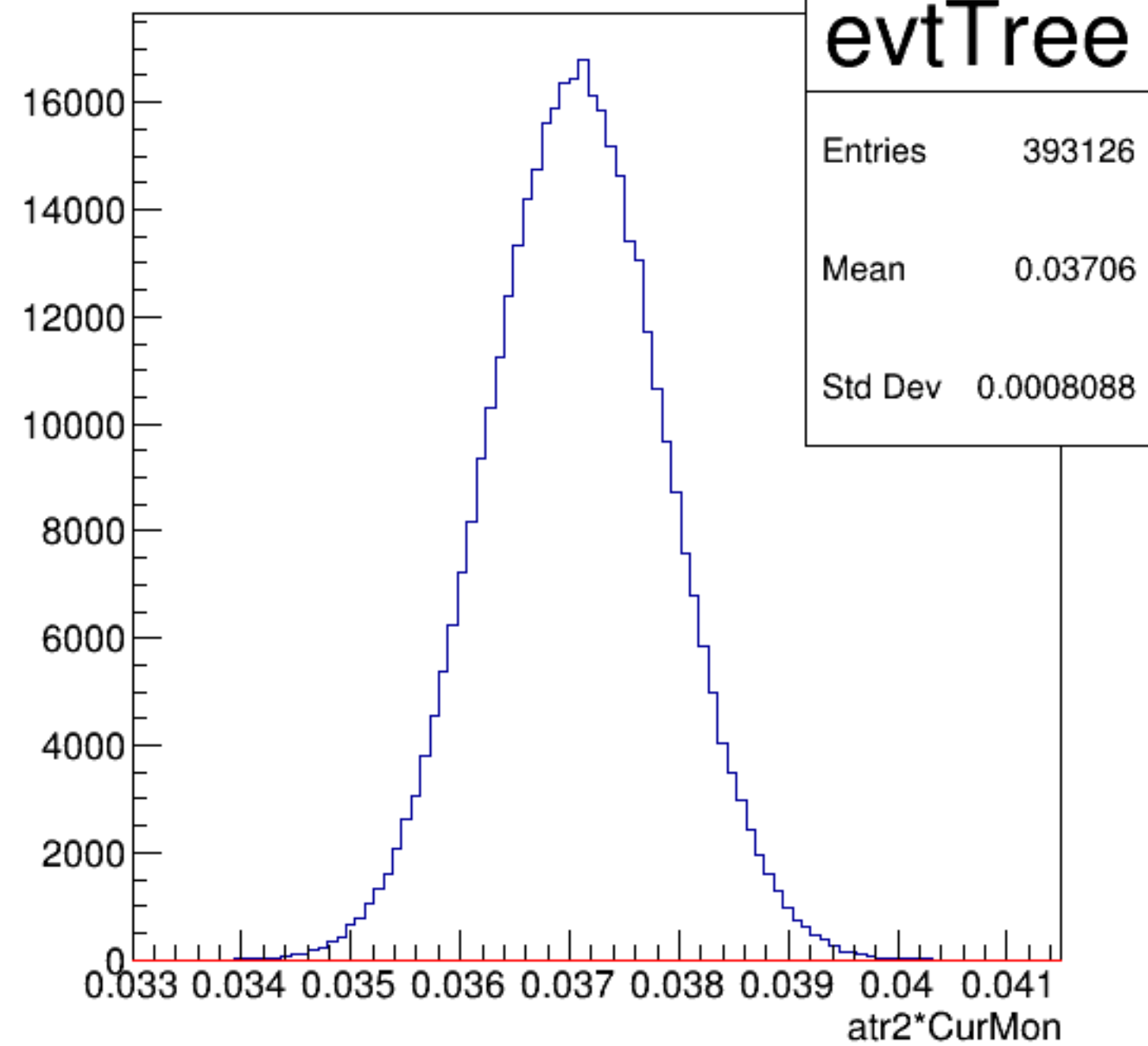


atr2\*CurMon:Entry\$

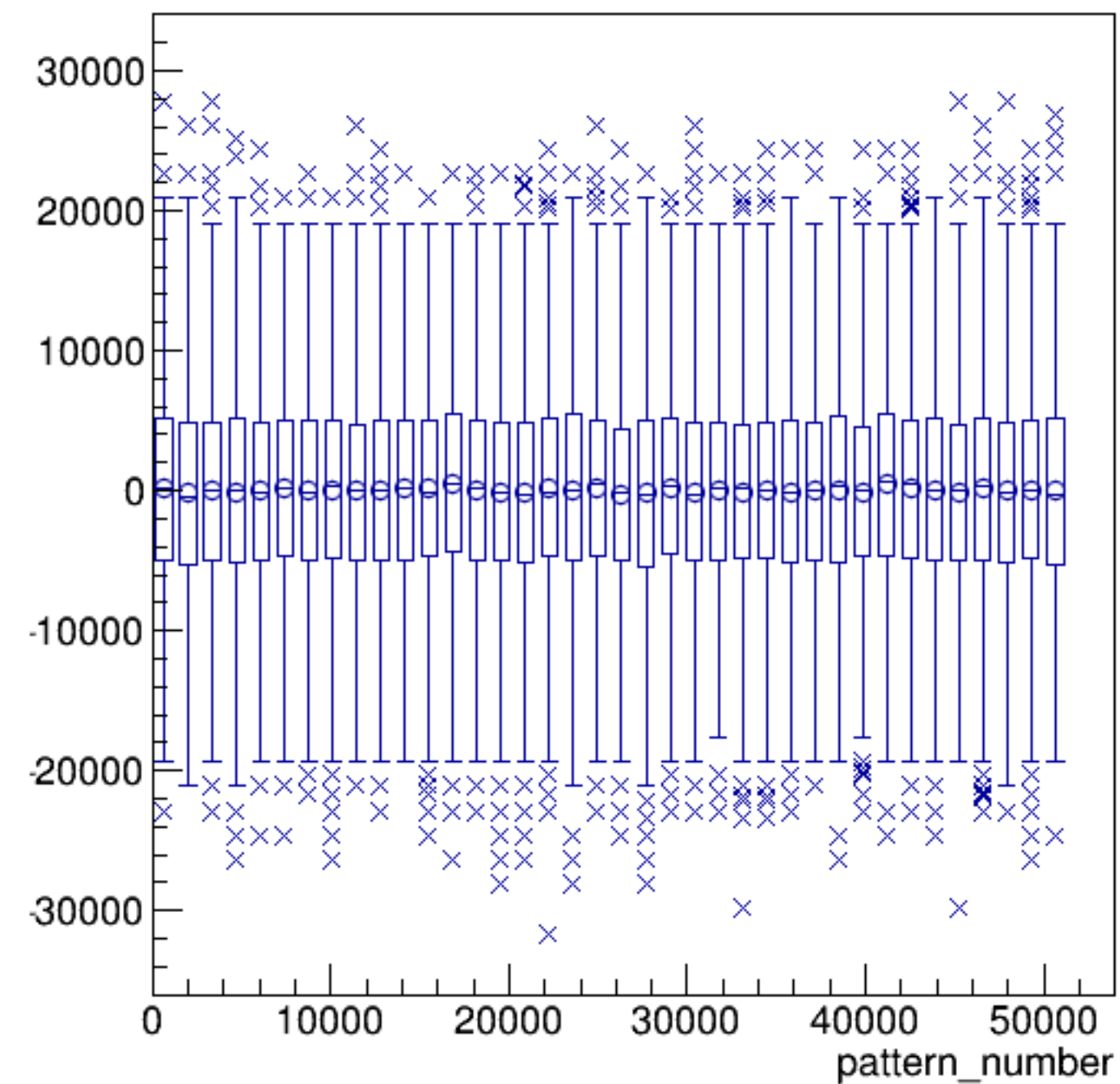


run4732\_000\_at atr2.png

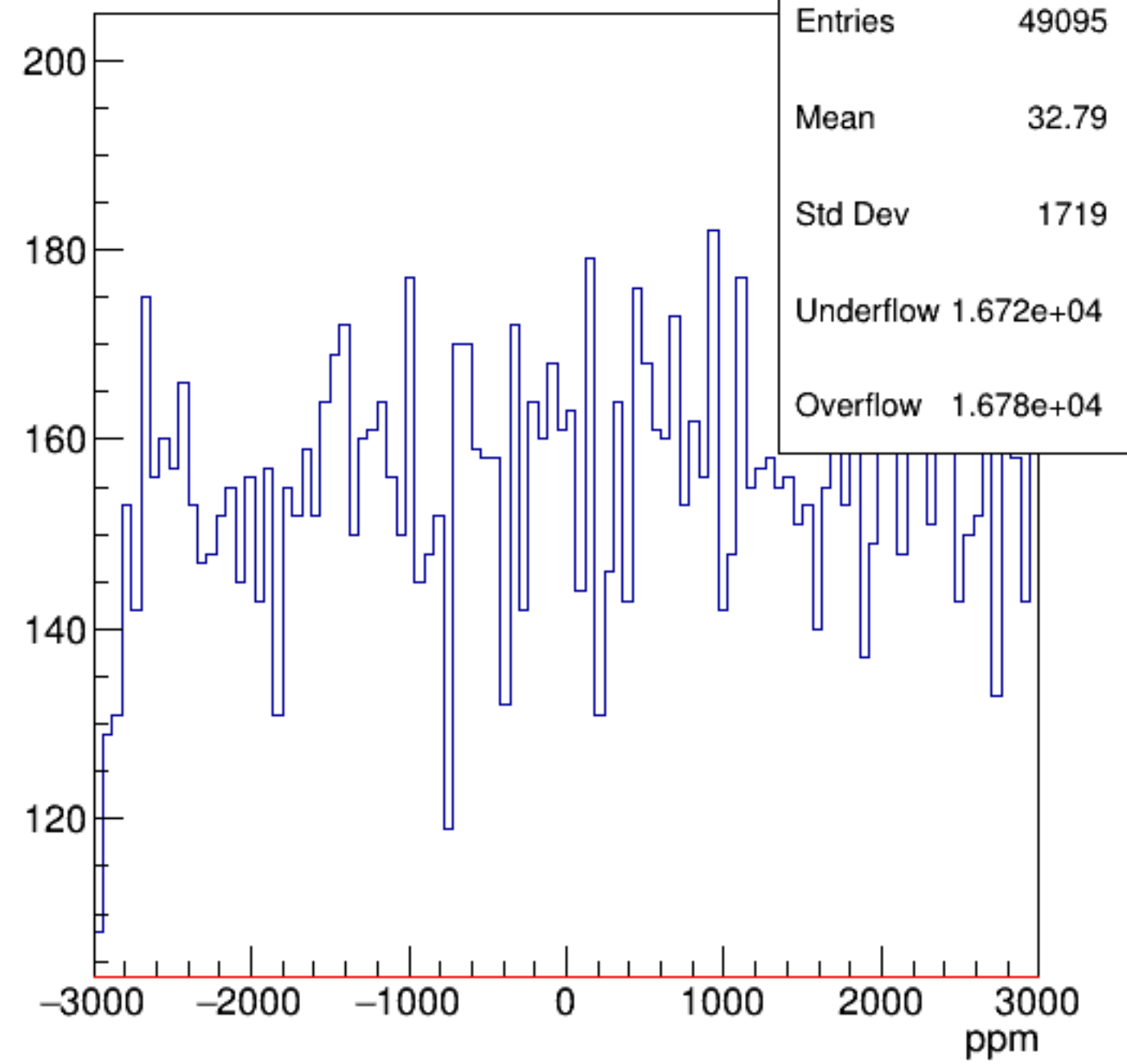
atr2\*CurMon {ErrorFlag==0}



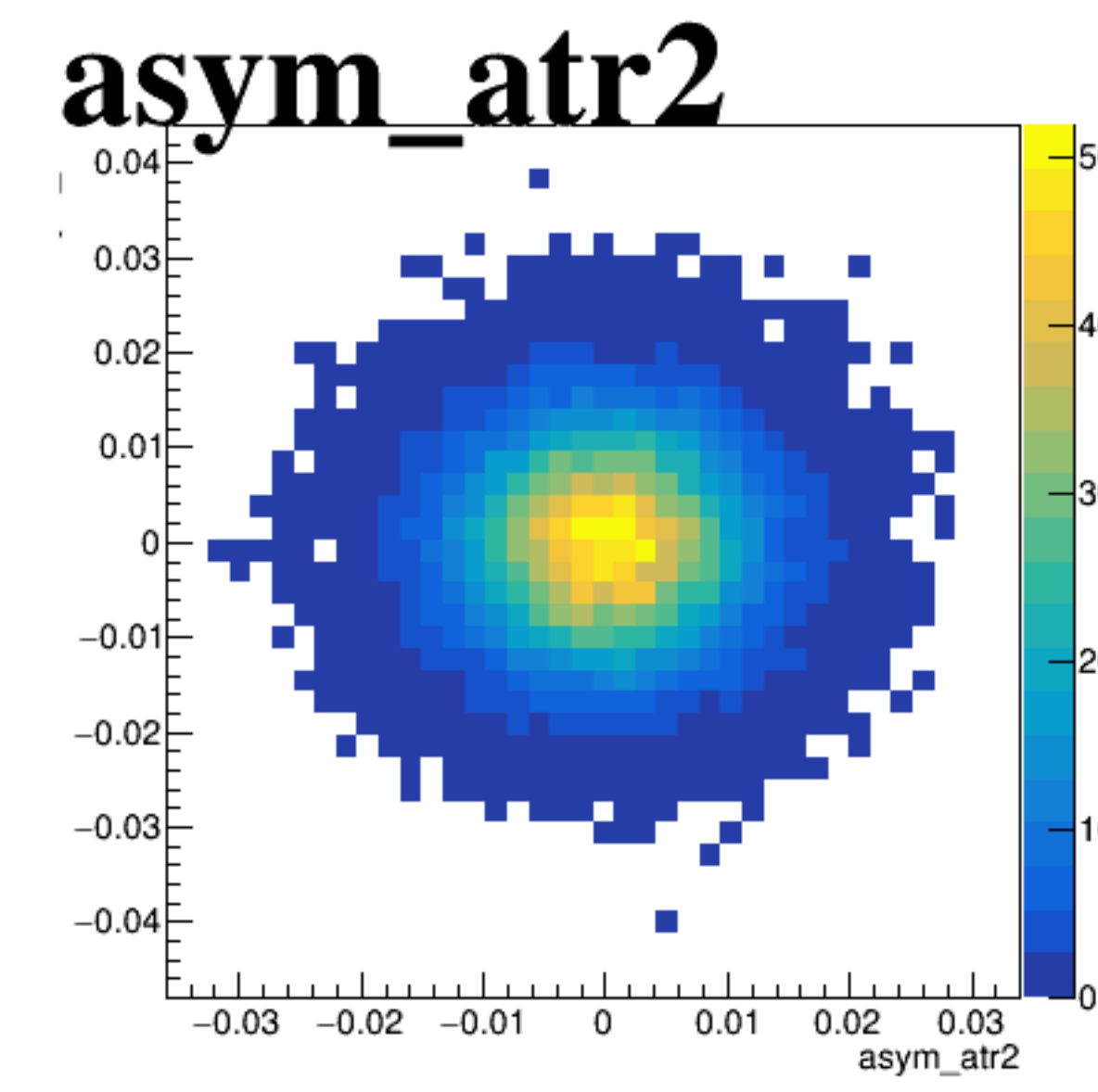
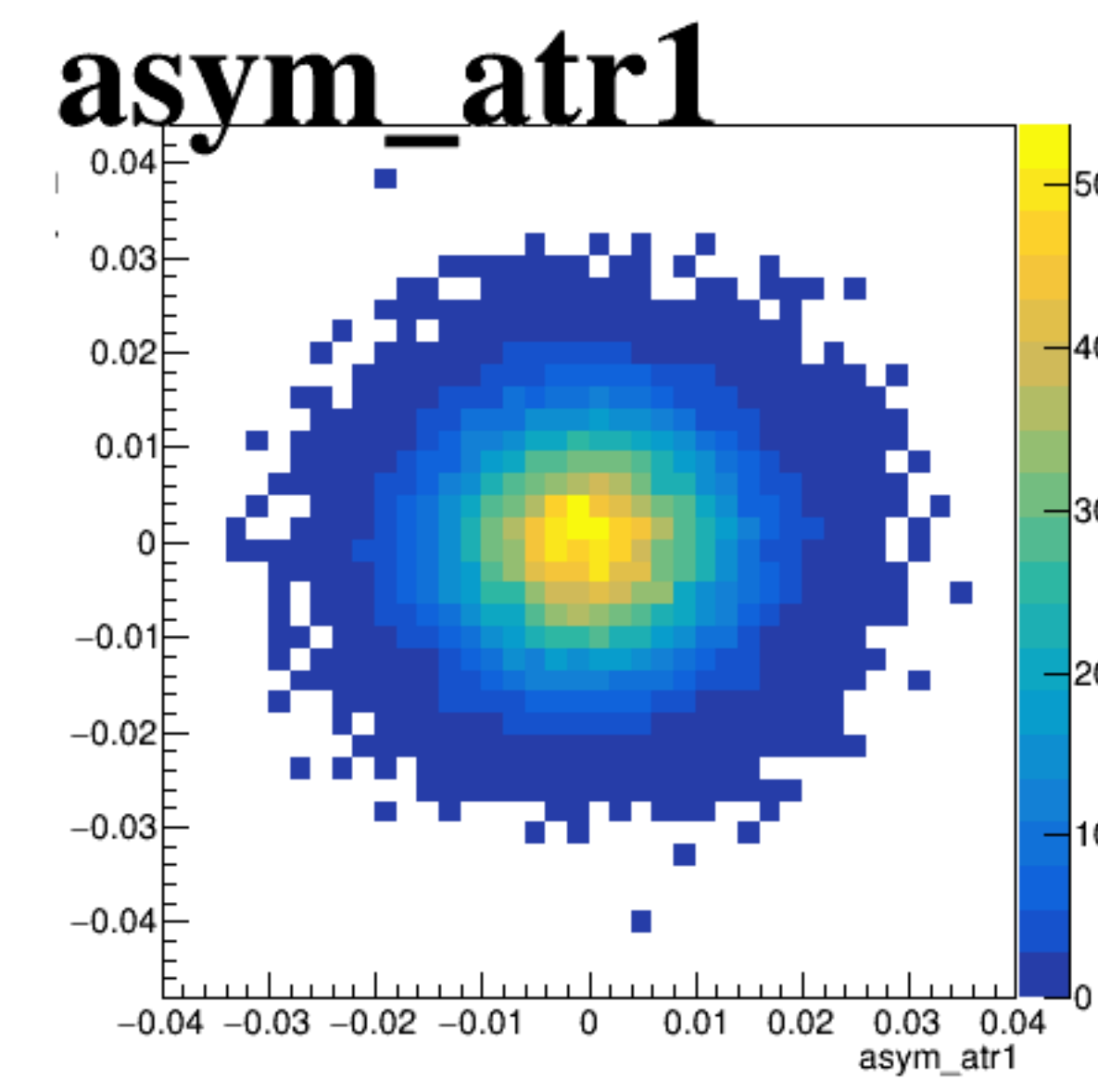
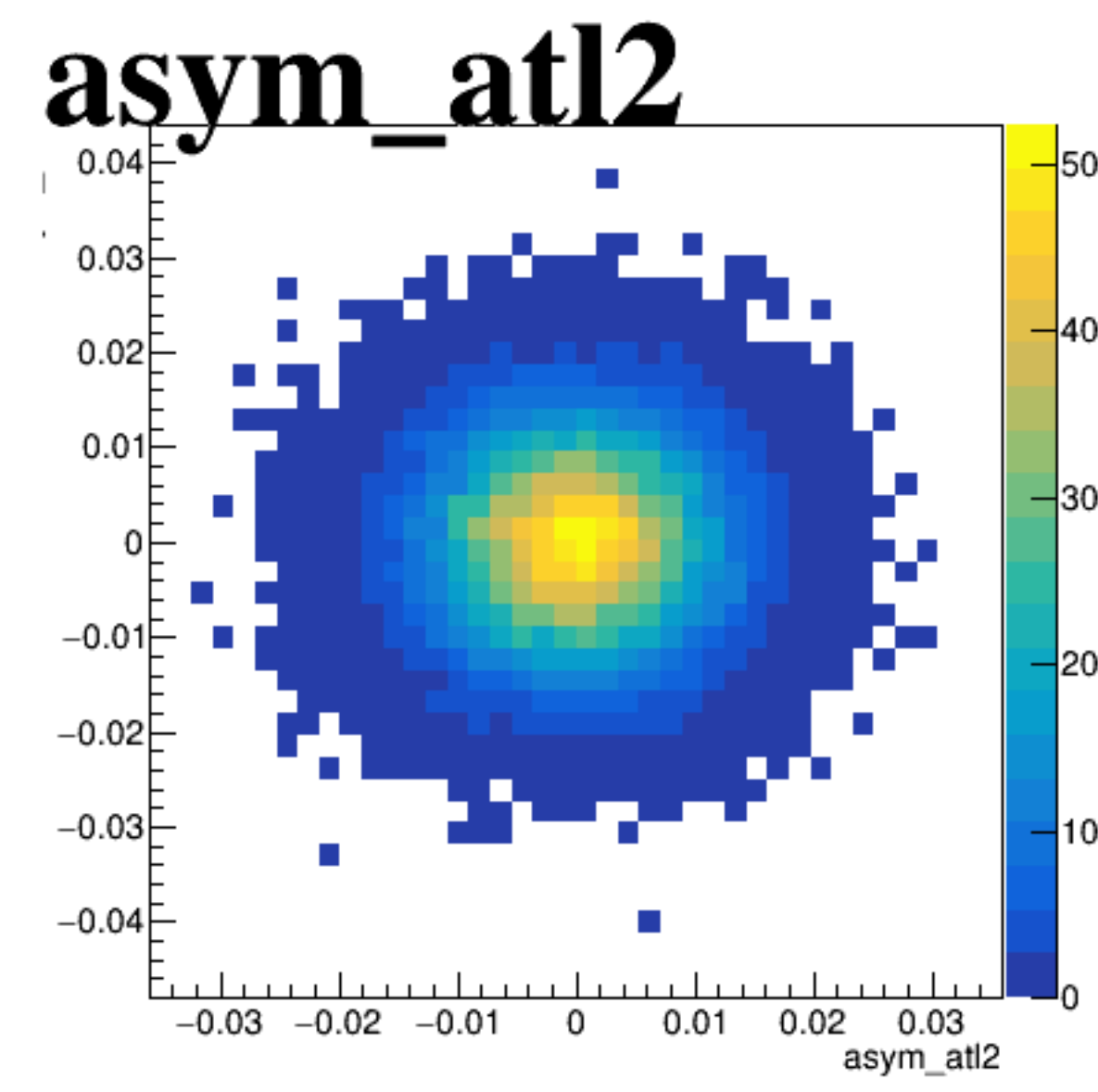
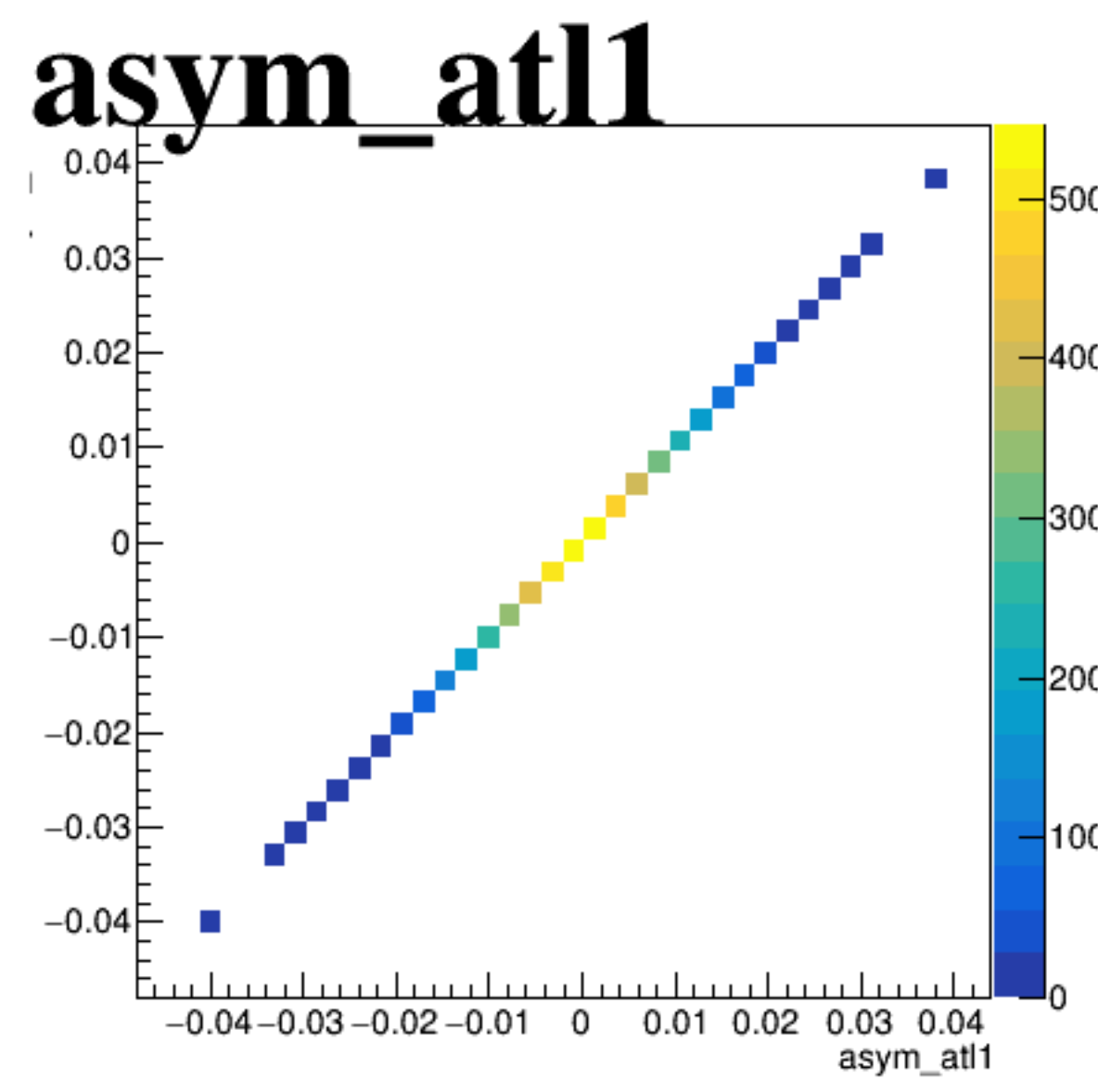
asym\_atr2/ppm:pattern\_number {ErrorFlag==0}



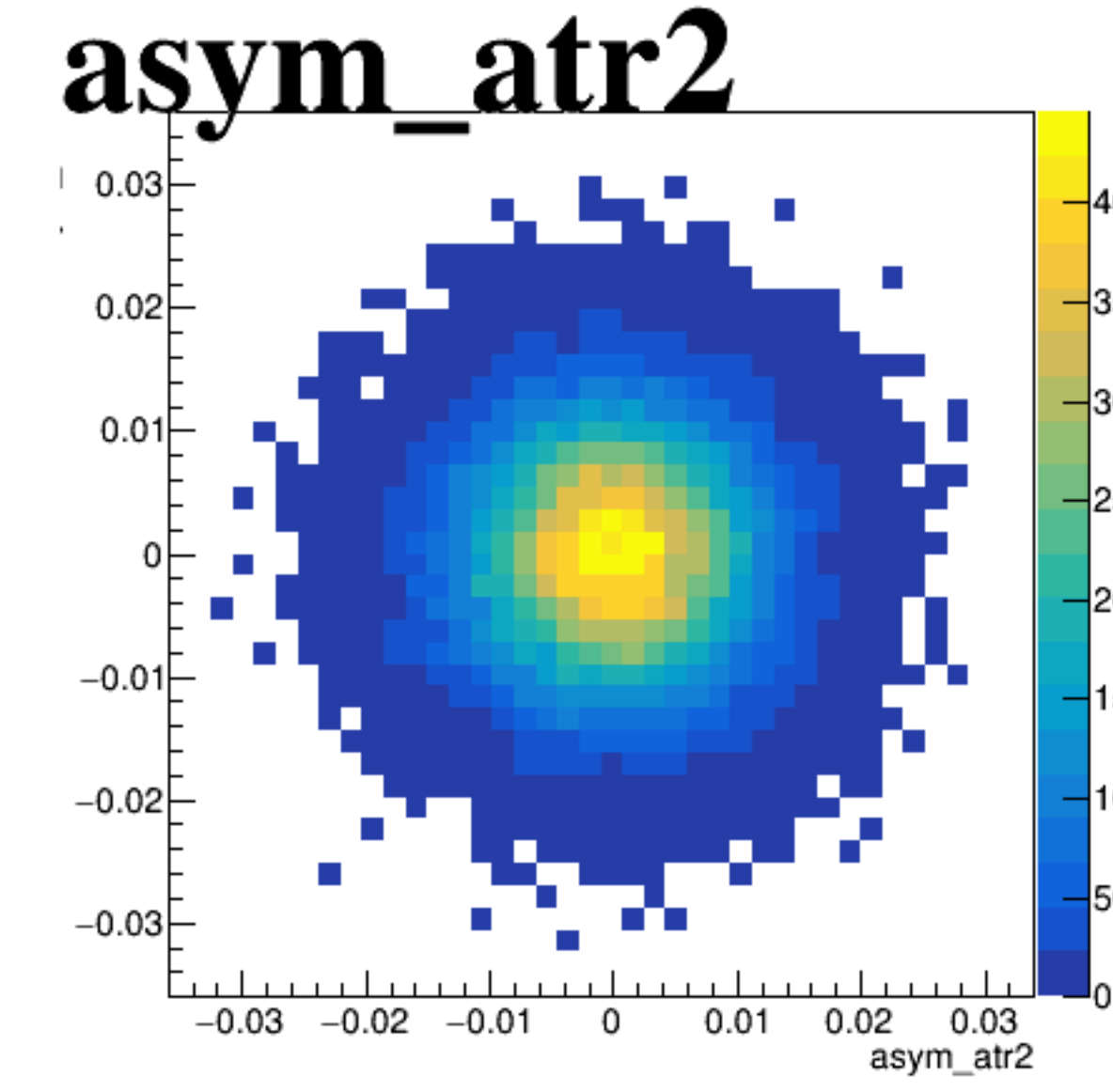
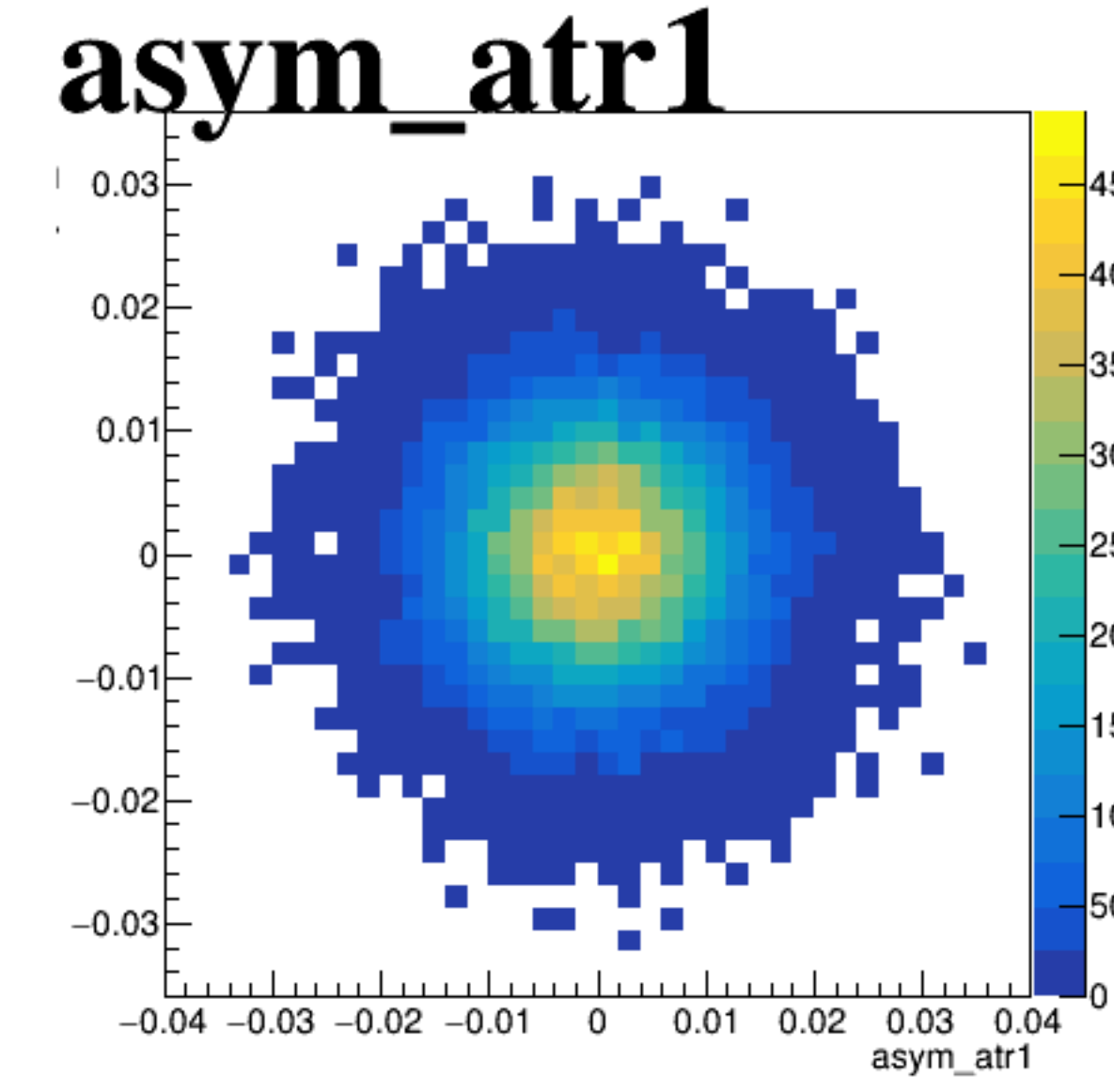
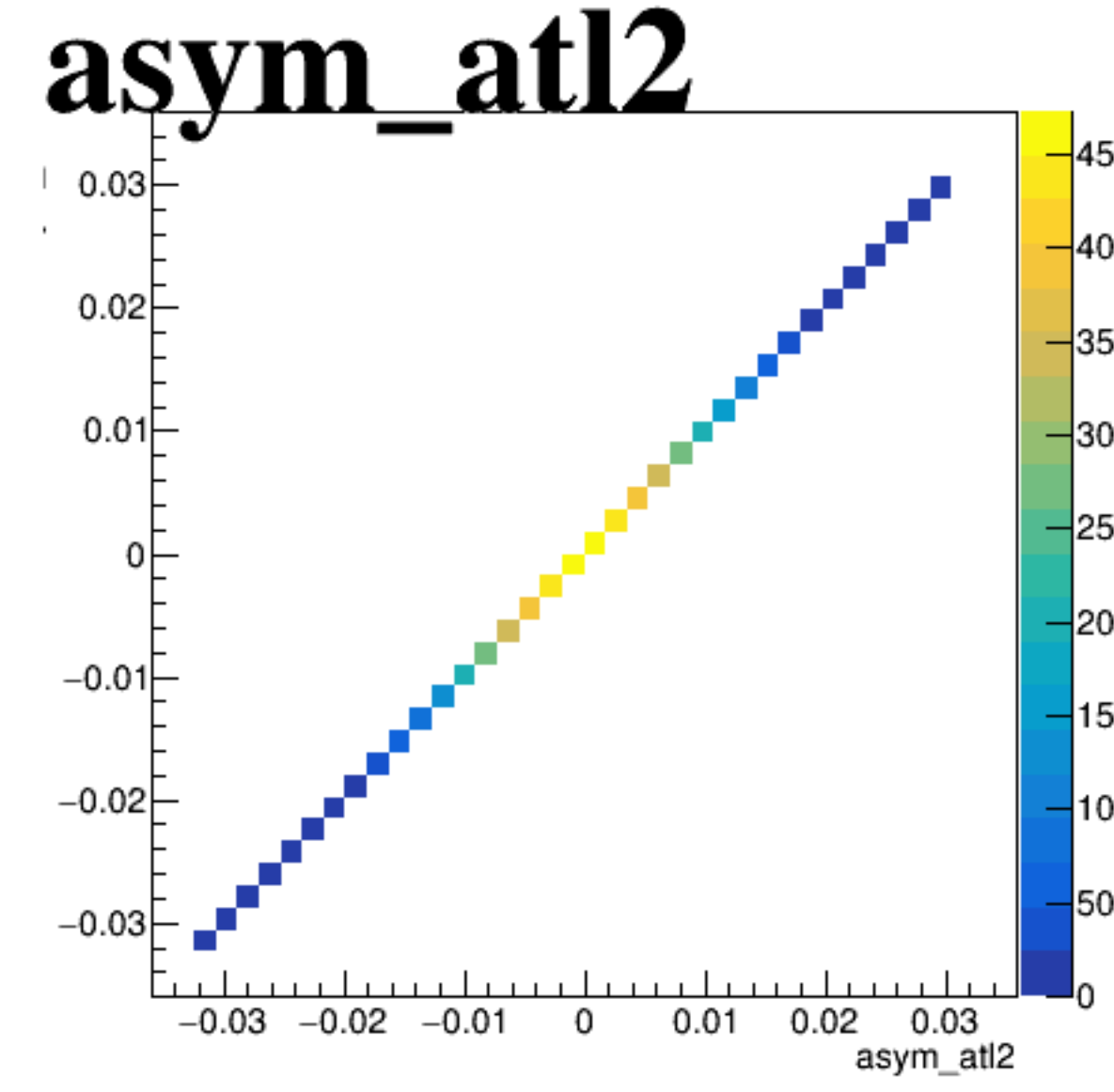
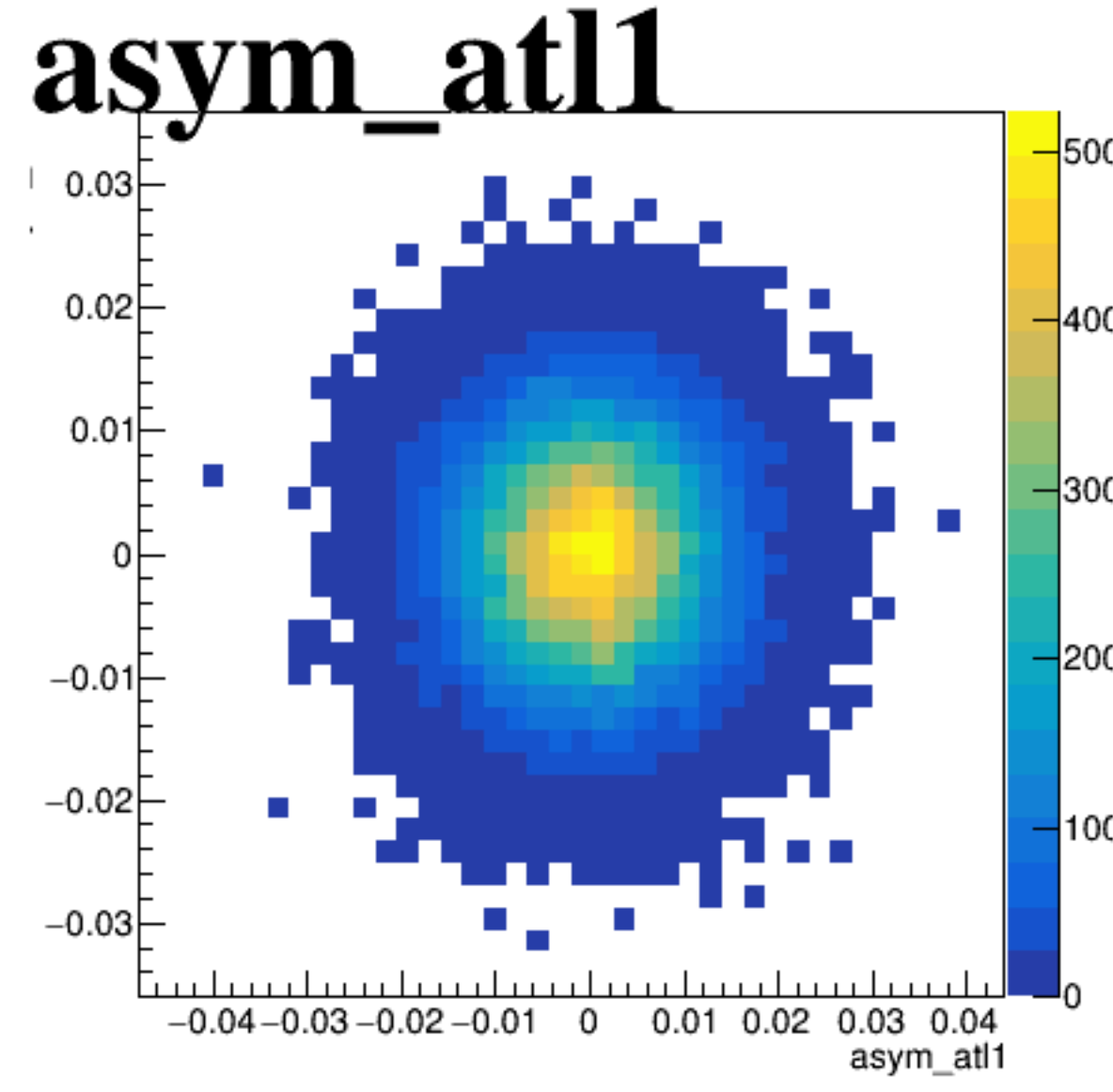
asym\_atr2



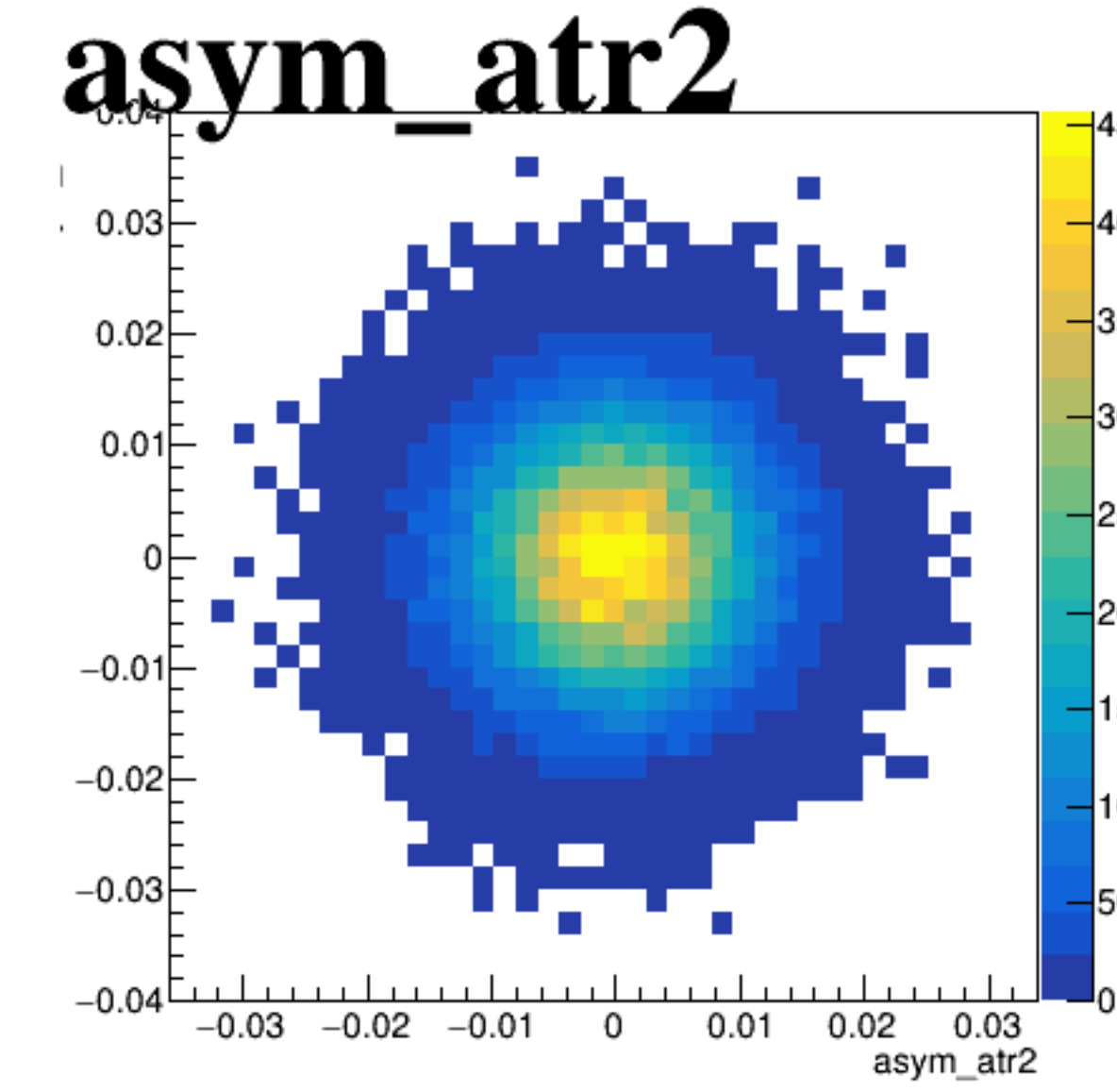
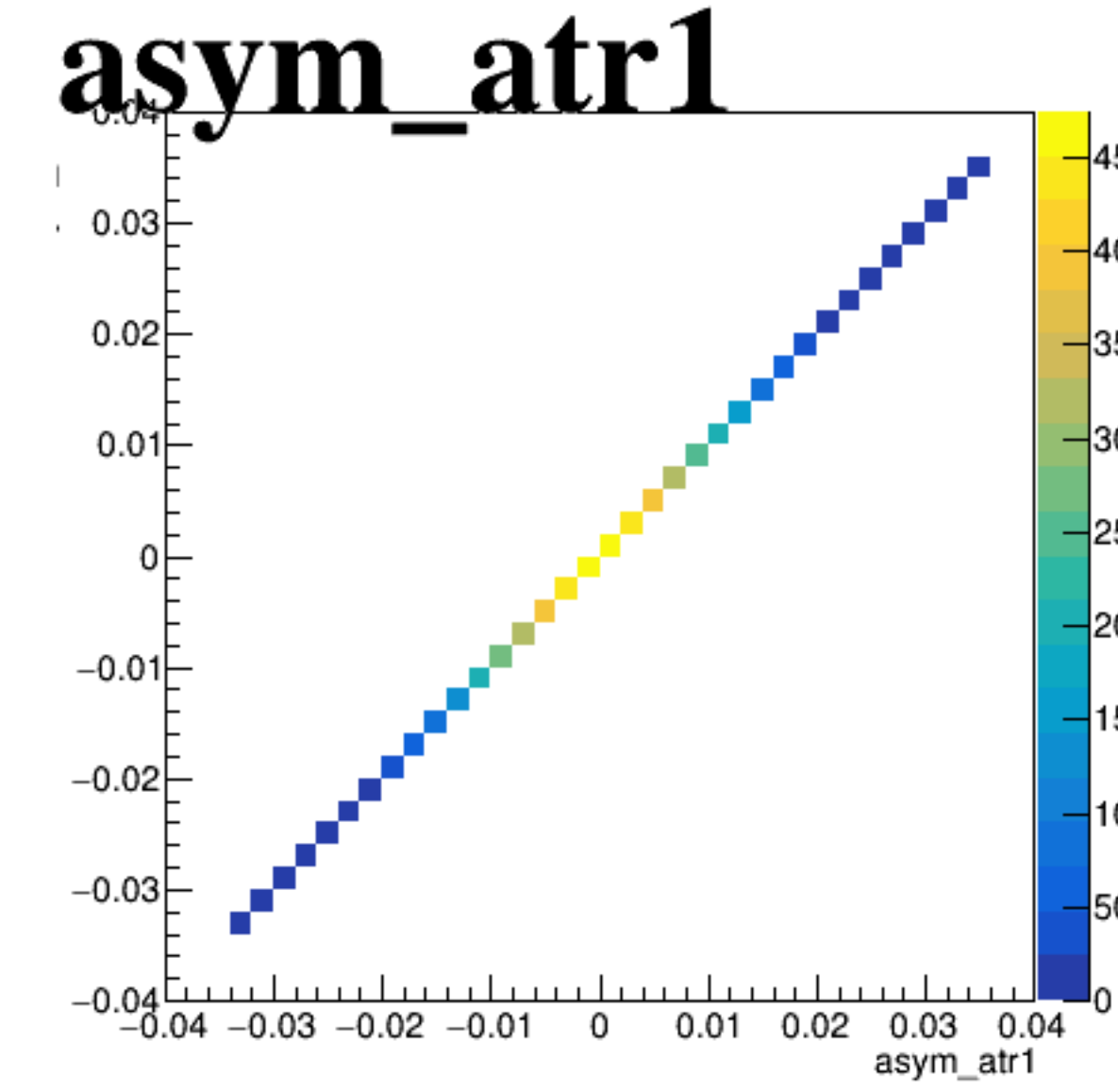
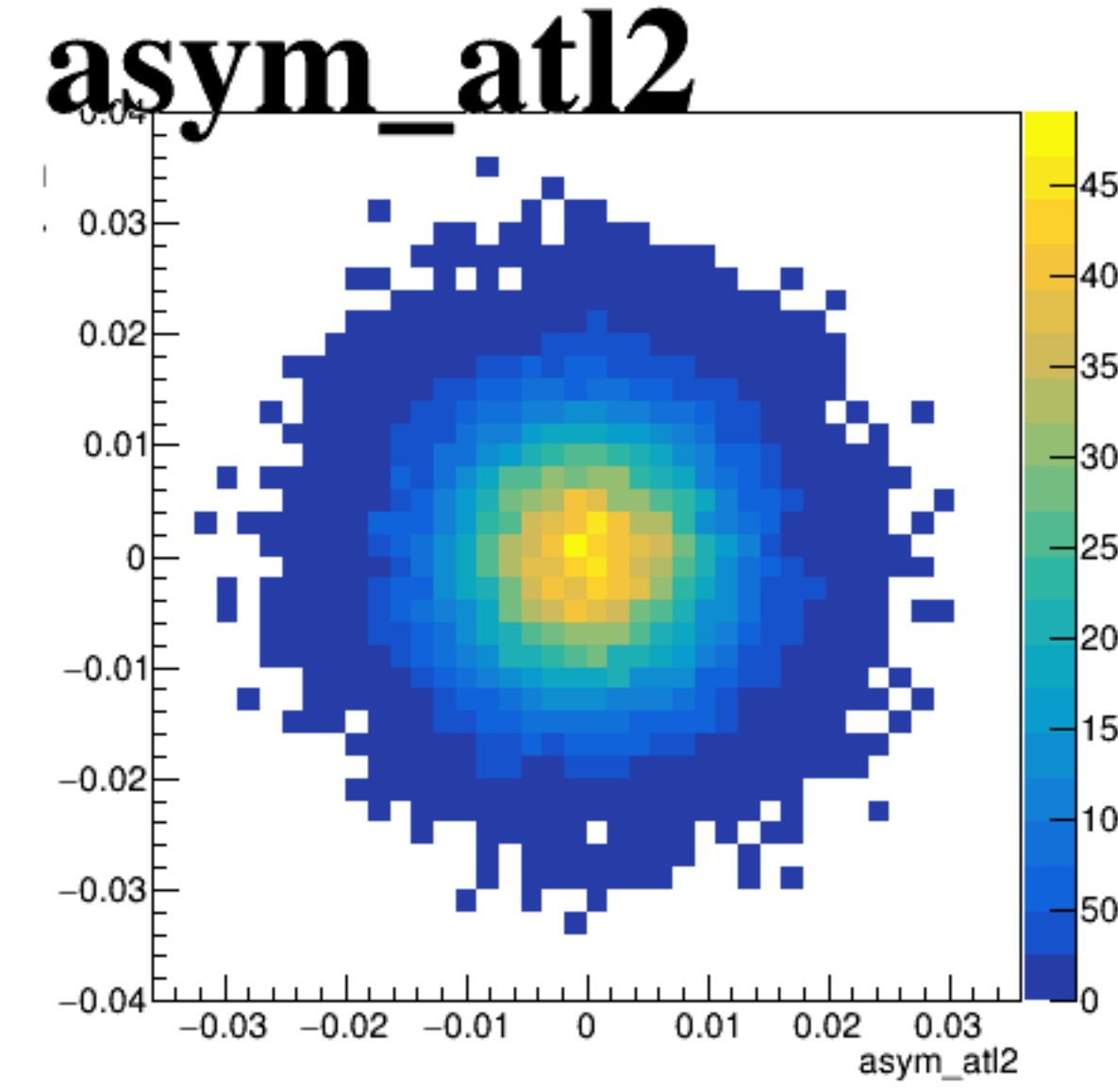
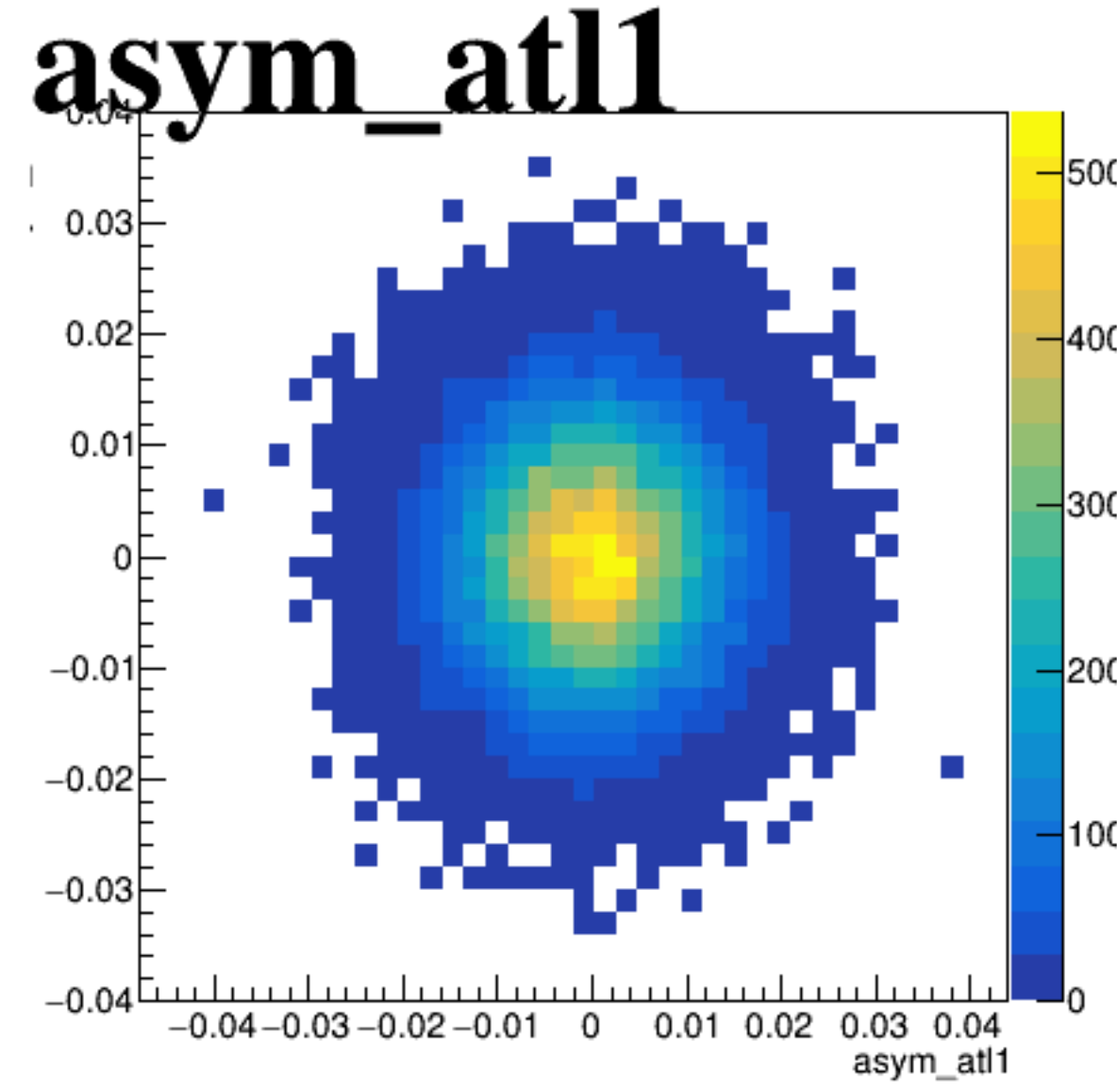
**asym\_atl1**



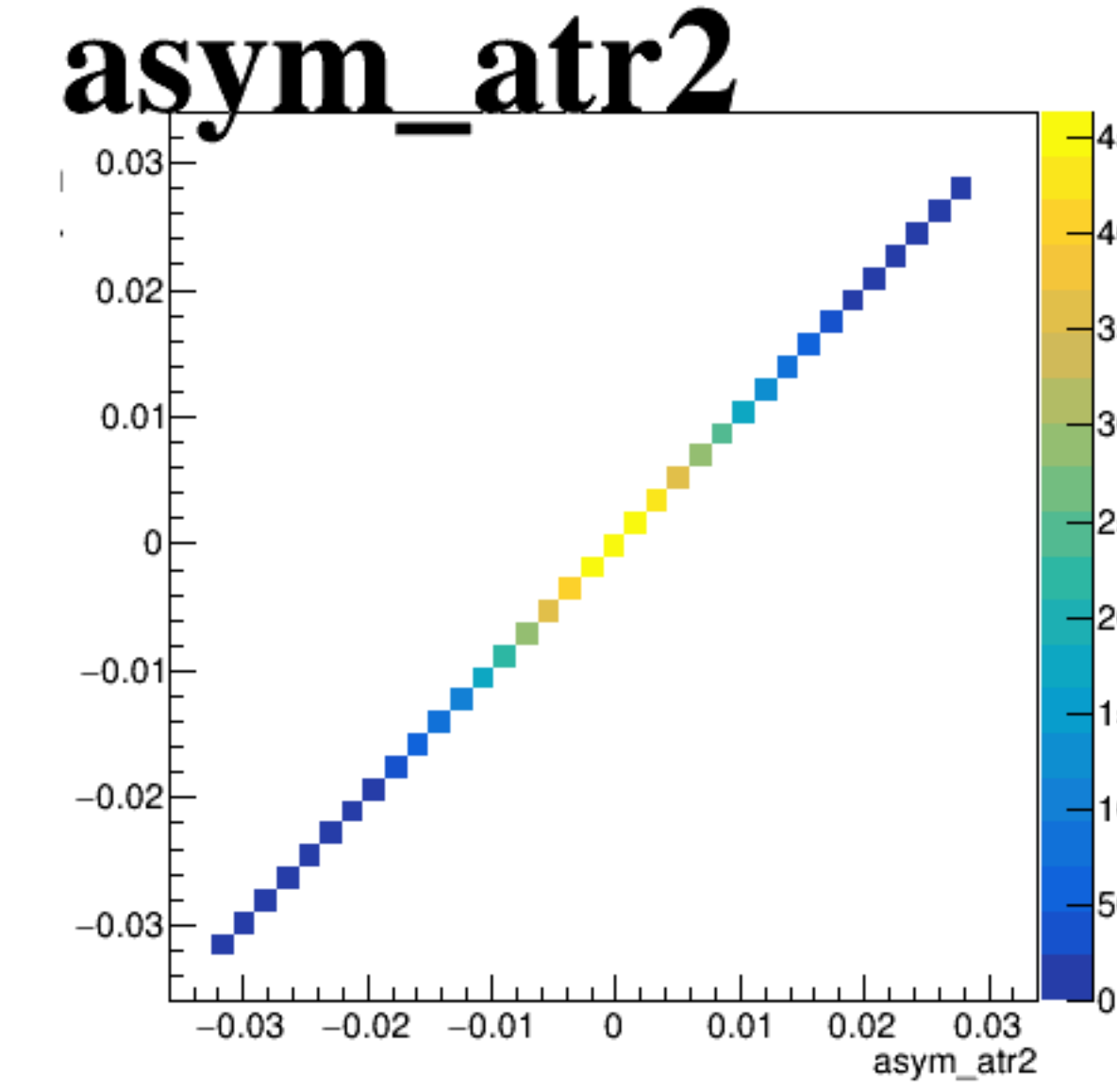
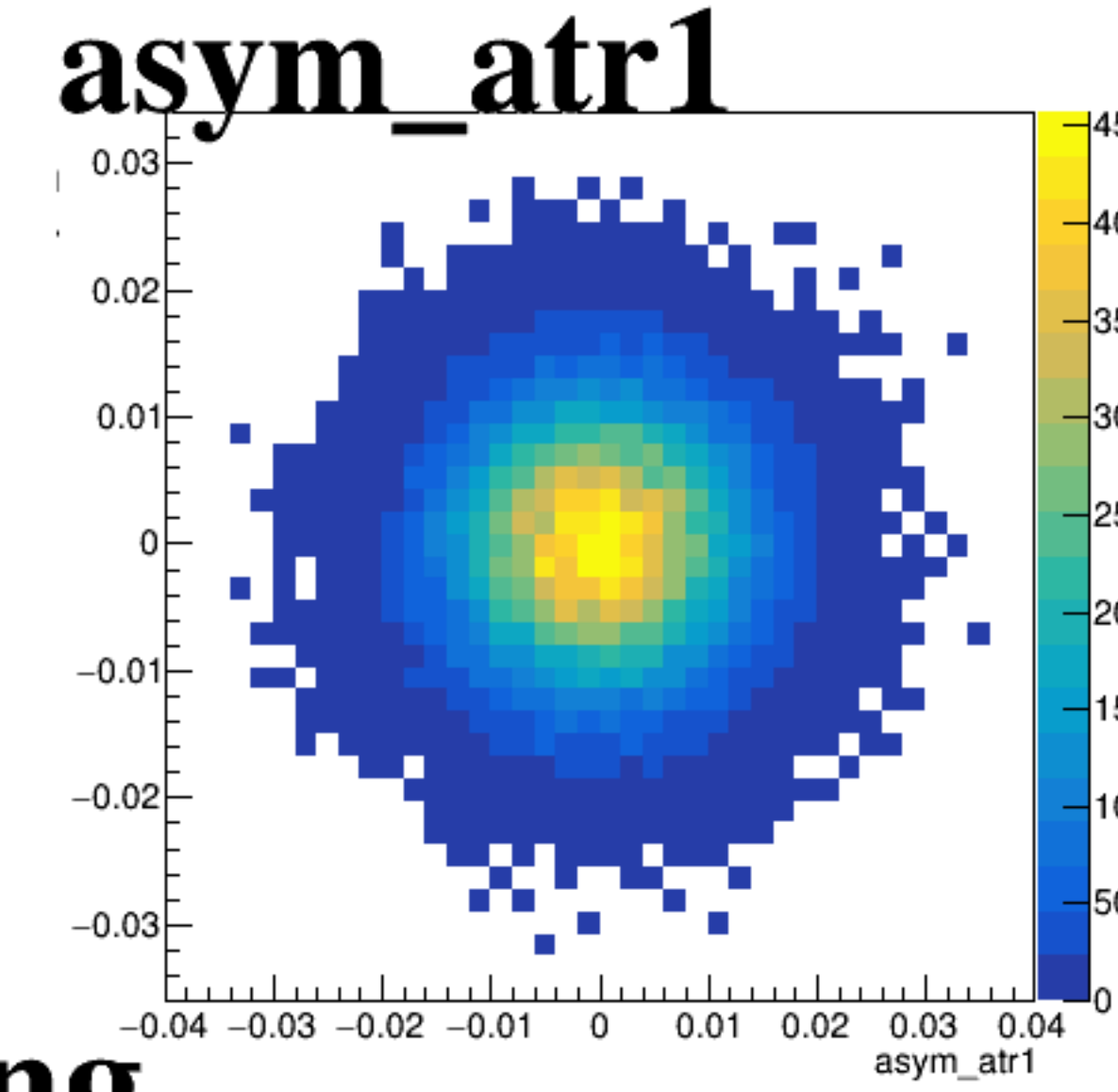
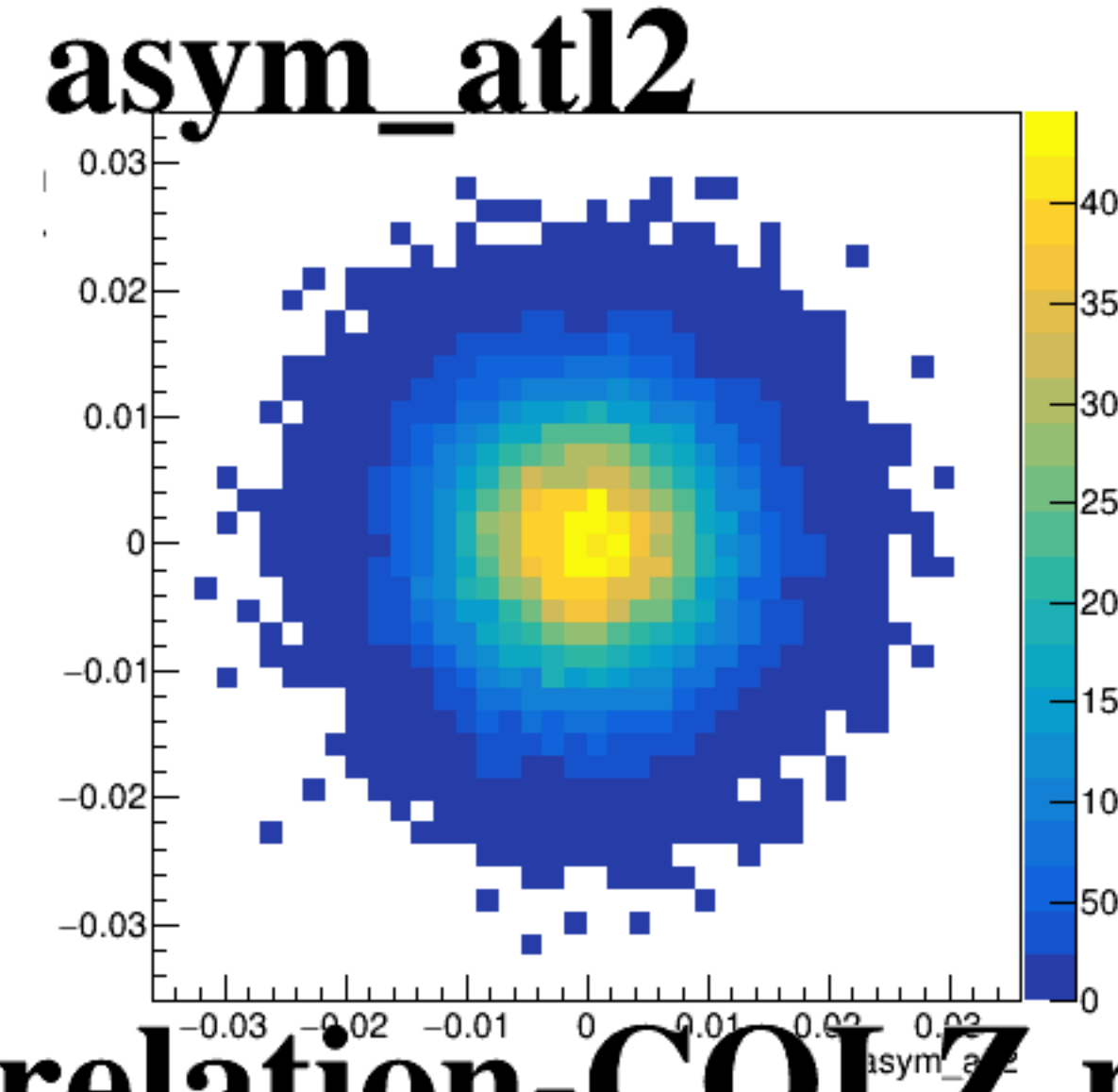
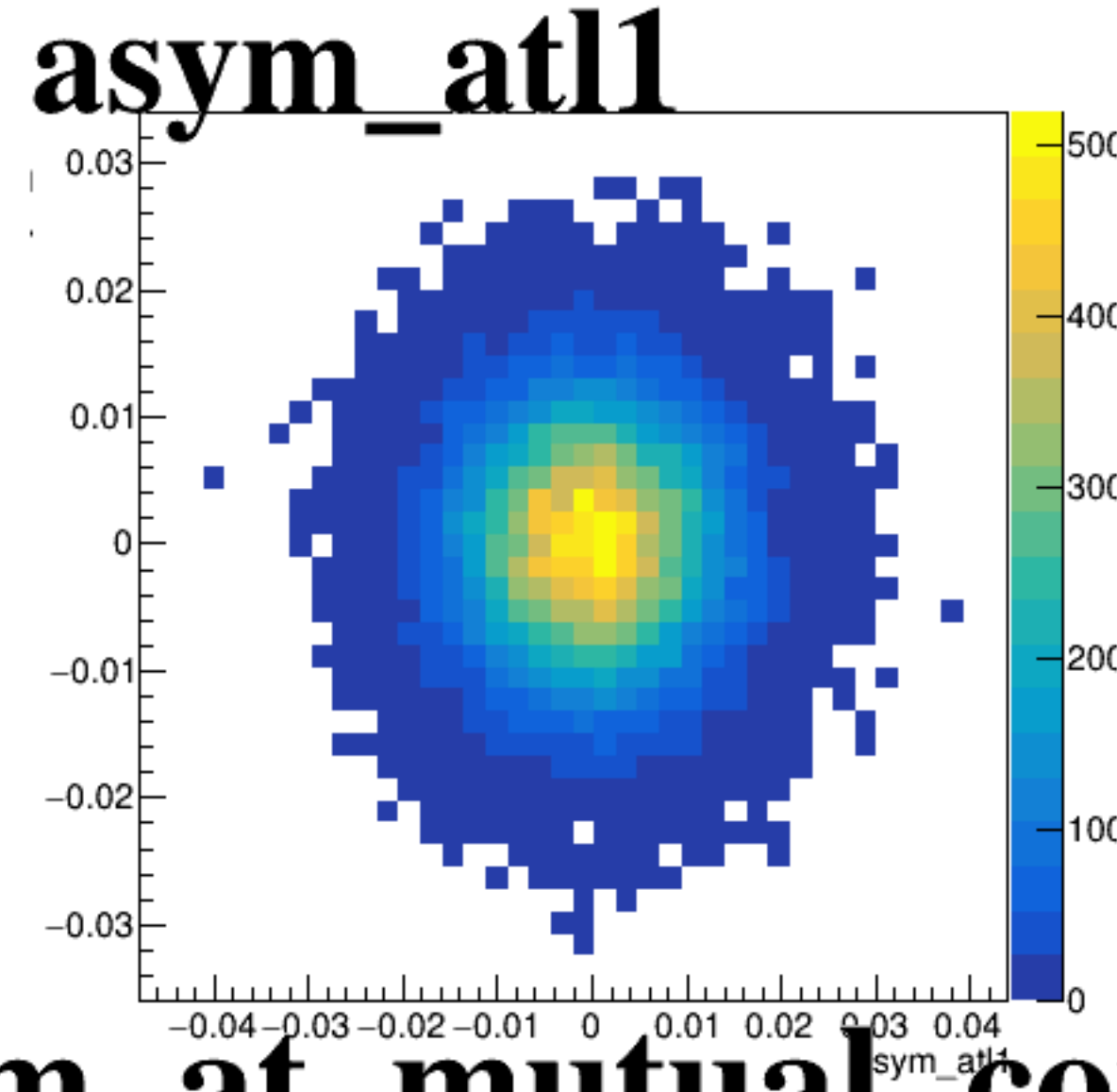
**asym\_atl2**



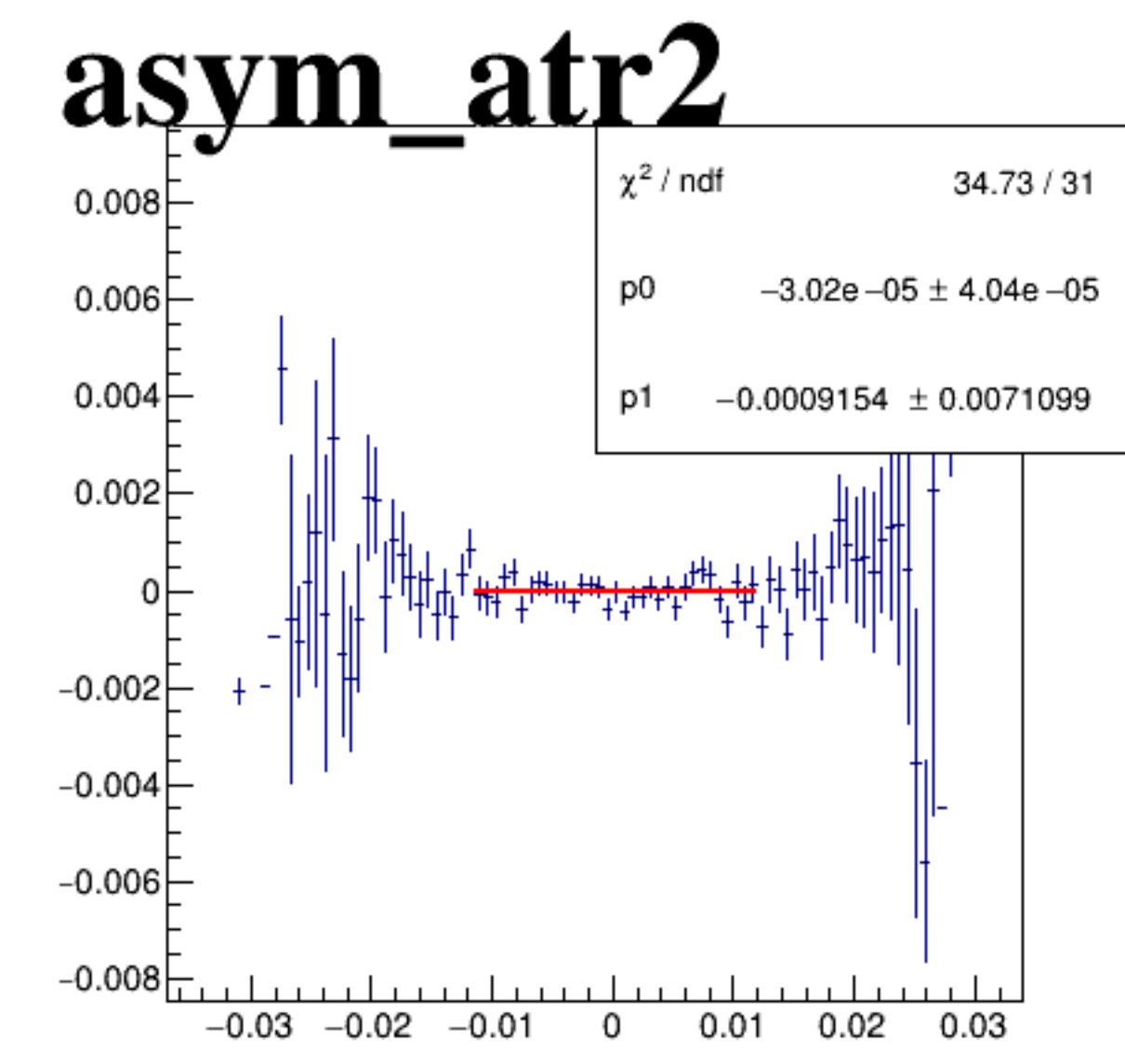
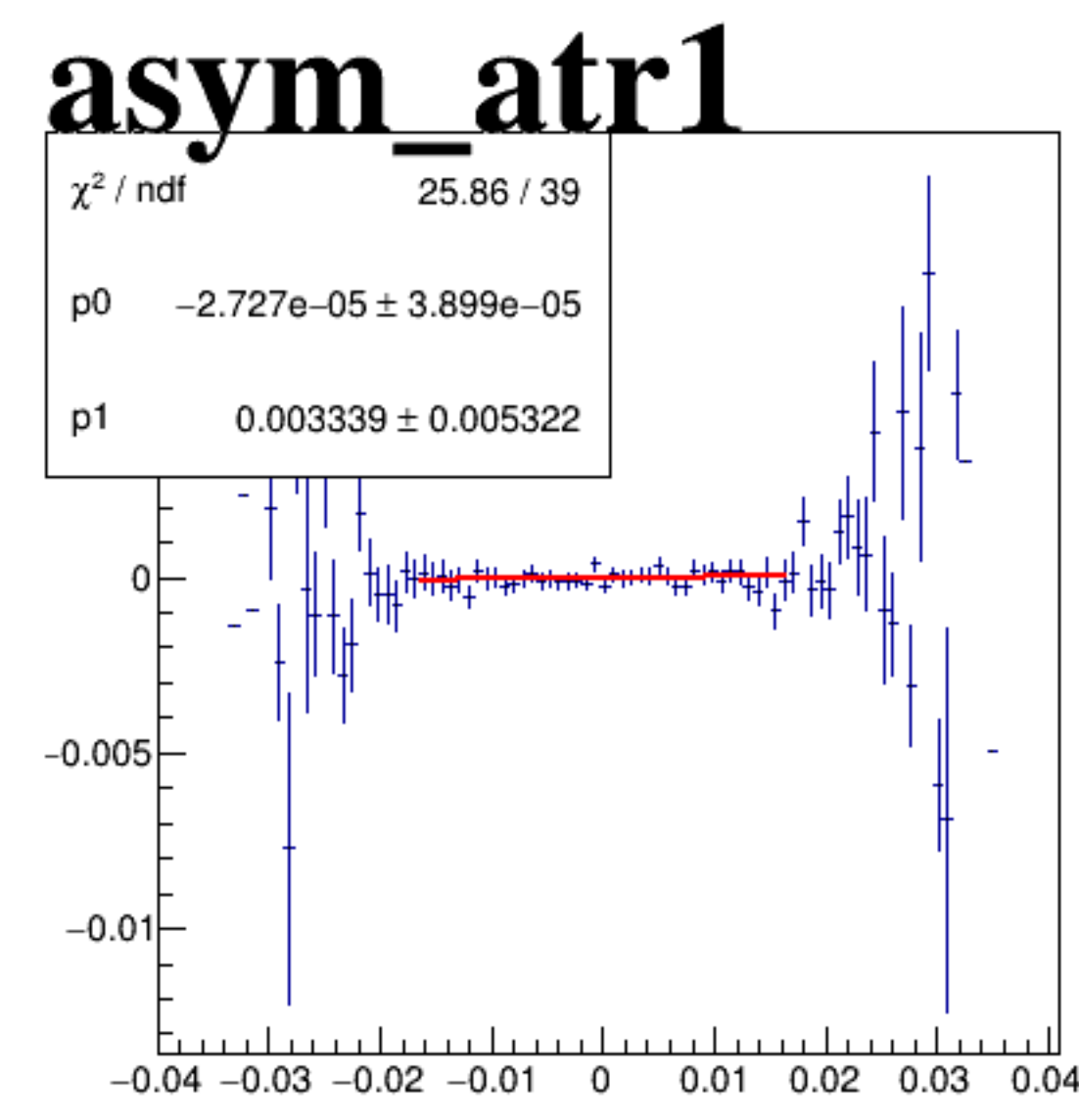
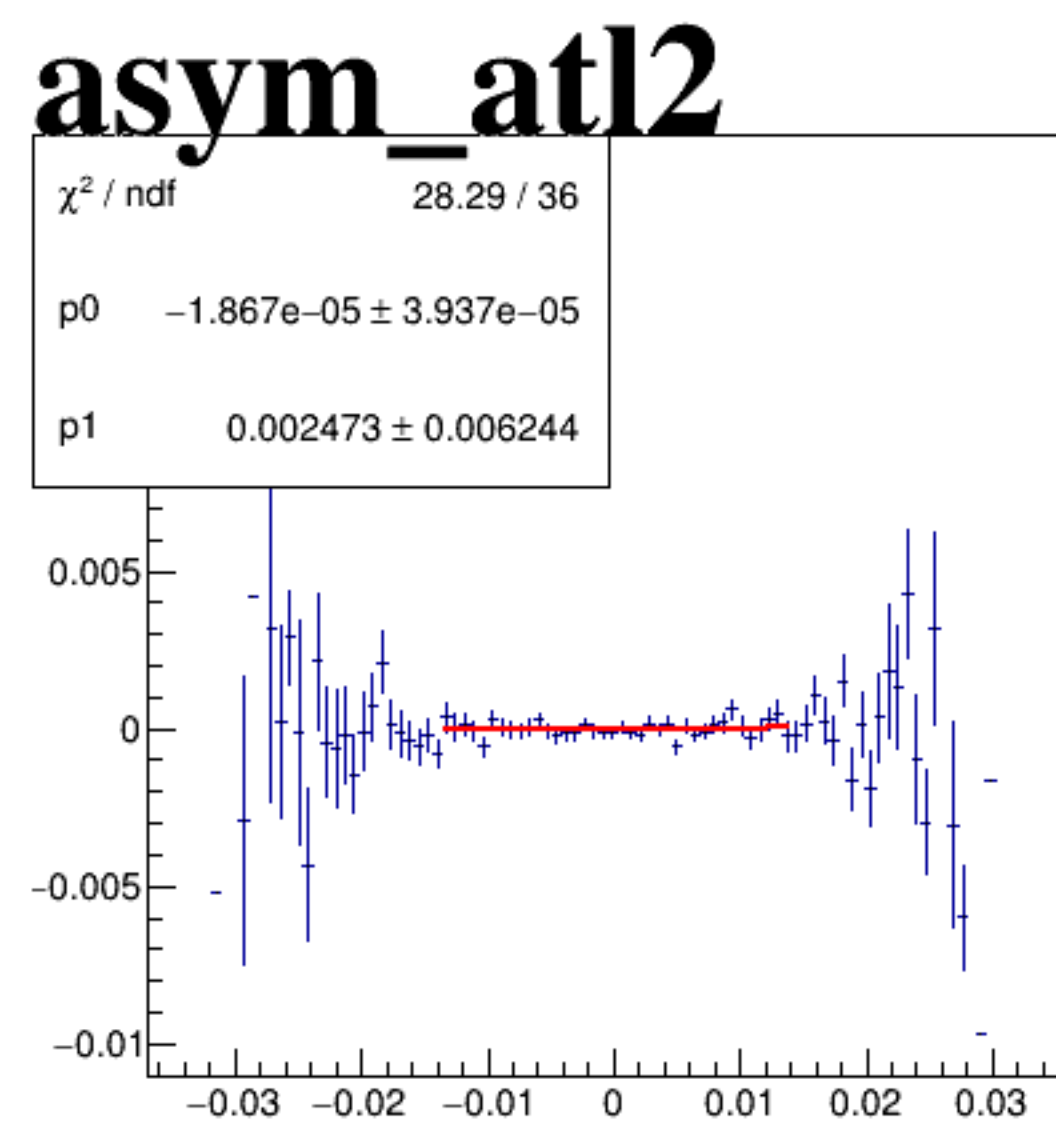
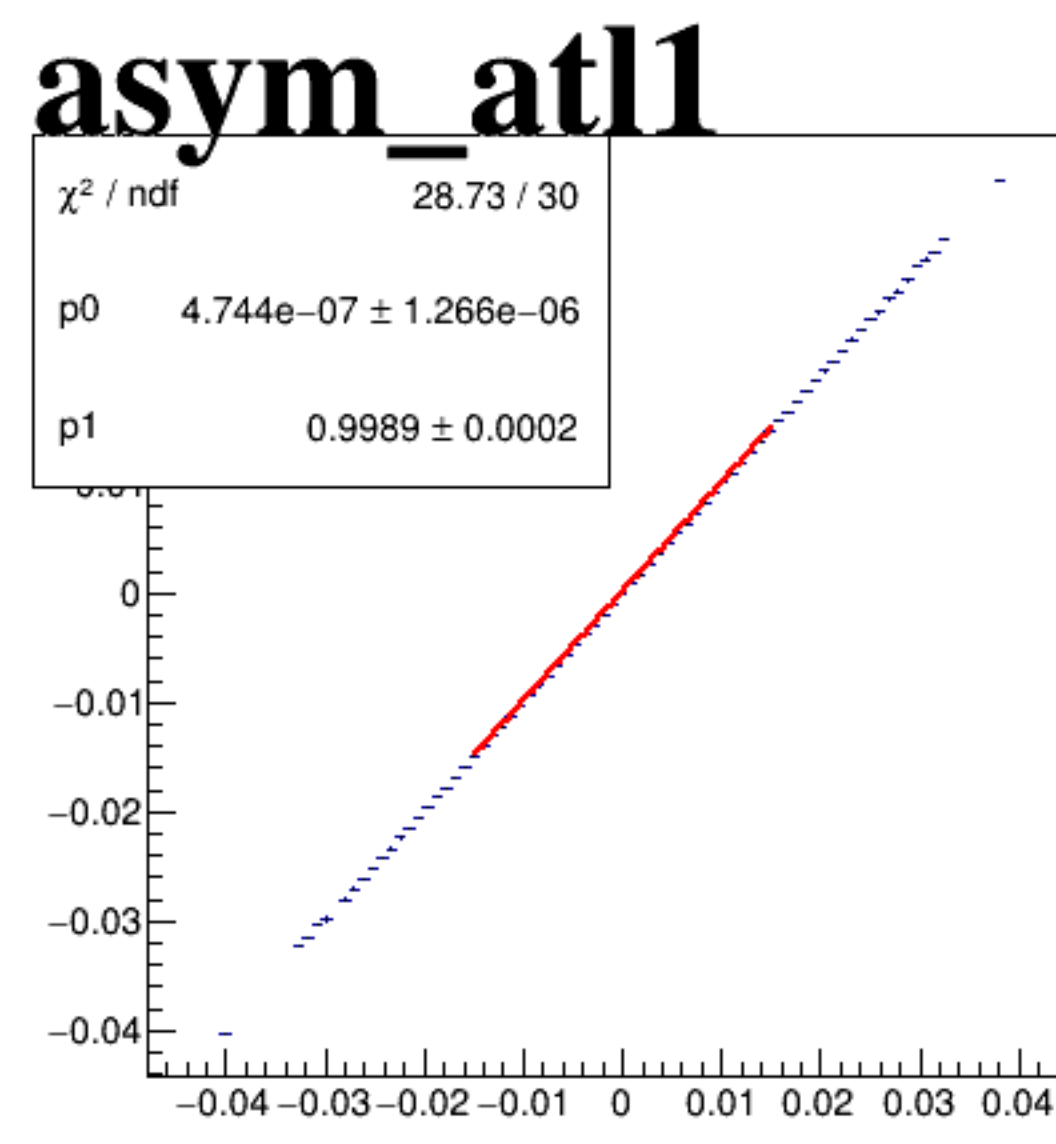
**asym\_atr1**



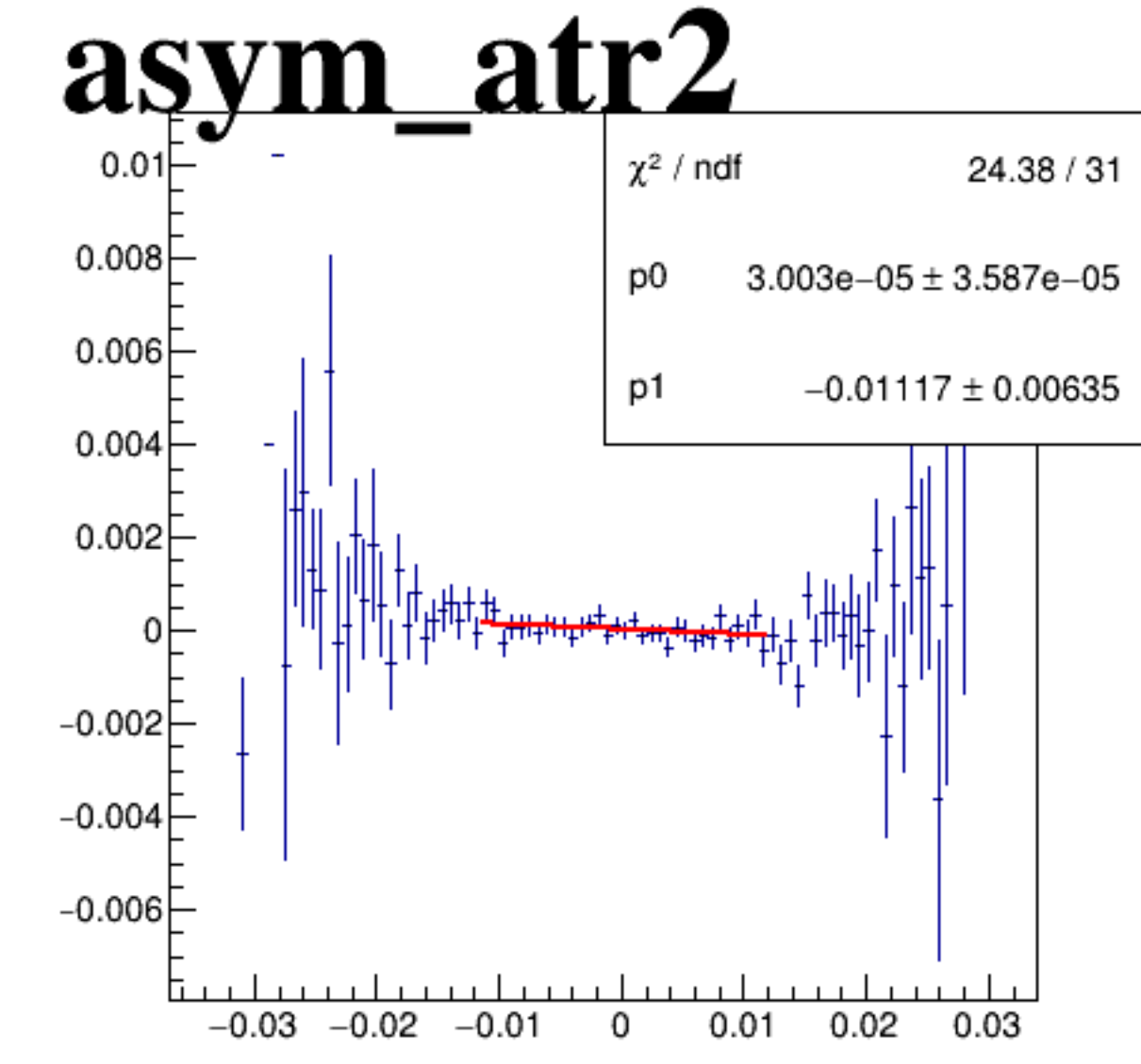
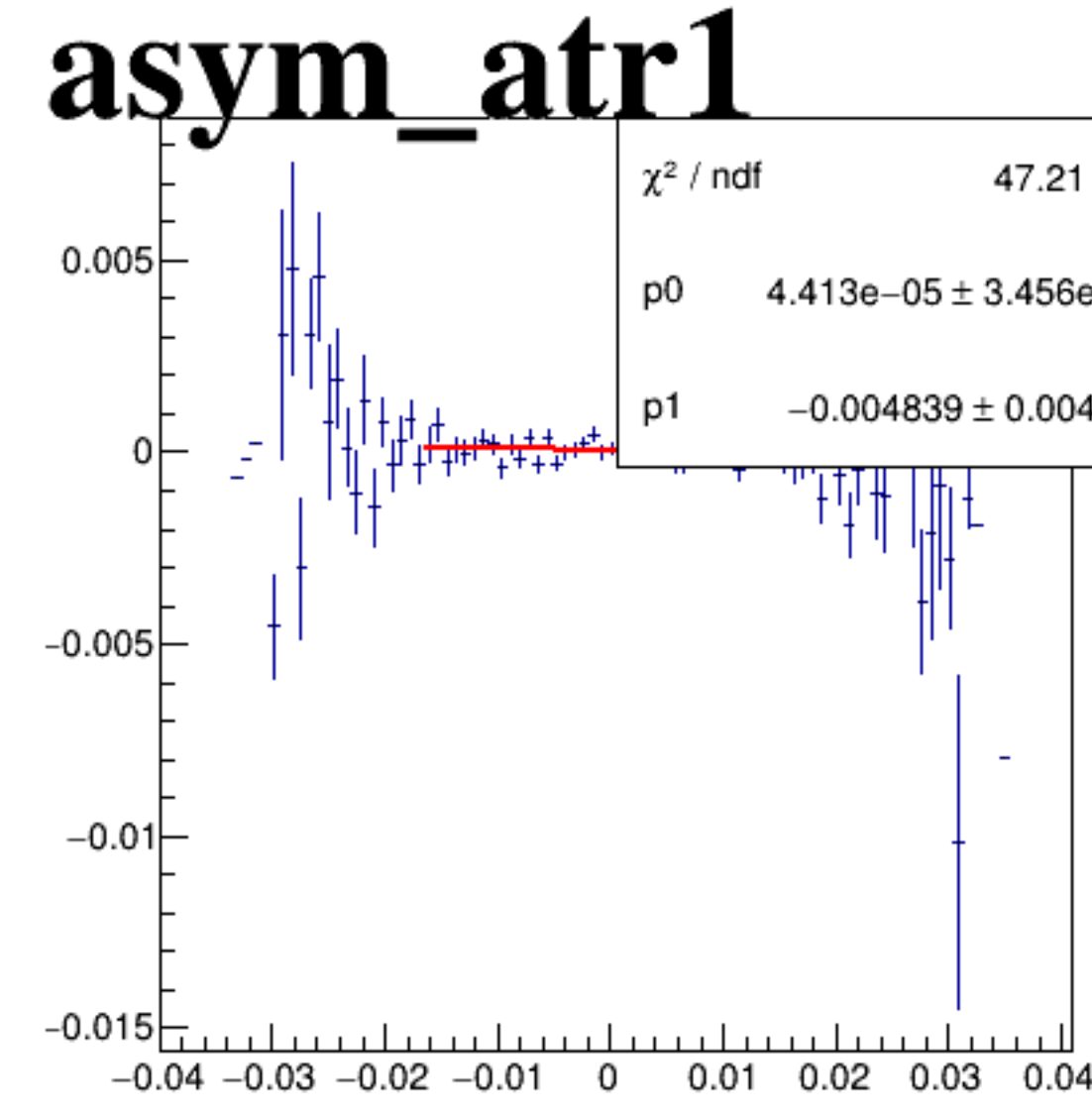
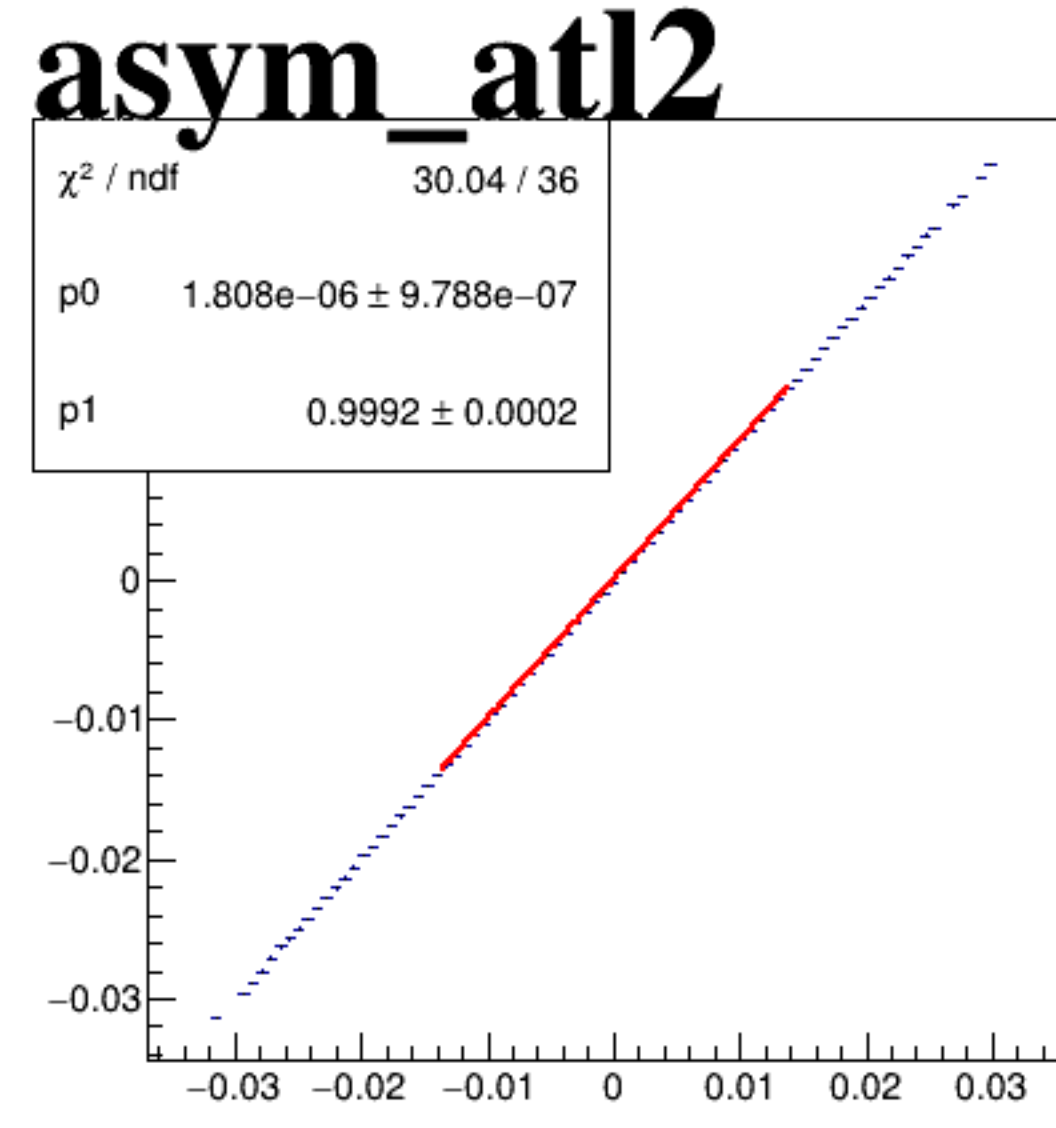
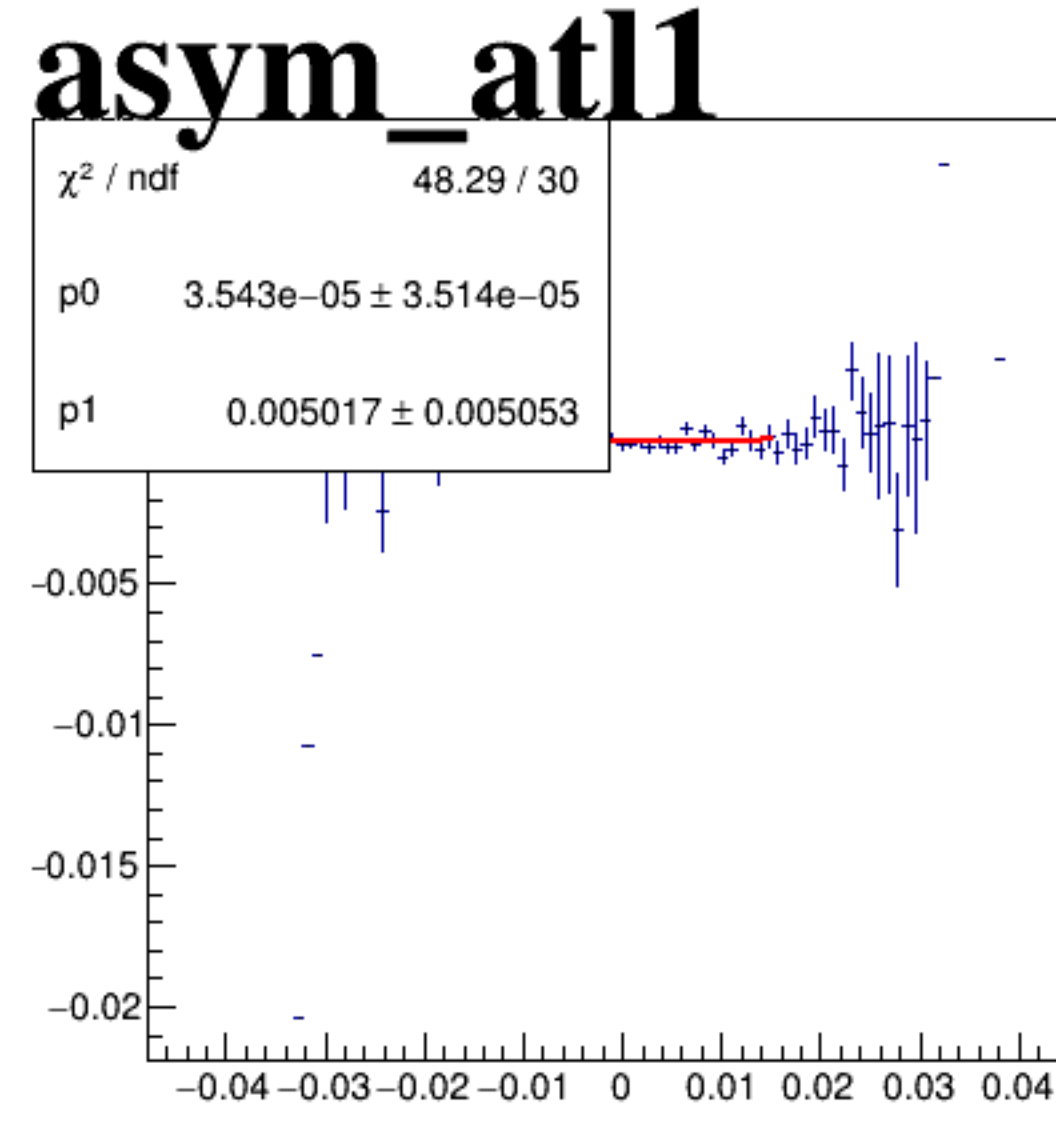
**asym\_atr2**



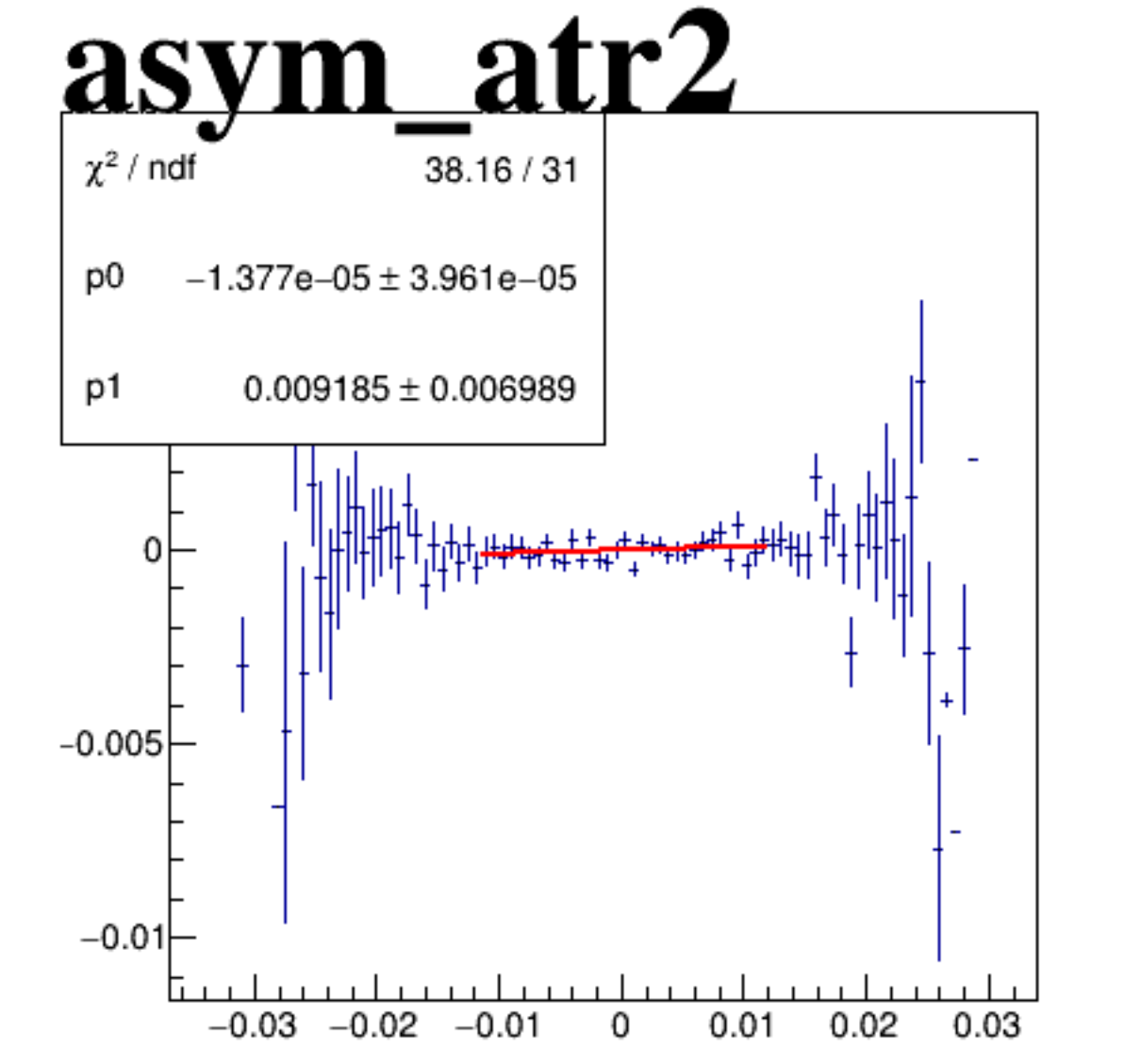
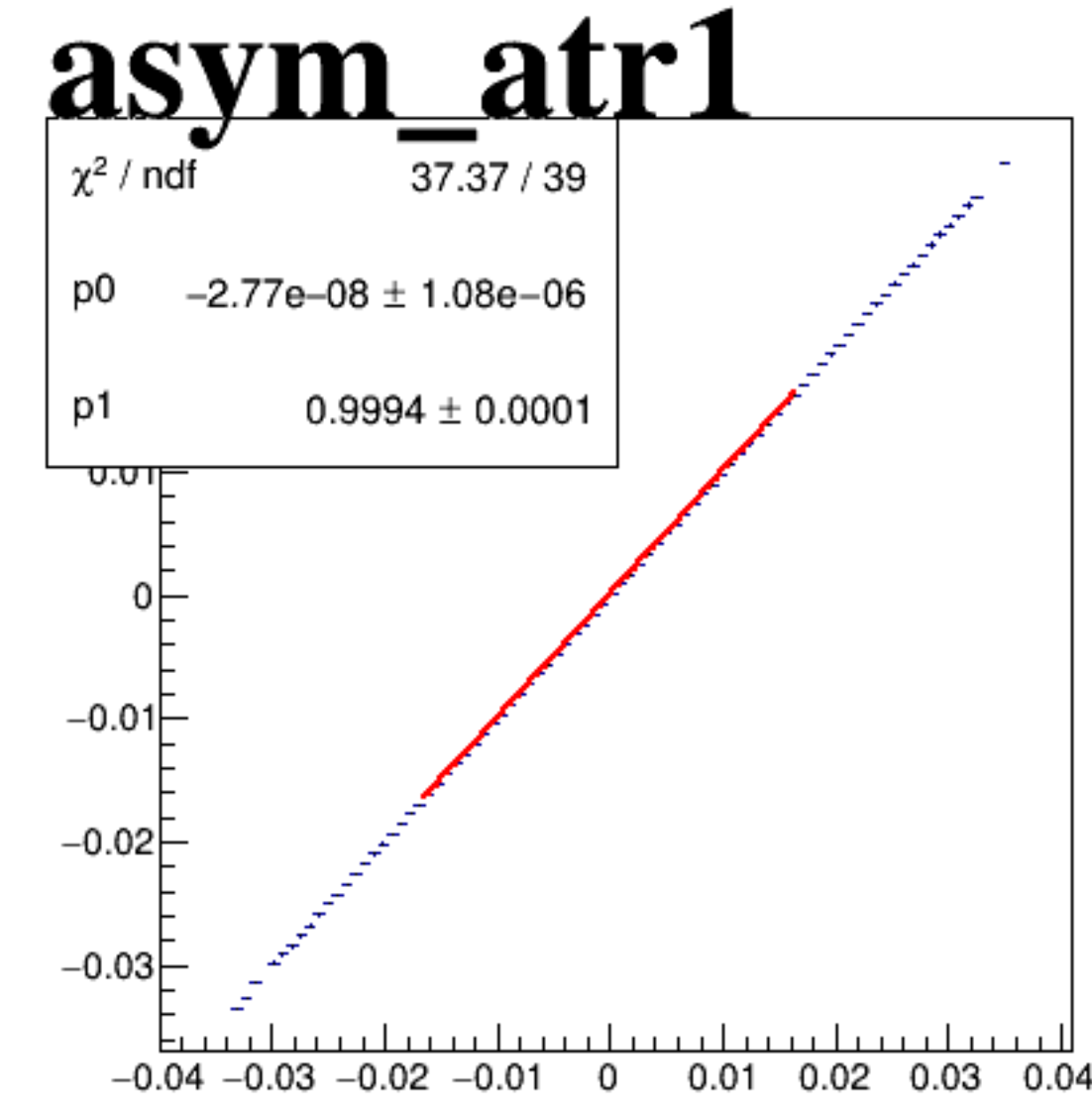
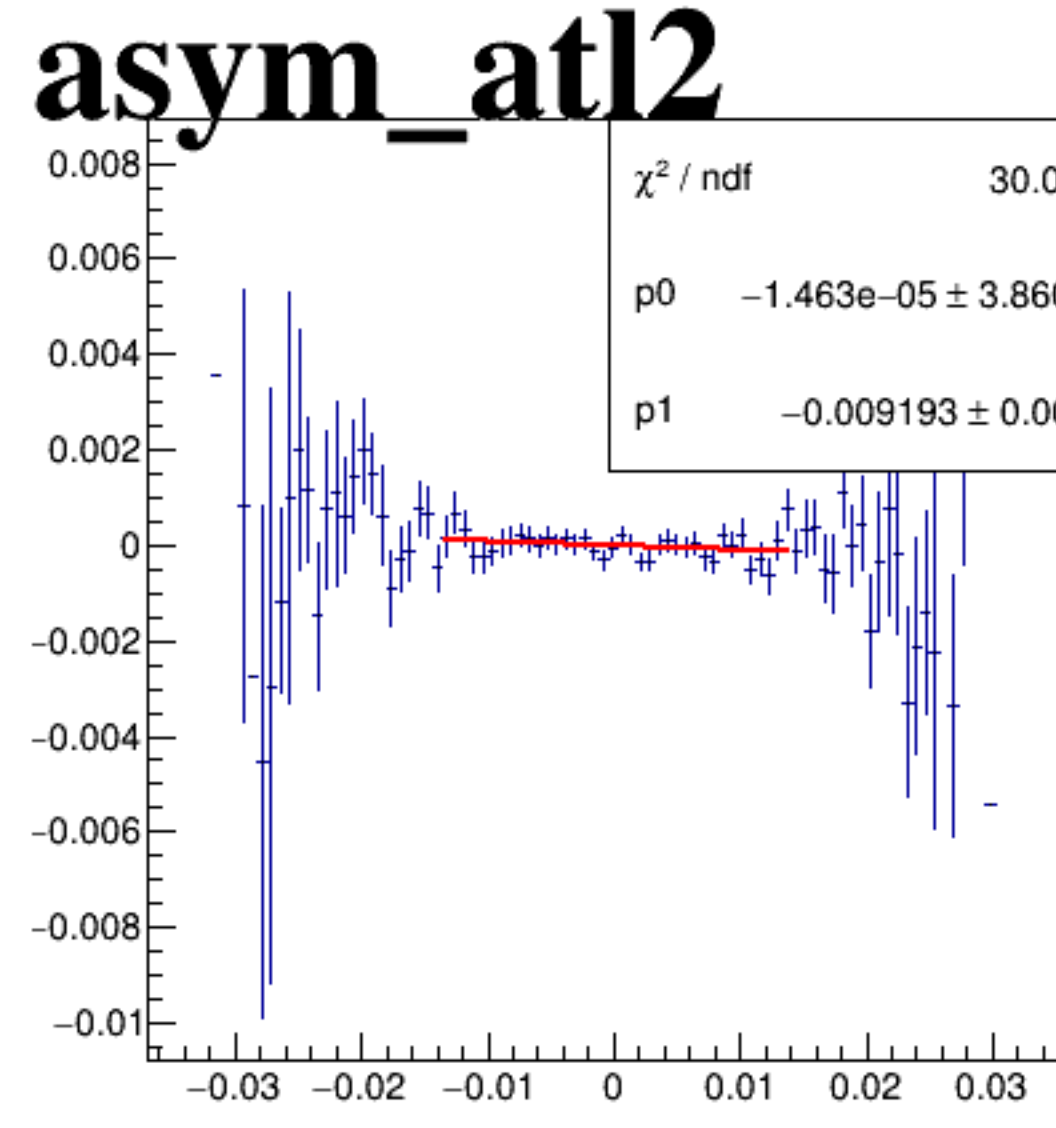
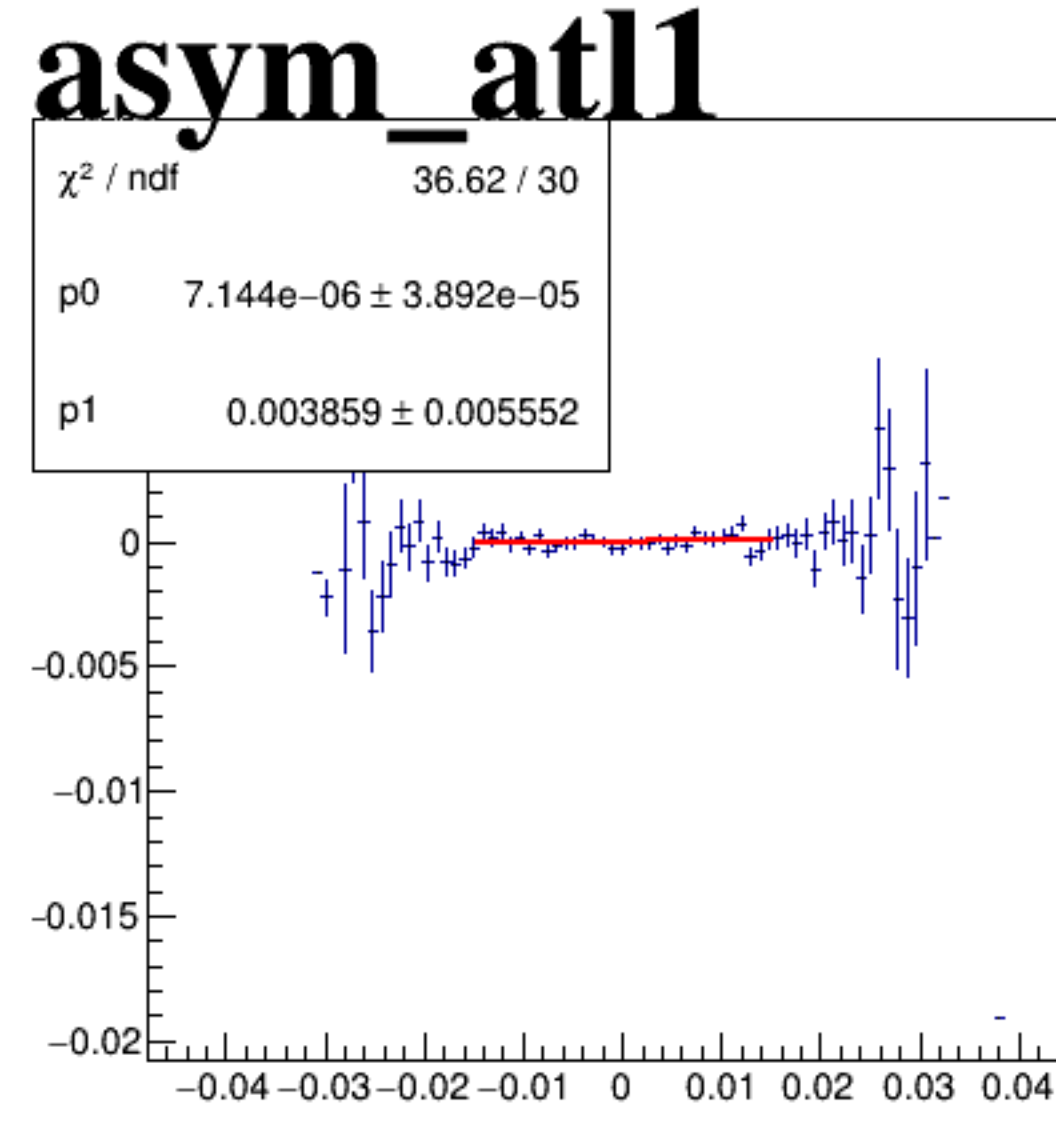
**asym\_atl1**



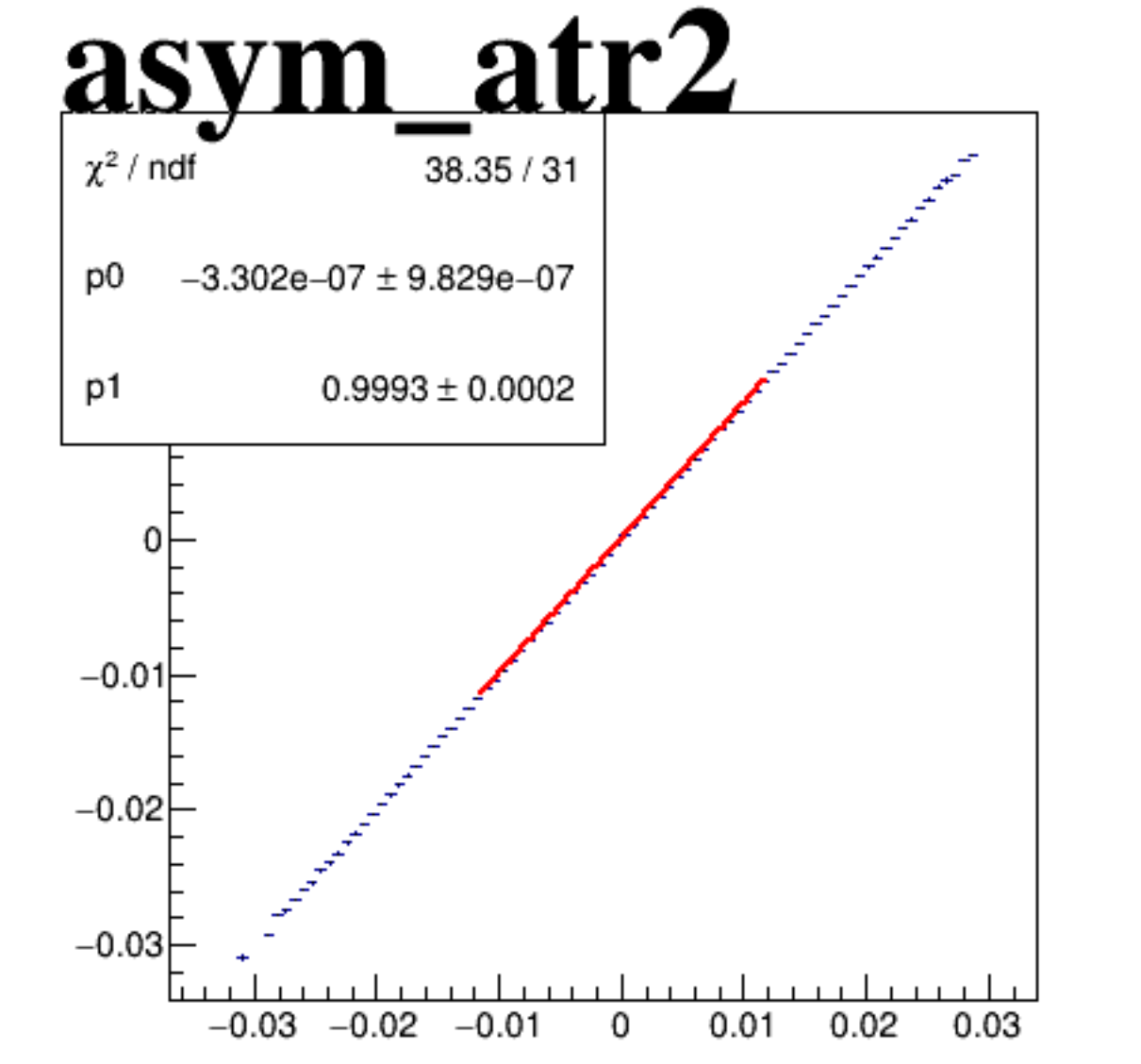
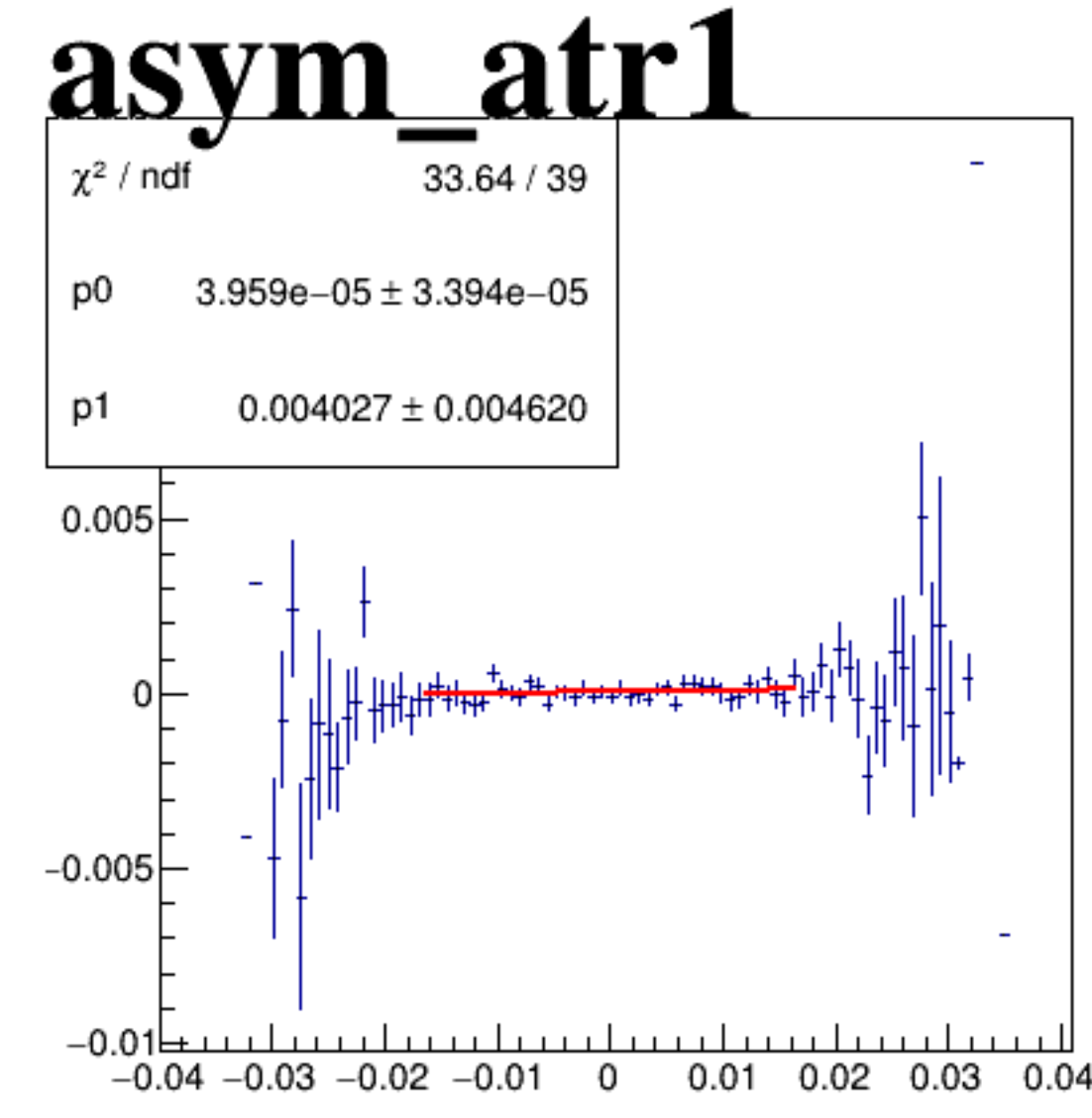
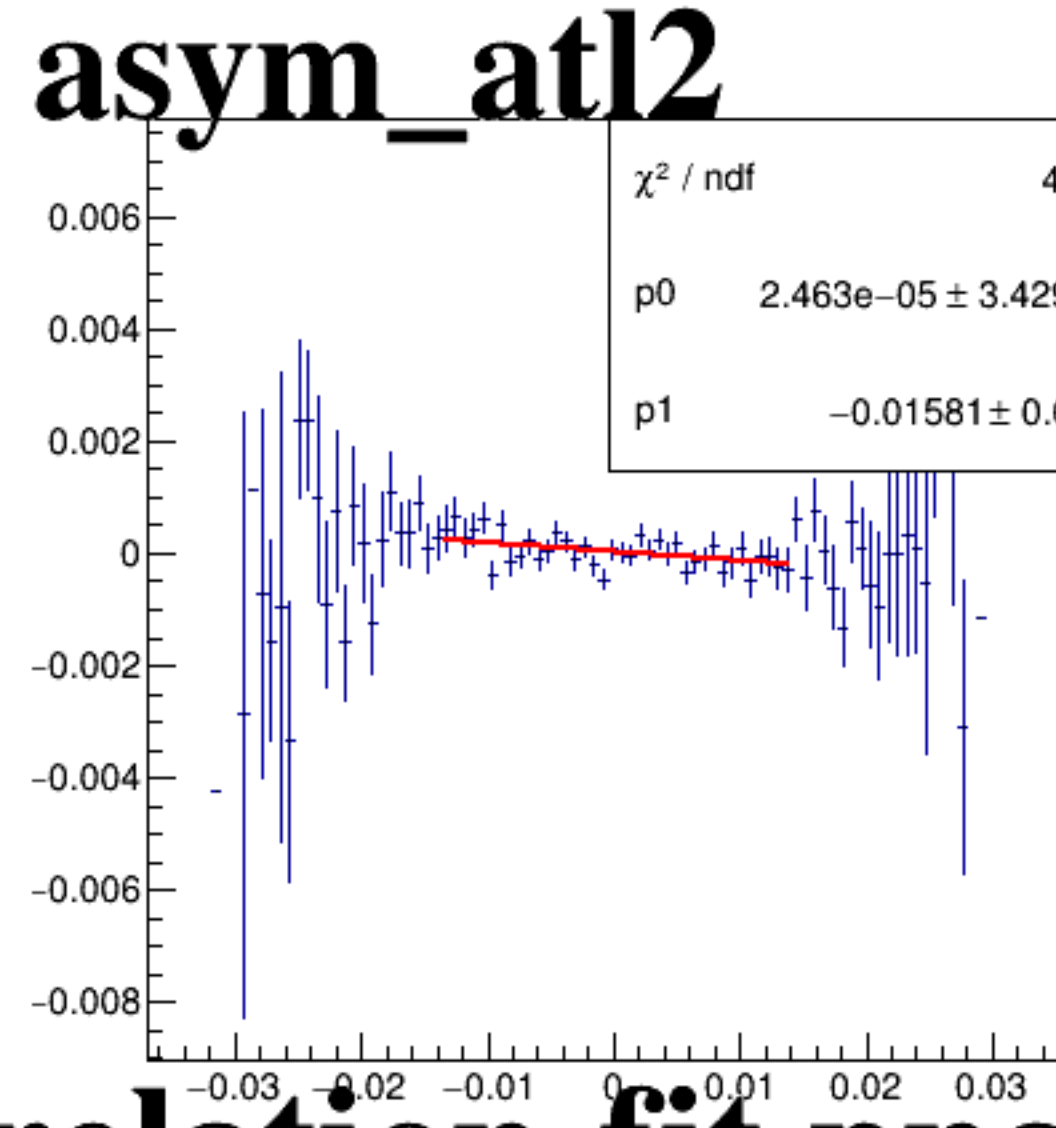
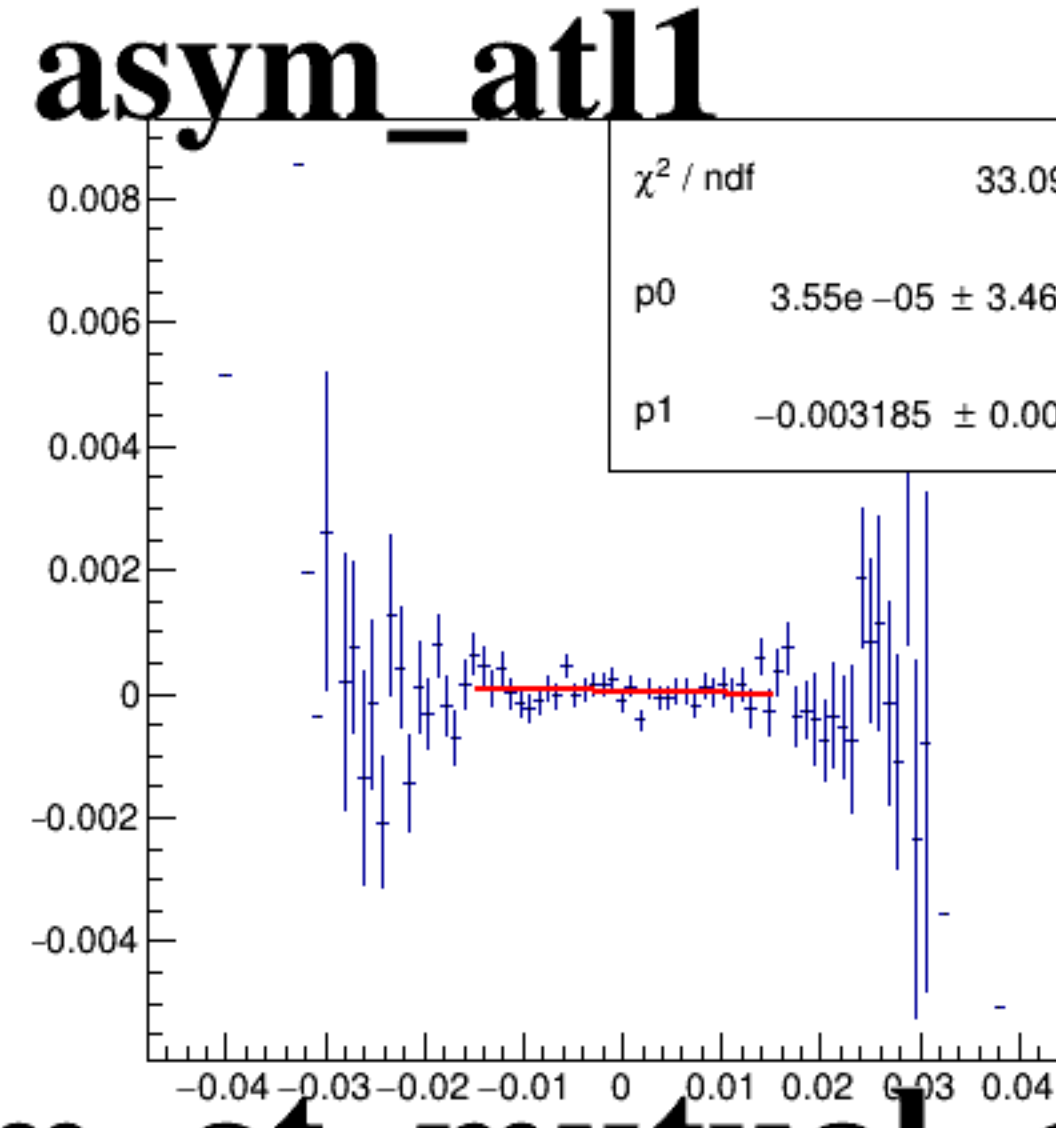
**asym\_atl2**



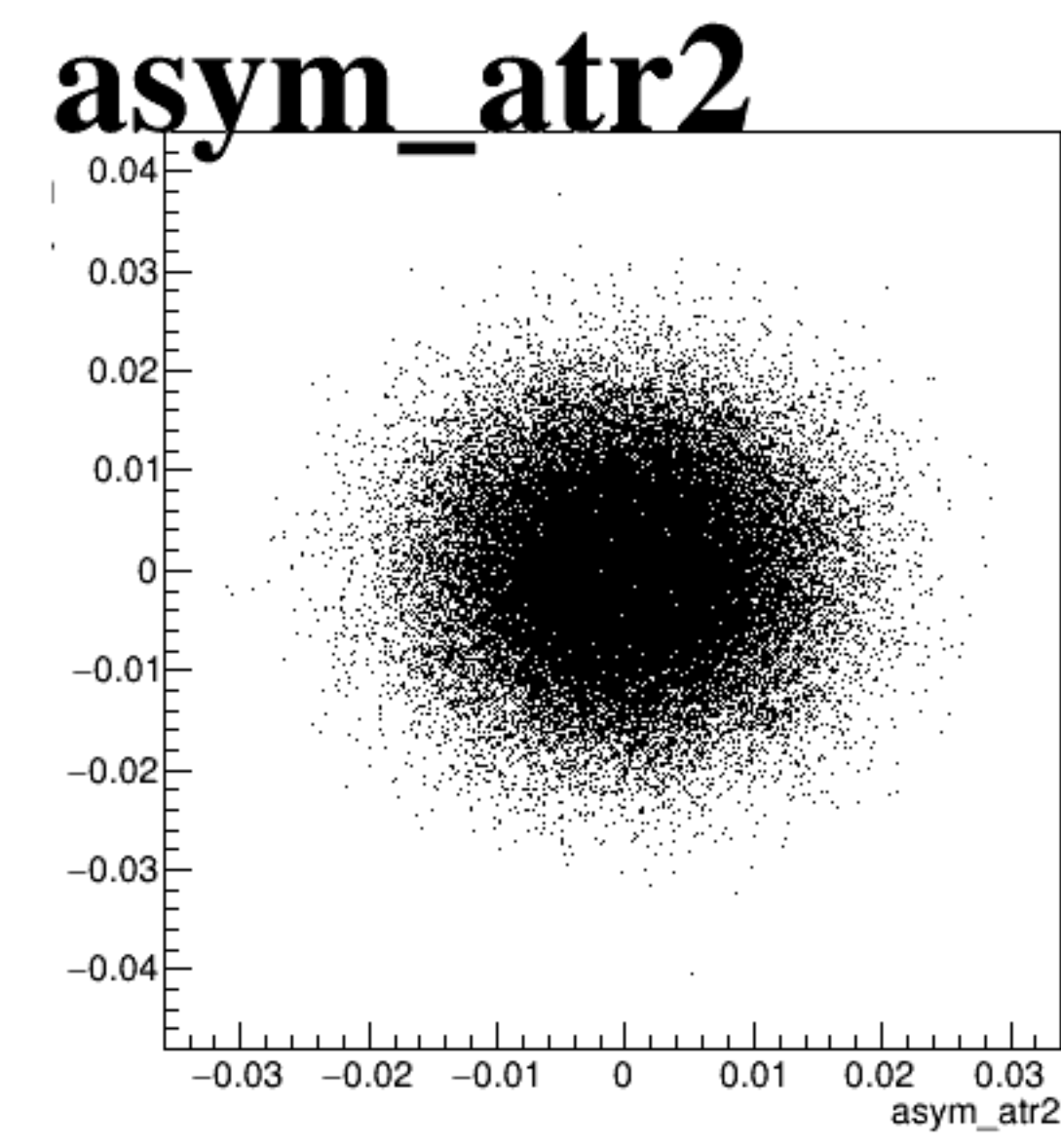
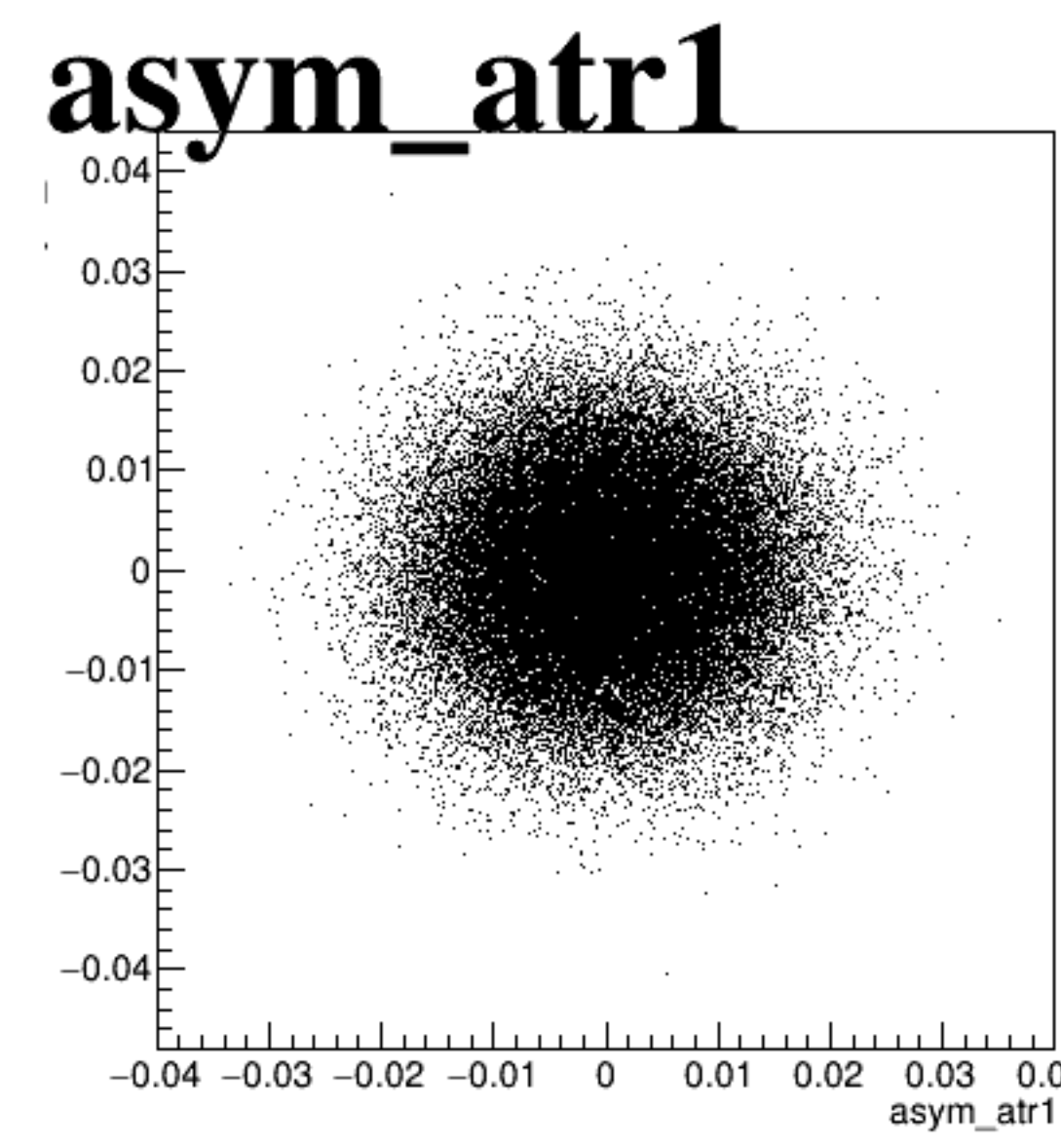
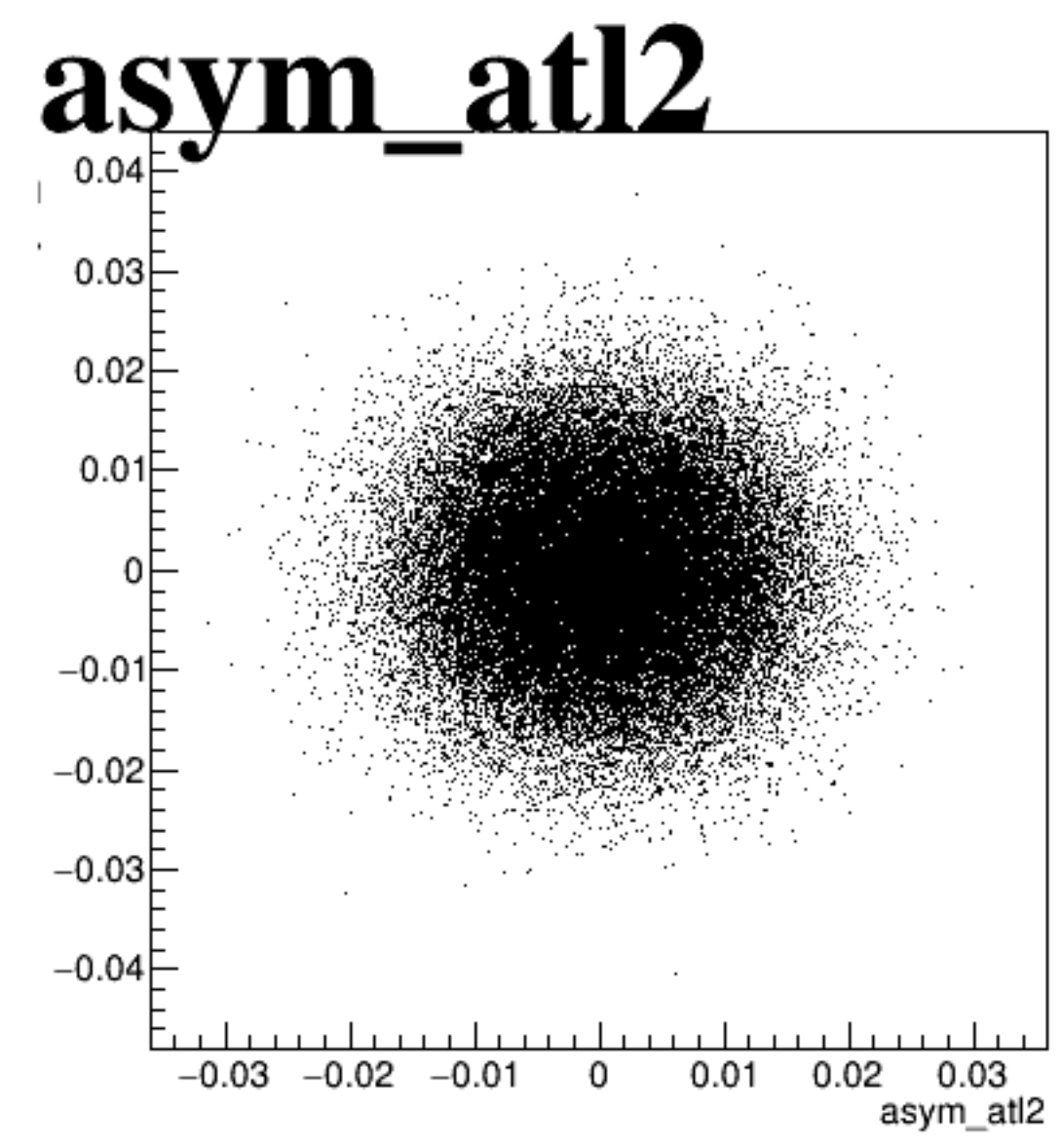
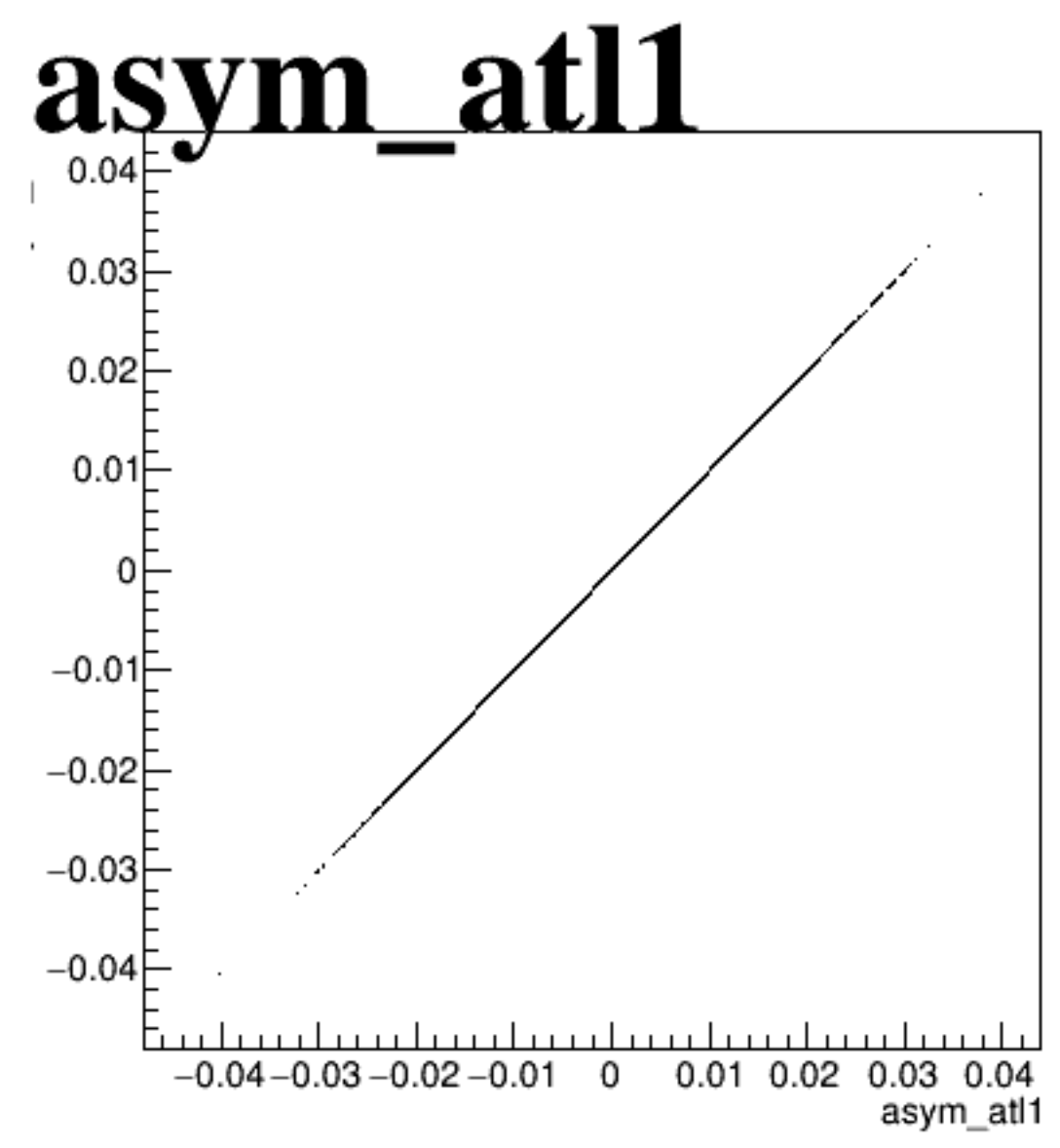
**asym\_atr1**



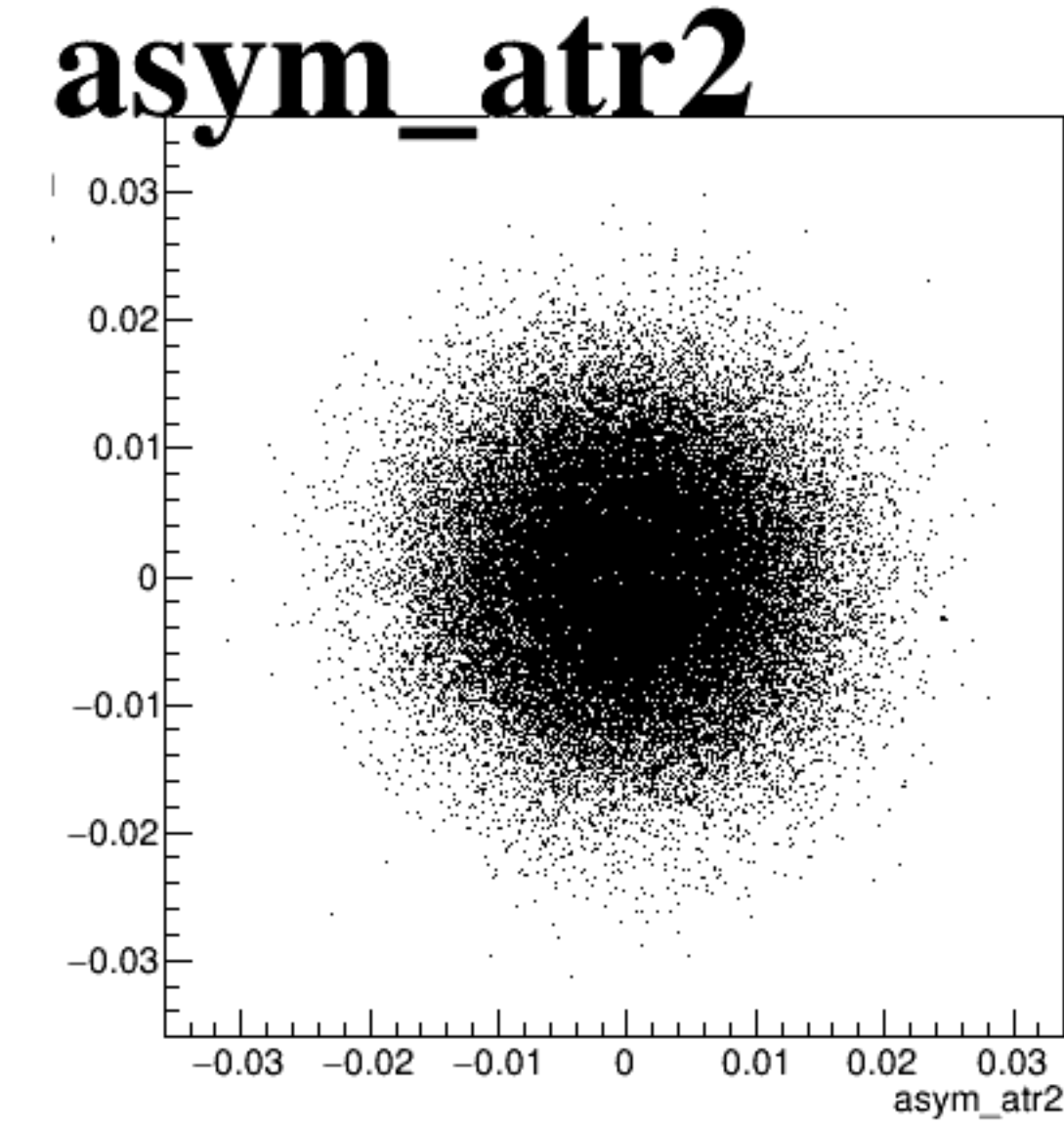
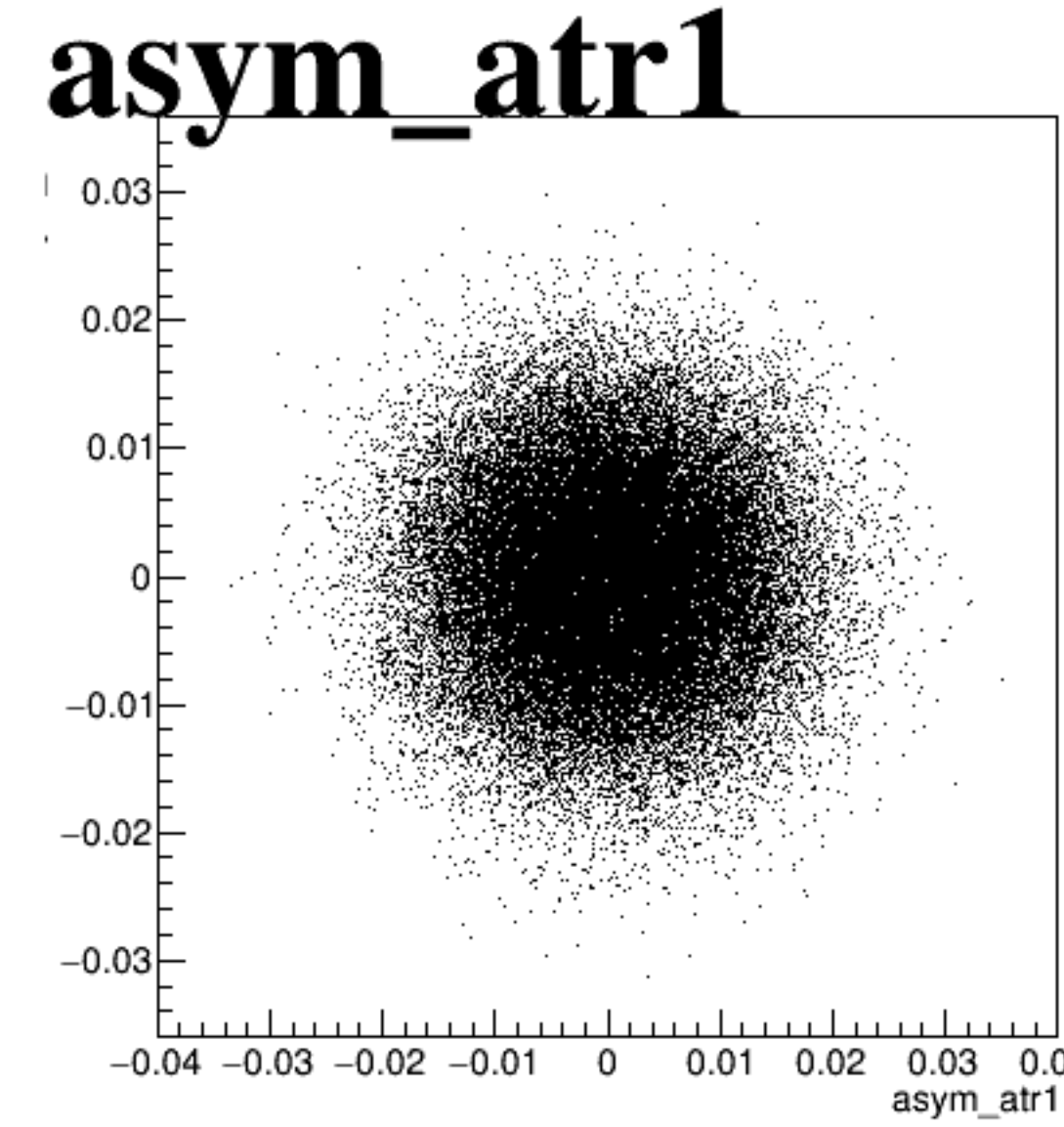
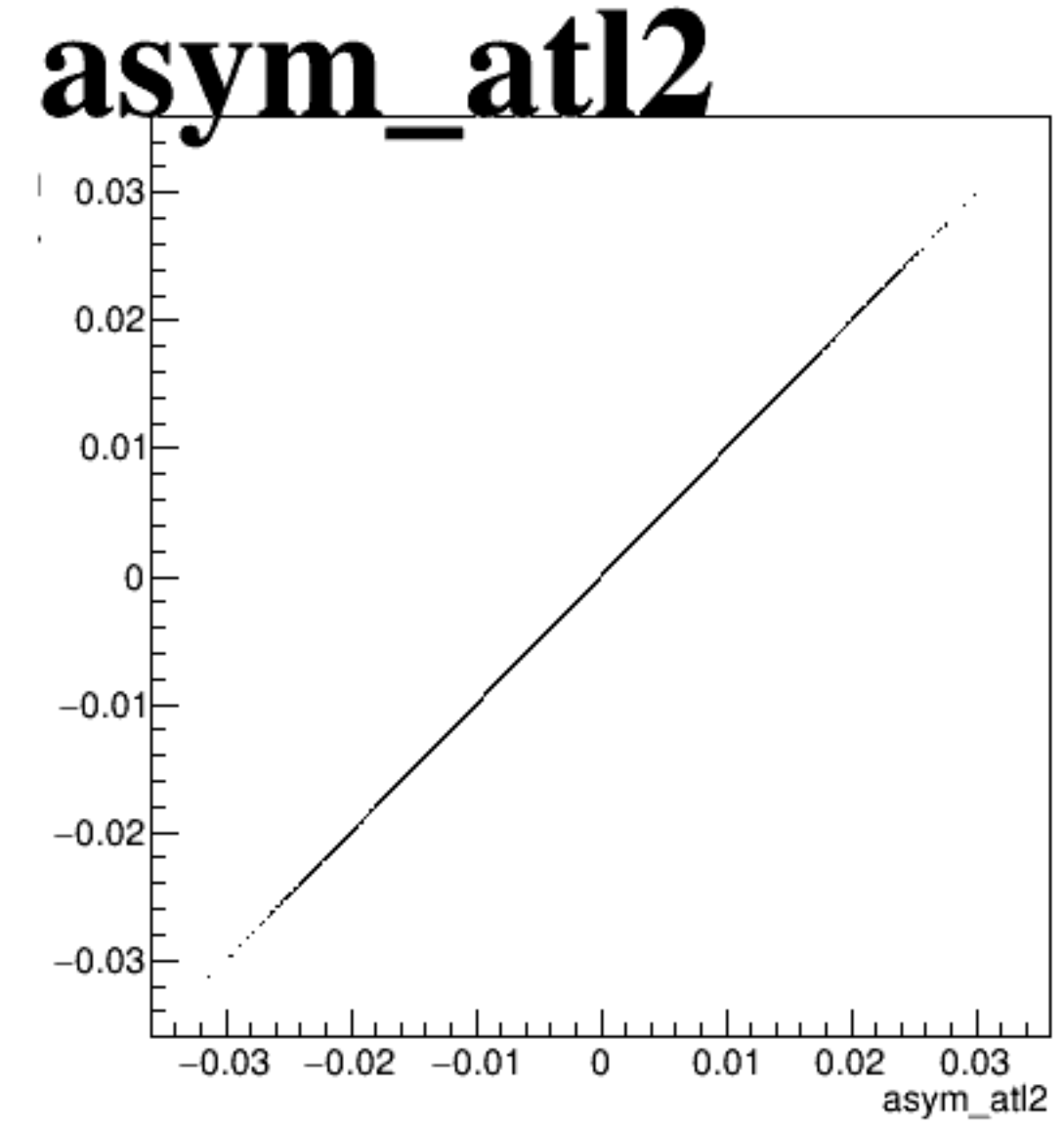
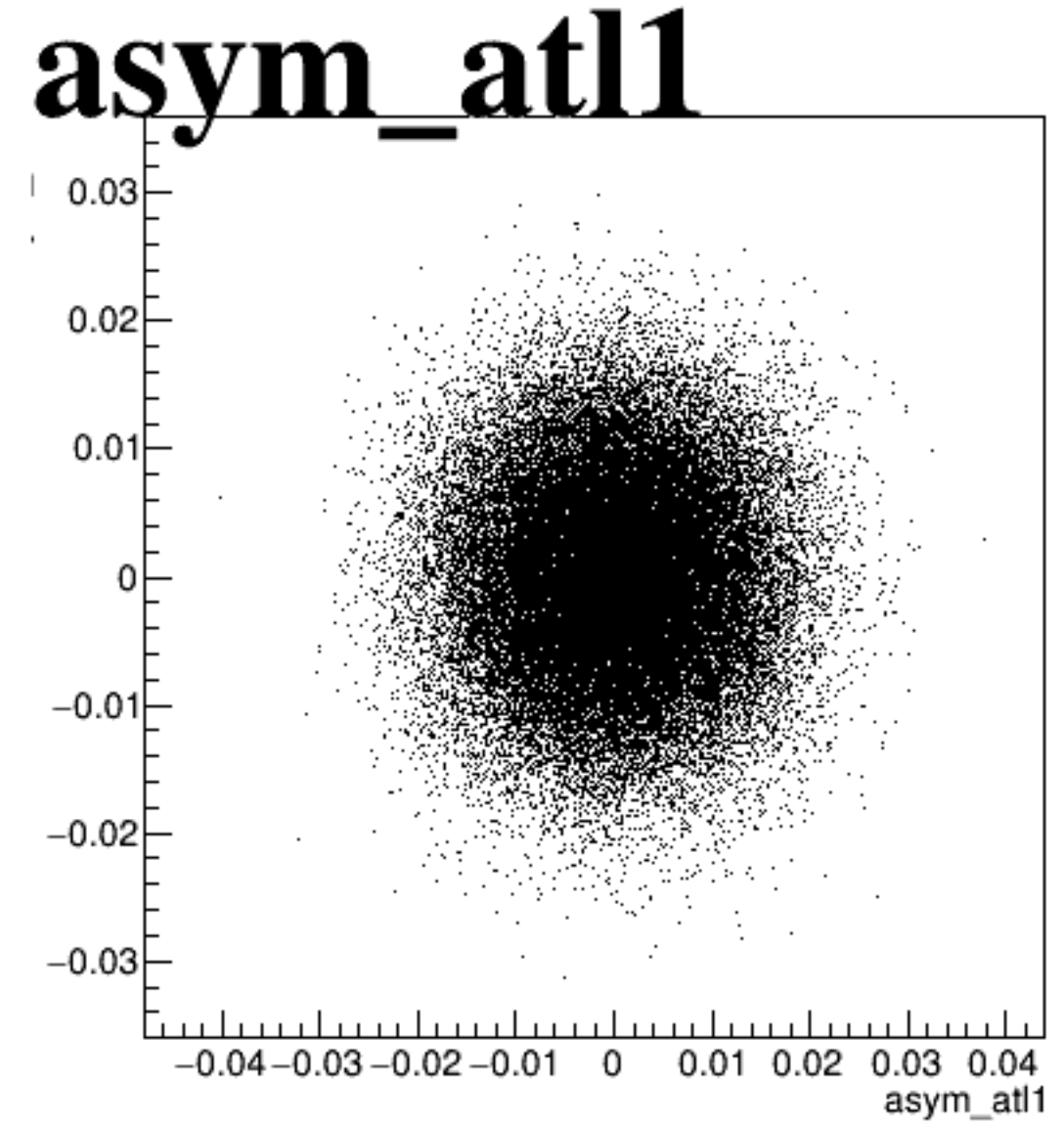
**asym\_atr2**



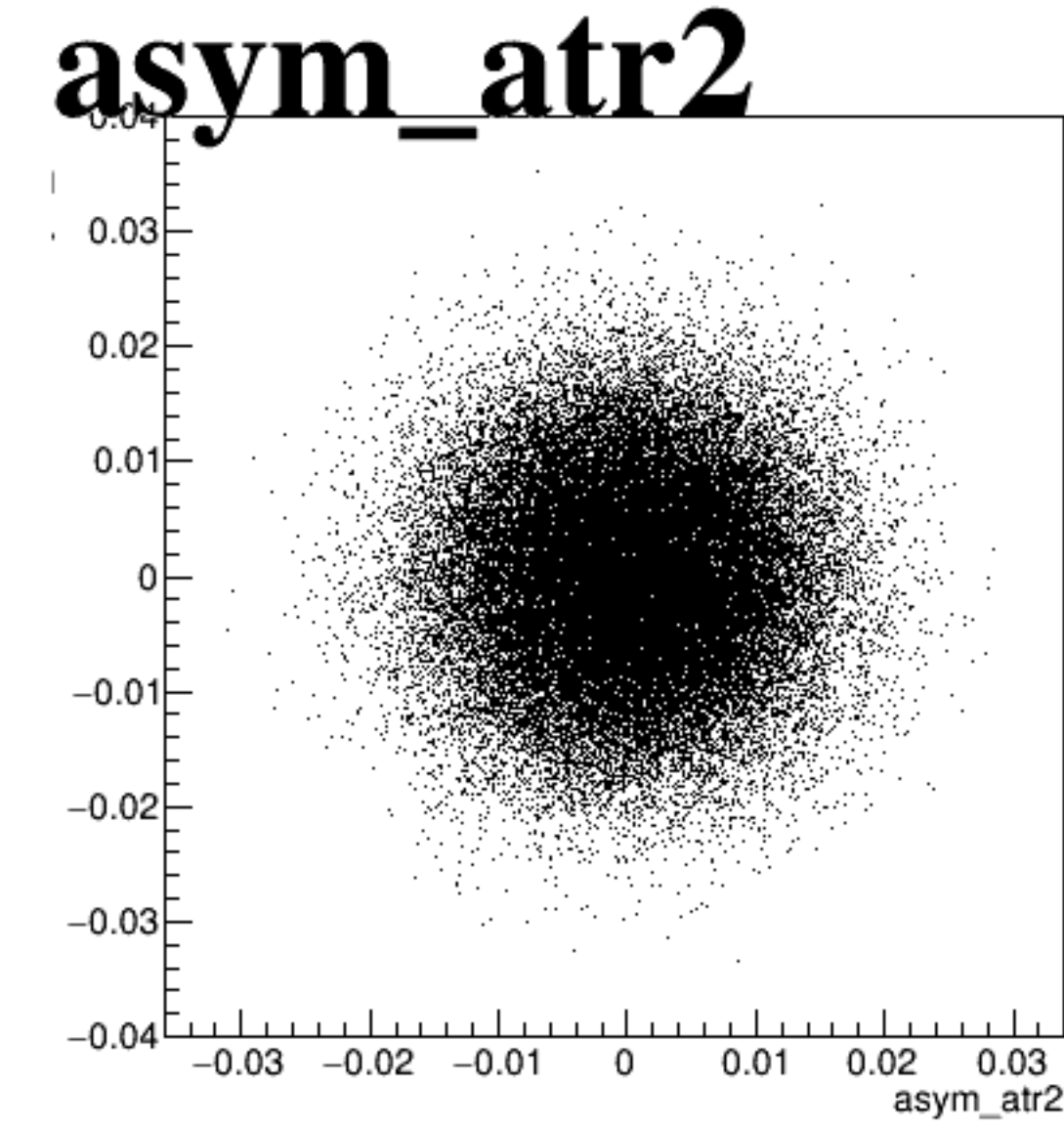
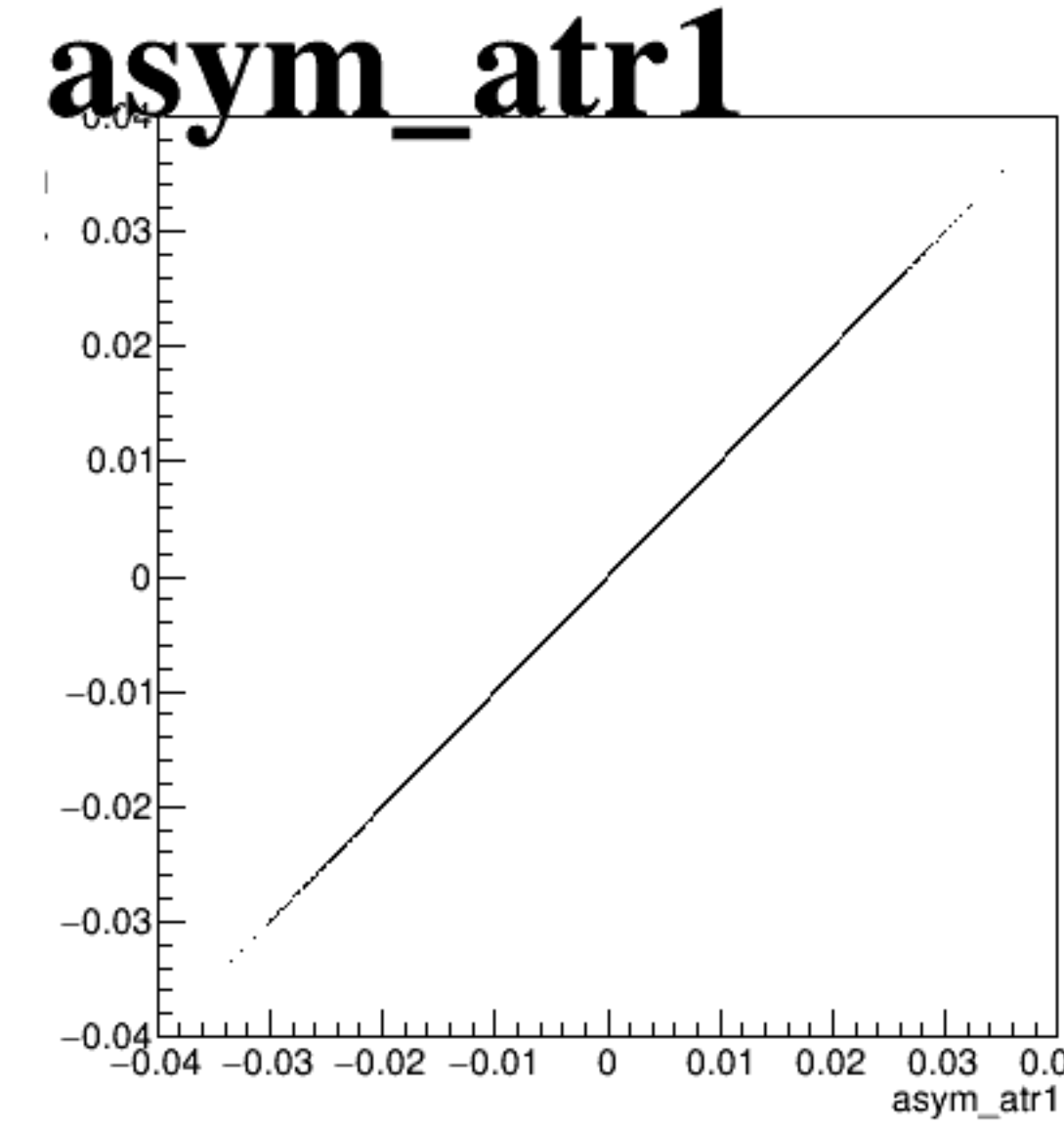
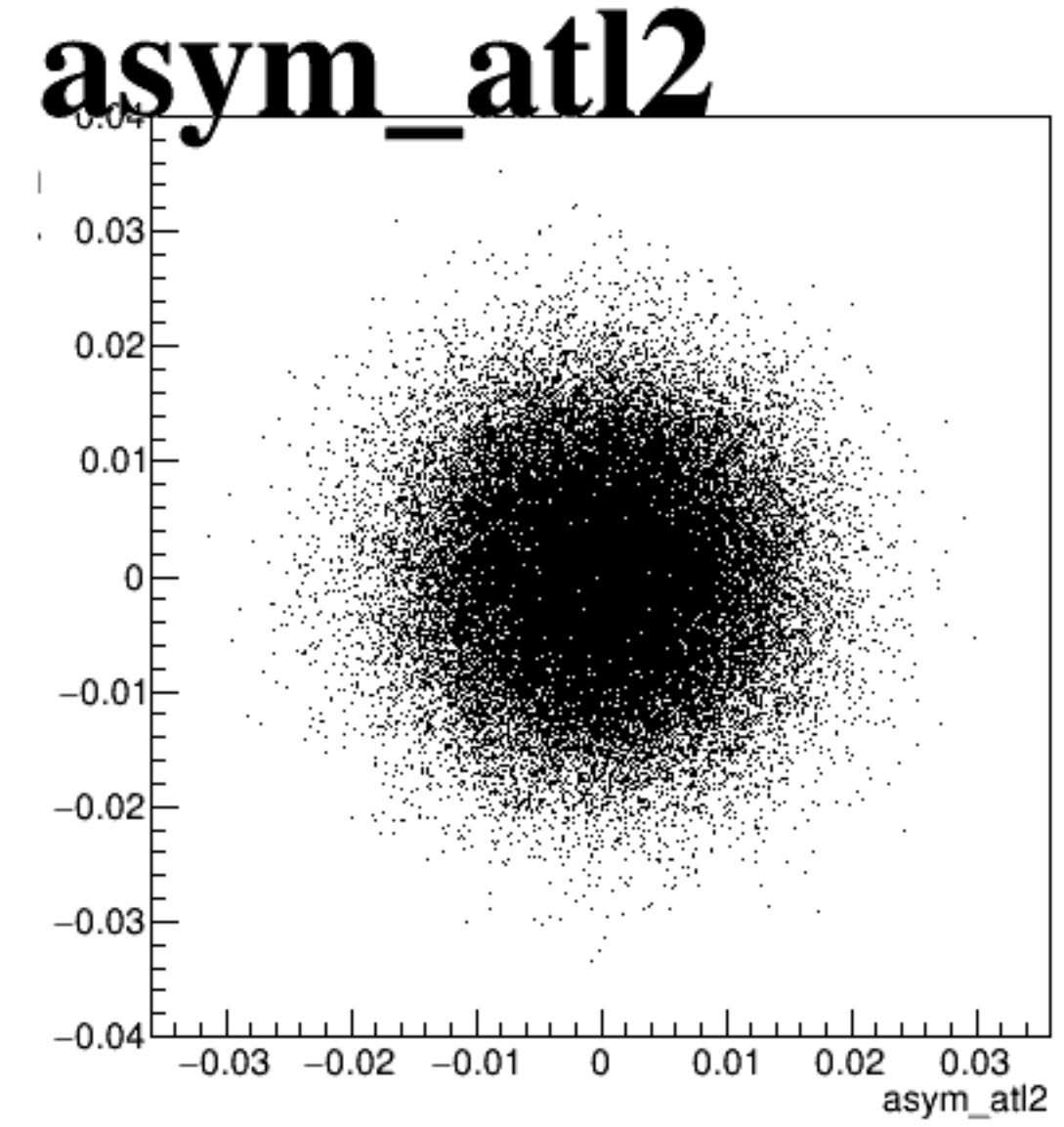
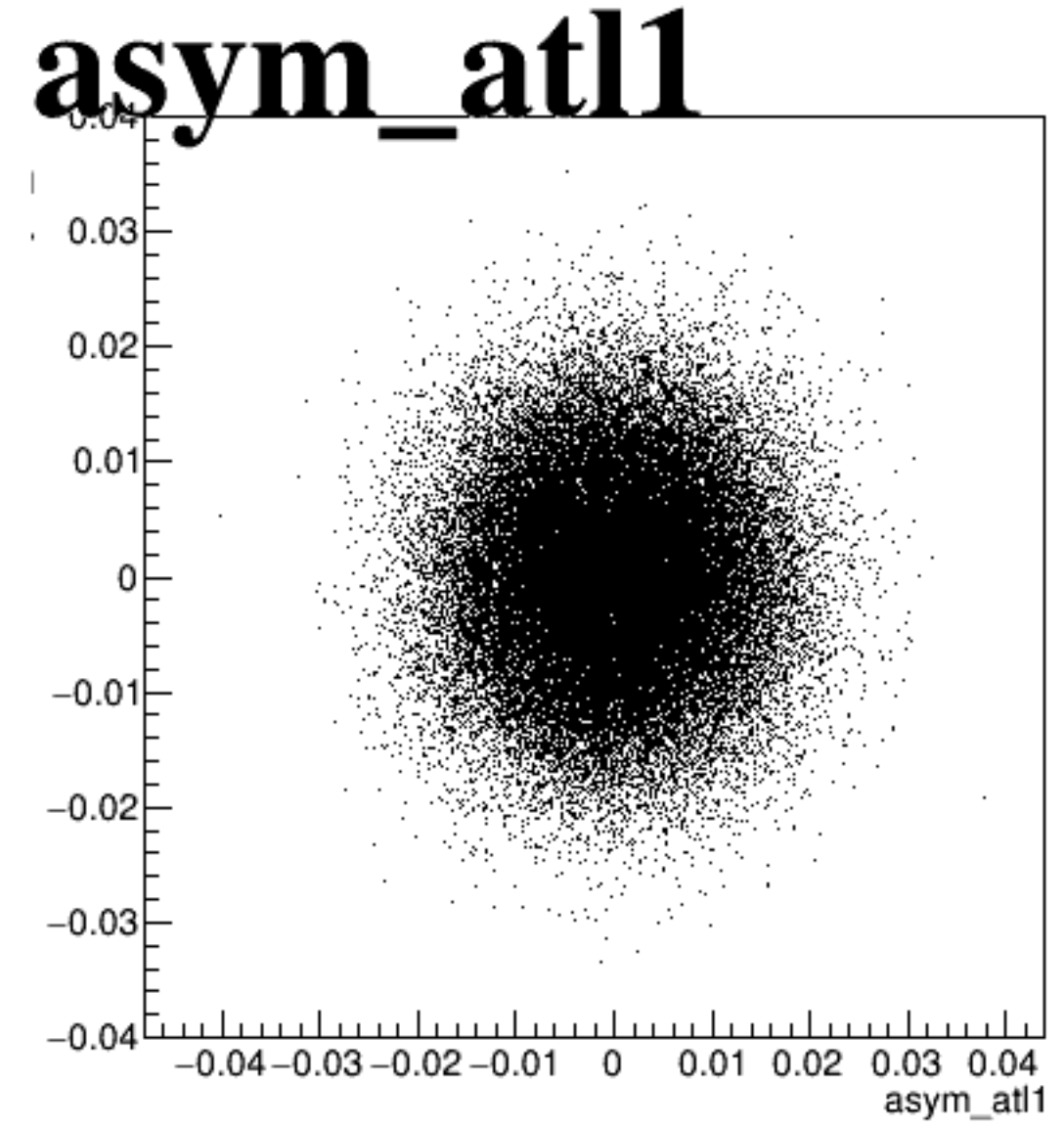
**asym\_atl1**



**asym\_atl2**



**asym\_atr1**



**asym\_atr2**

