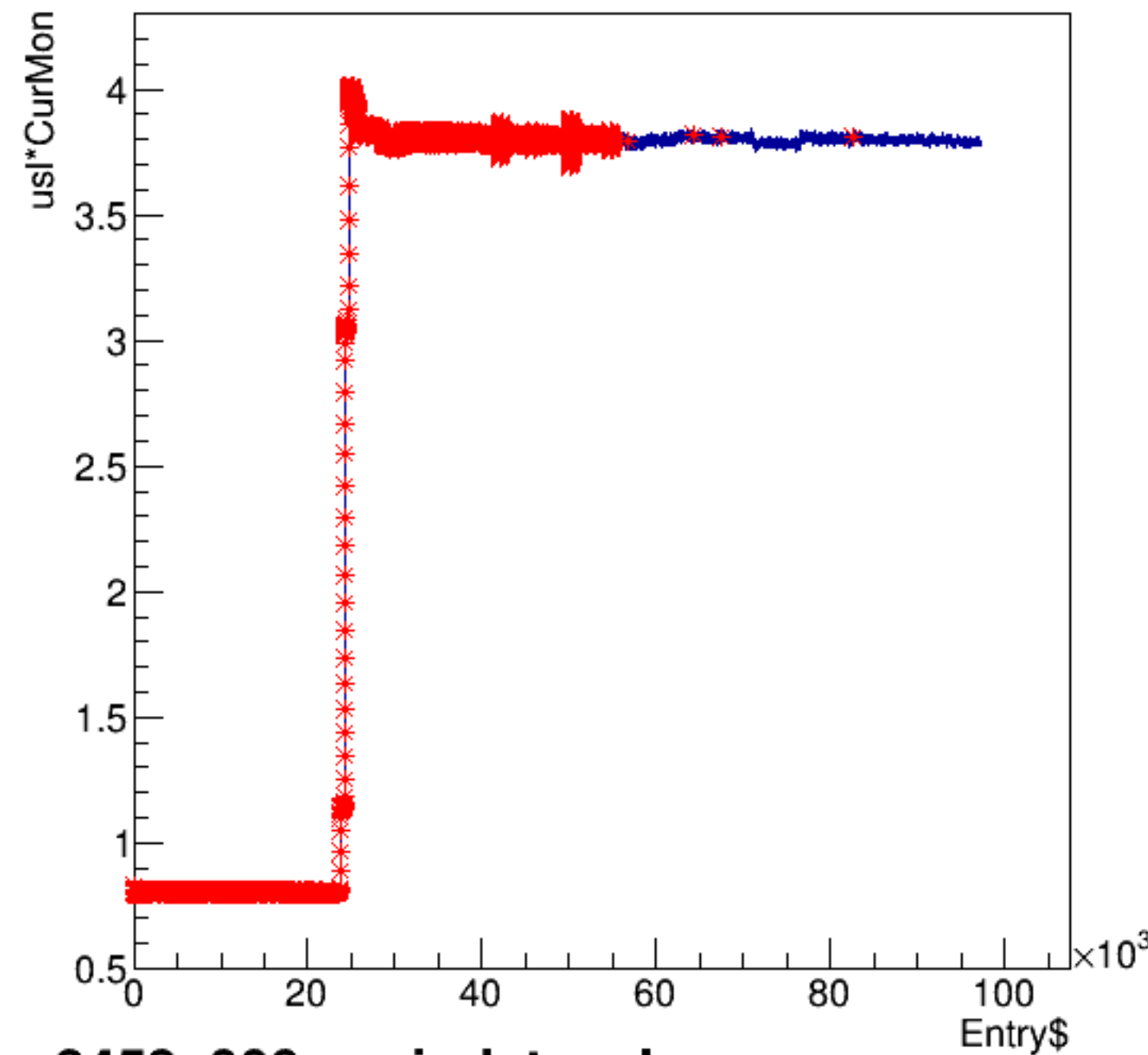
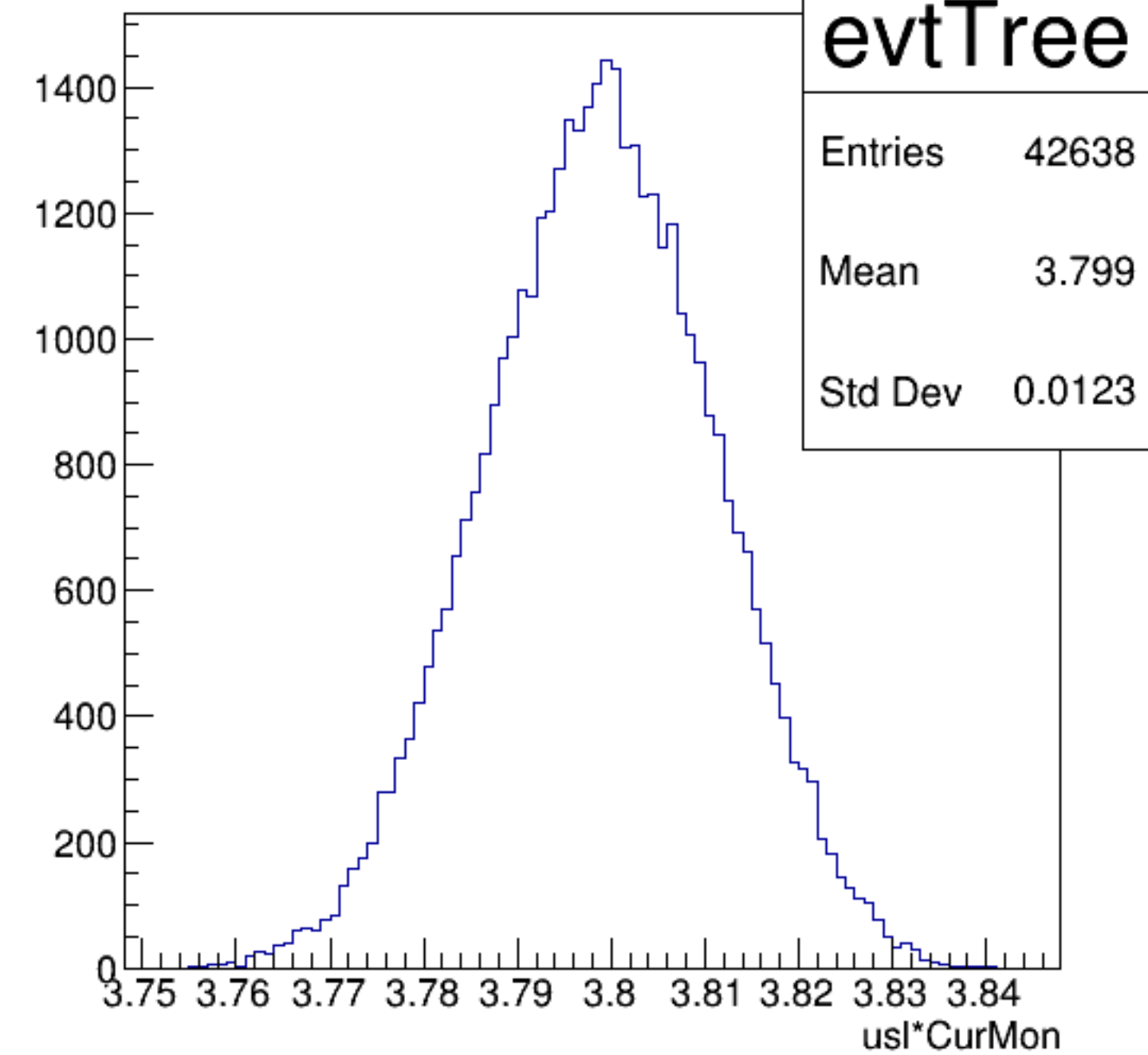


usl*CurMon:Entry\$

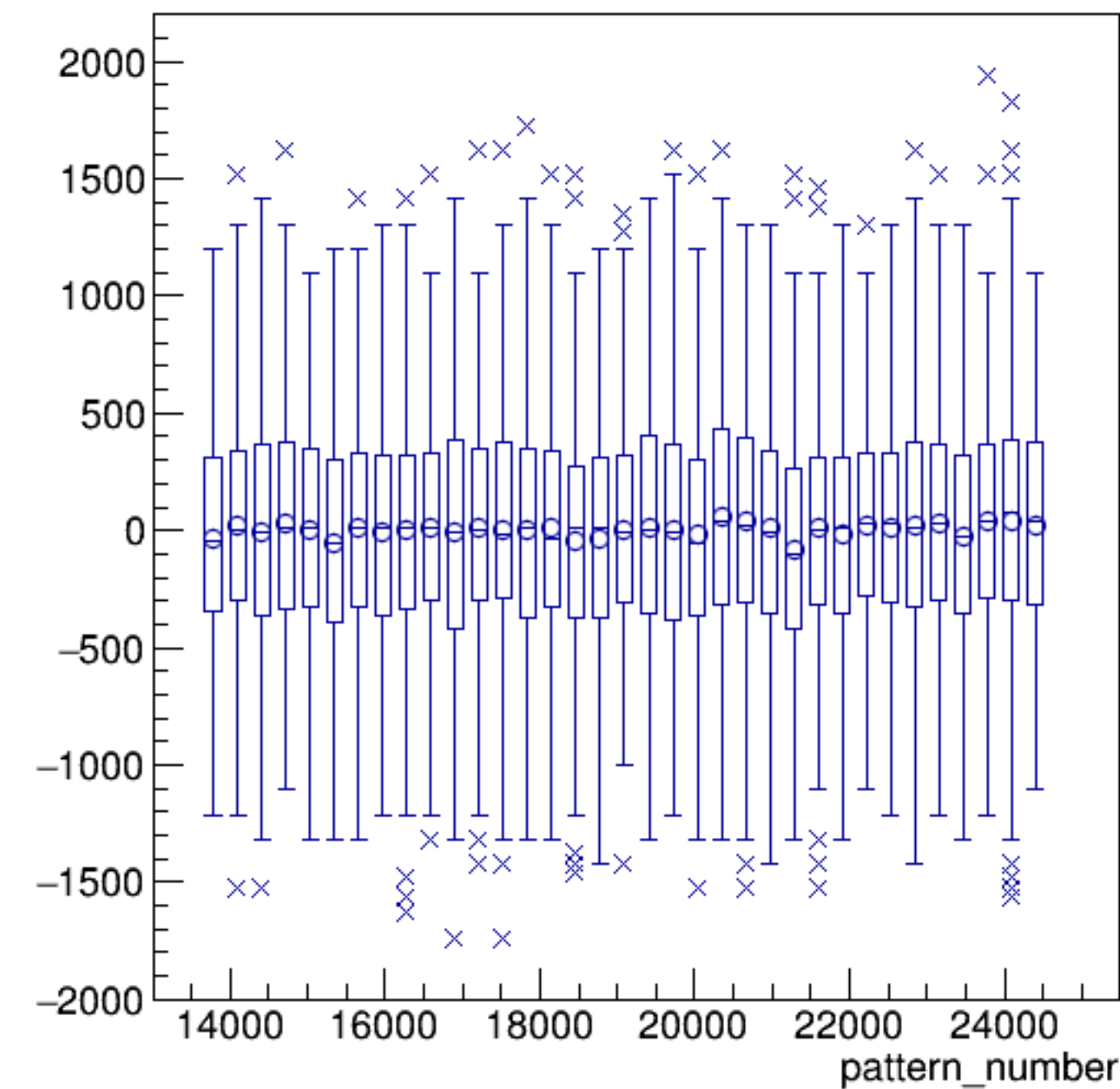


run3453_000_maindet_usl.png

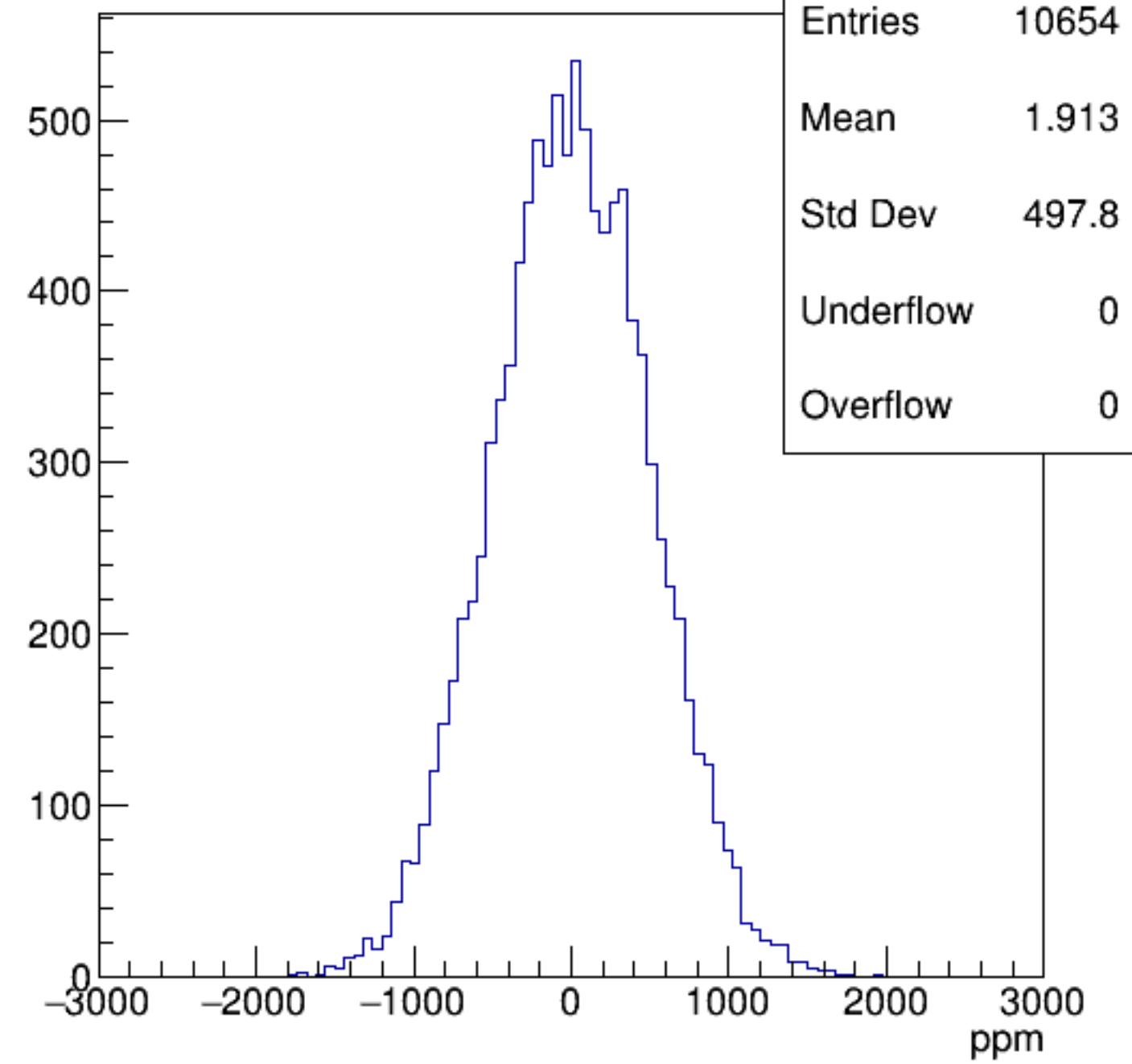
usl*CurMon {ErrorFlag==0}



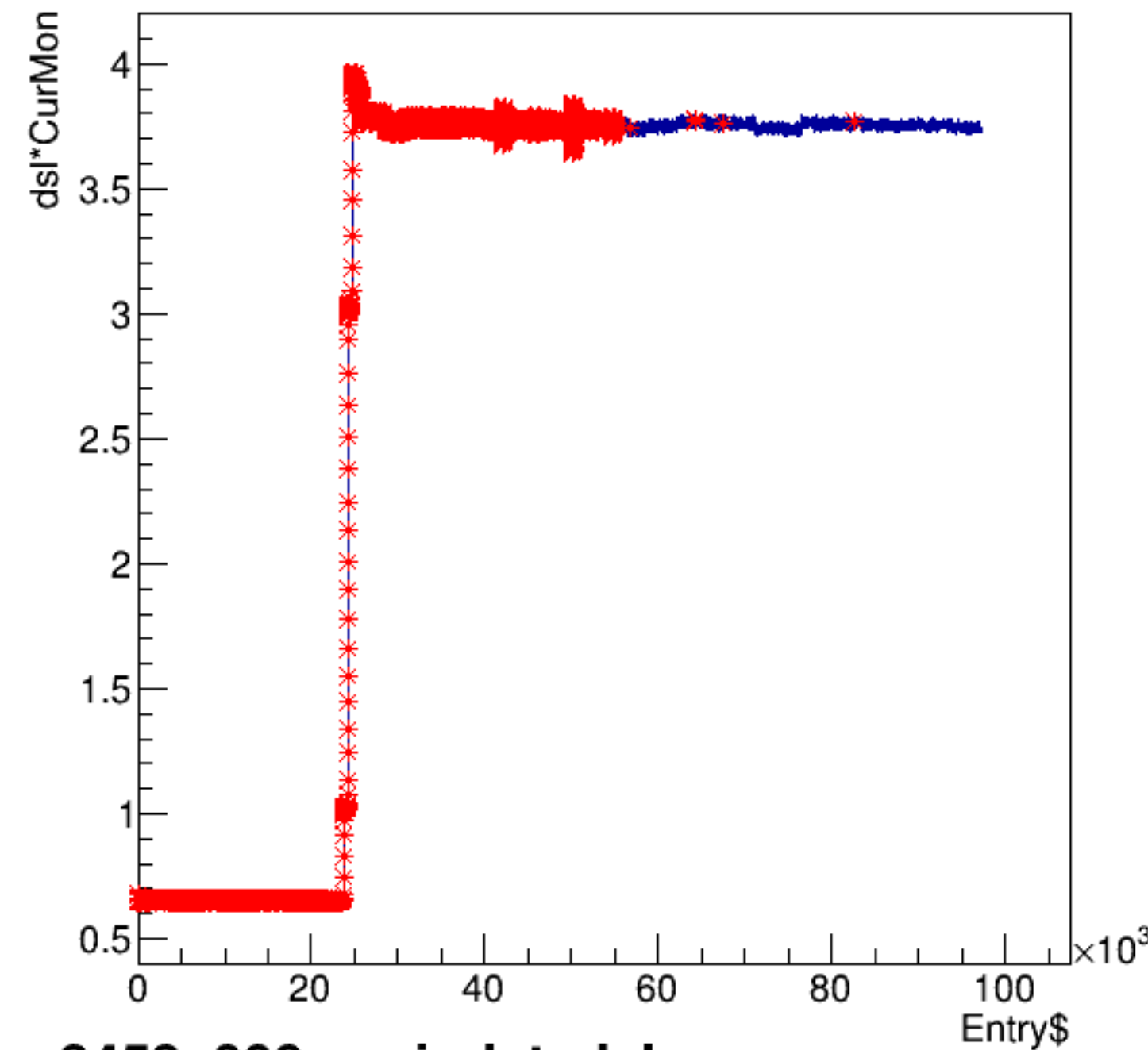
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl

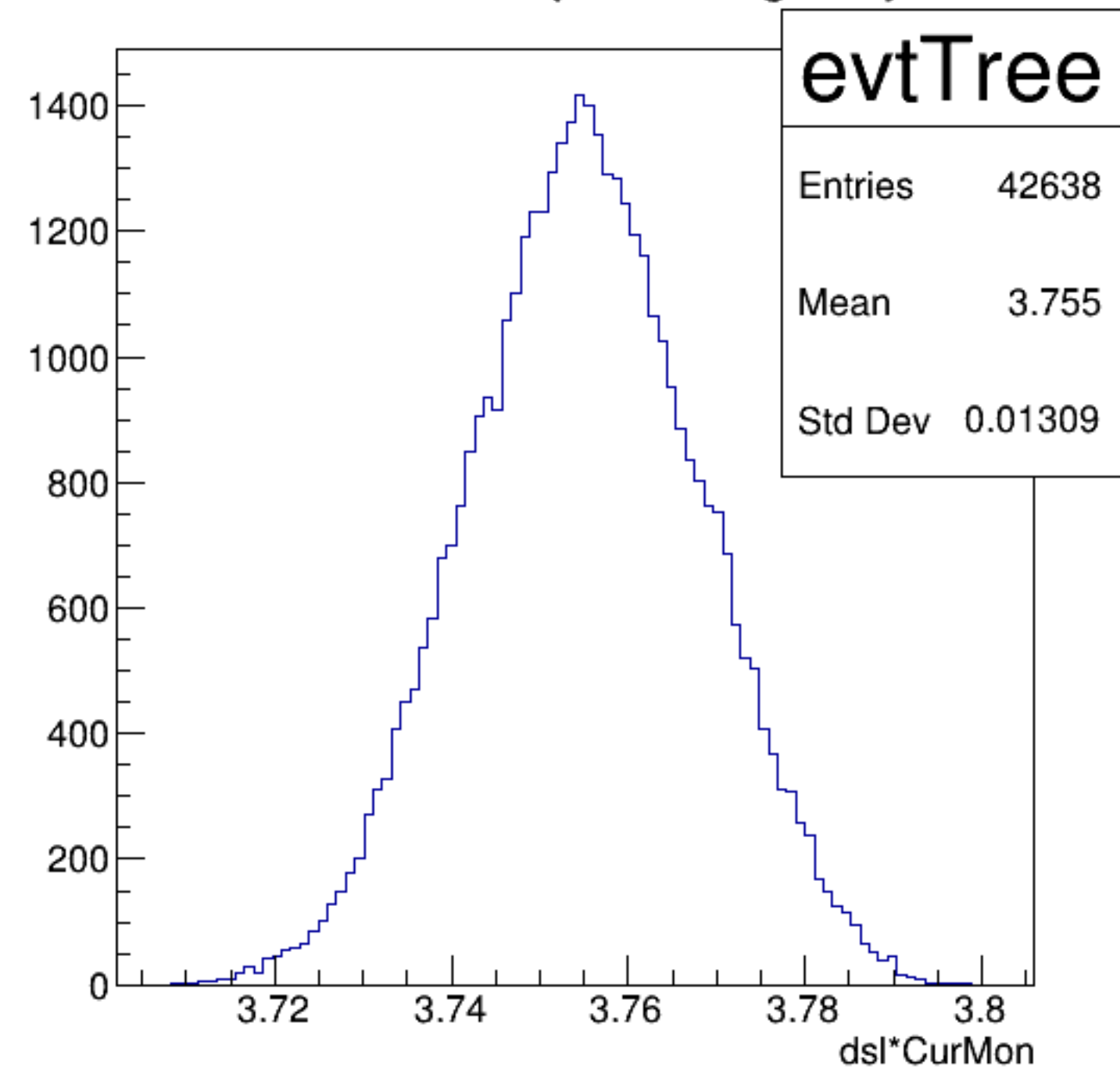


dsl*CurMon:Entry\$

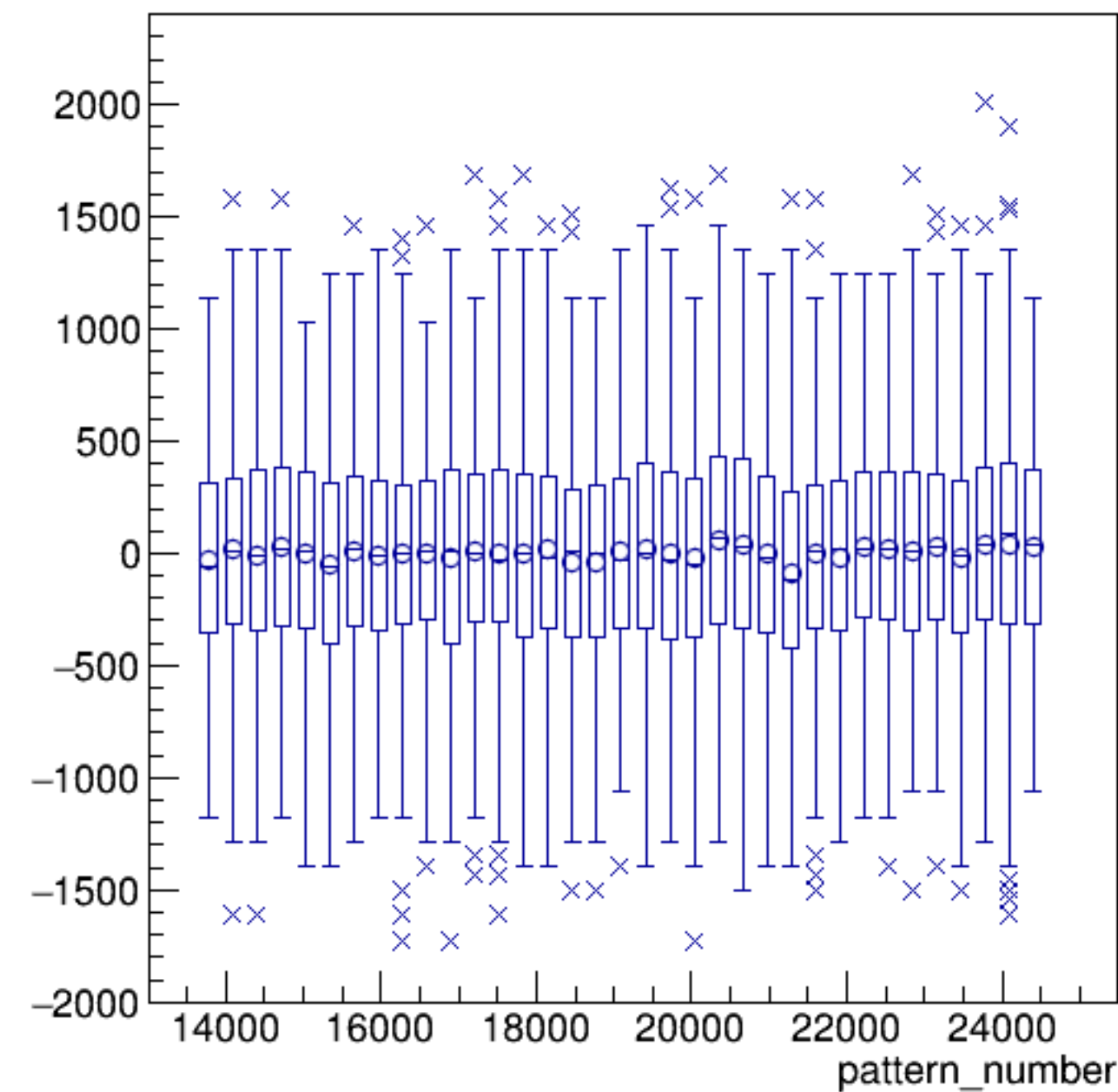


run3453_000_maindet_dsl.png

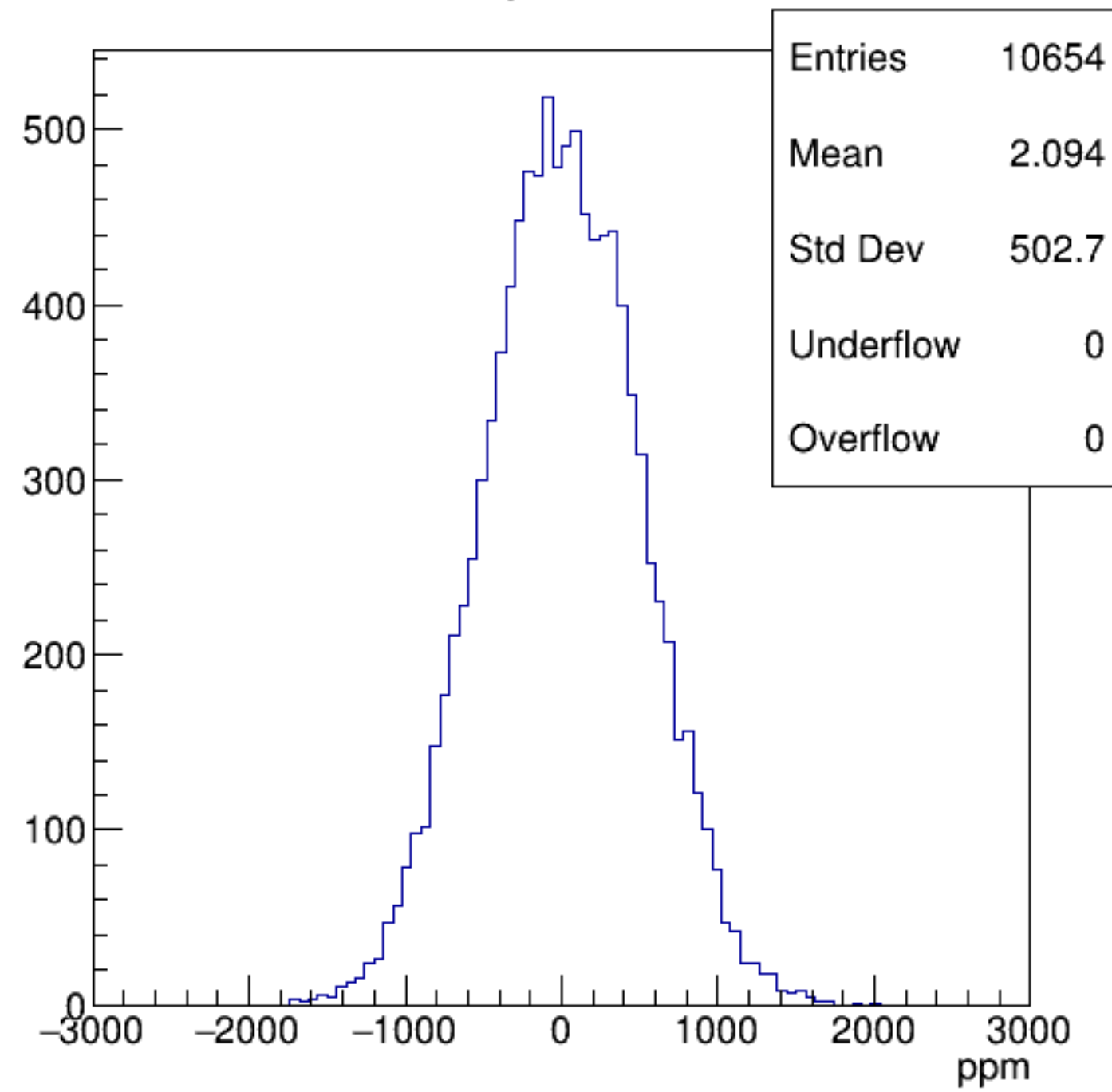
dsl*CurMon {ErrorFlag==0}



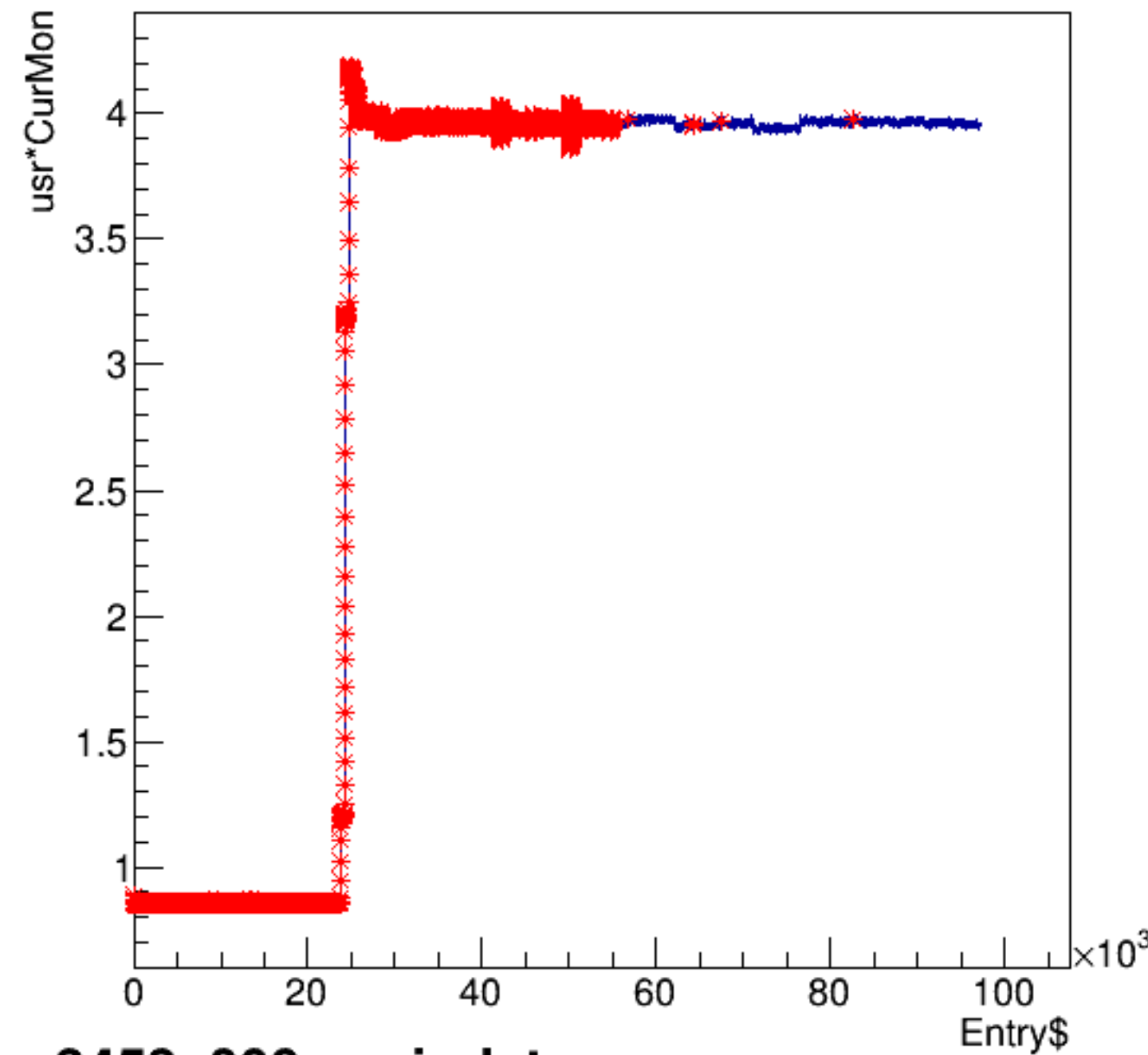
asym_dsl/ppm:pattern_number {ErrorFlag==0}



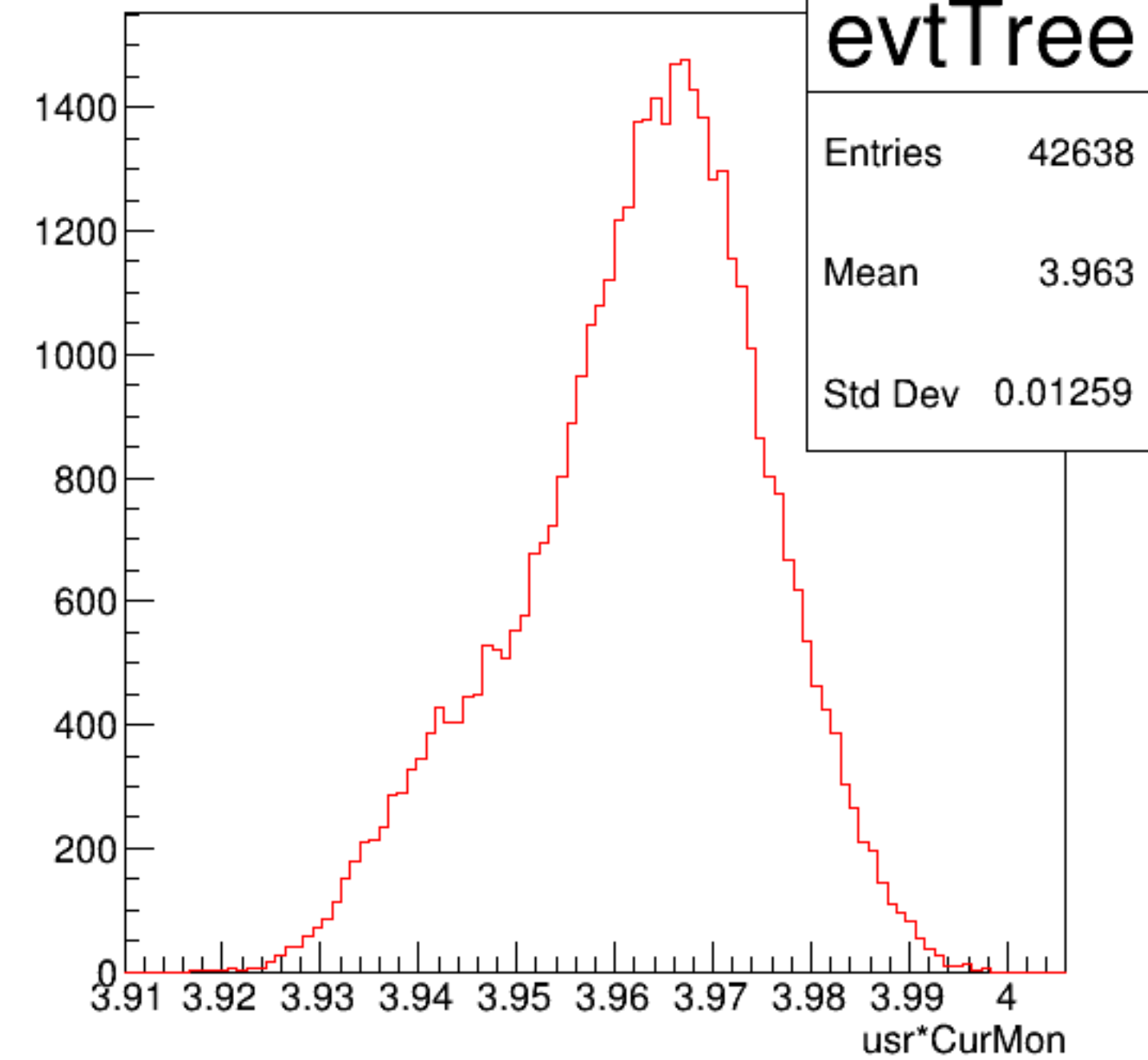
asym_dsl



usr*CurMon:Entry\$

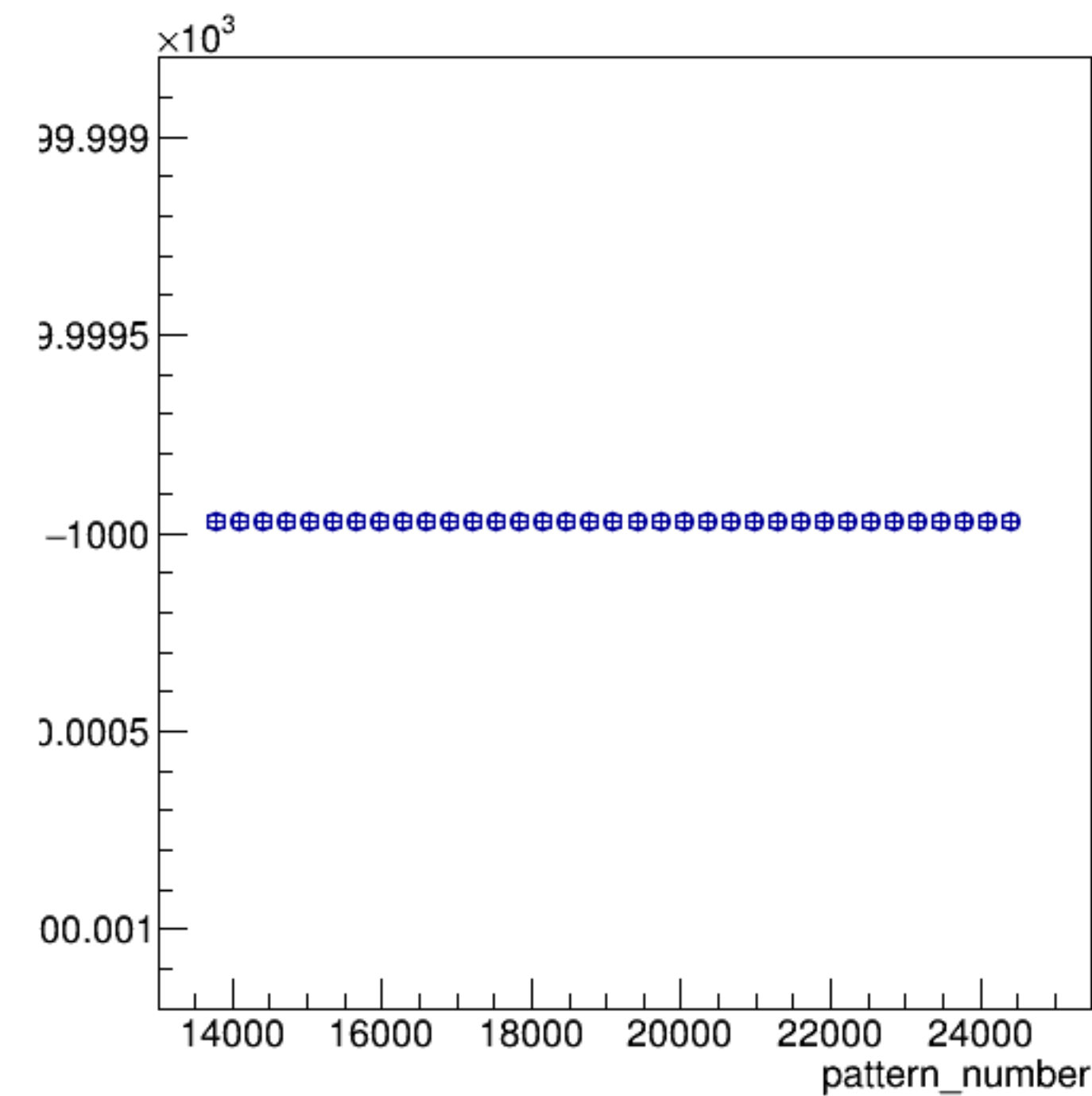


usr*CurMon {ErrorFlag==0}

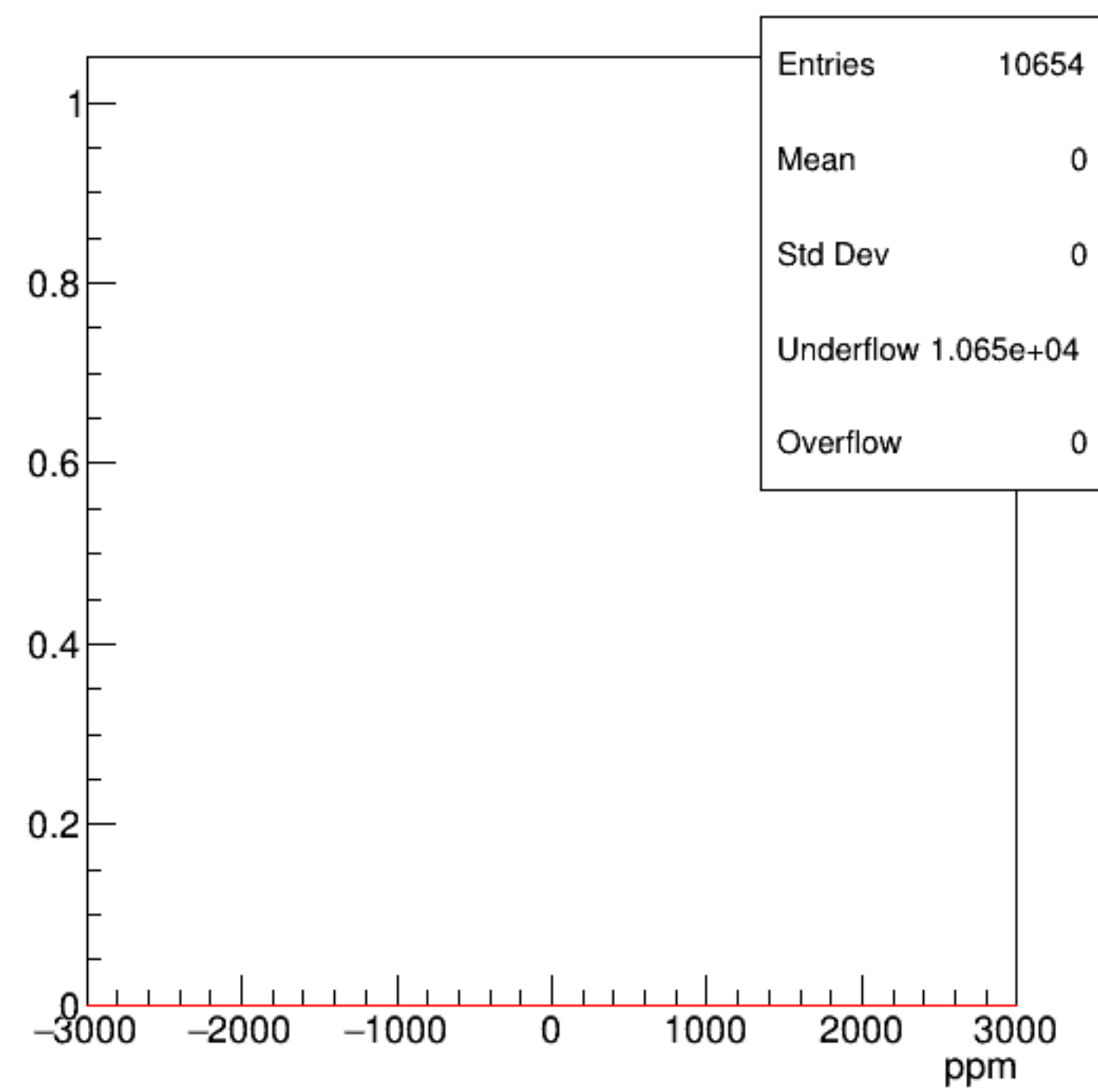


evtTree	
Entries	42638
Mean	3.963
Std Dev	0.01259

asym_usr/ppm:pattern_number {ErrorFlag==0}

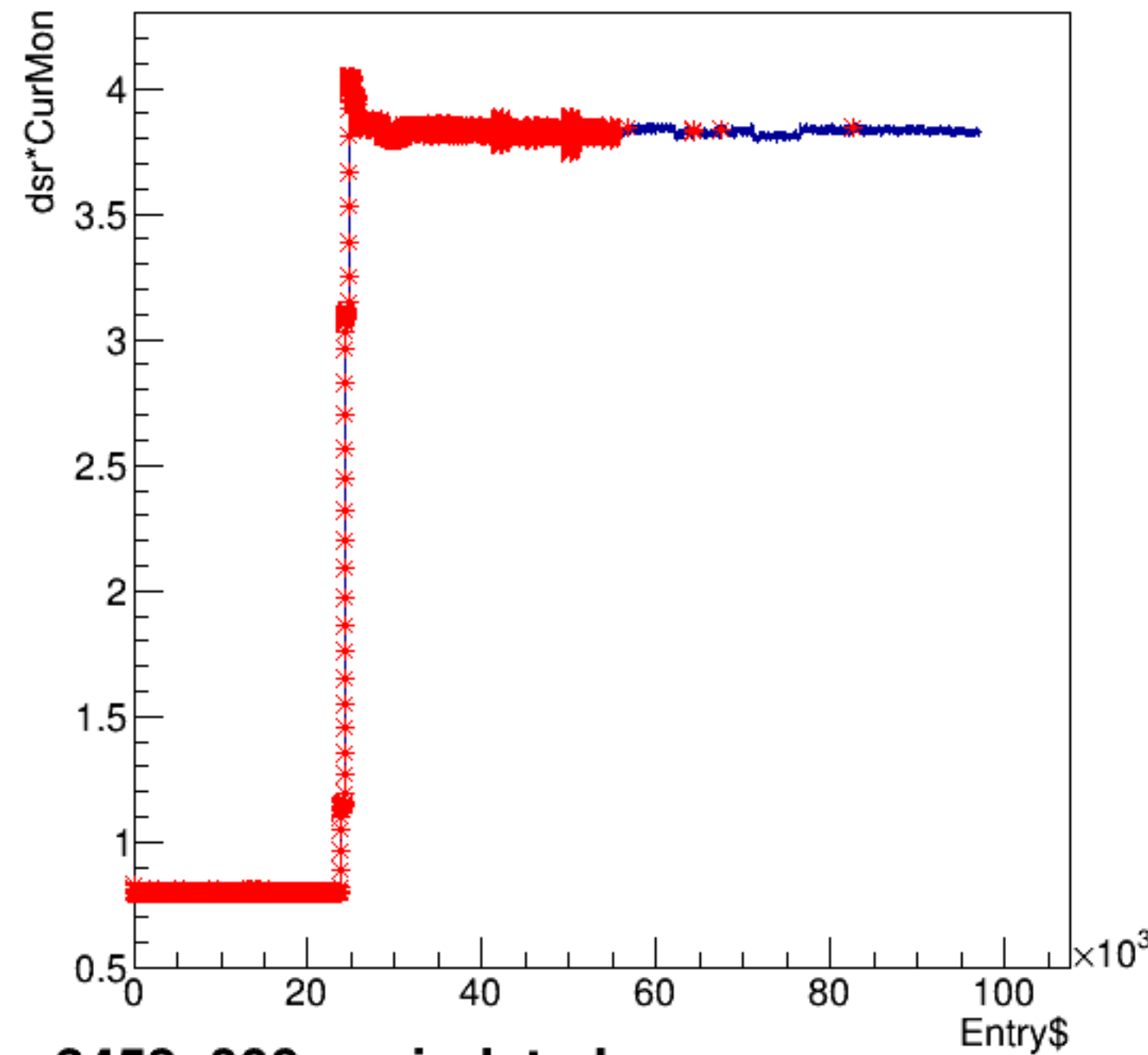


asym_usr

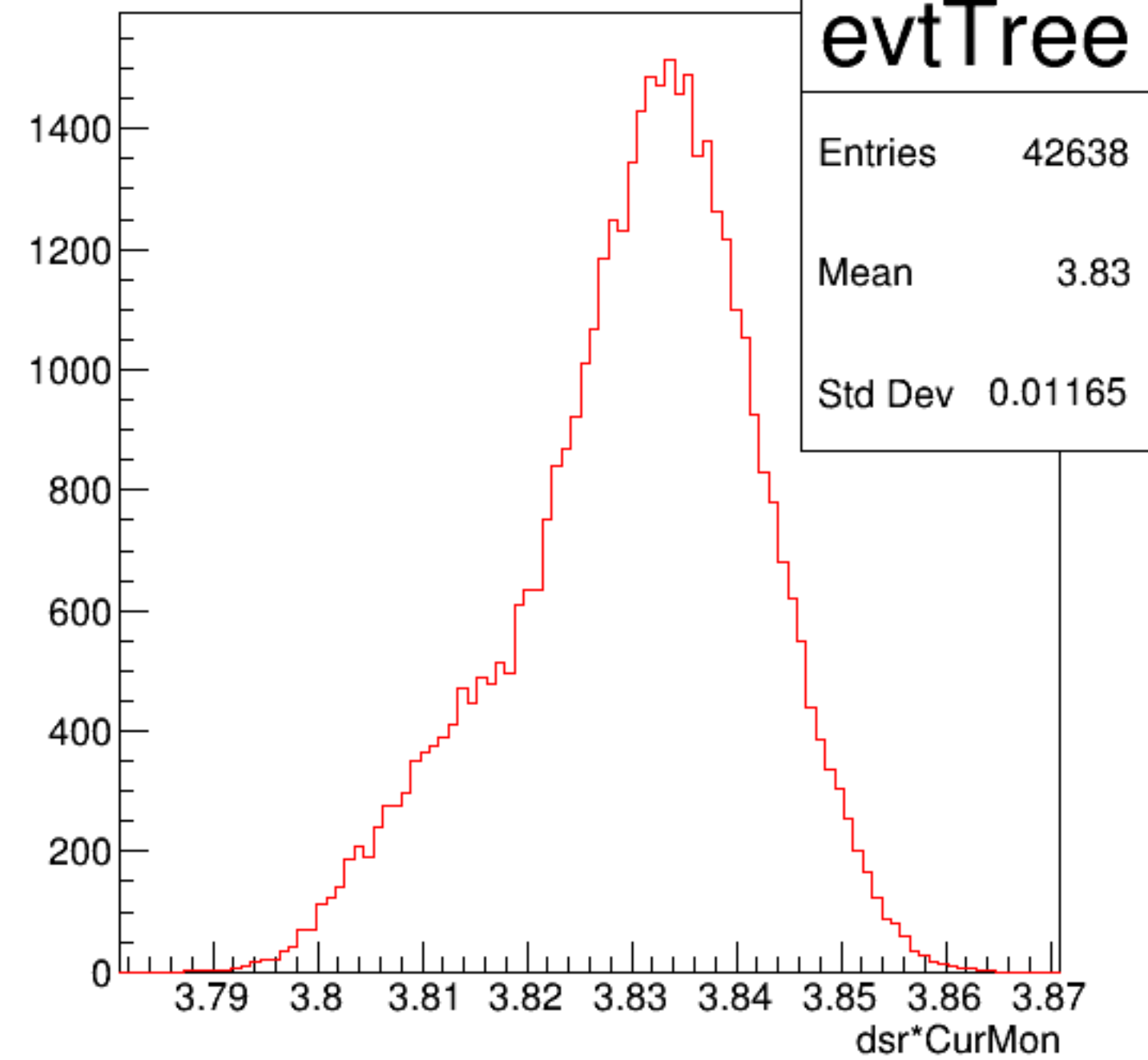


Entries	10654
Mean	0
Std Dev	0
Underflow	1.065e+04
Overflow	0

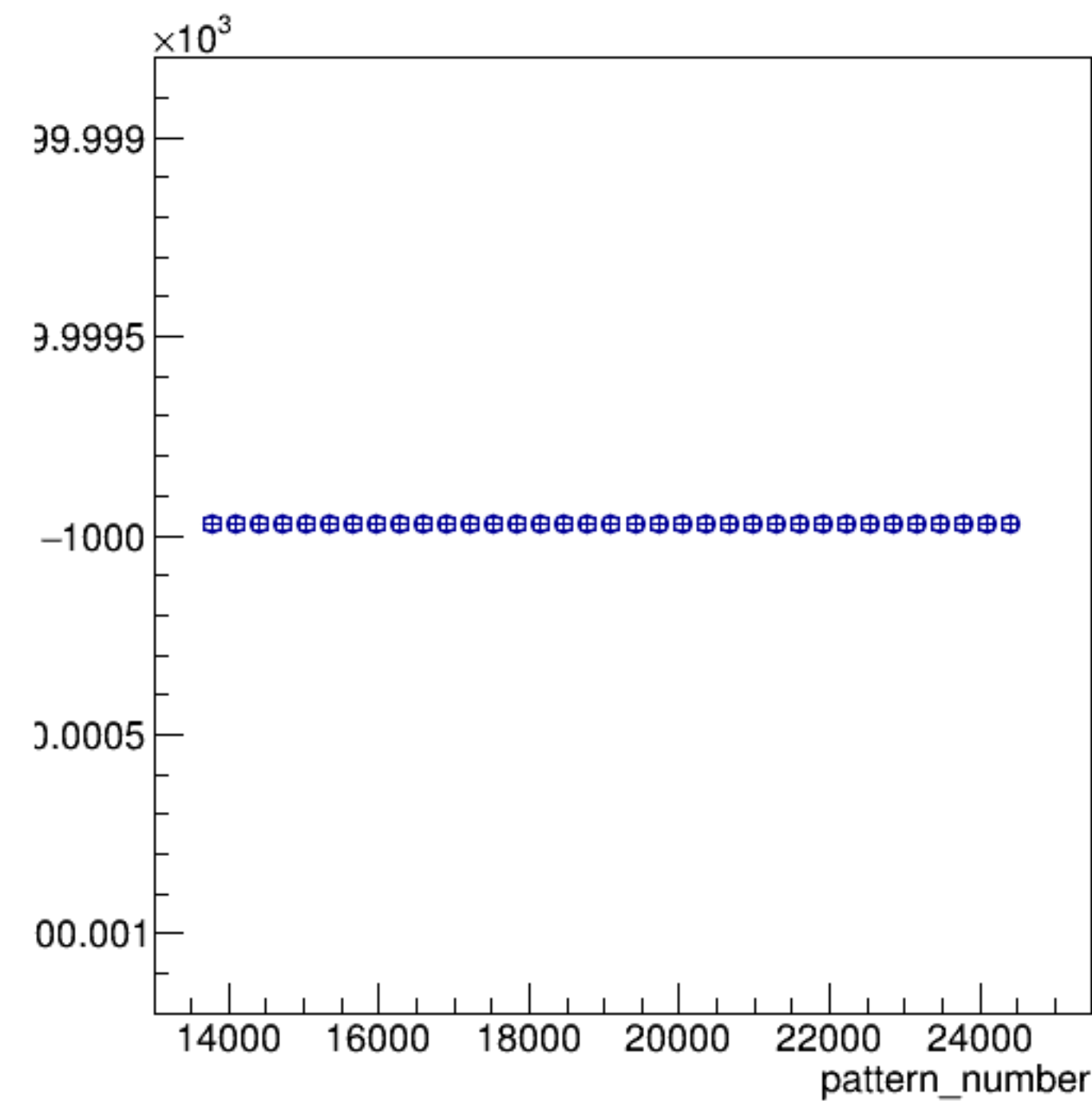
dsr*CurMon:Entry\$



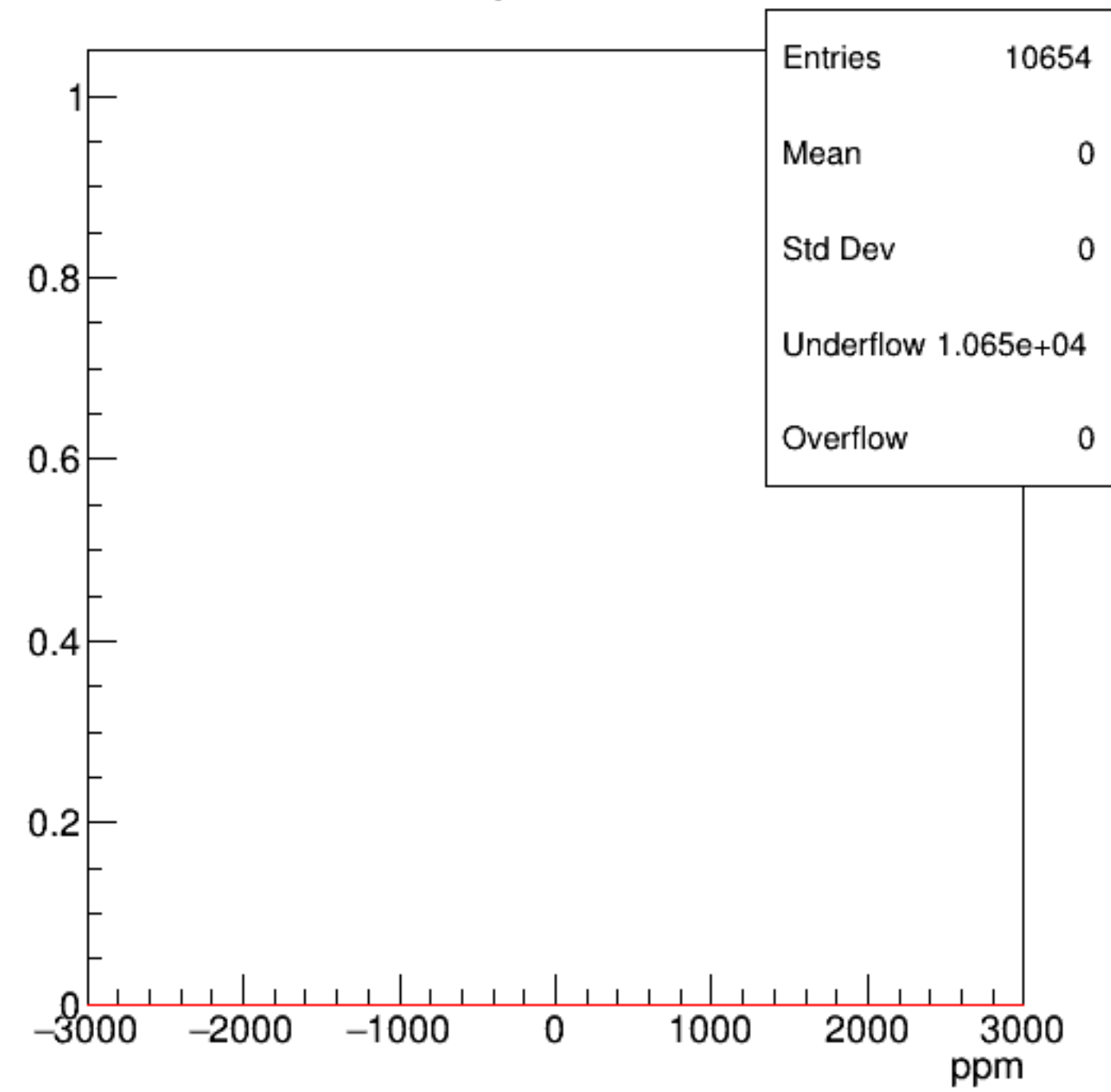
dsr*CurMon {ErrorFlag==0}



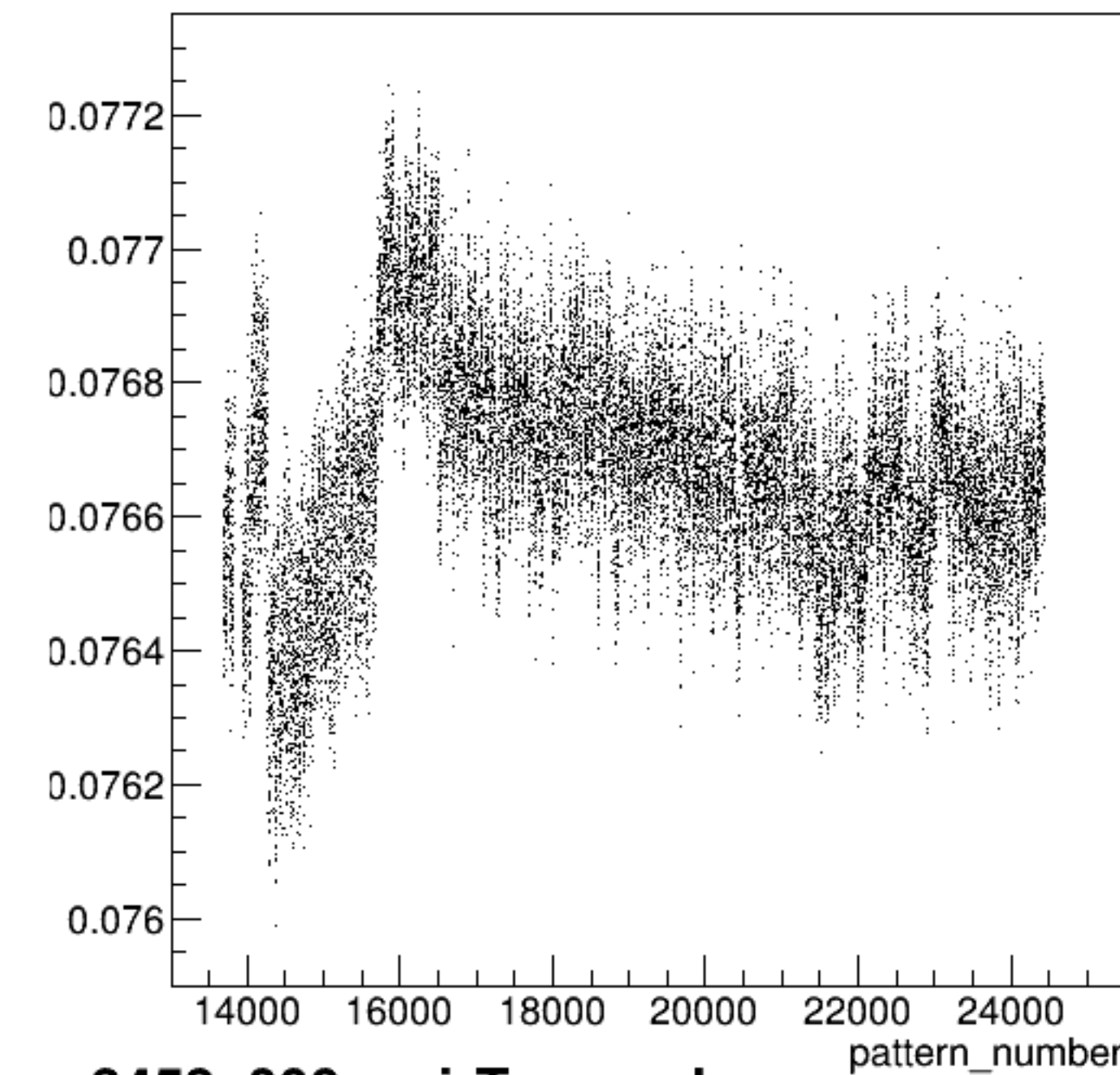
asym_dsr/ppm:pattern_number {ErrorFlag==0}



asym_dsr

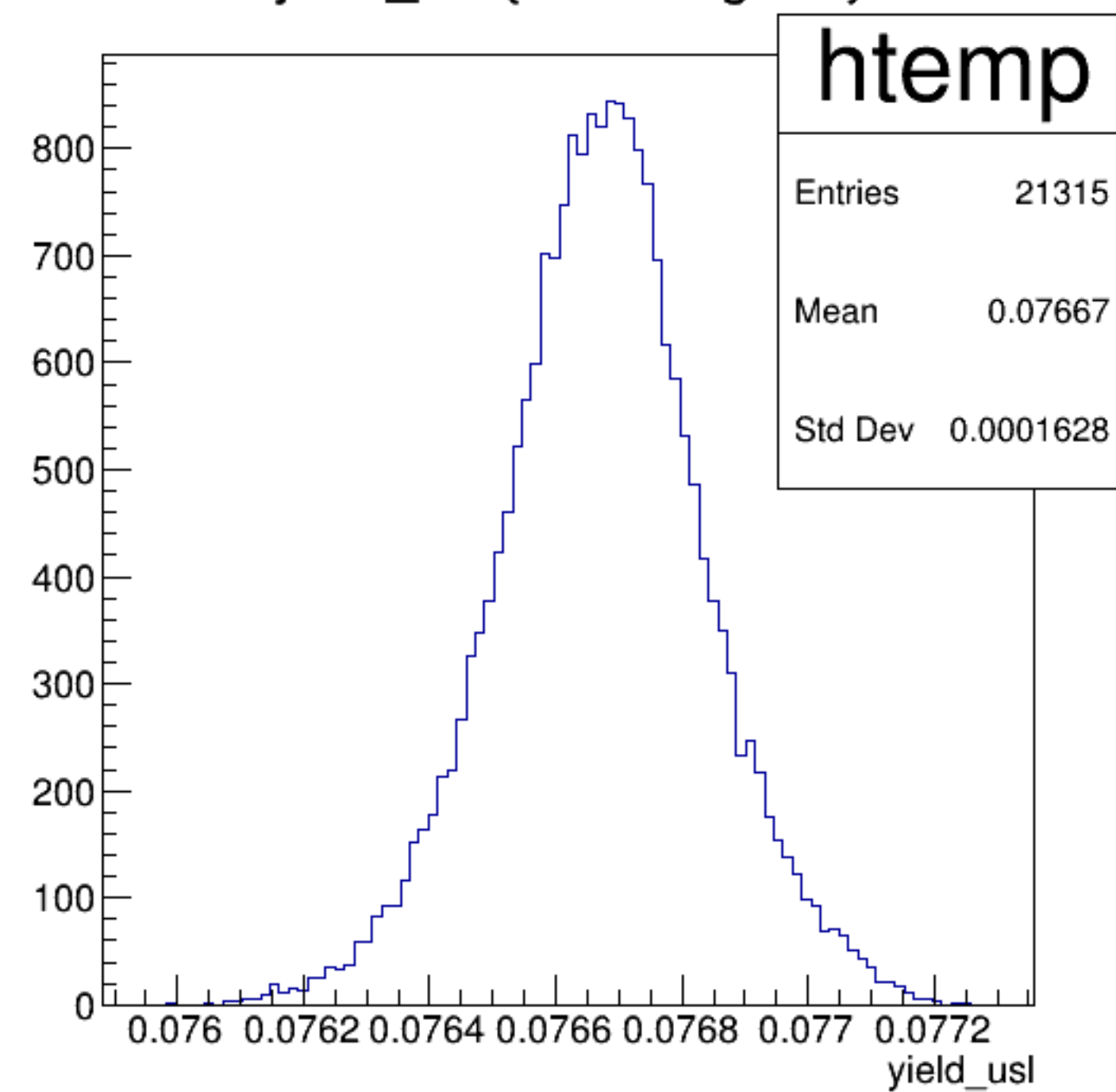


yield_usl:pattern_number {ErrorFlag==0}

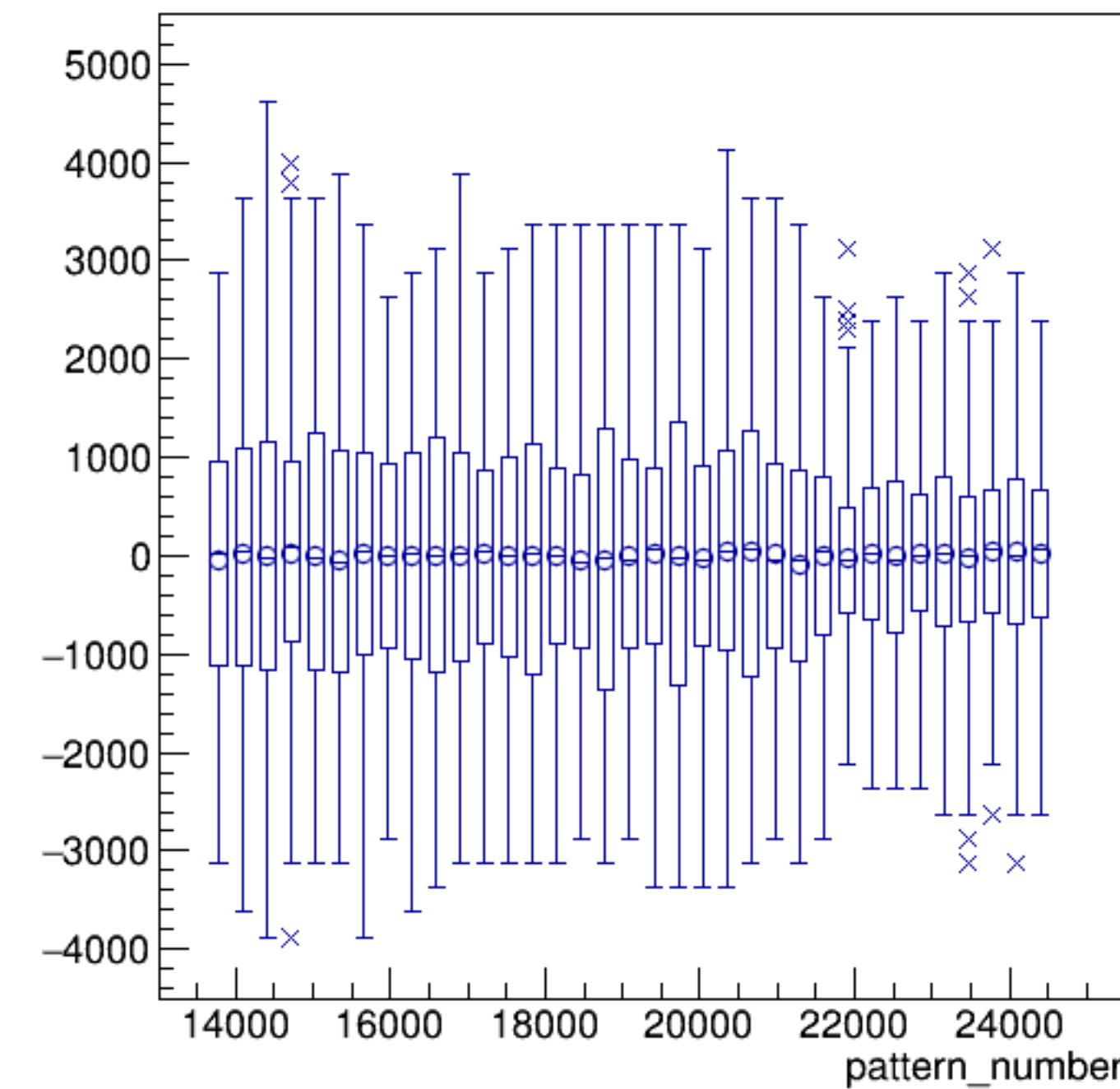


run3453_000_pairTree_usl.png

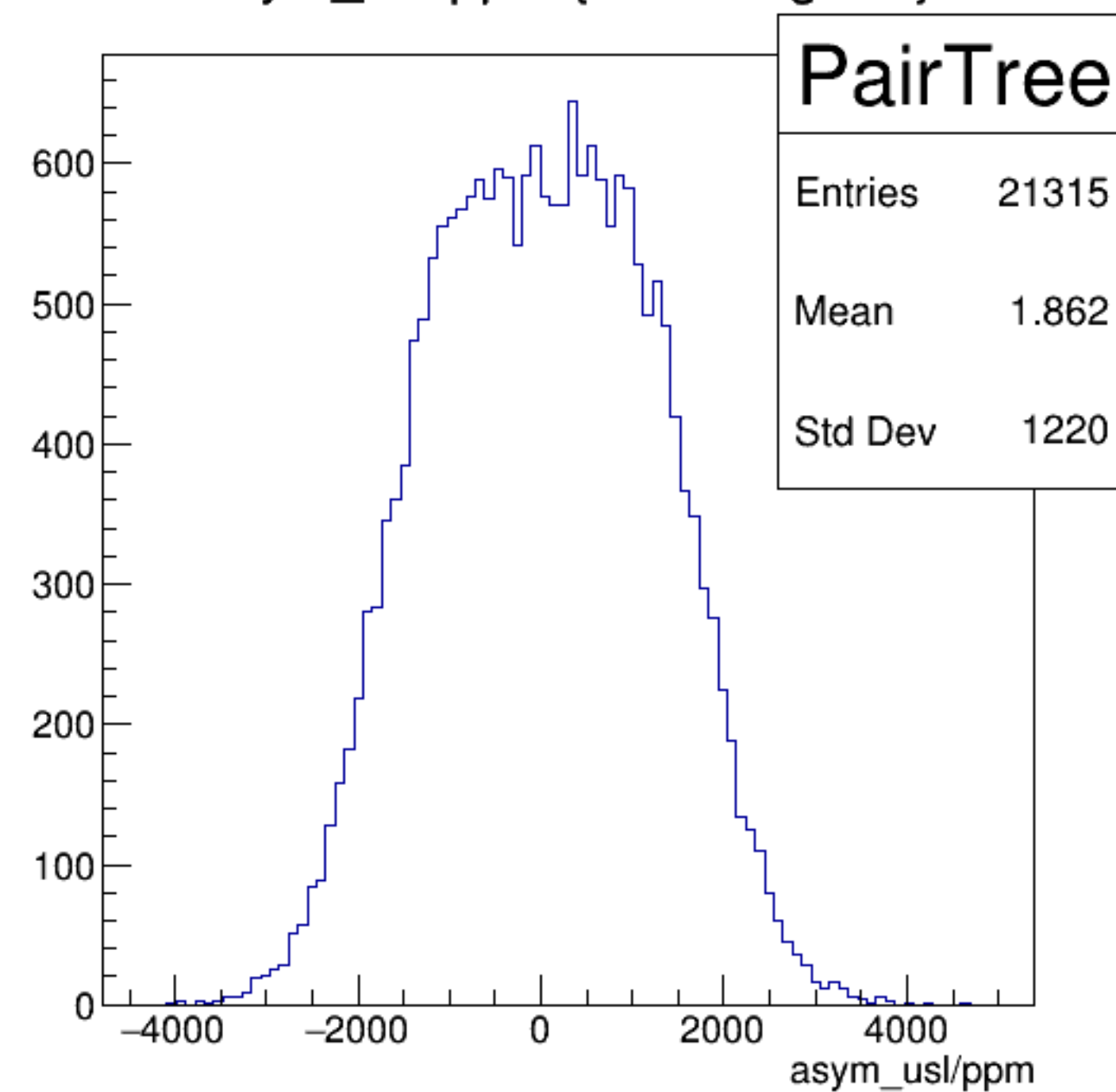
yield_usl {ErrorFlag==0}



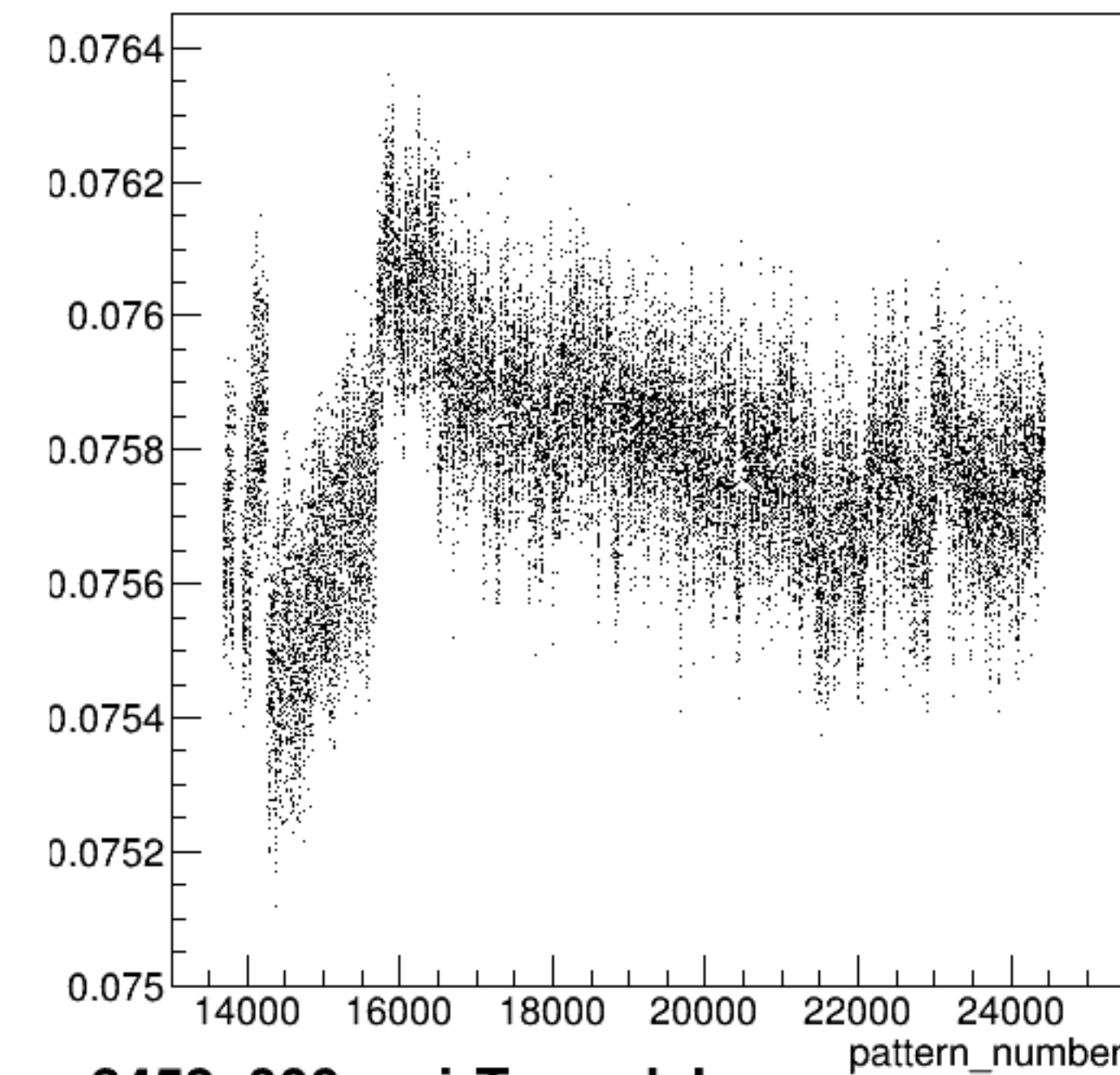
asym_usl/ppm:pattern_number {ErrorFlag==0}



asym_usl/ppm {ErrorFlag==0}

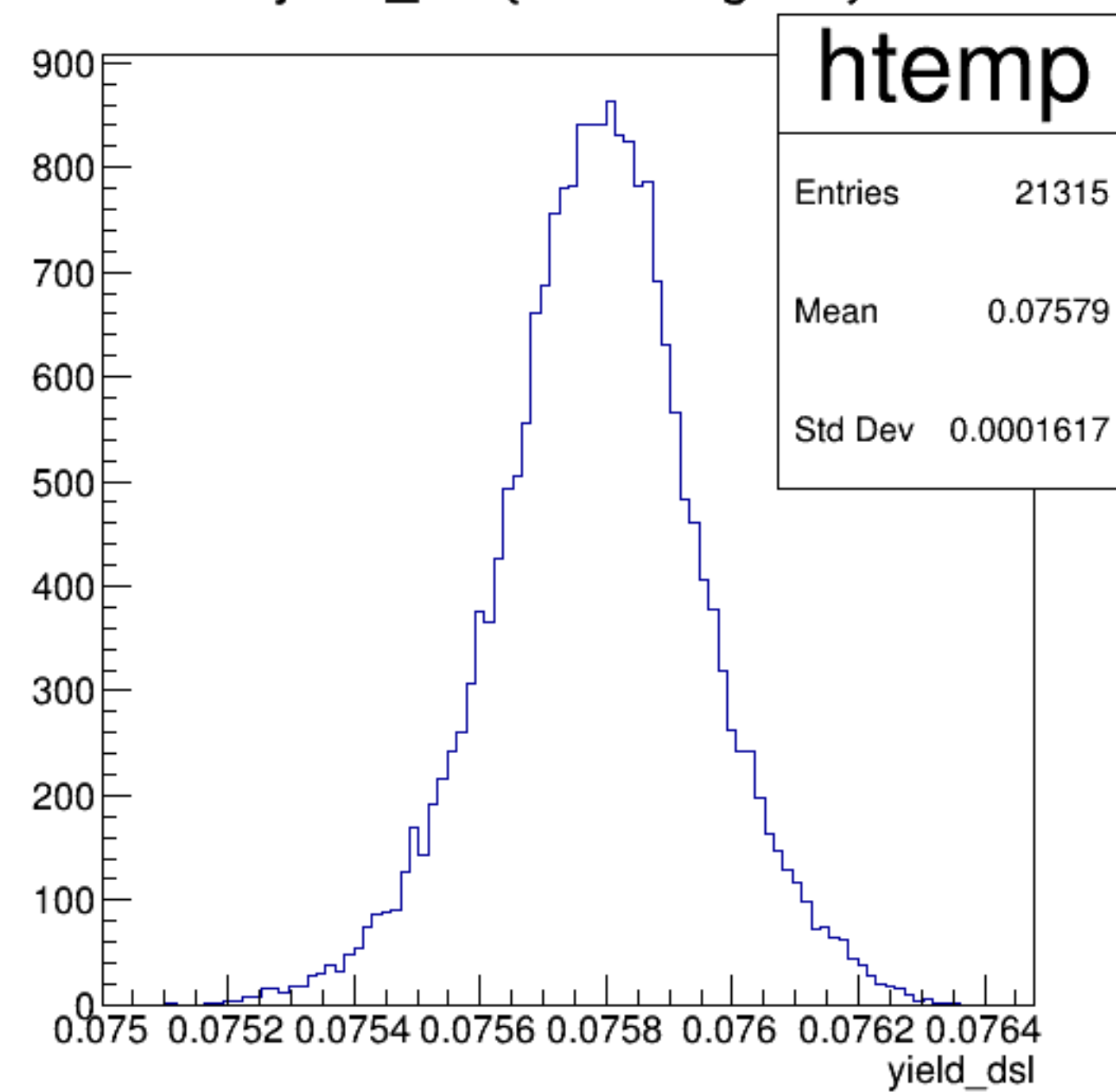


yield_dsl:pattern_number {ErrorFlag==0}

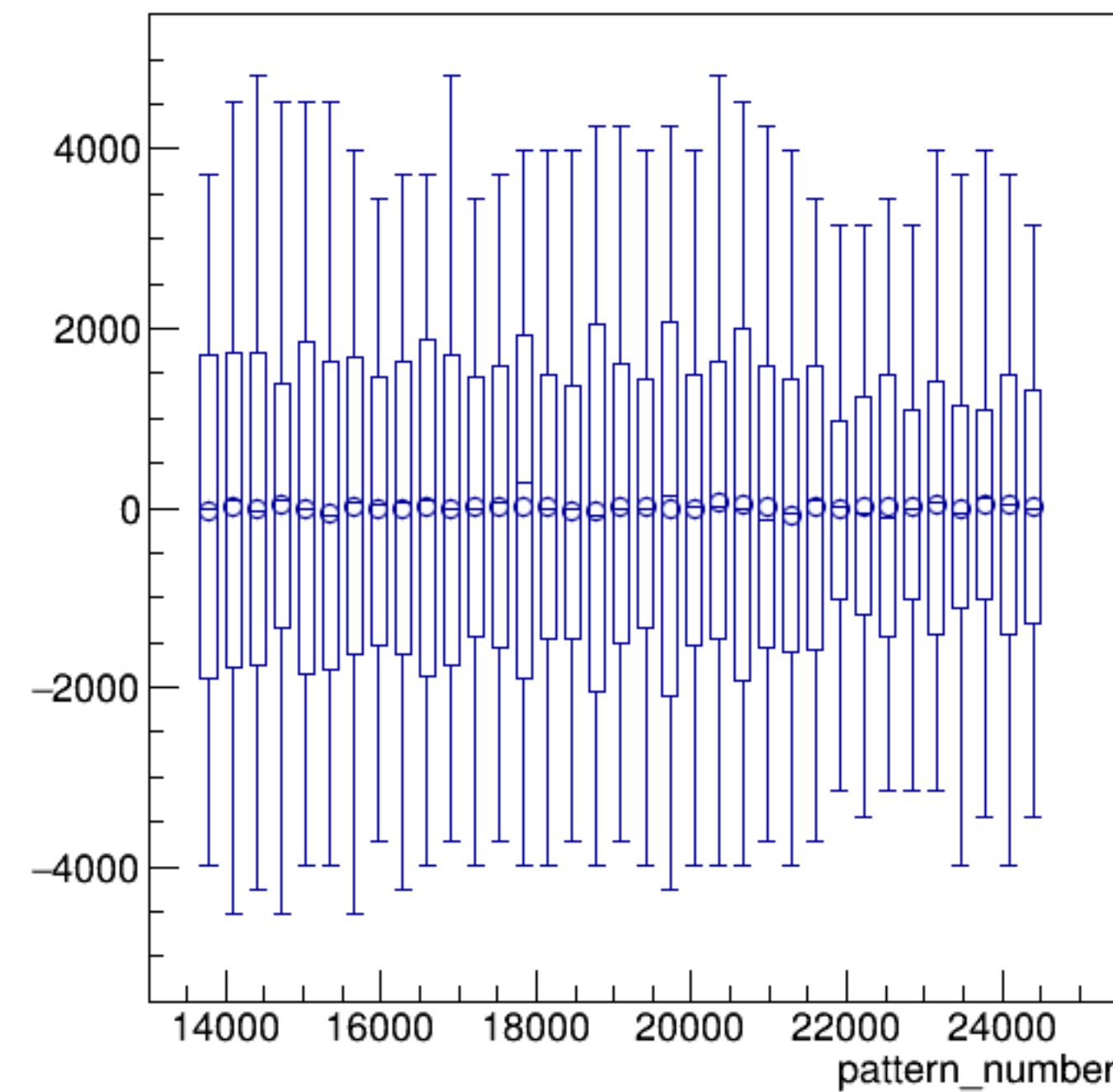


run3453_000_pairTree_dsl.png

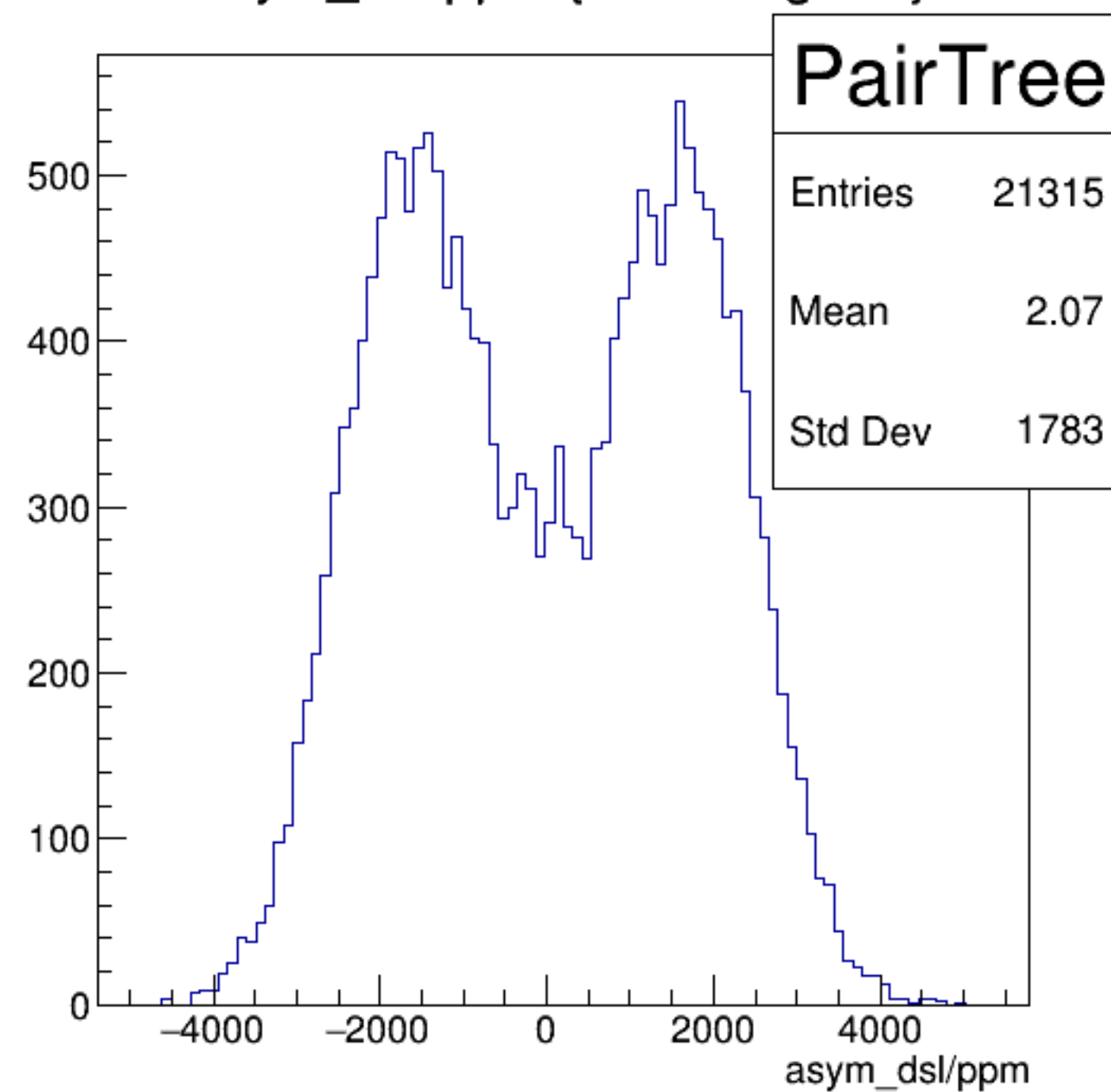
yield_dsl {ErrorFlag==0}

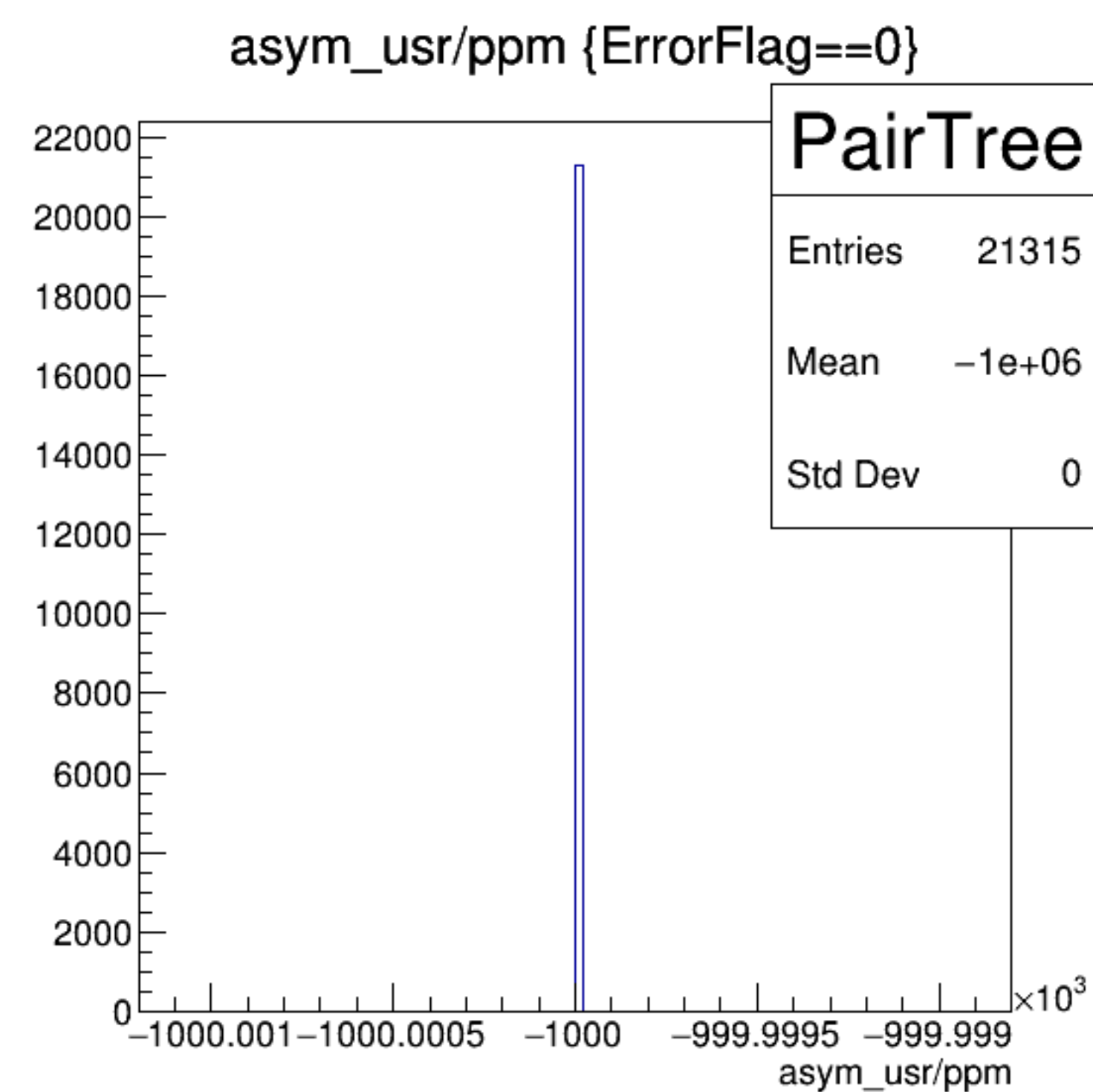
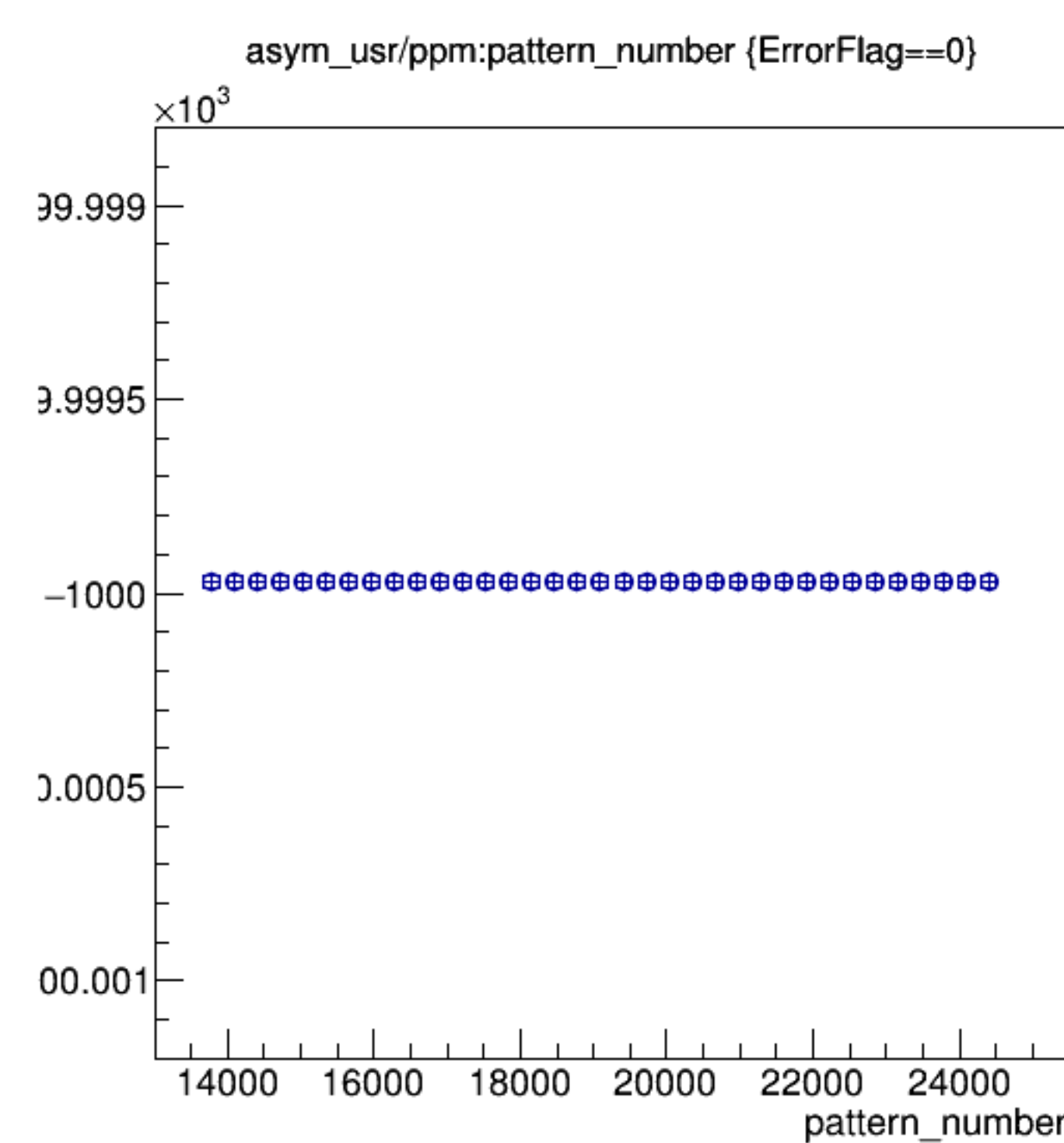
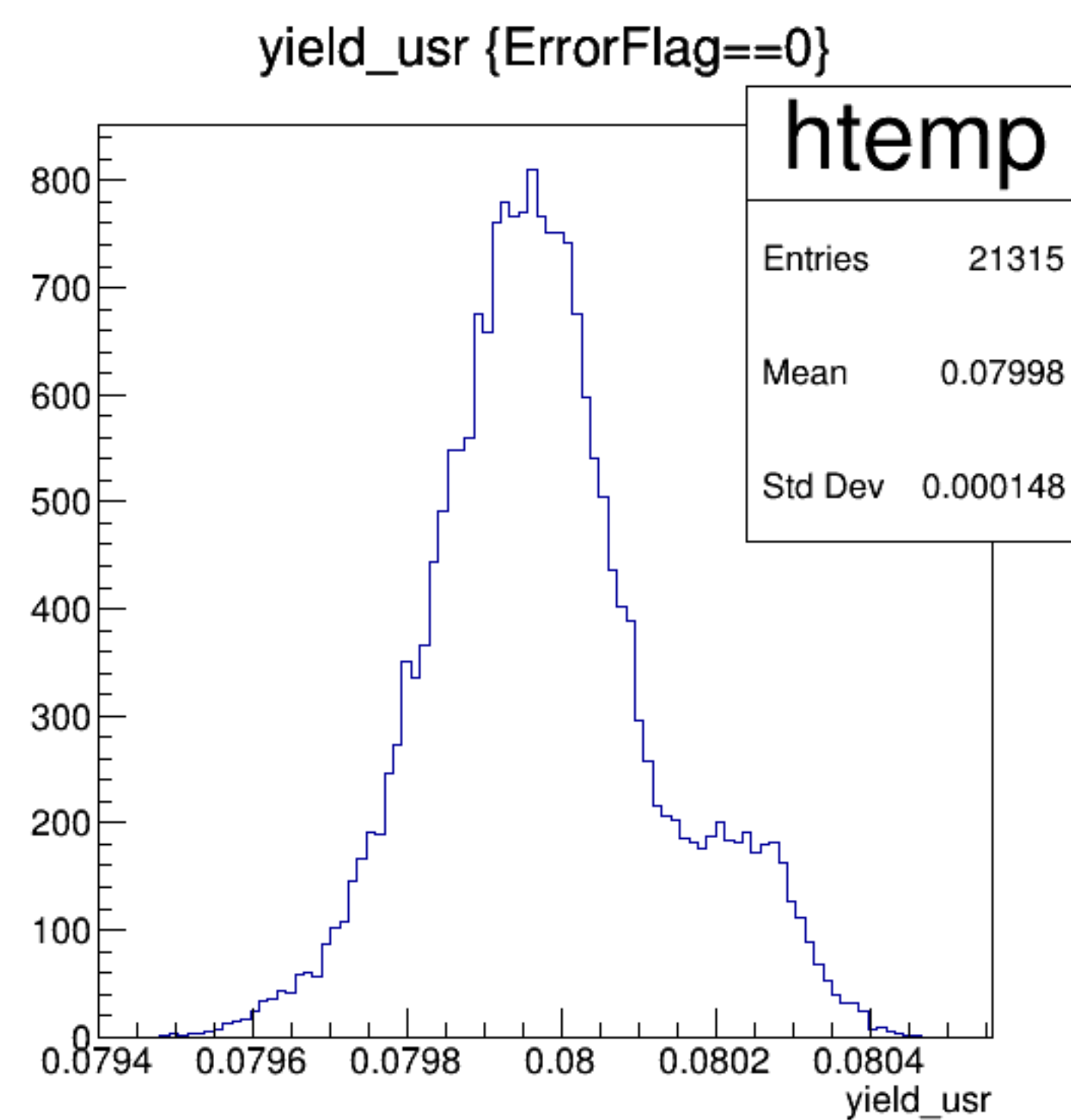
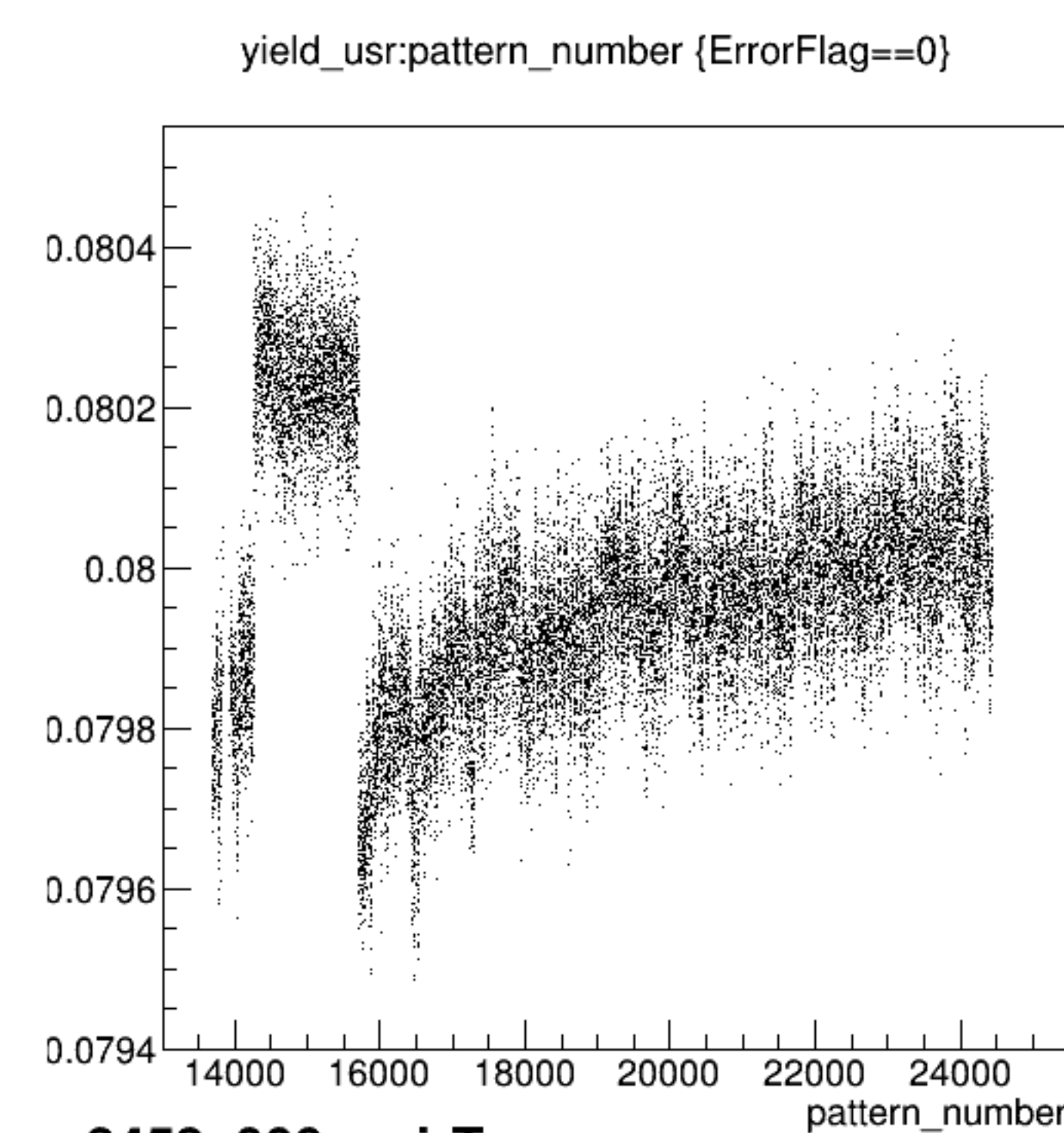


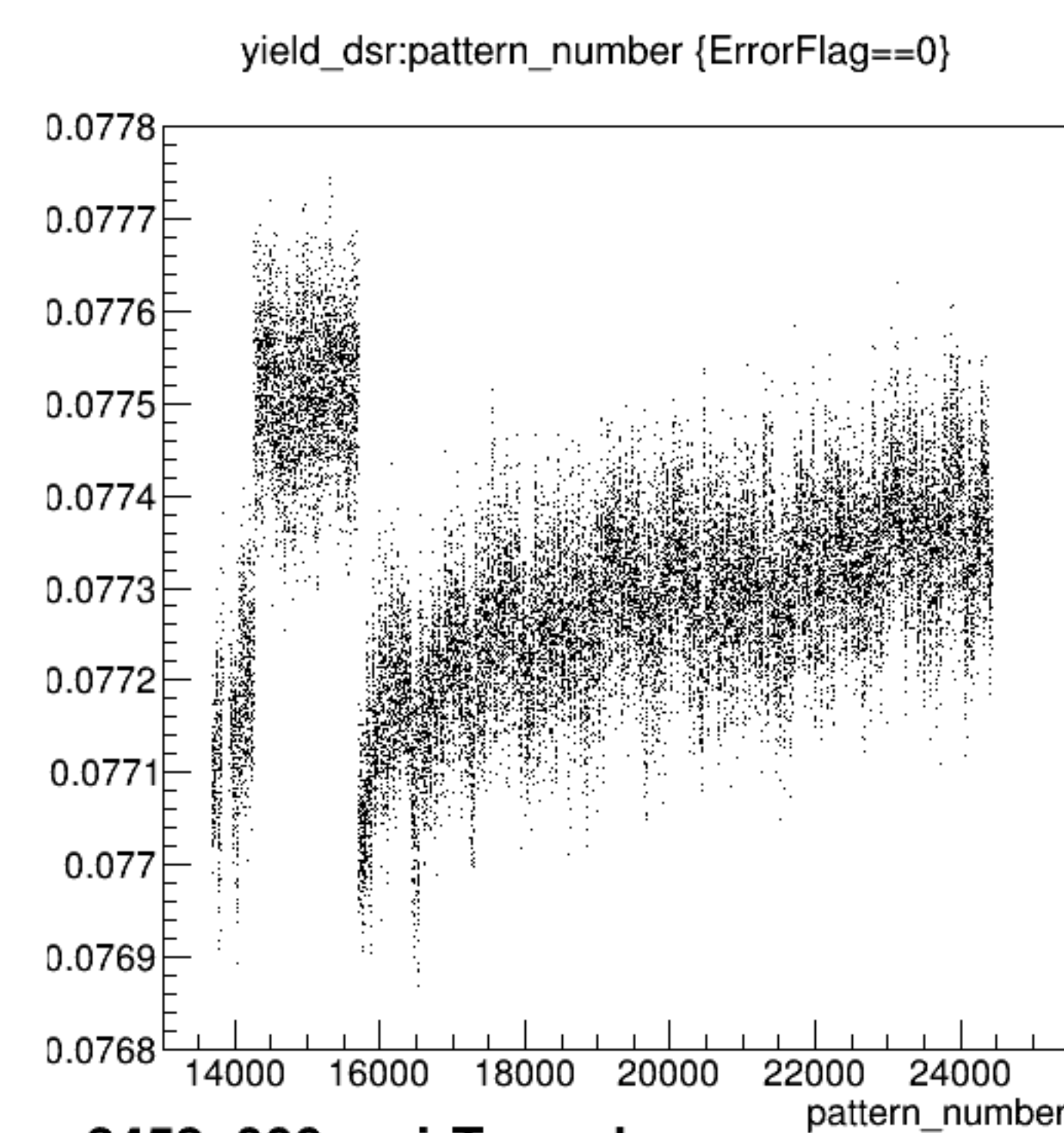
asym_dsl/ppm:pattern_number {ErrorFlag==0}



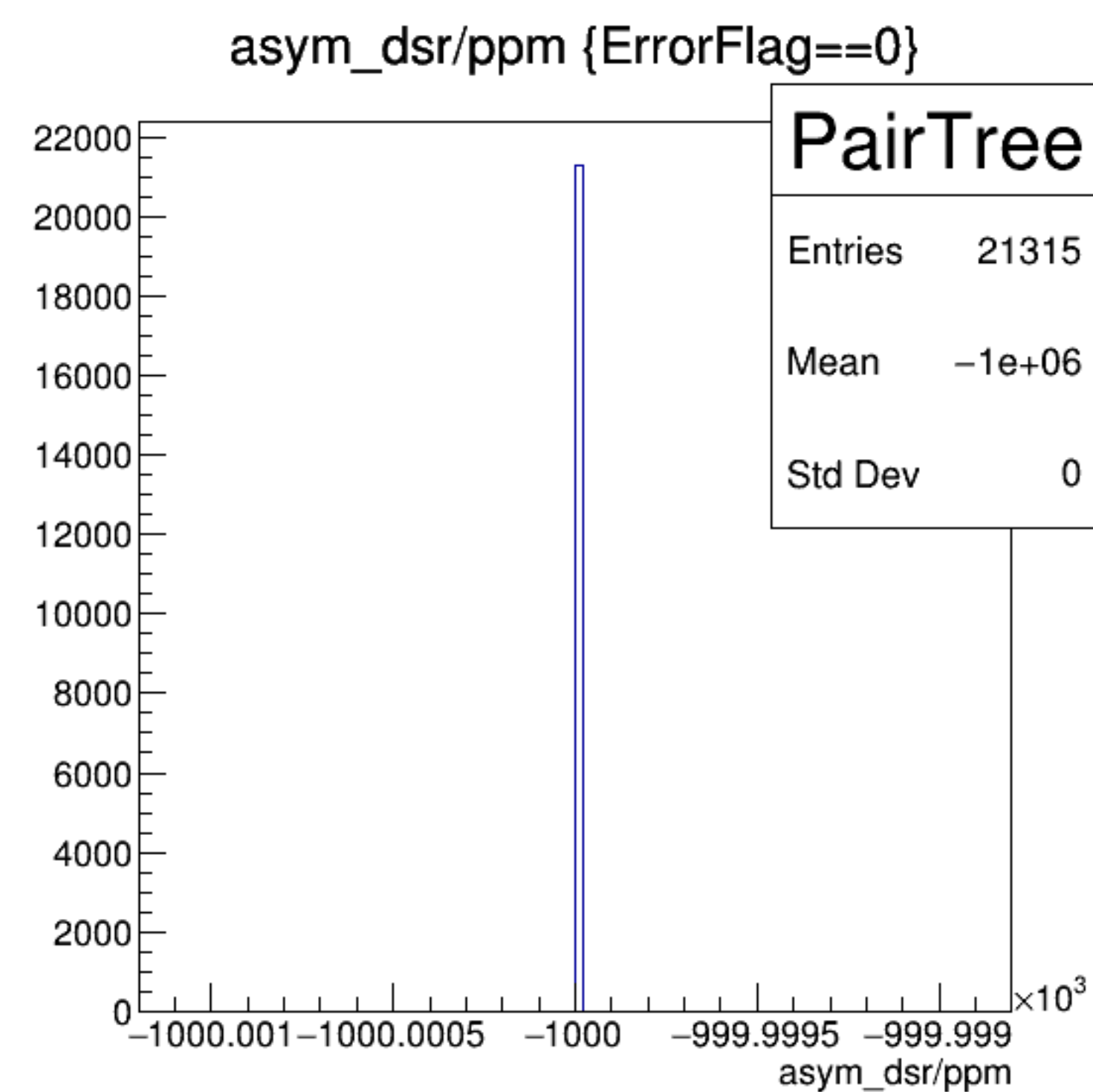
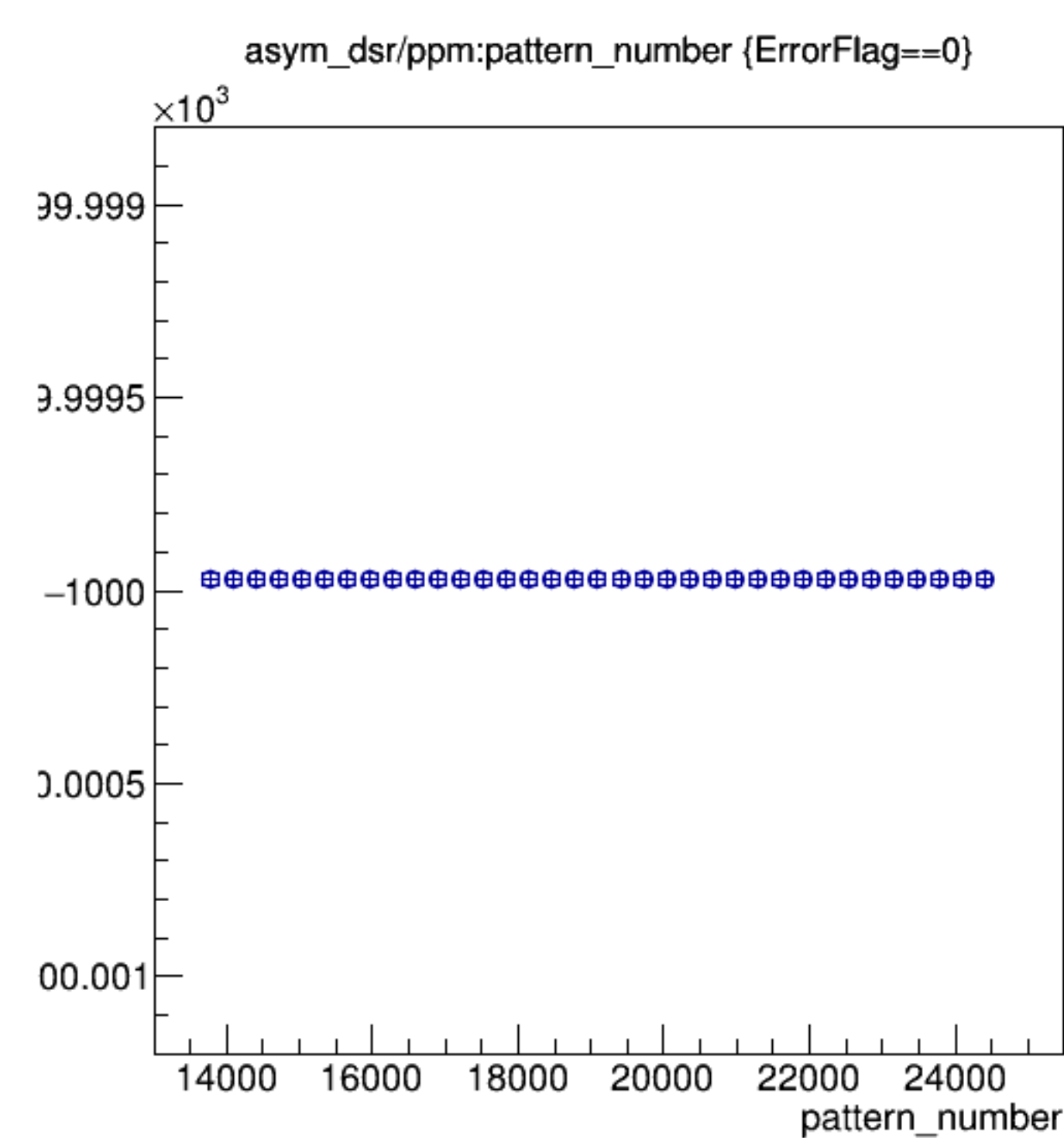
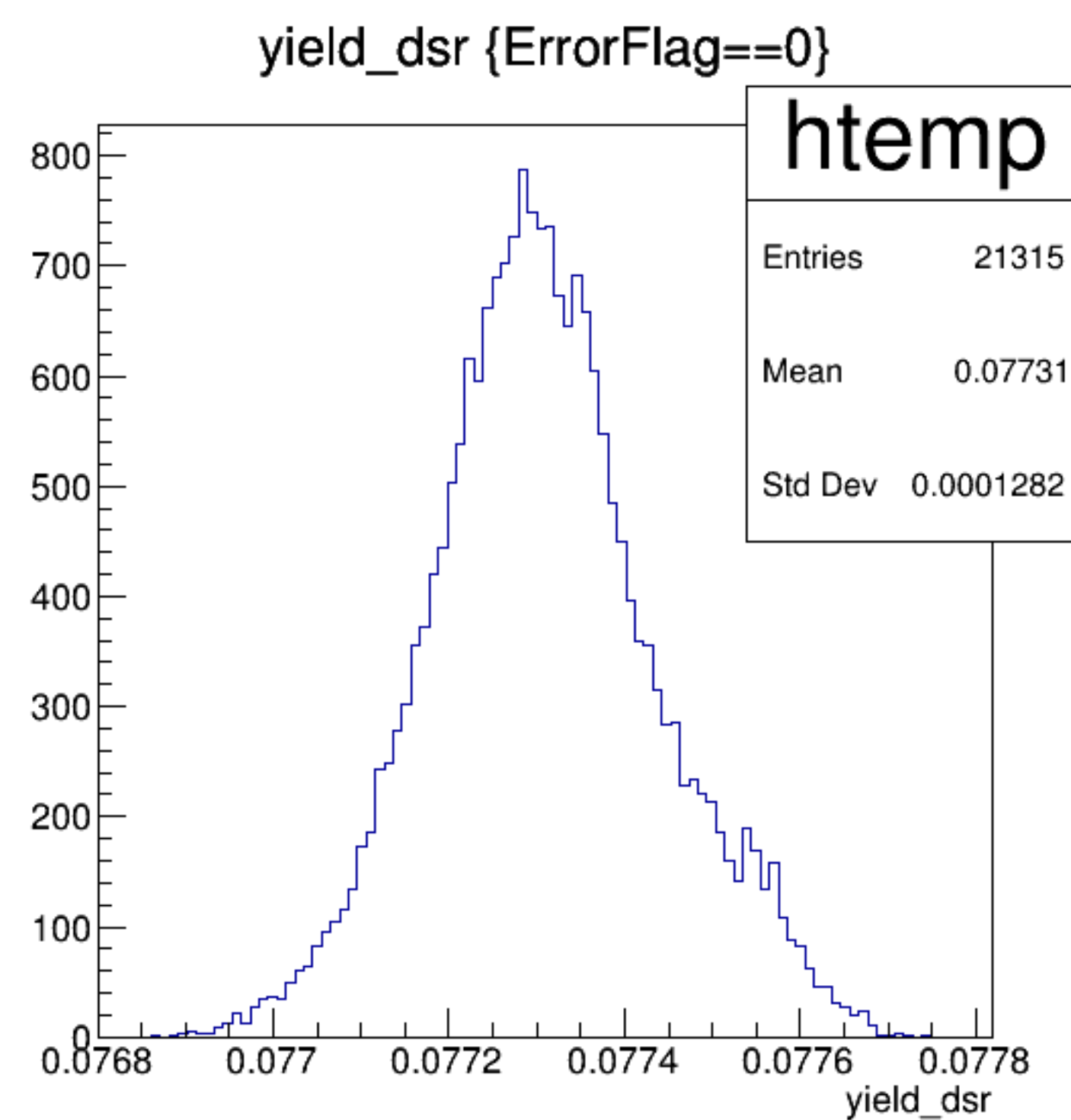
asym_dsl/ppm {ErrorFlag==0}



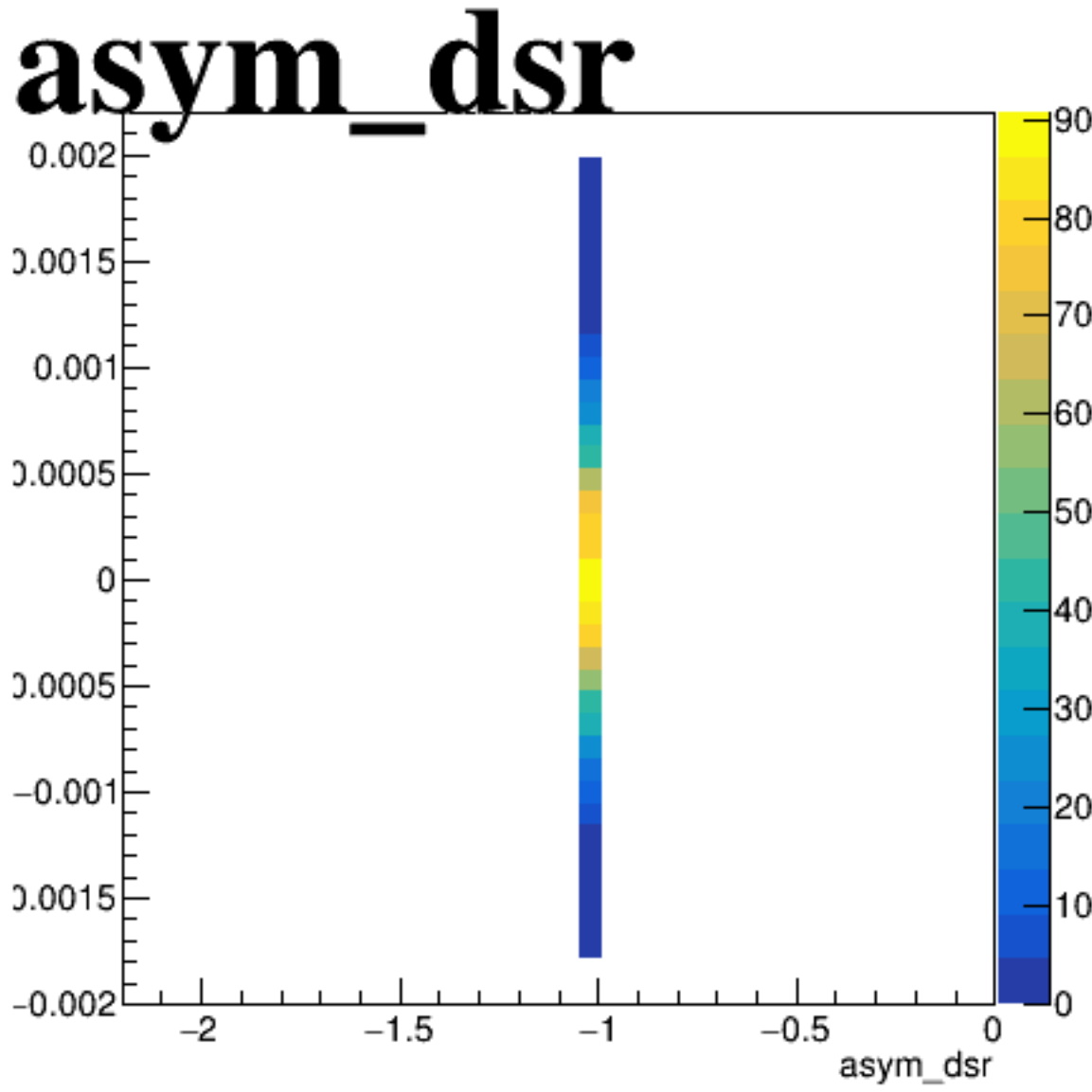
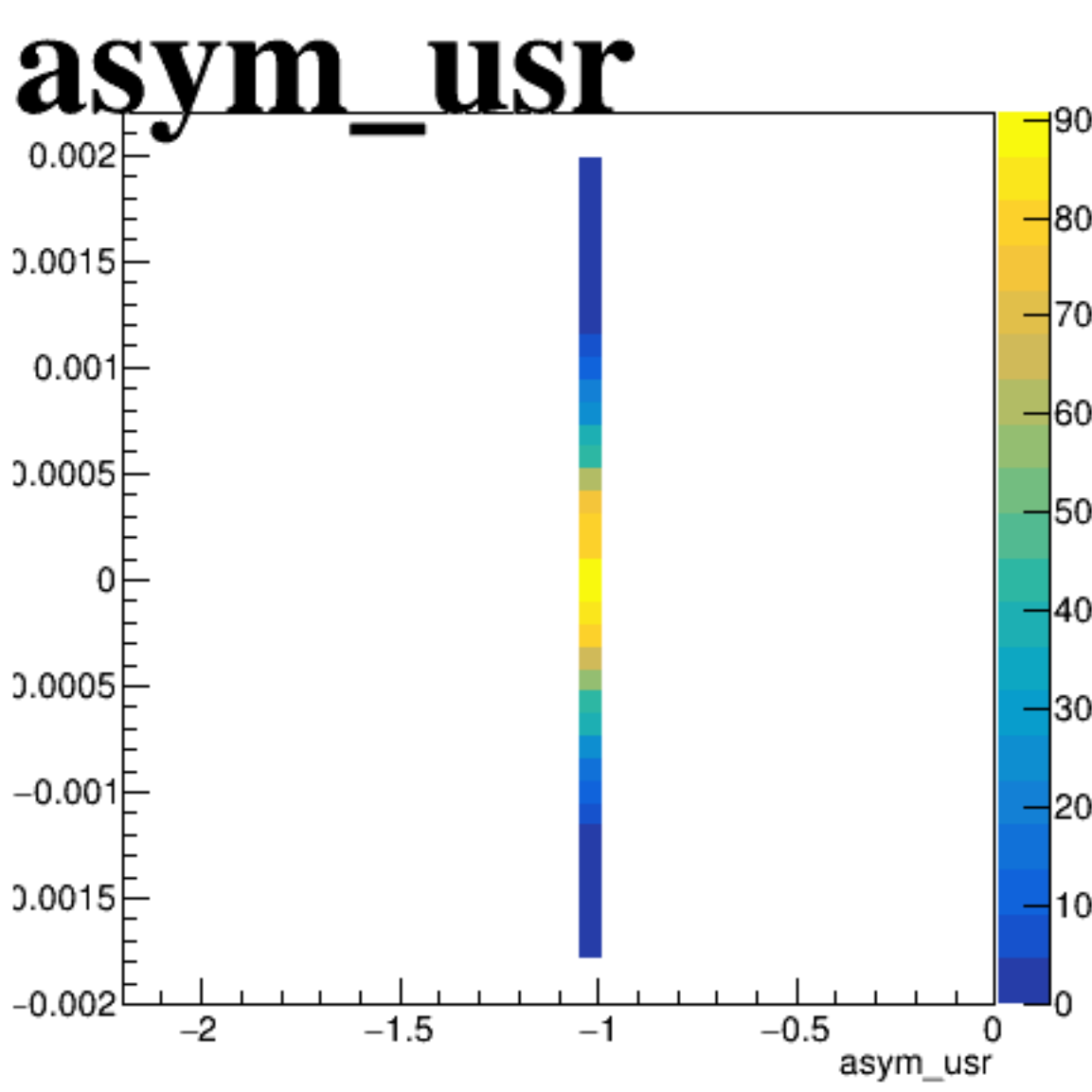
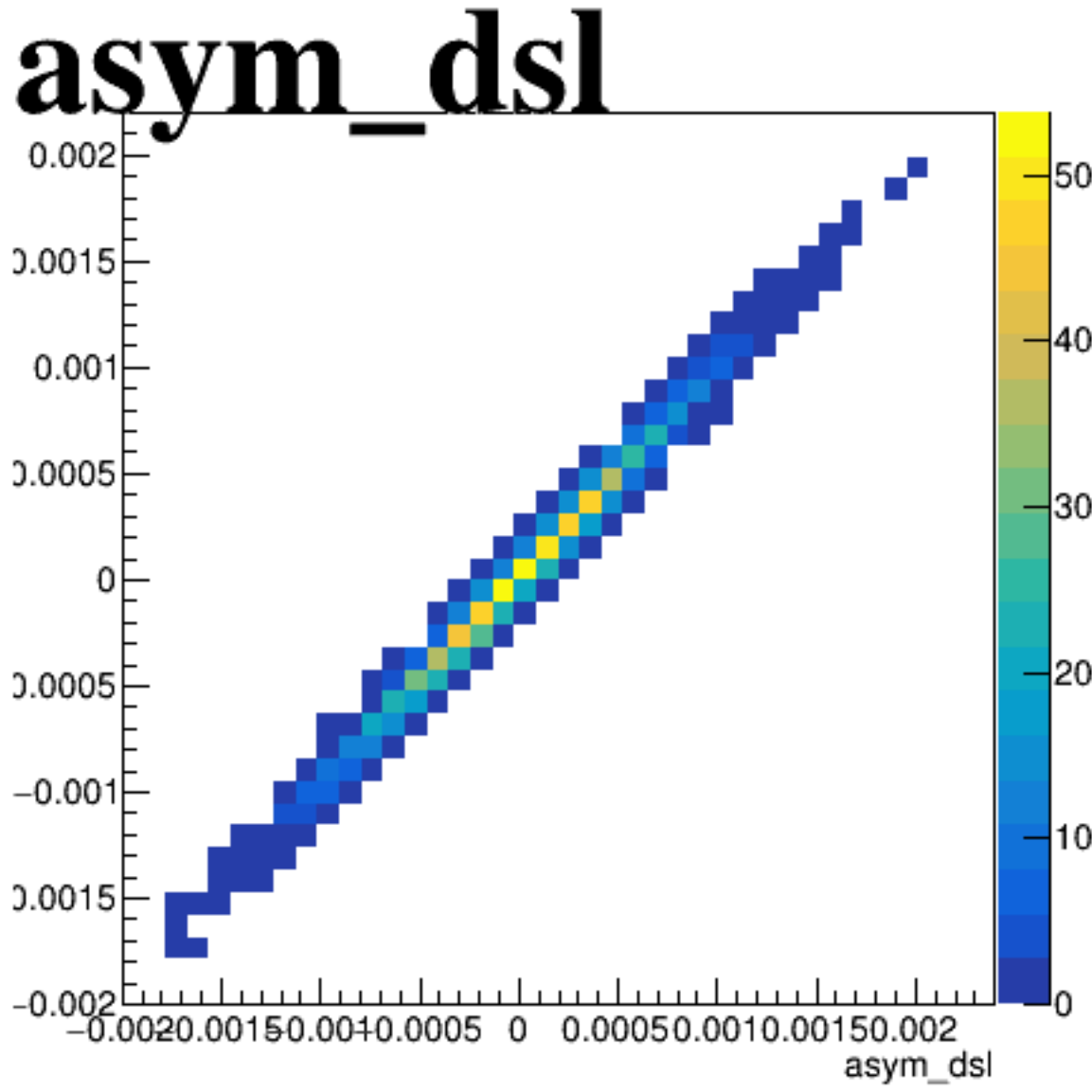
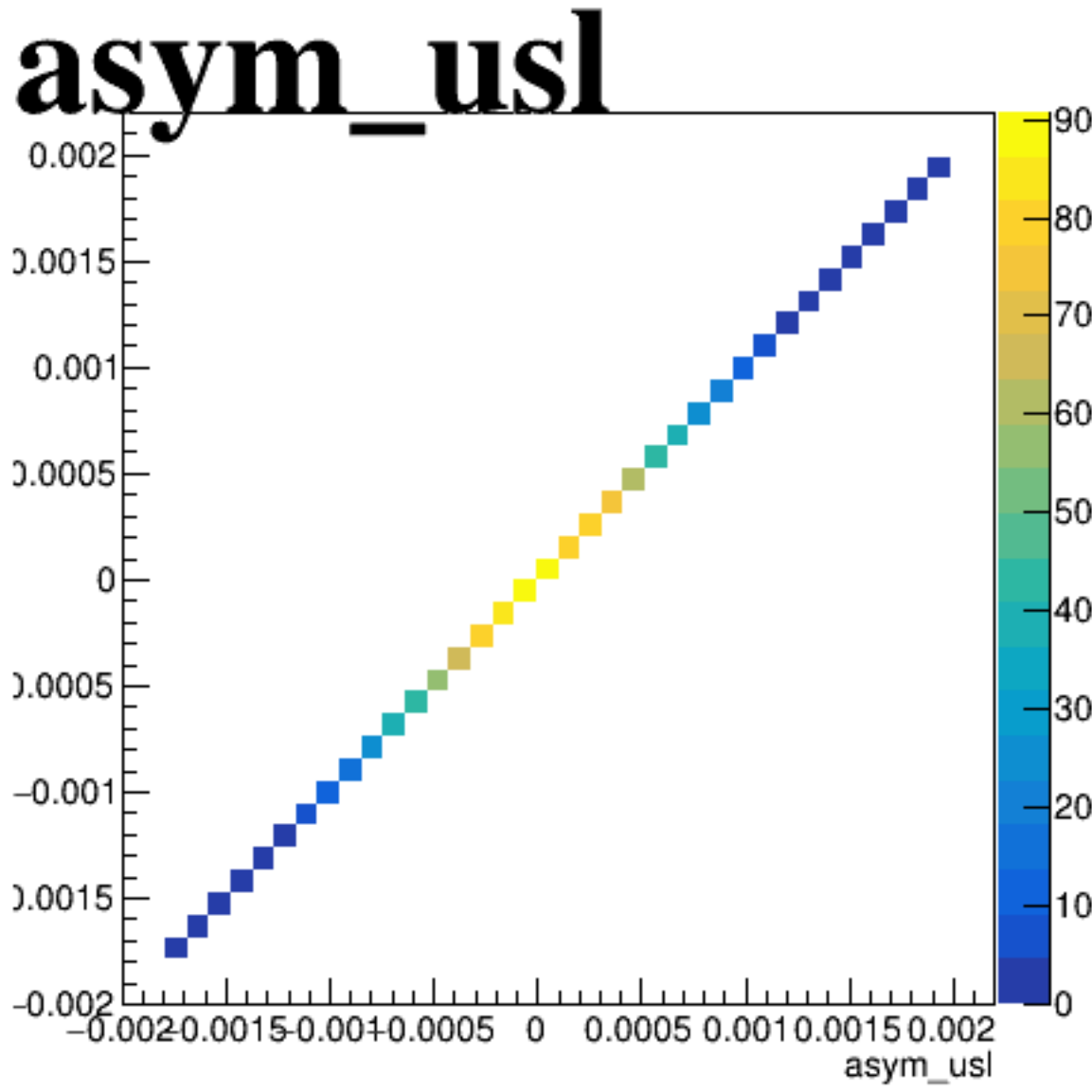




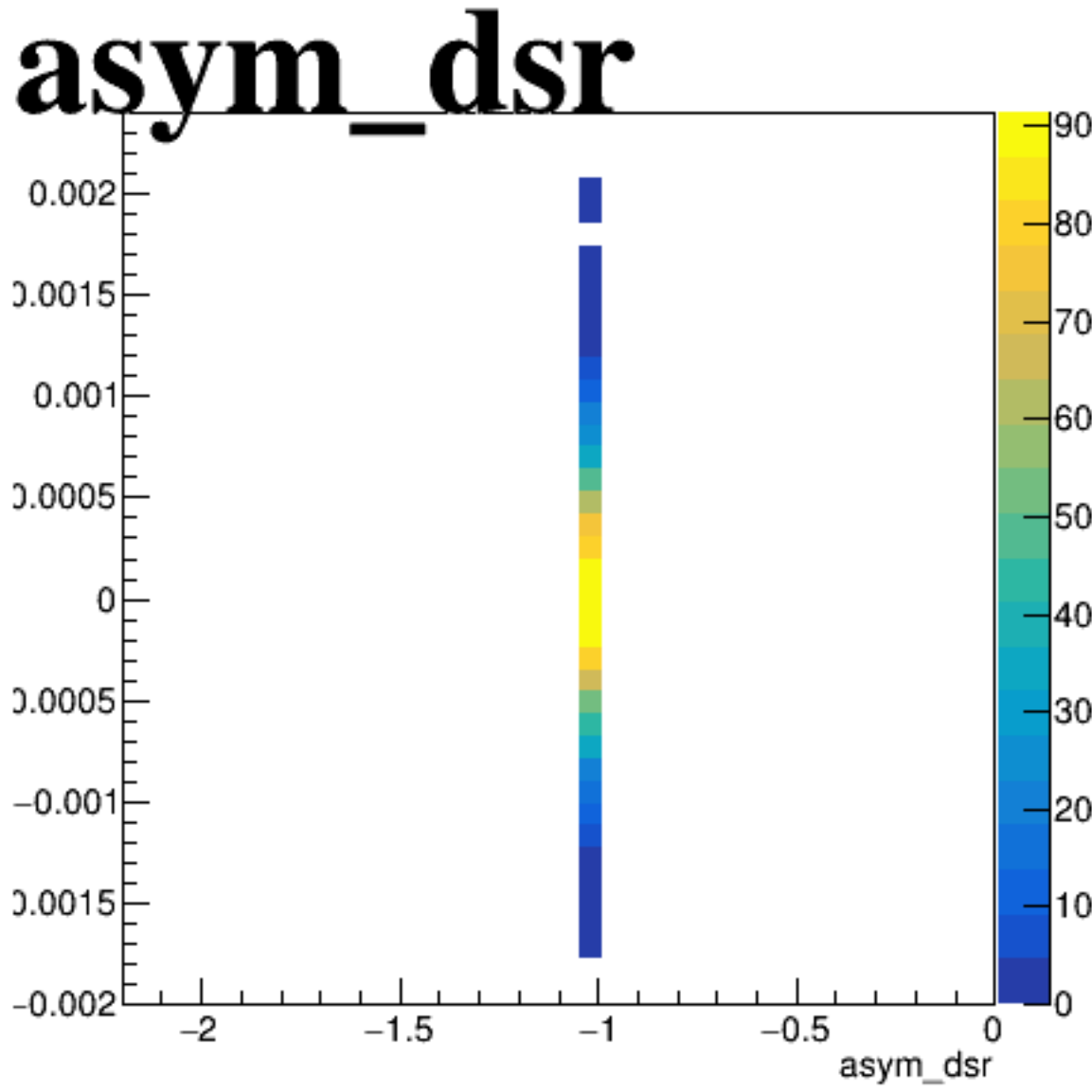
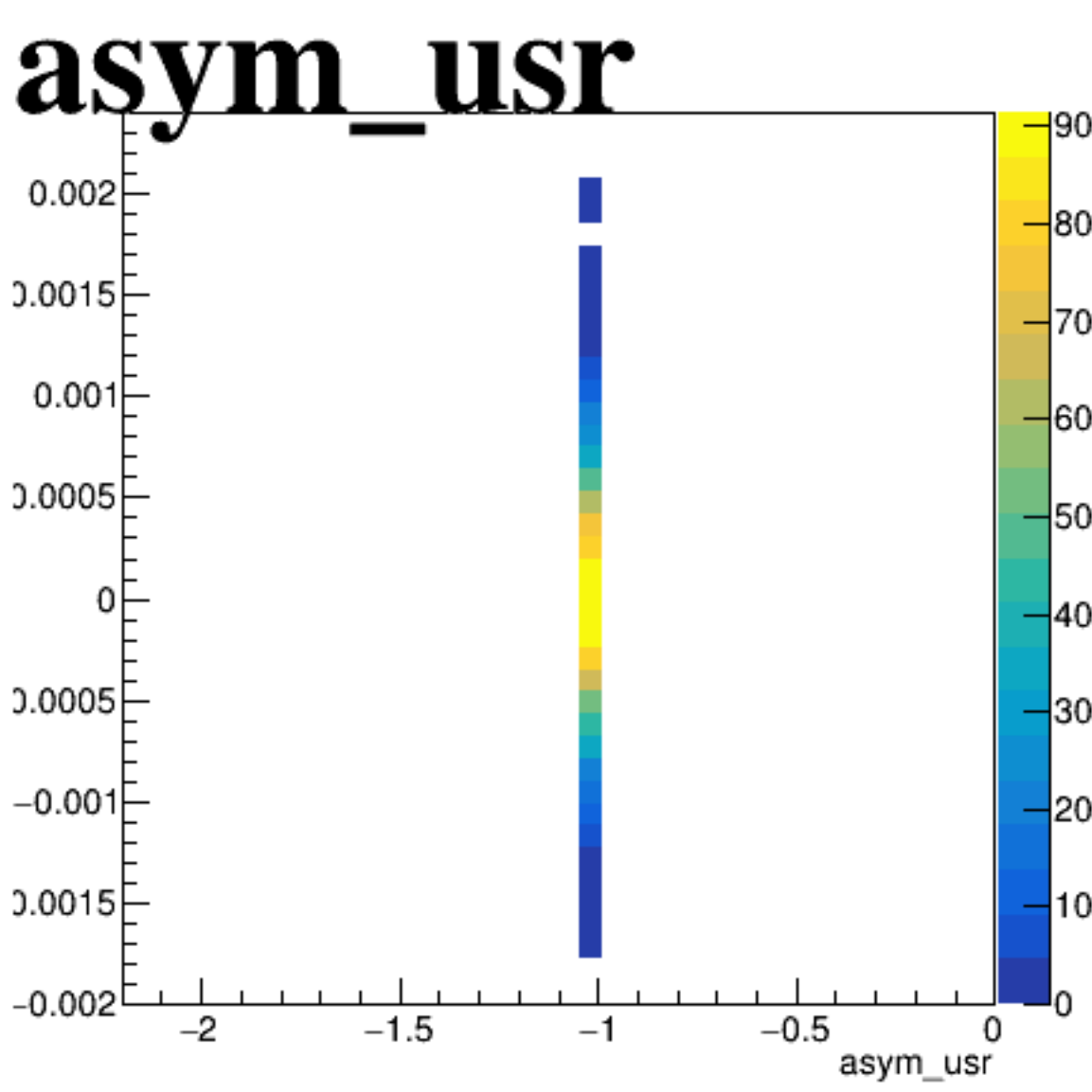
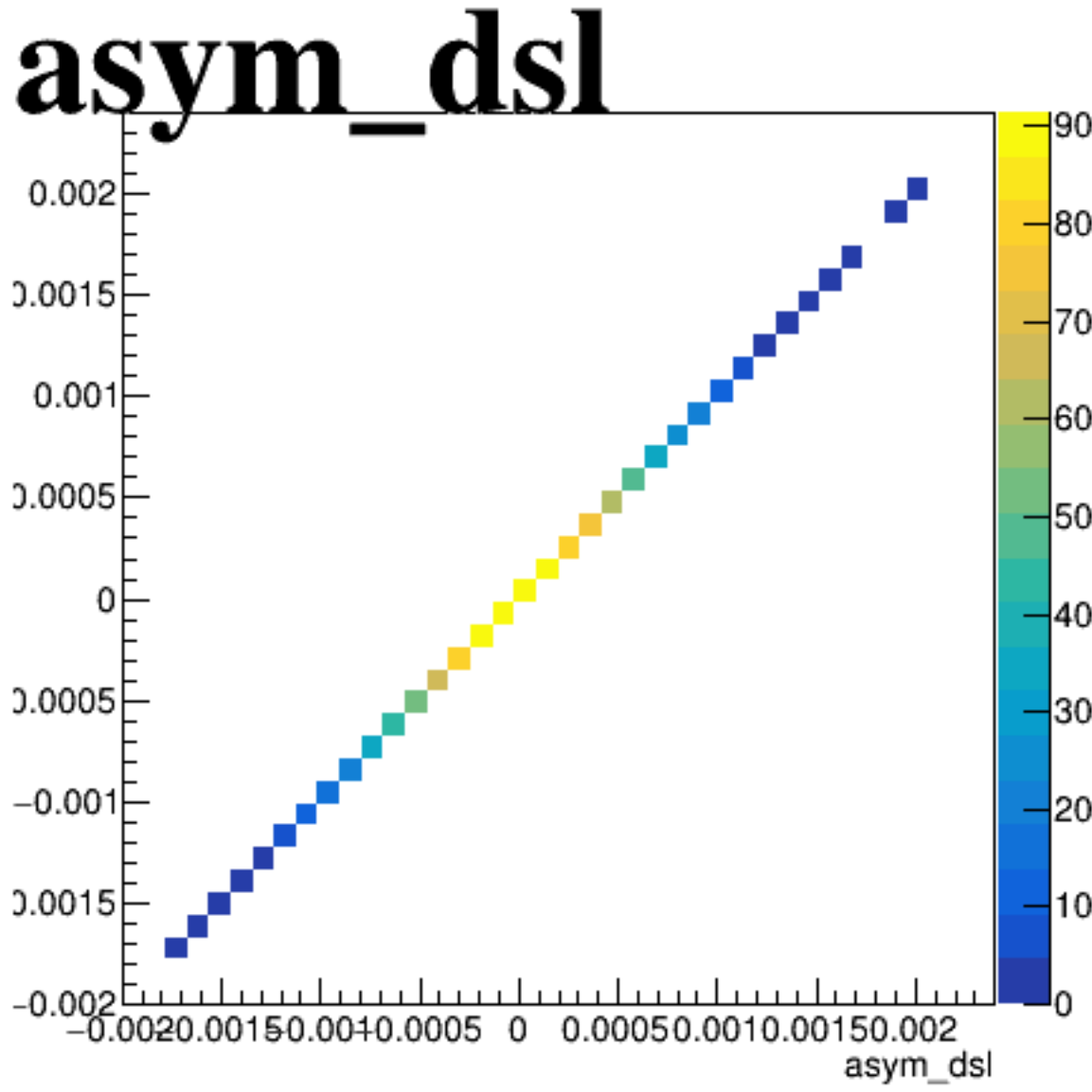
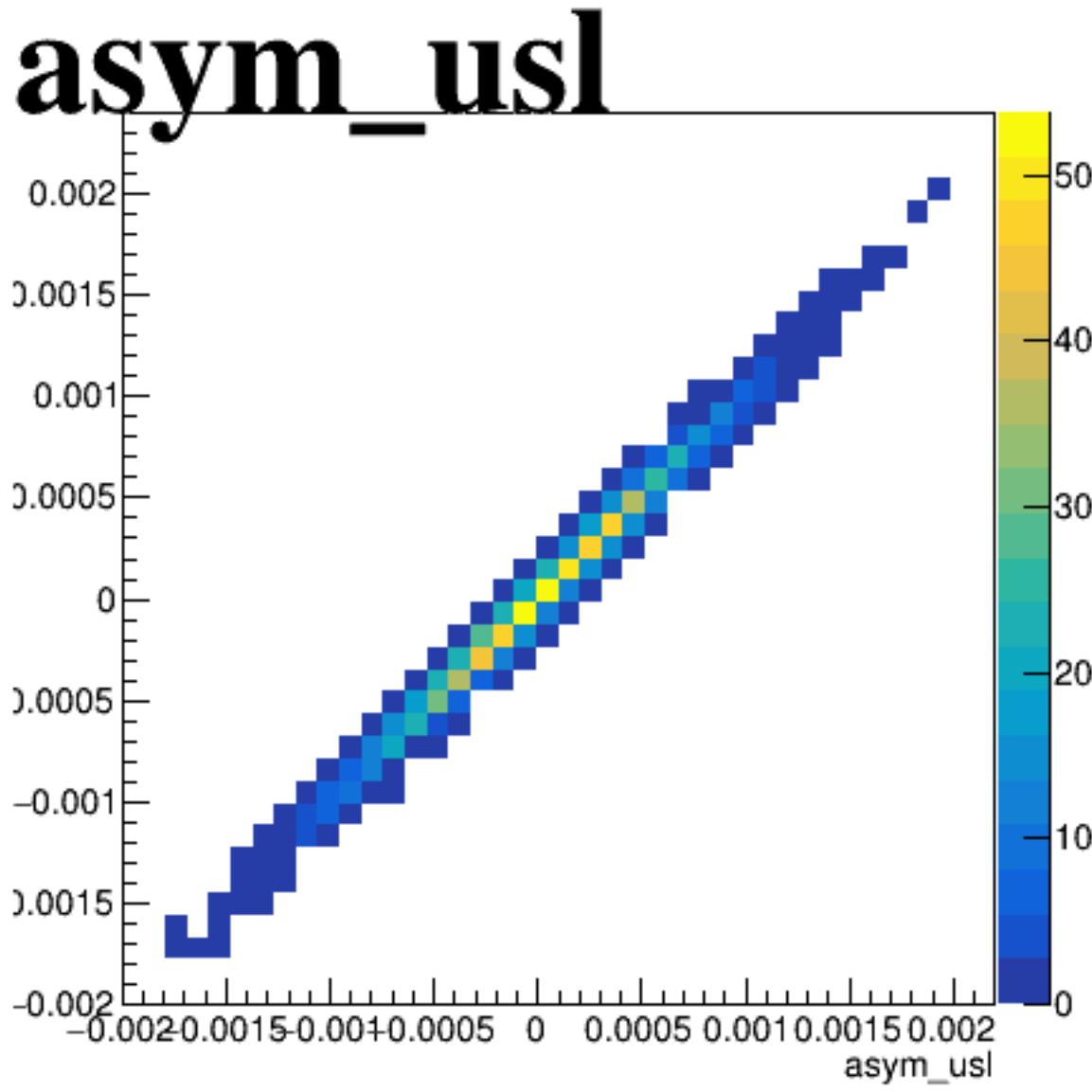
run3453_000_pairTree_dsr.png



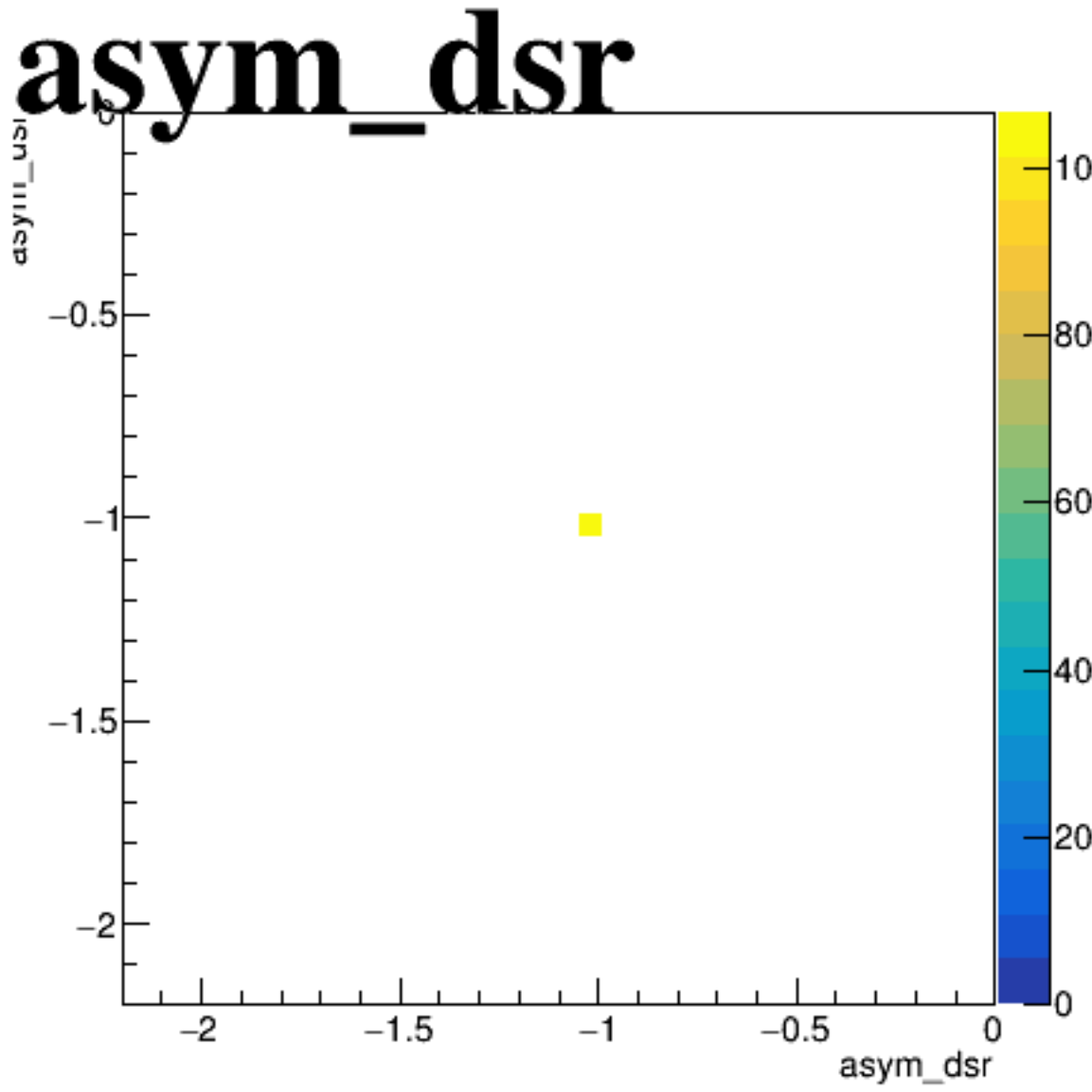
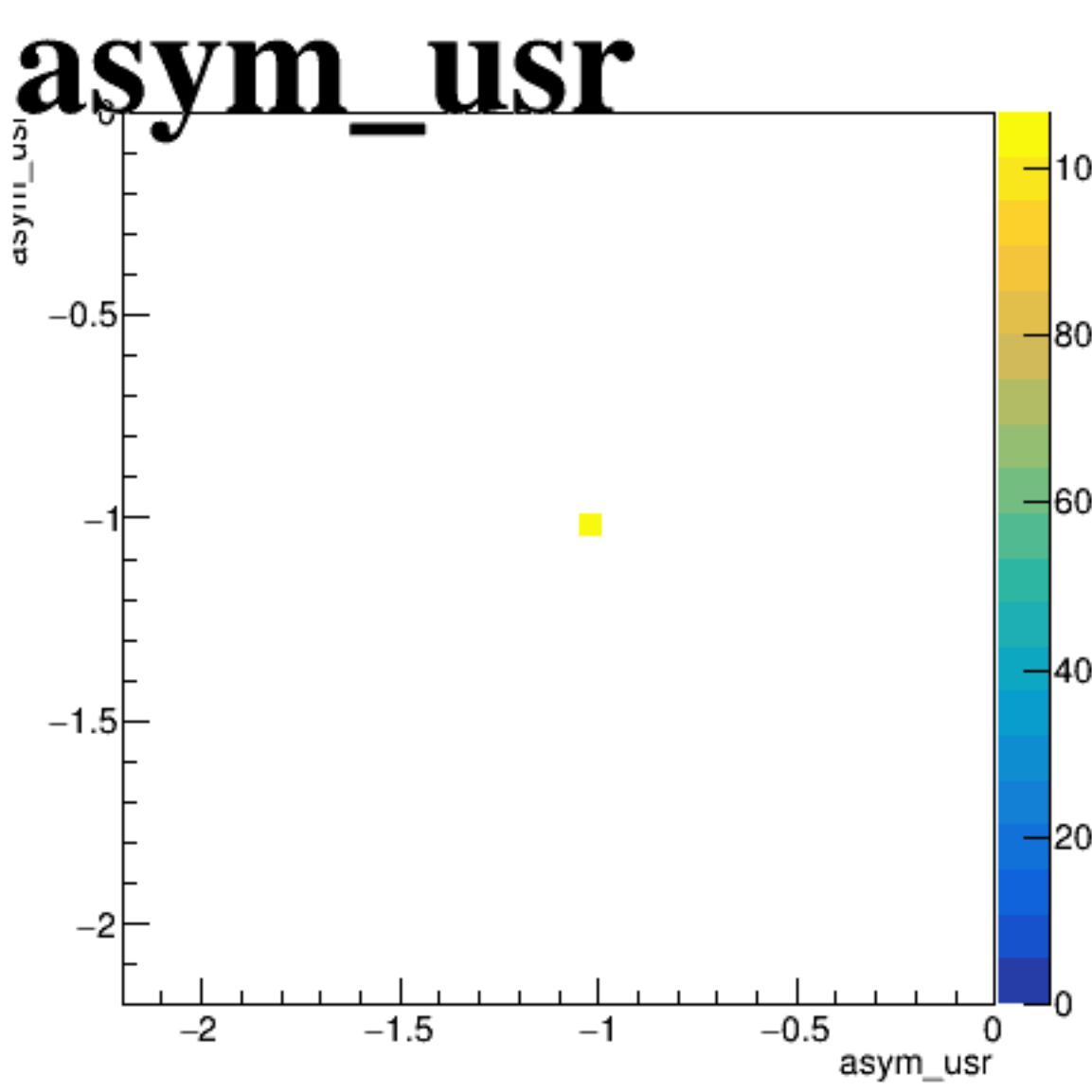
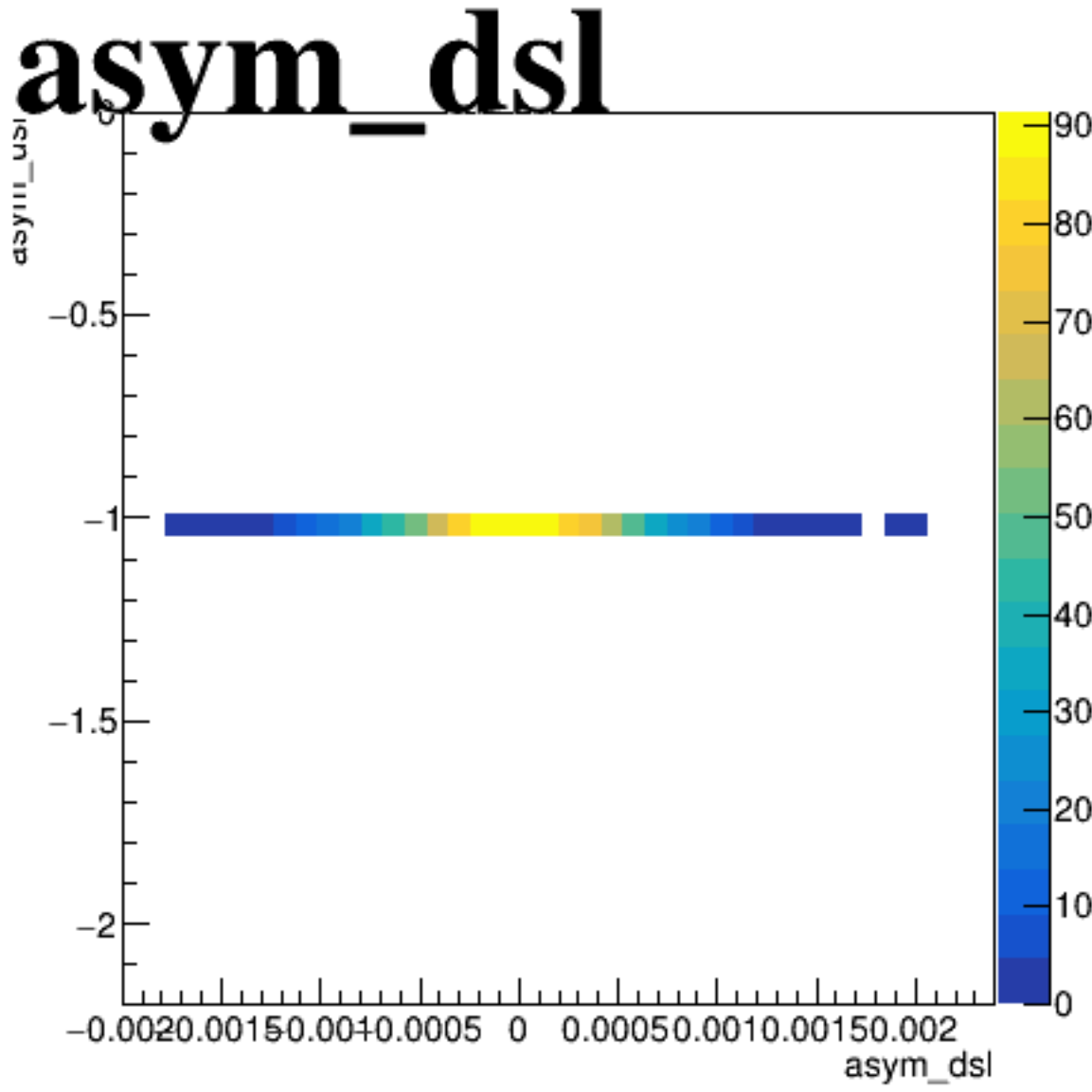
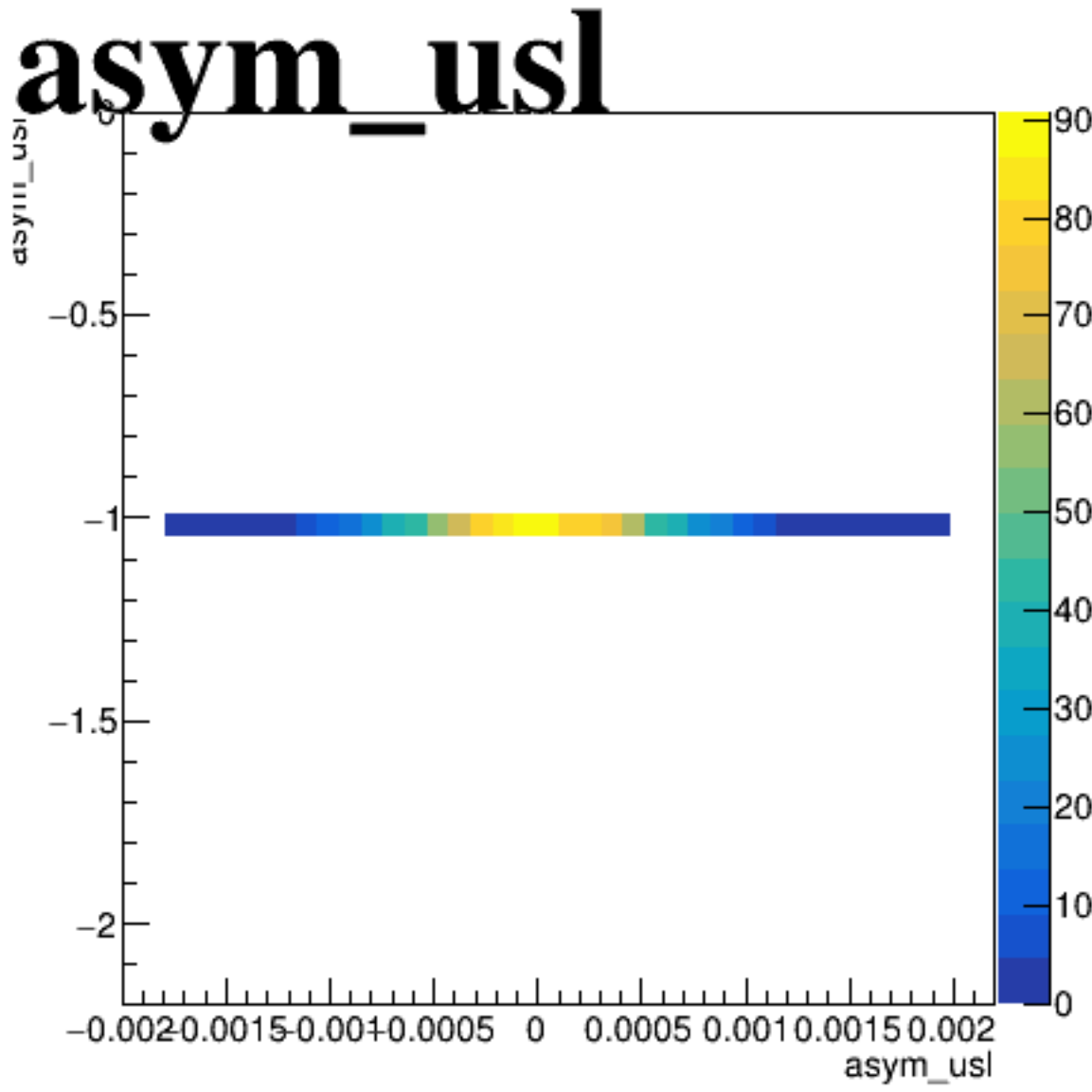
asym_usl



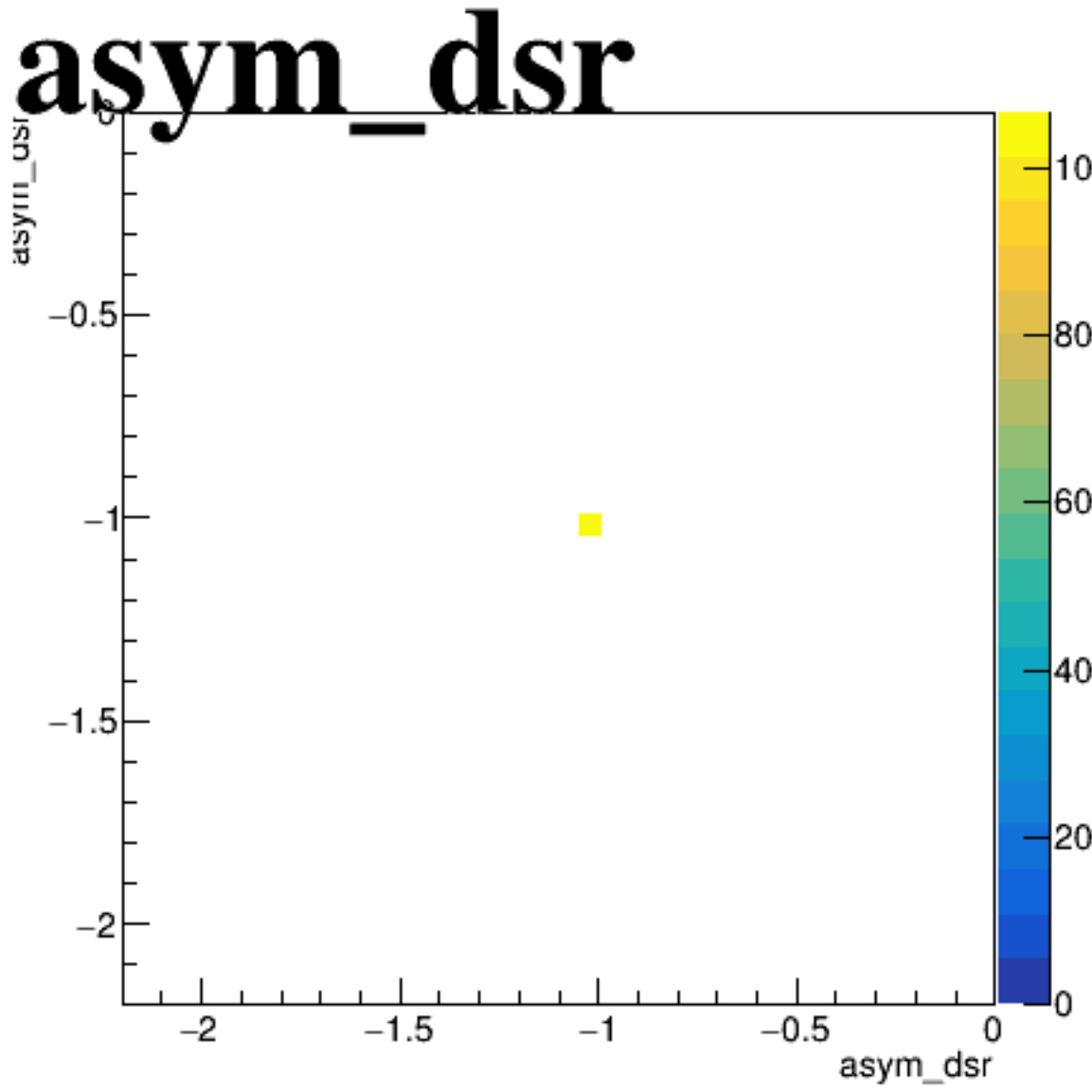
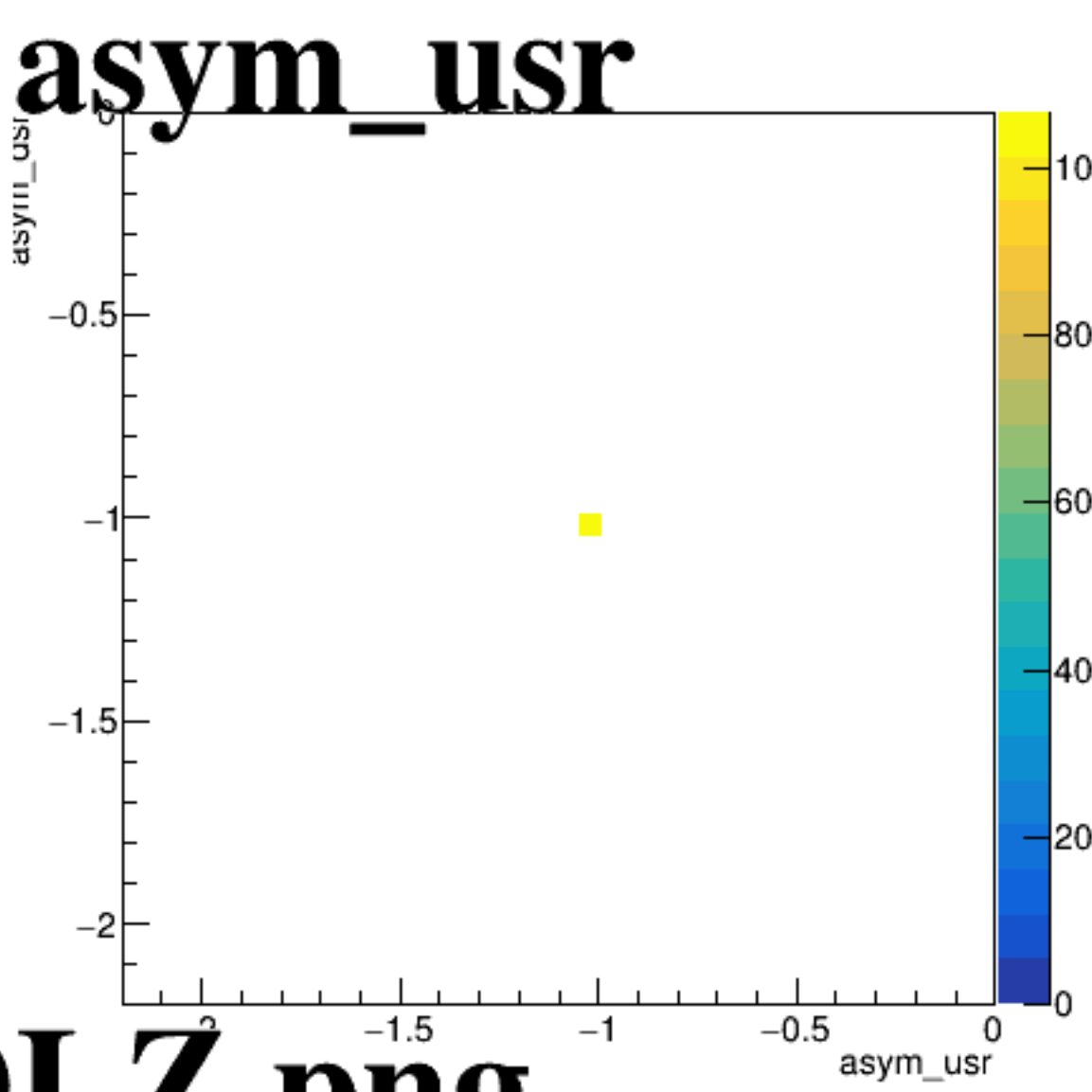
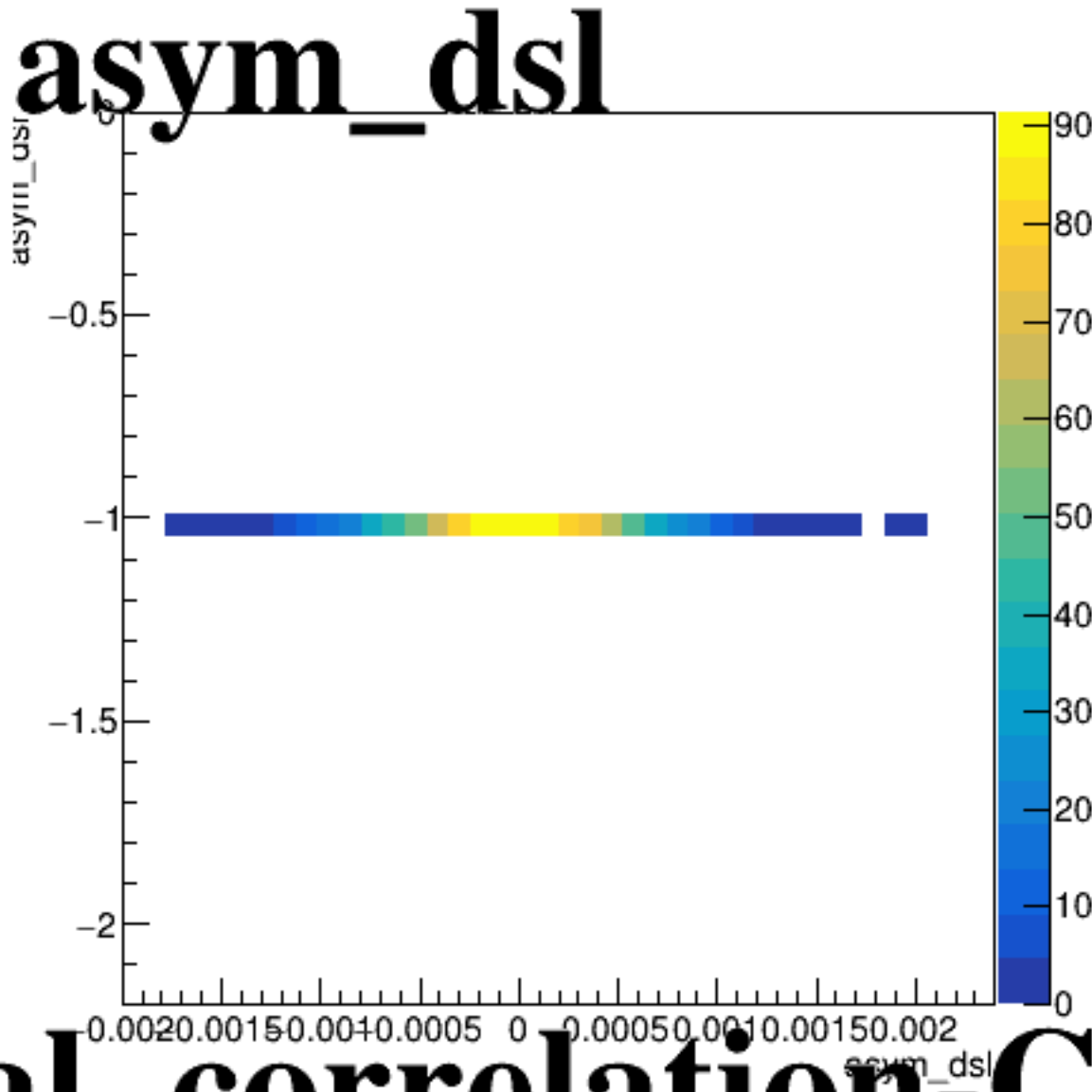
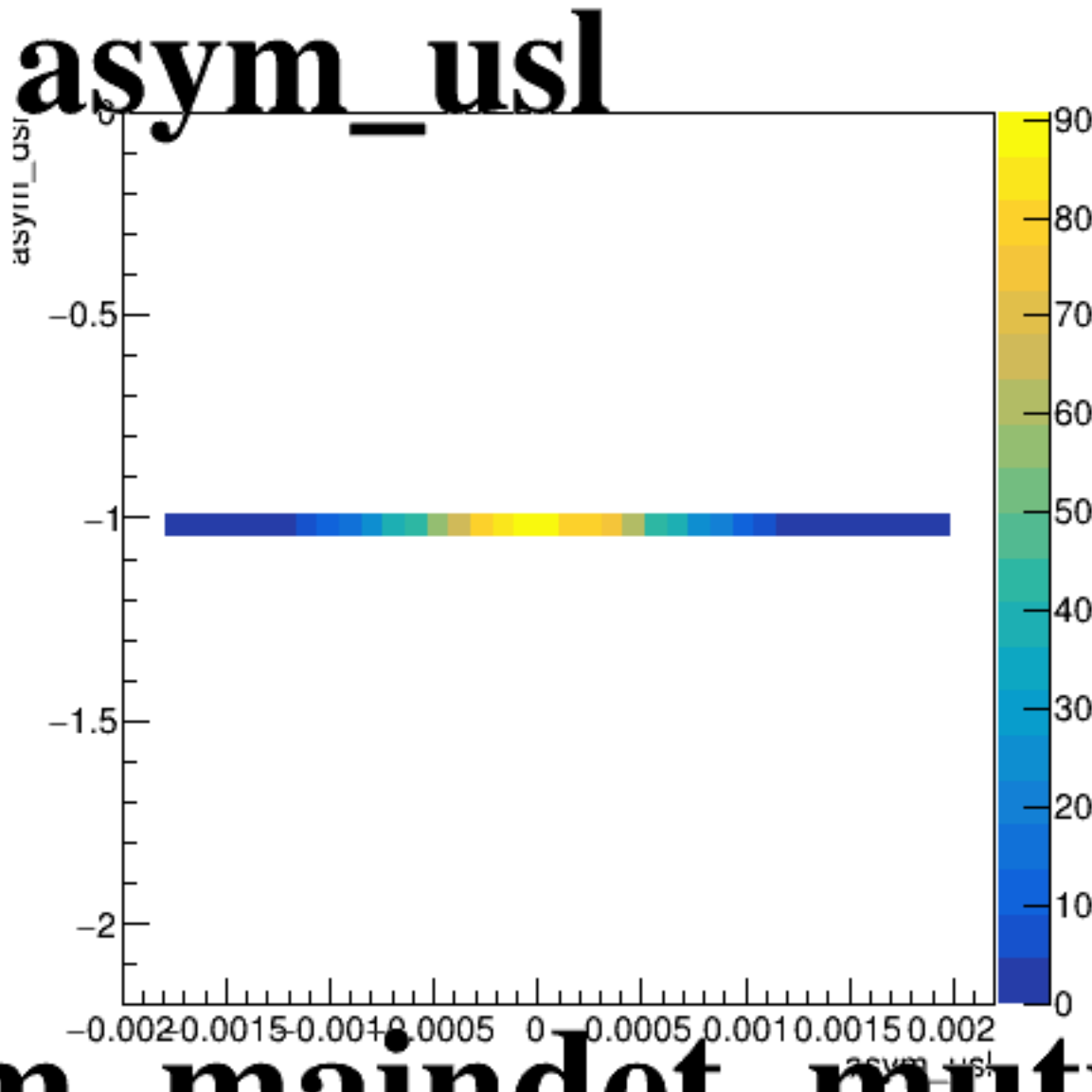
asym_dsl



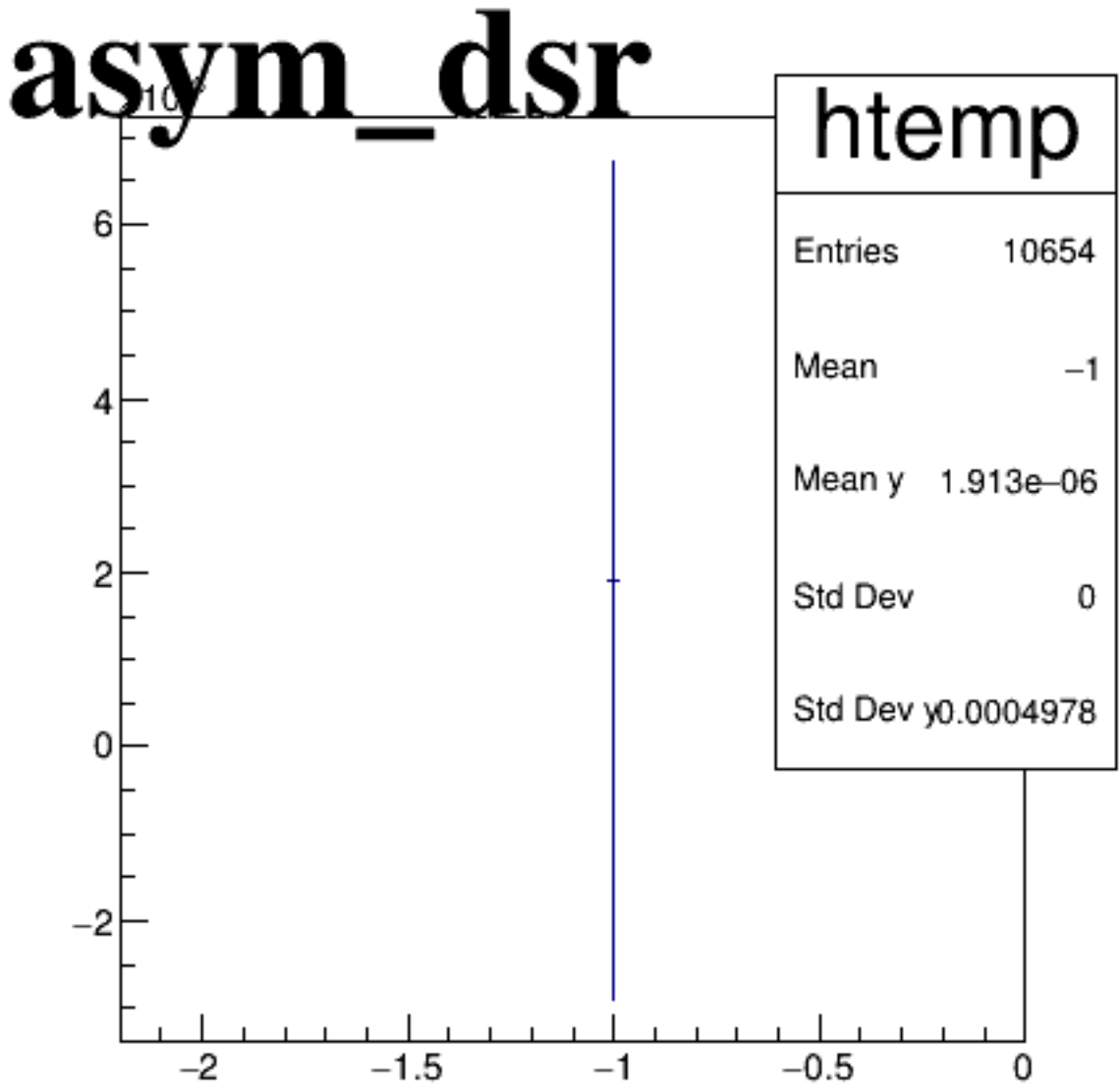
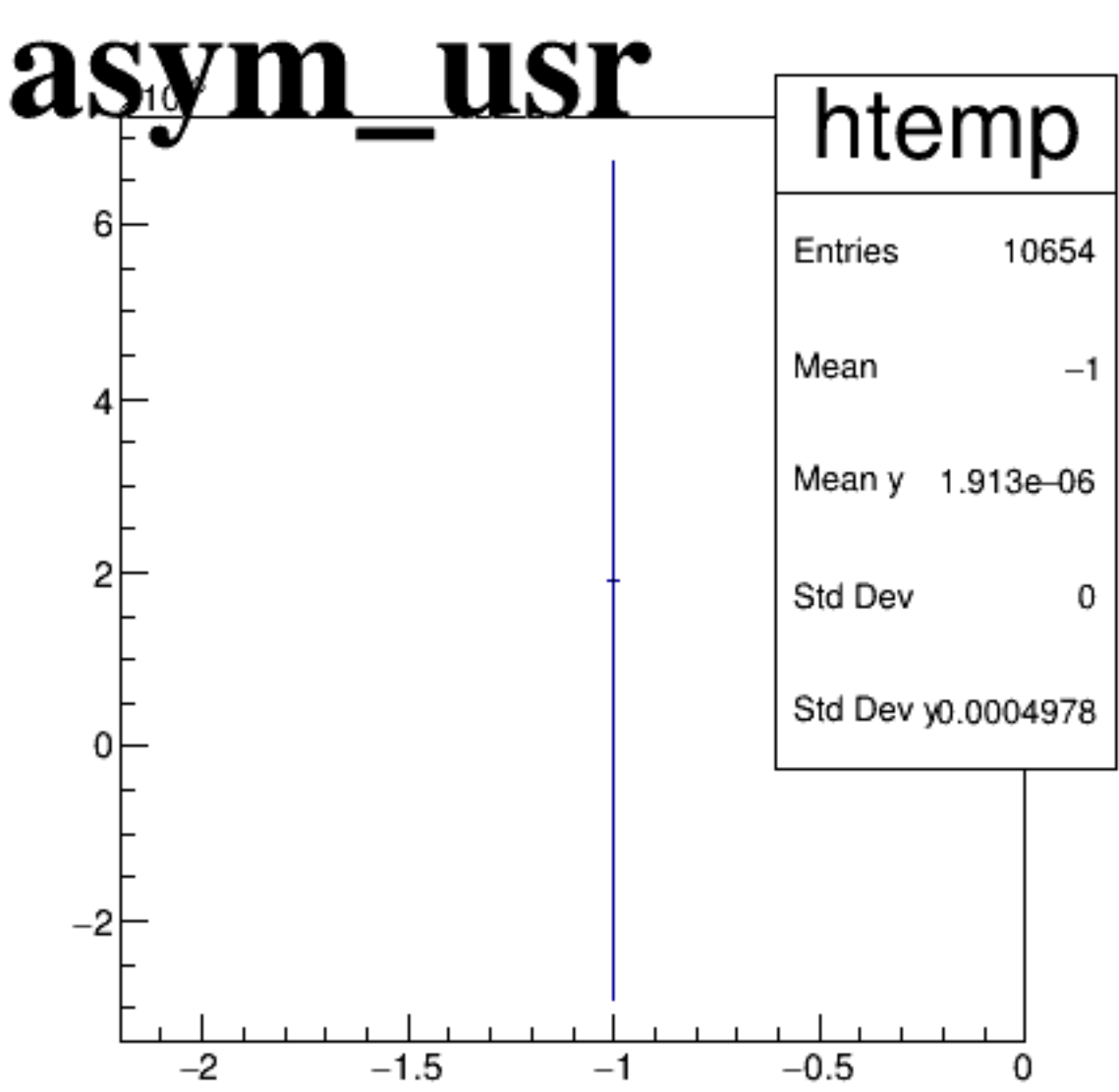
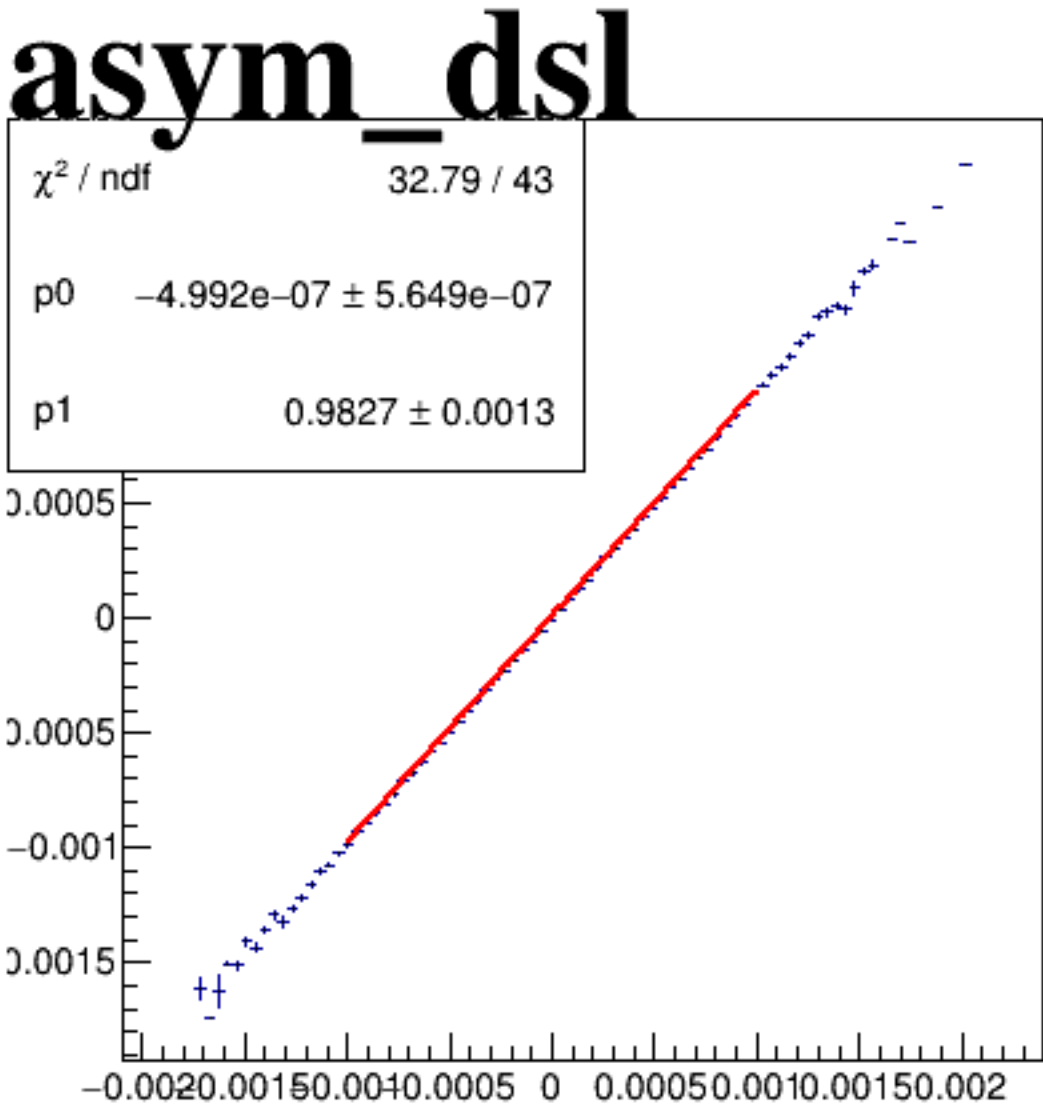
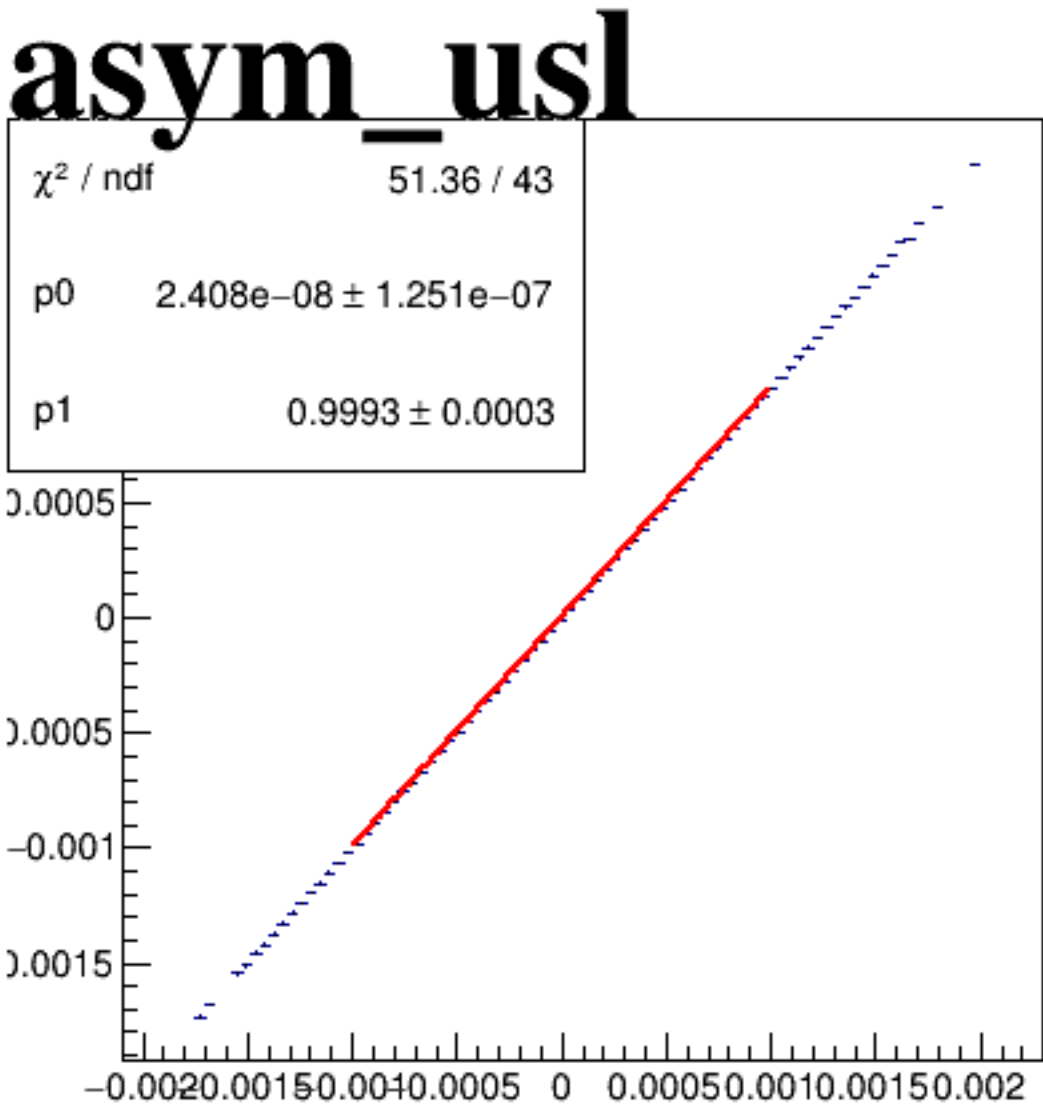
asym_usr



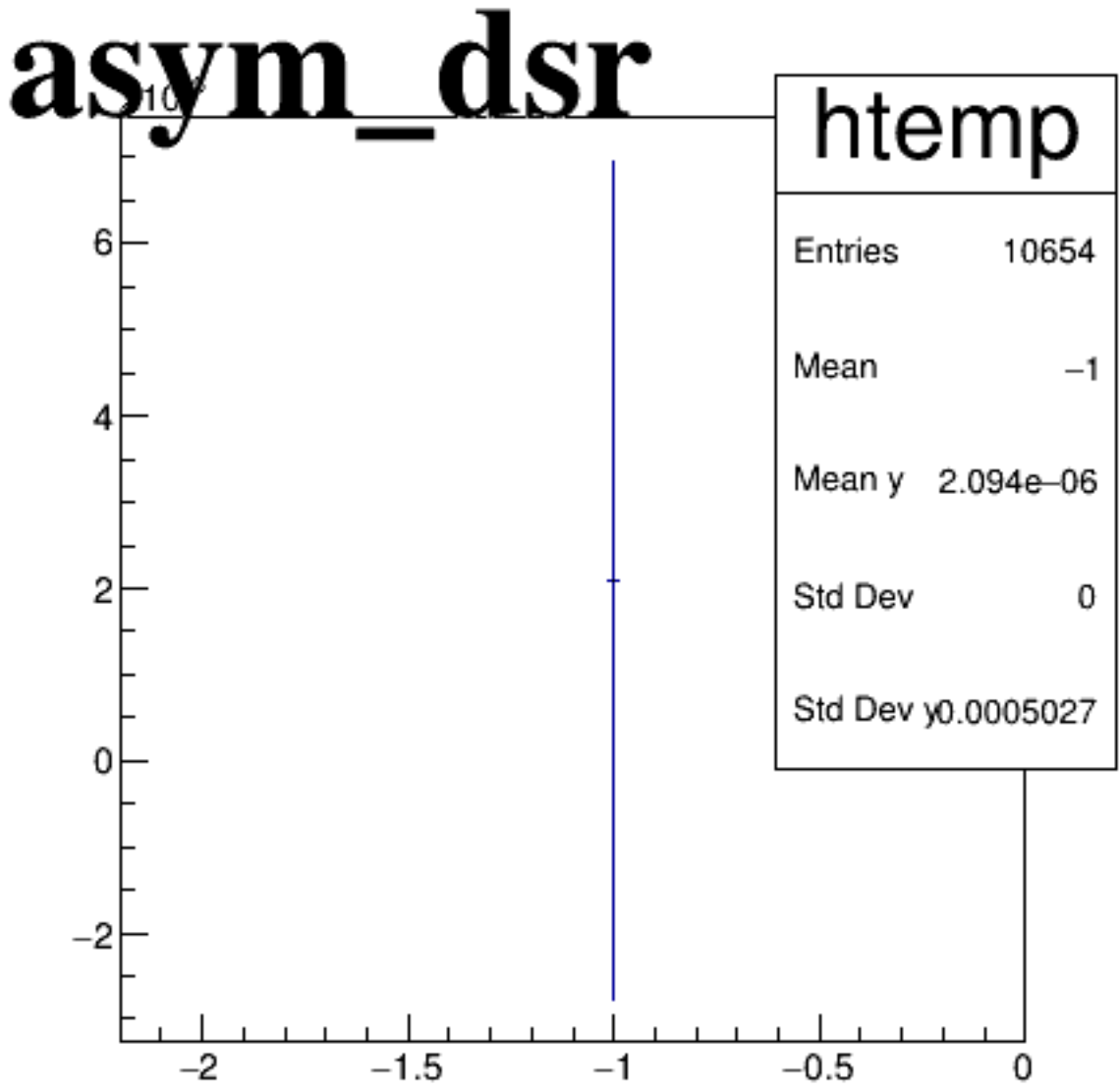
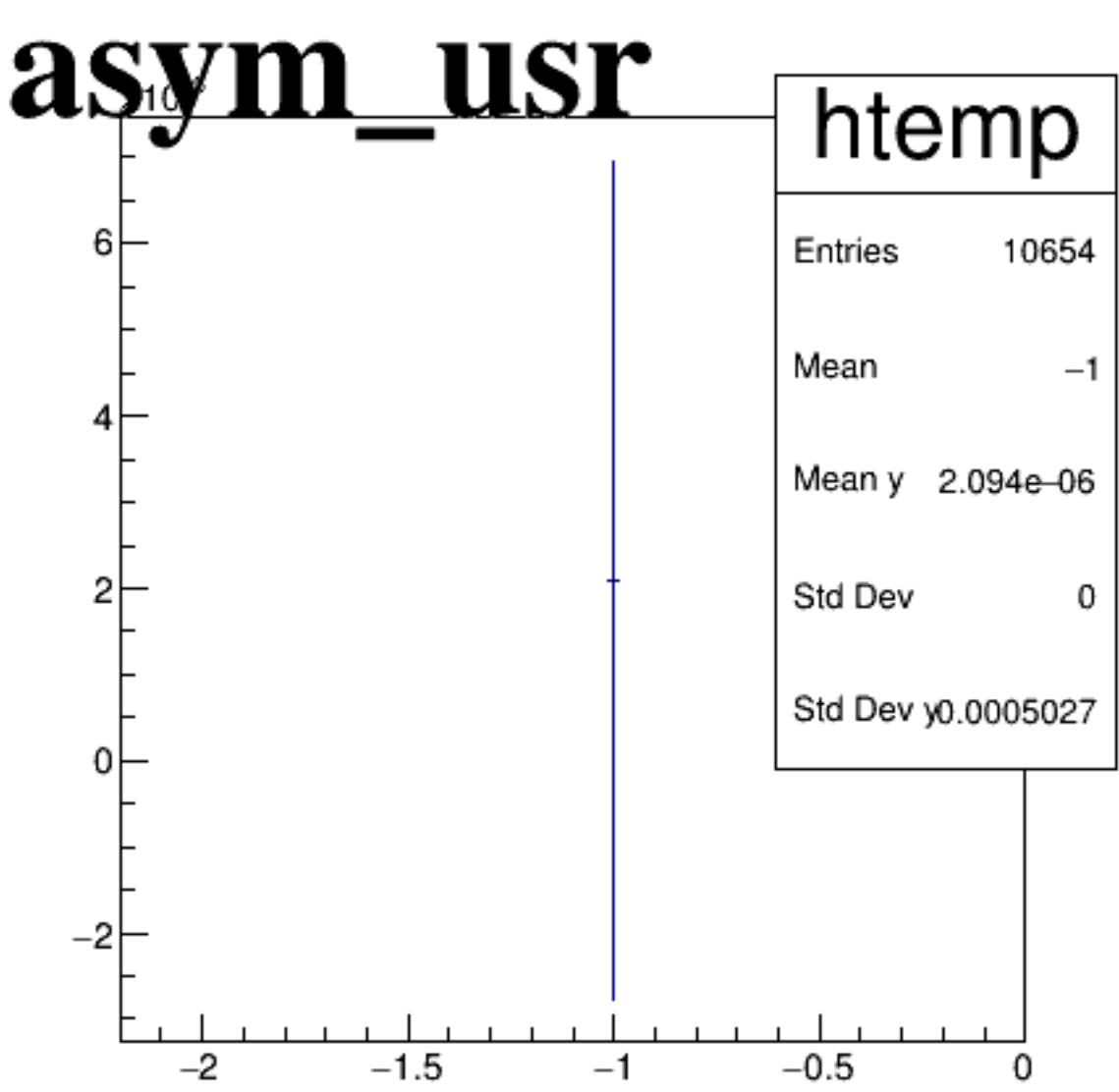
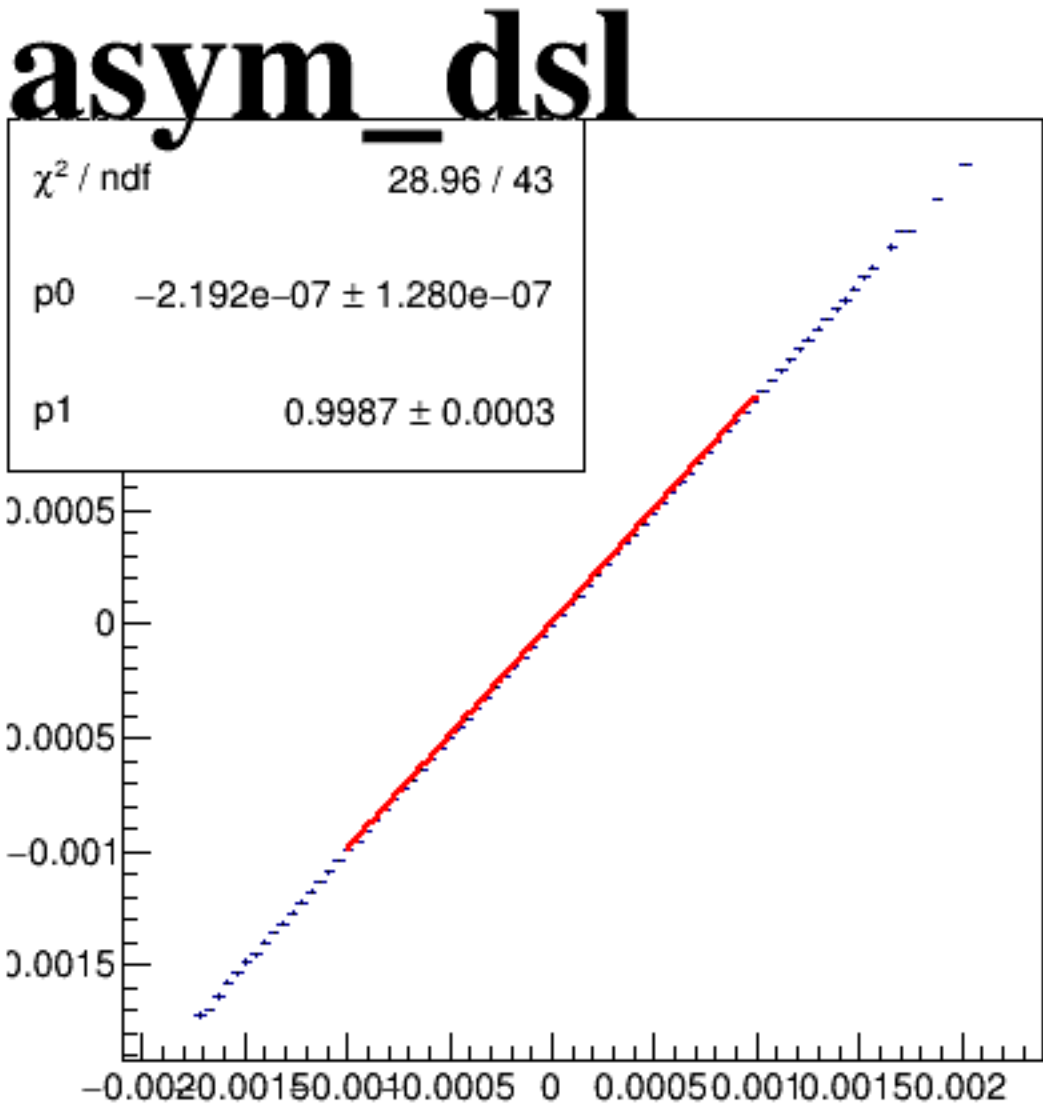
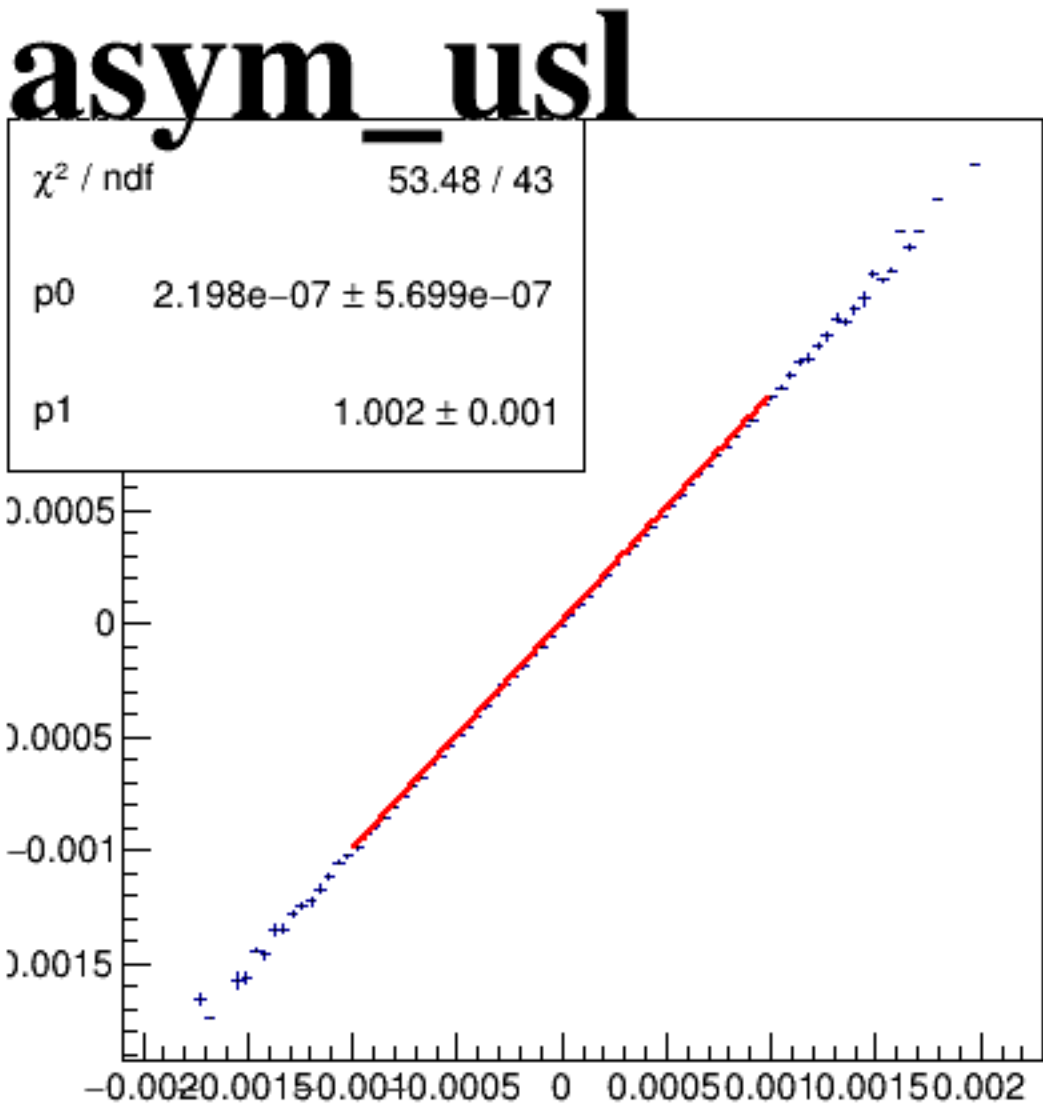
asym_dsr



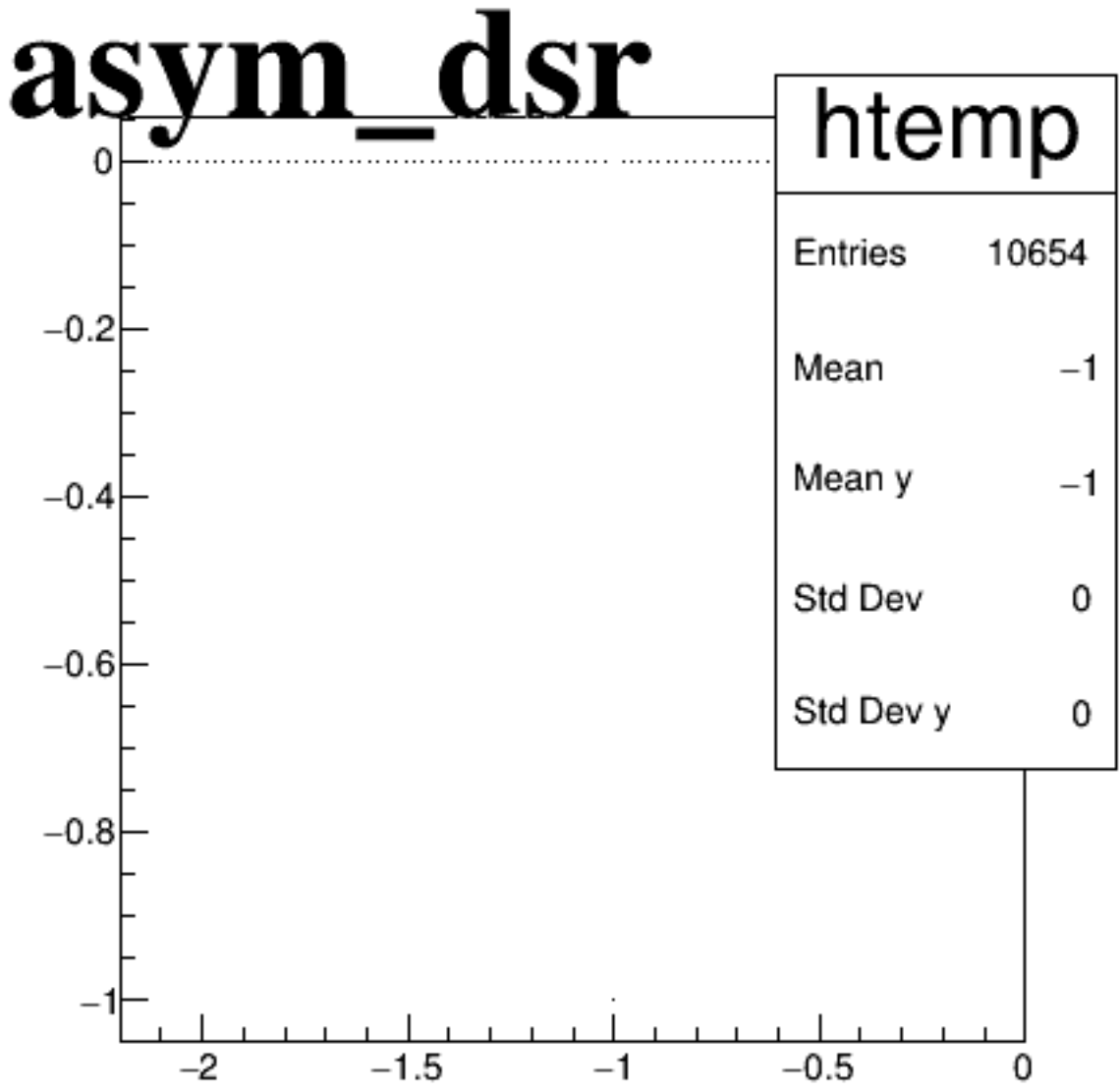
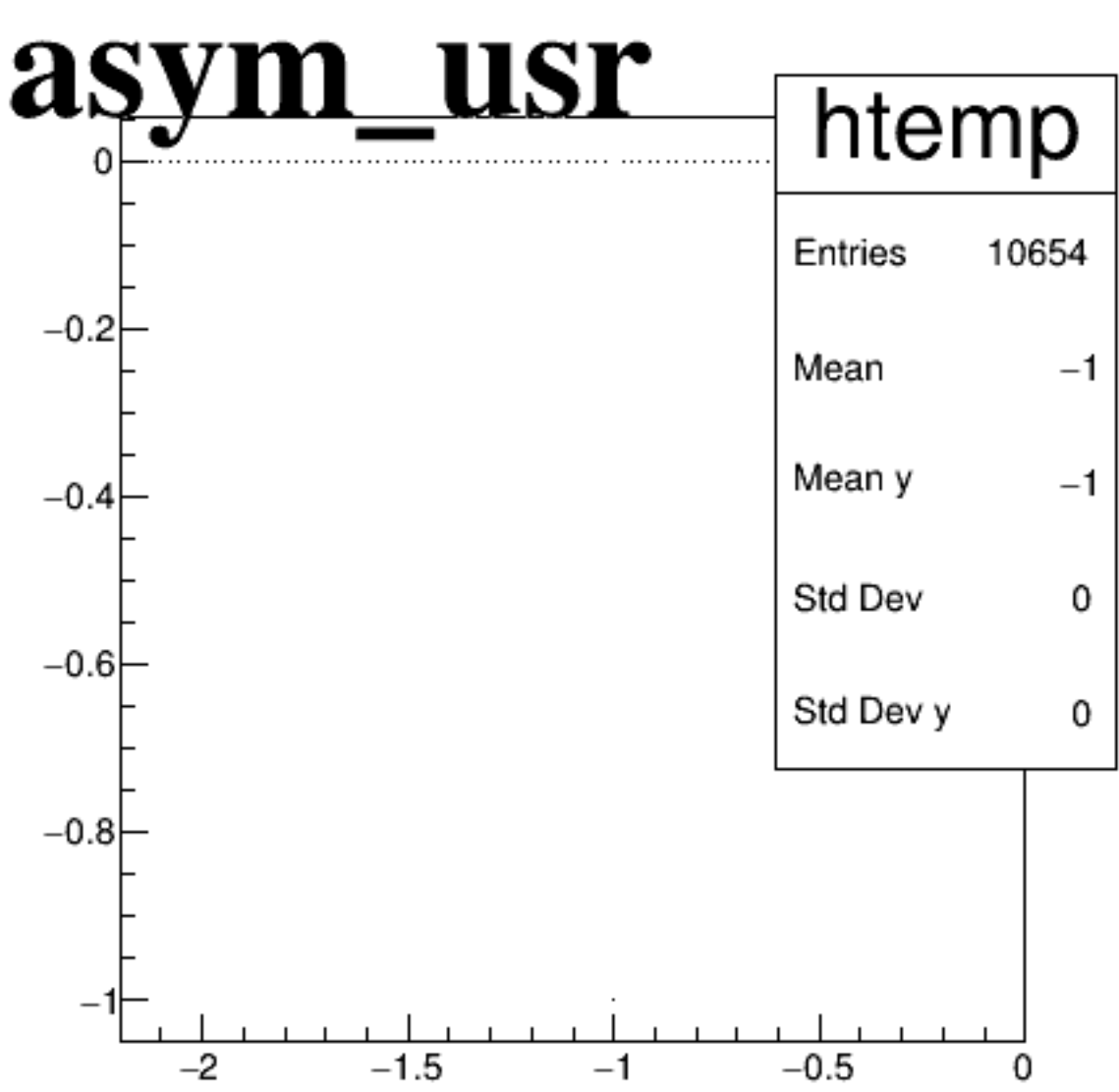
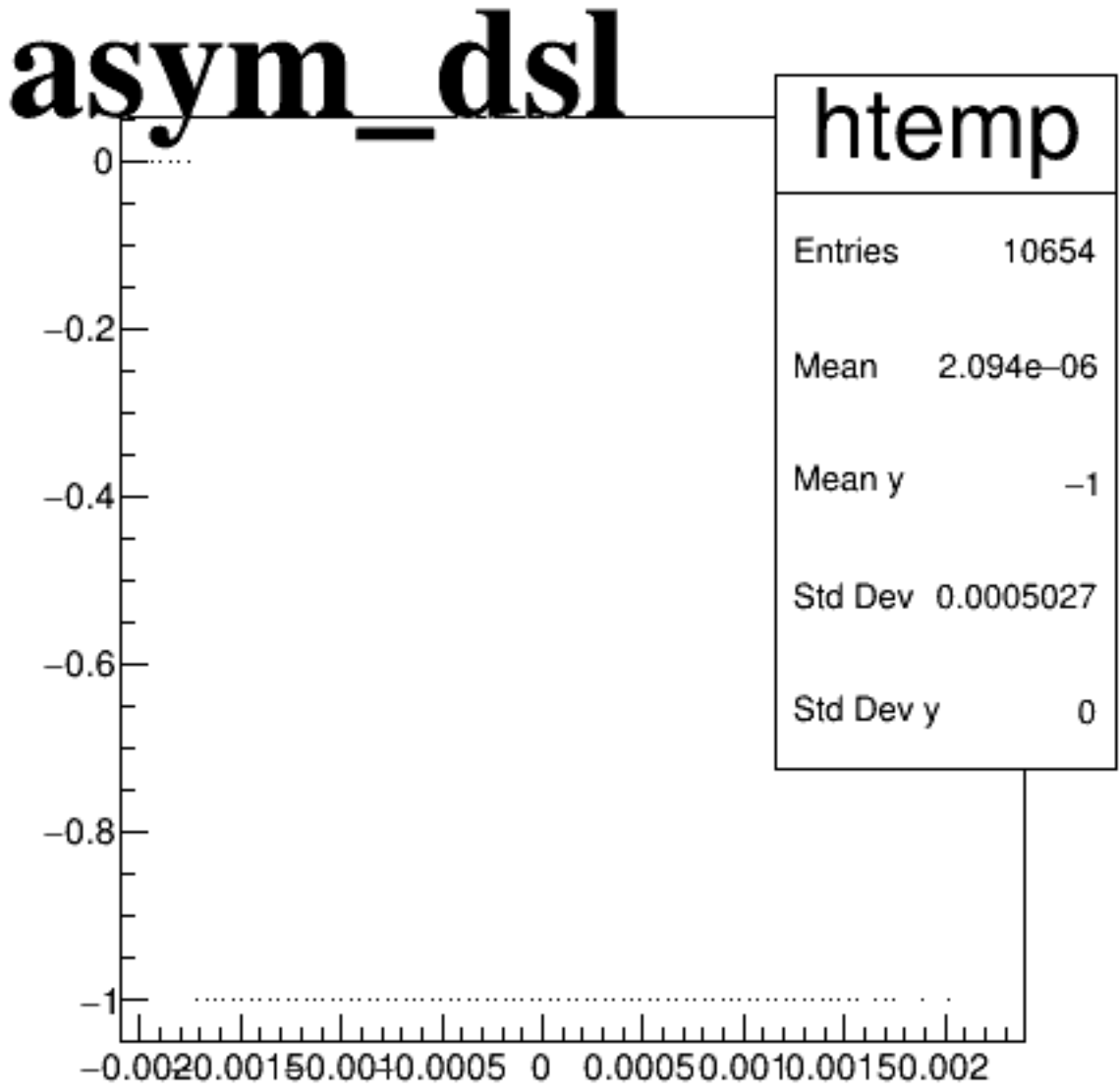
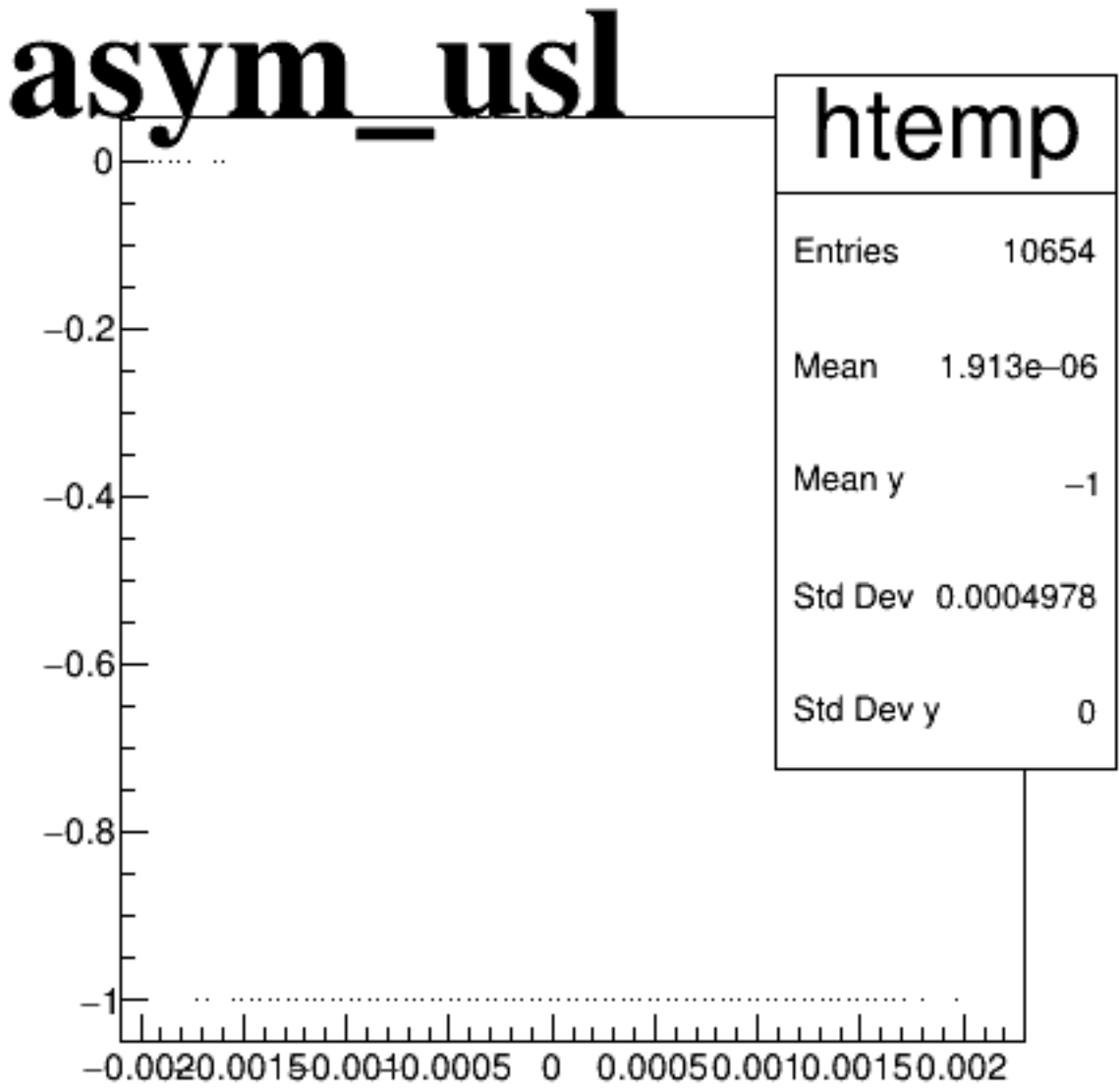
asym_usl



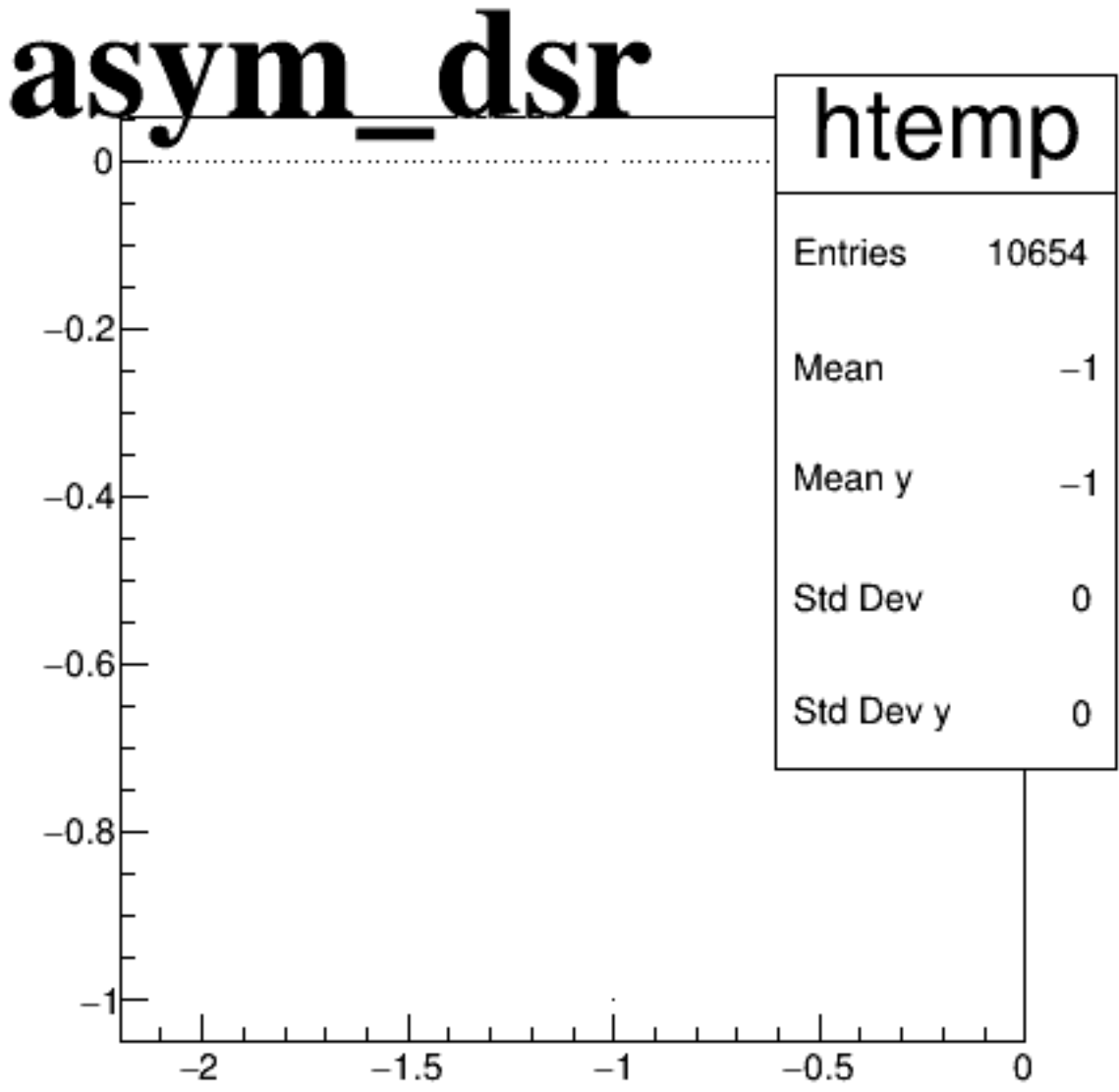
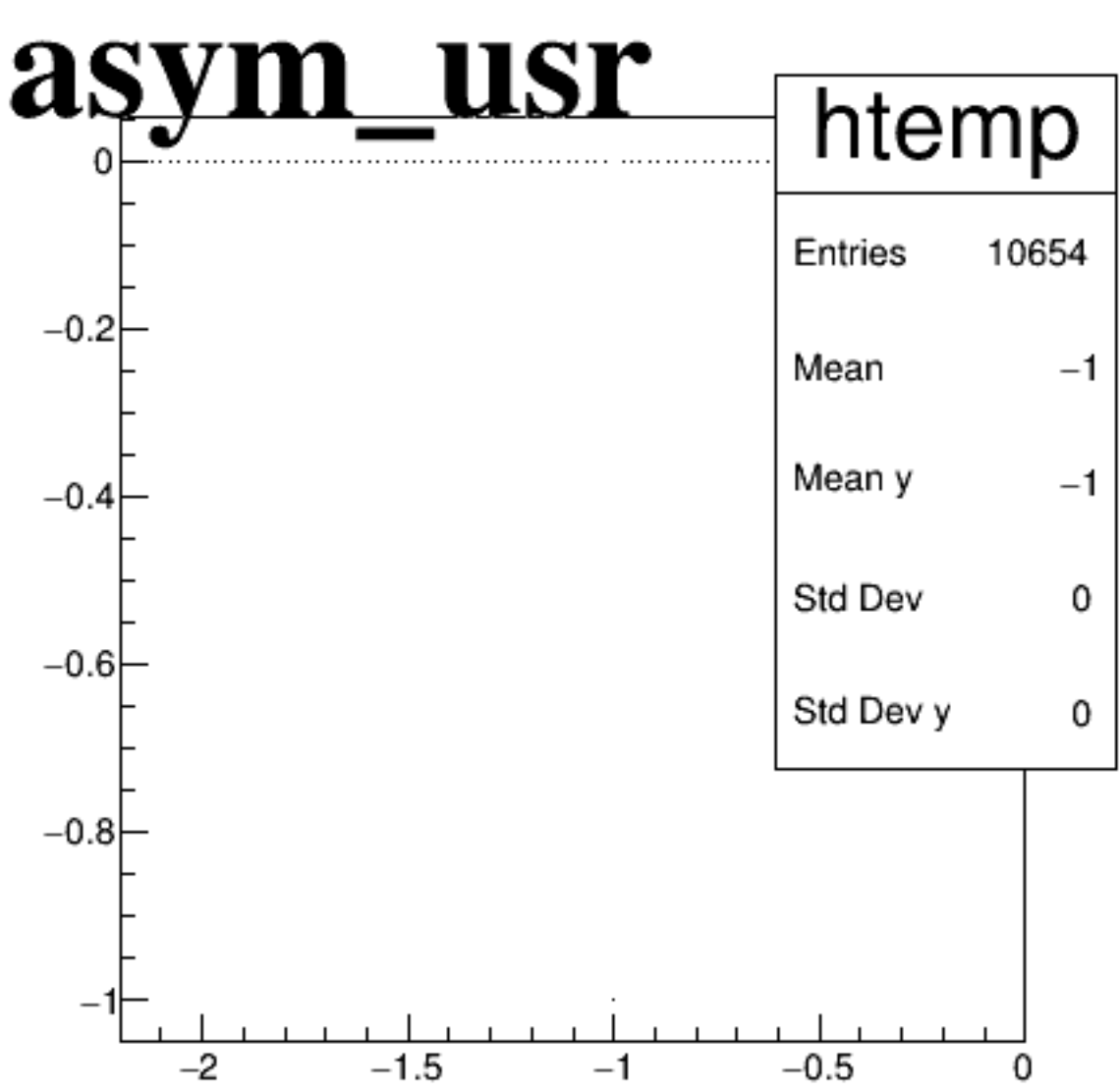
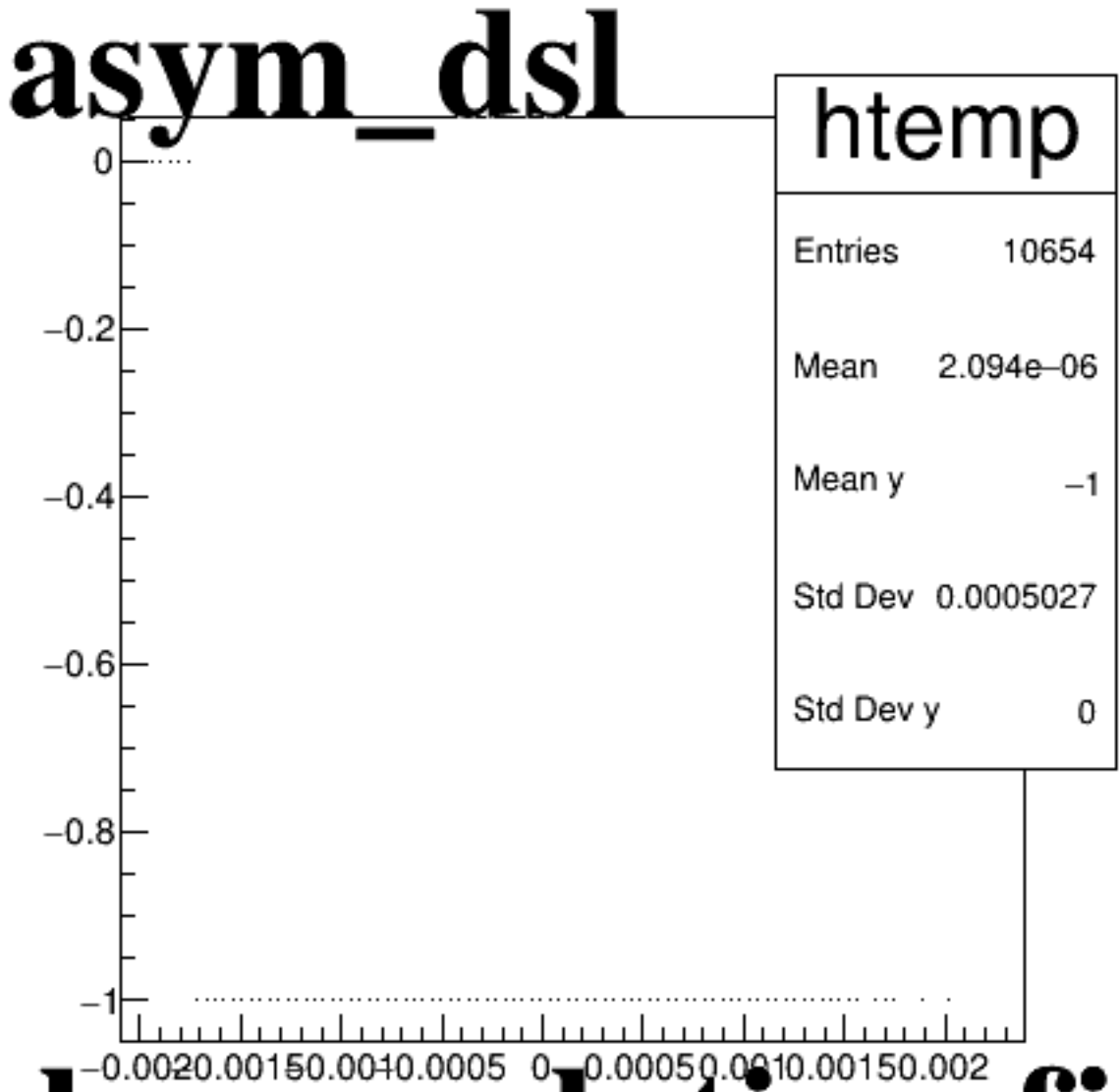
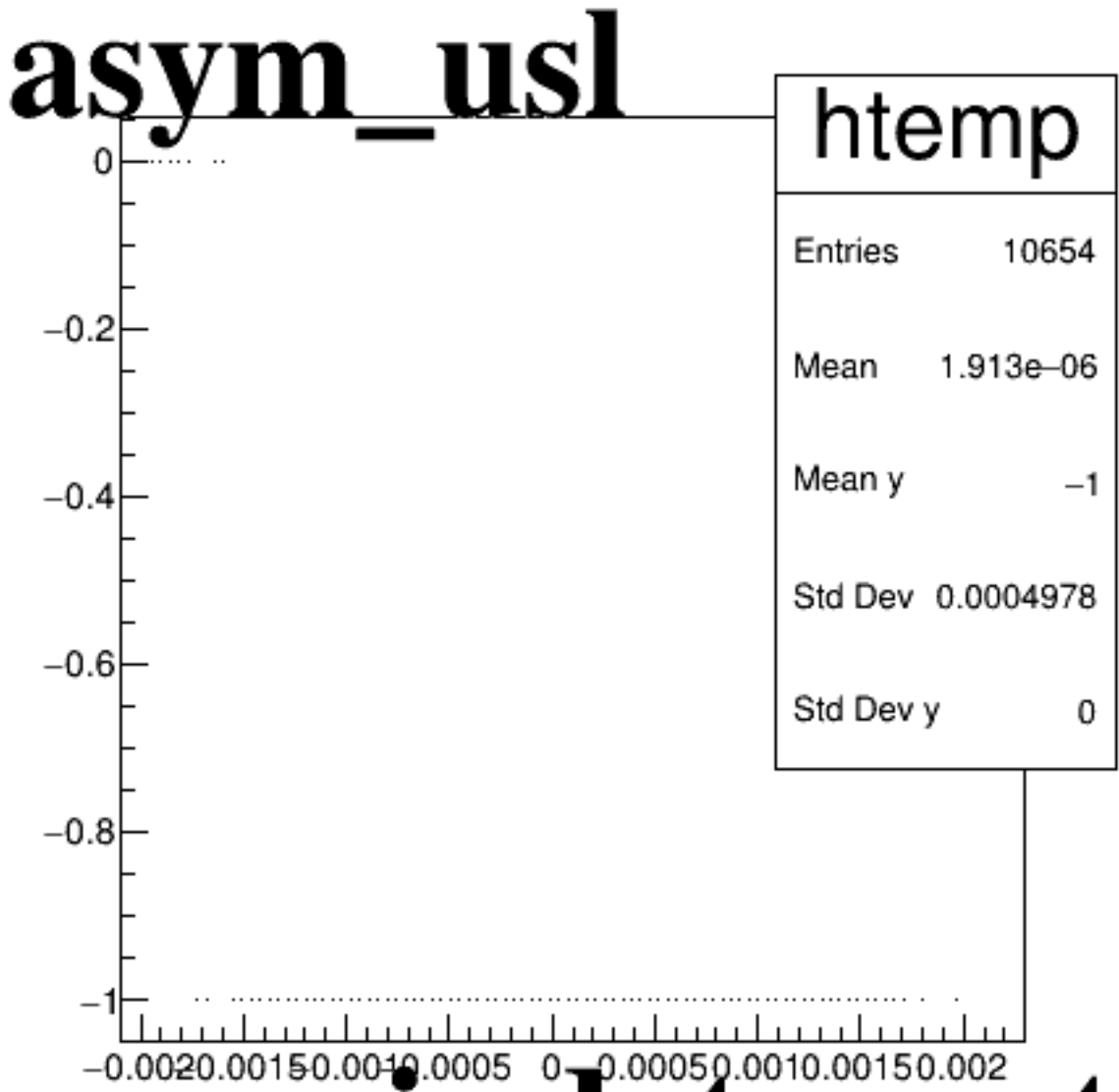
asym_dsl



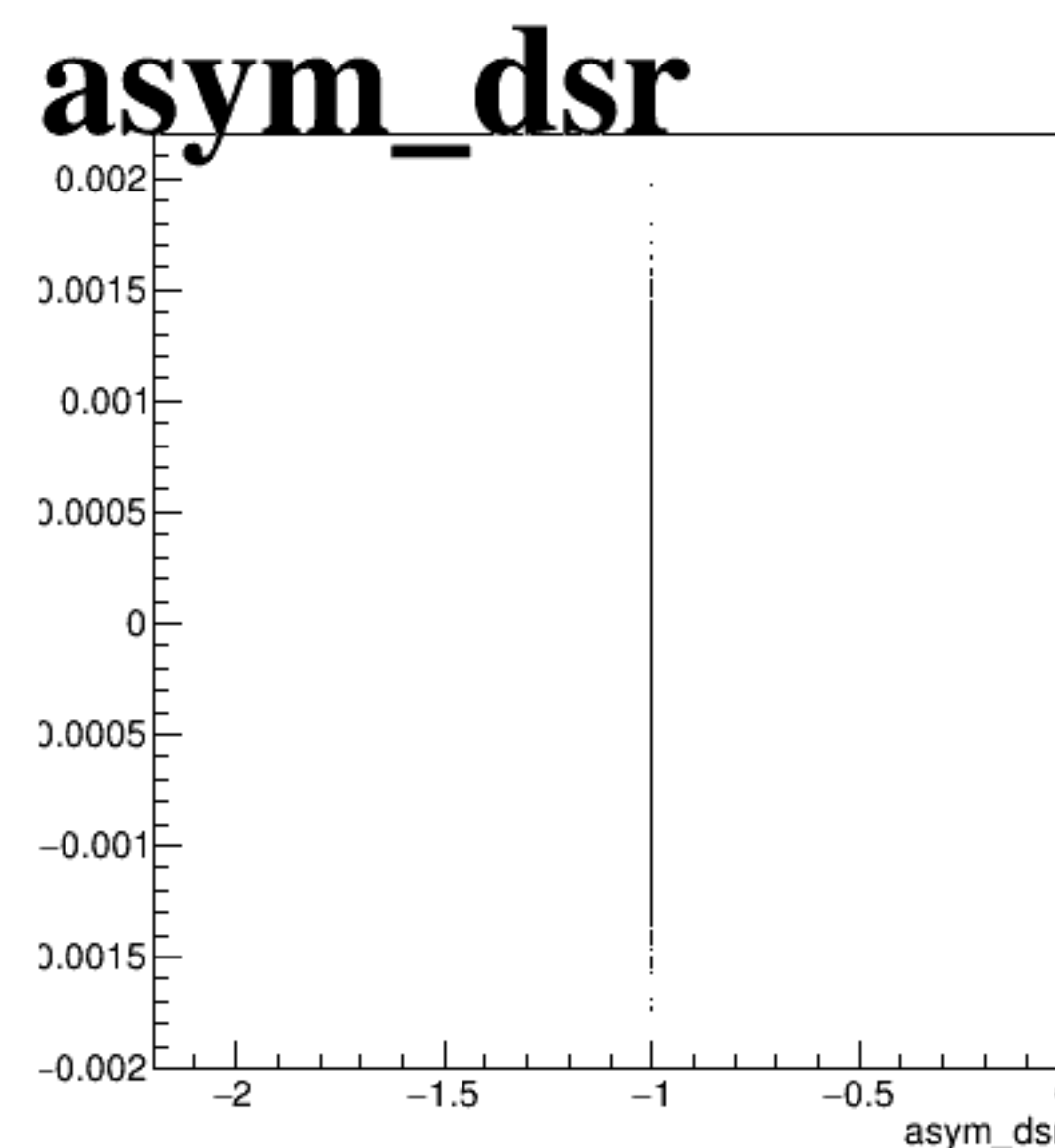
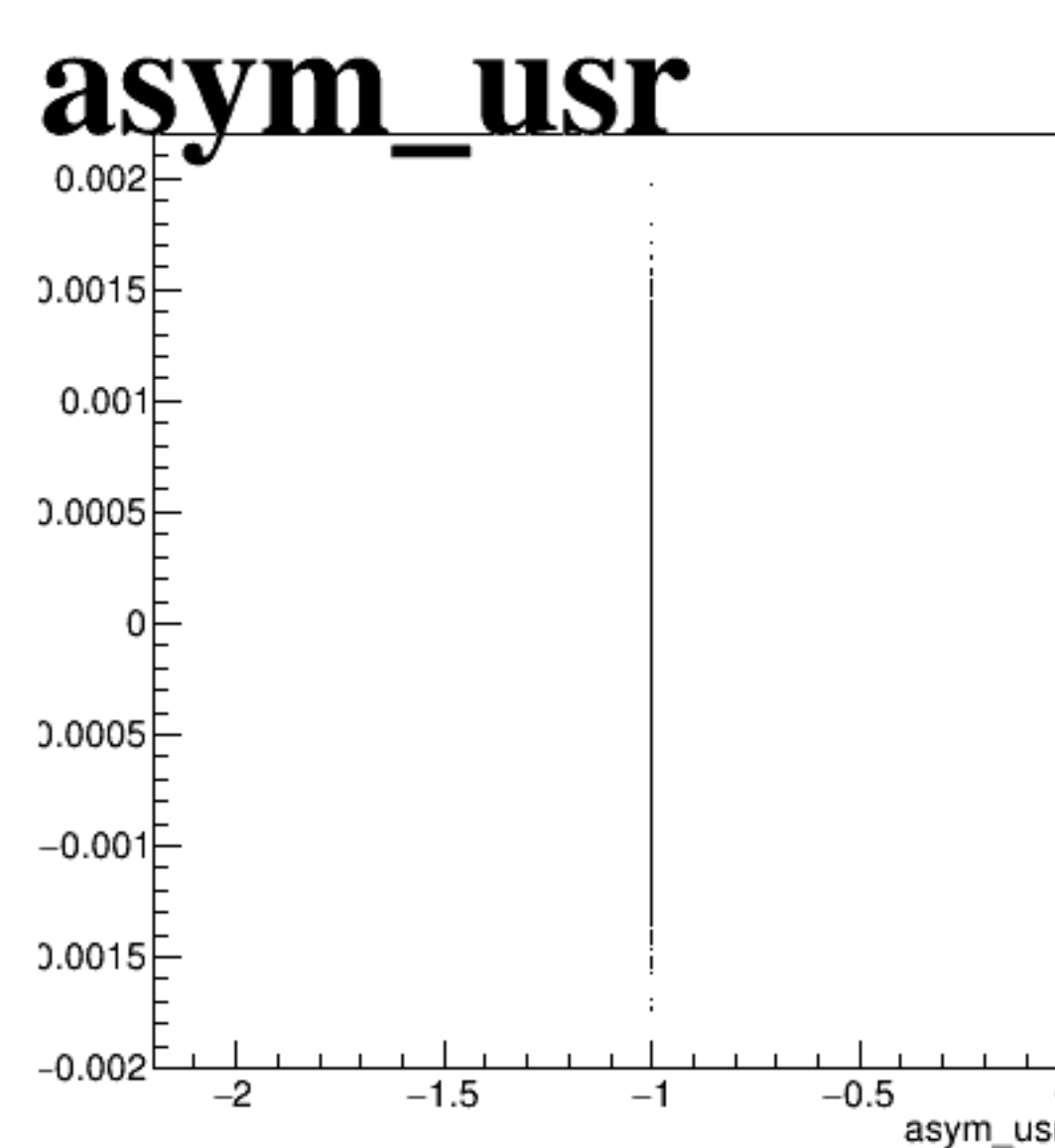
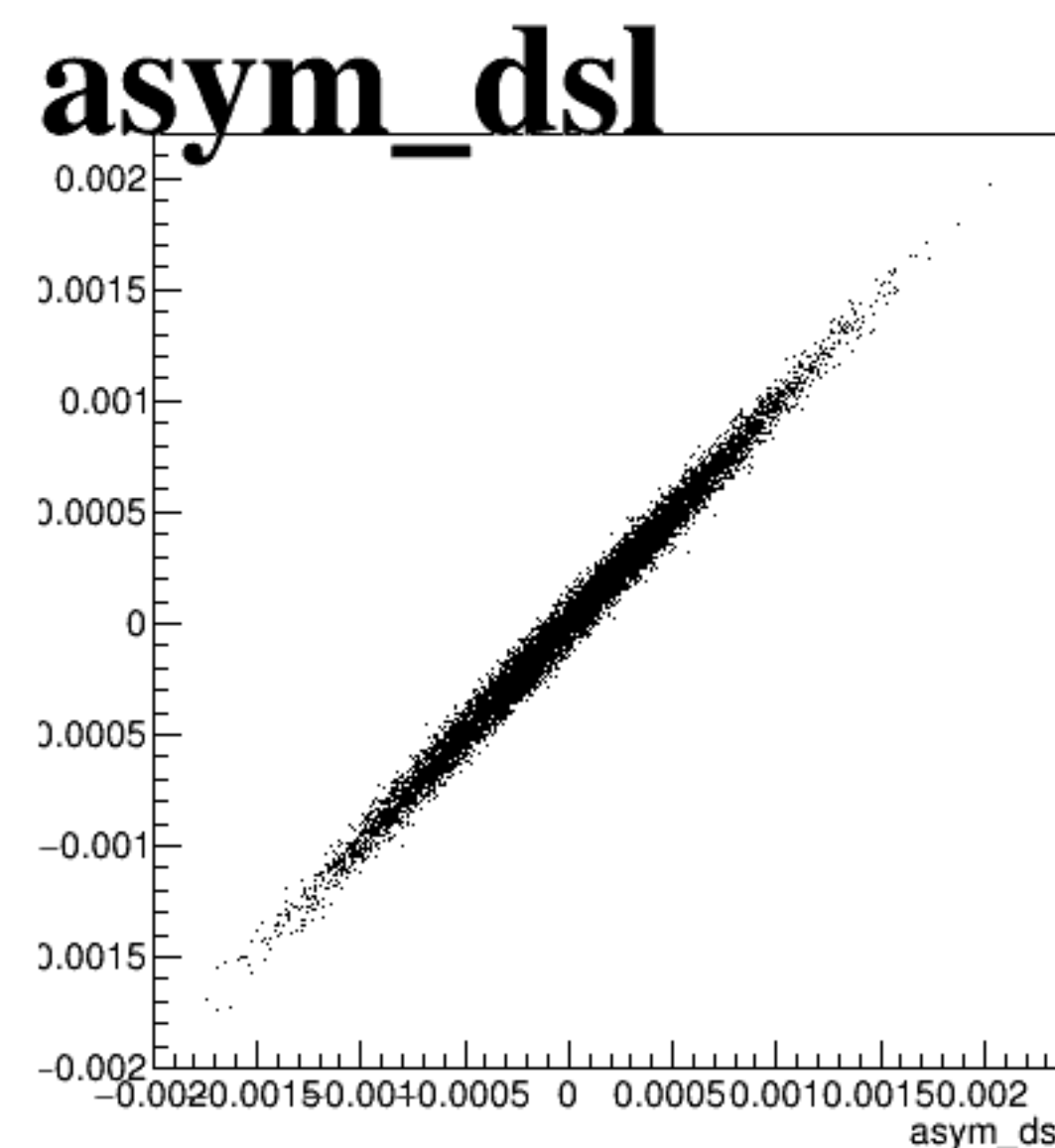
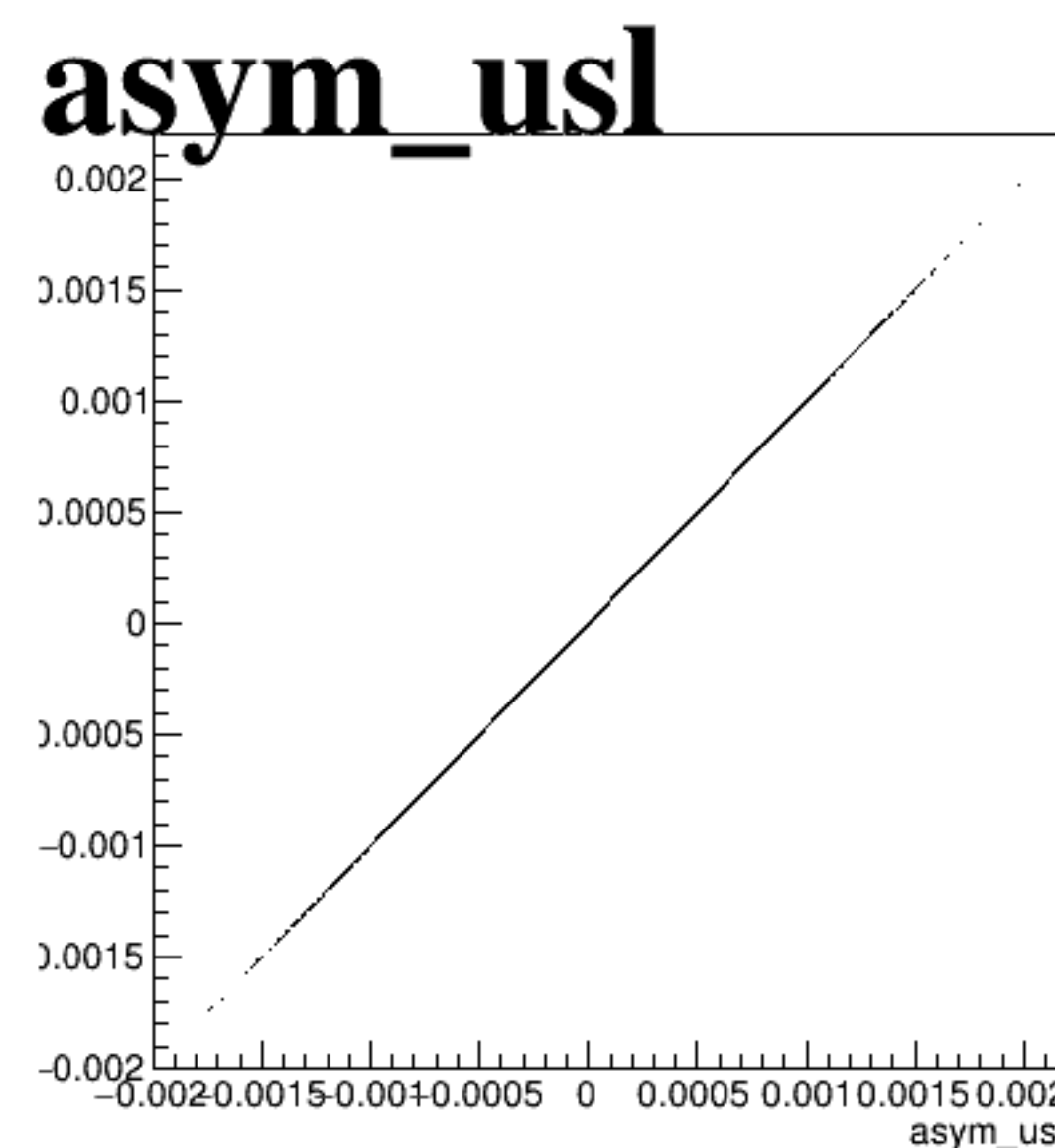
asym_usr



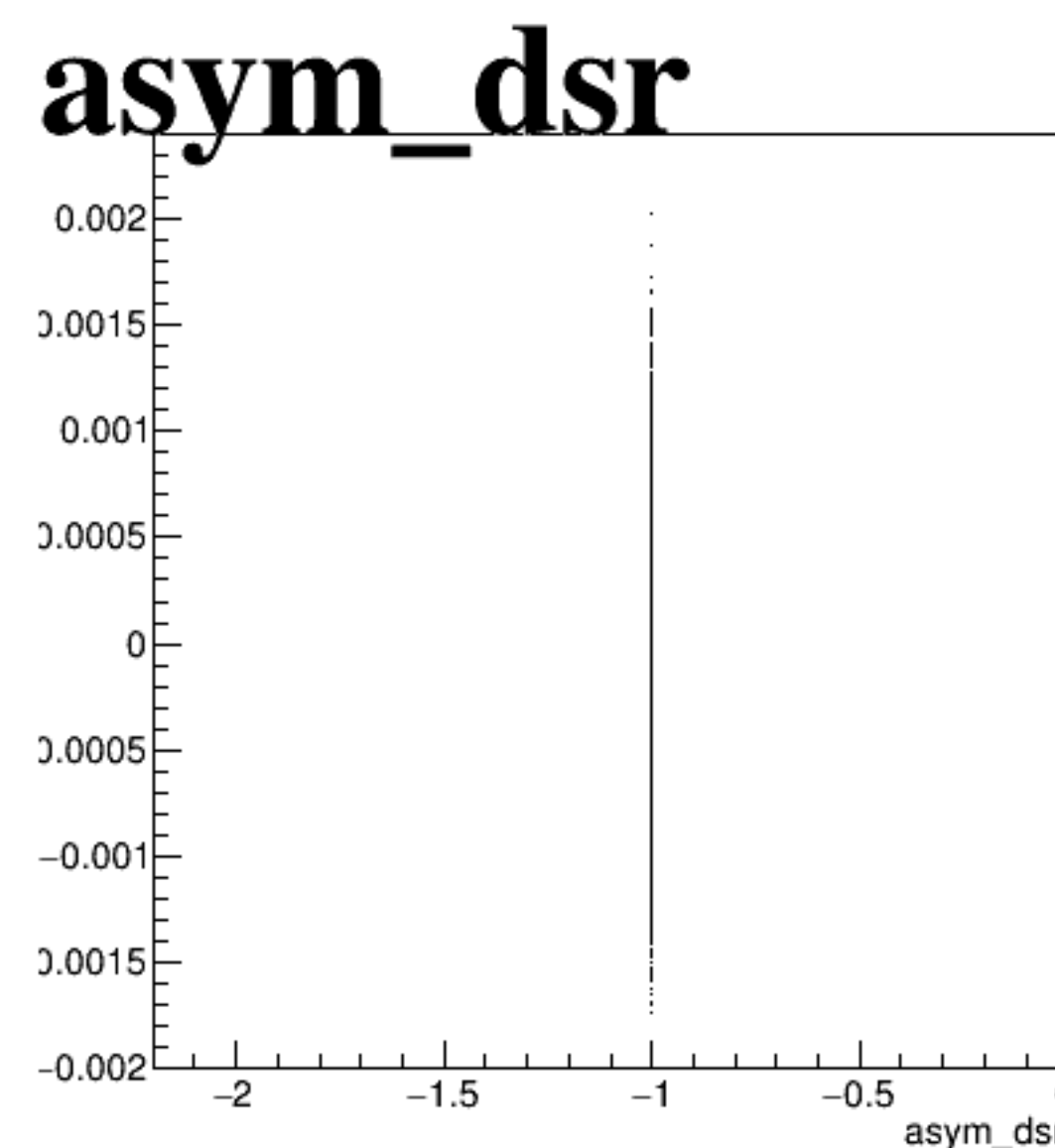
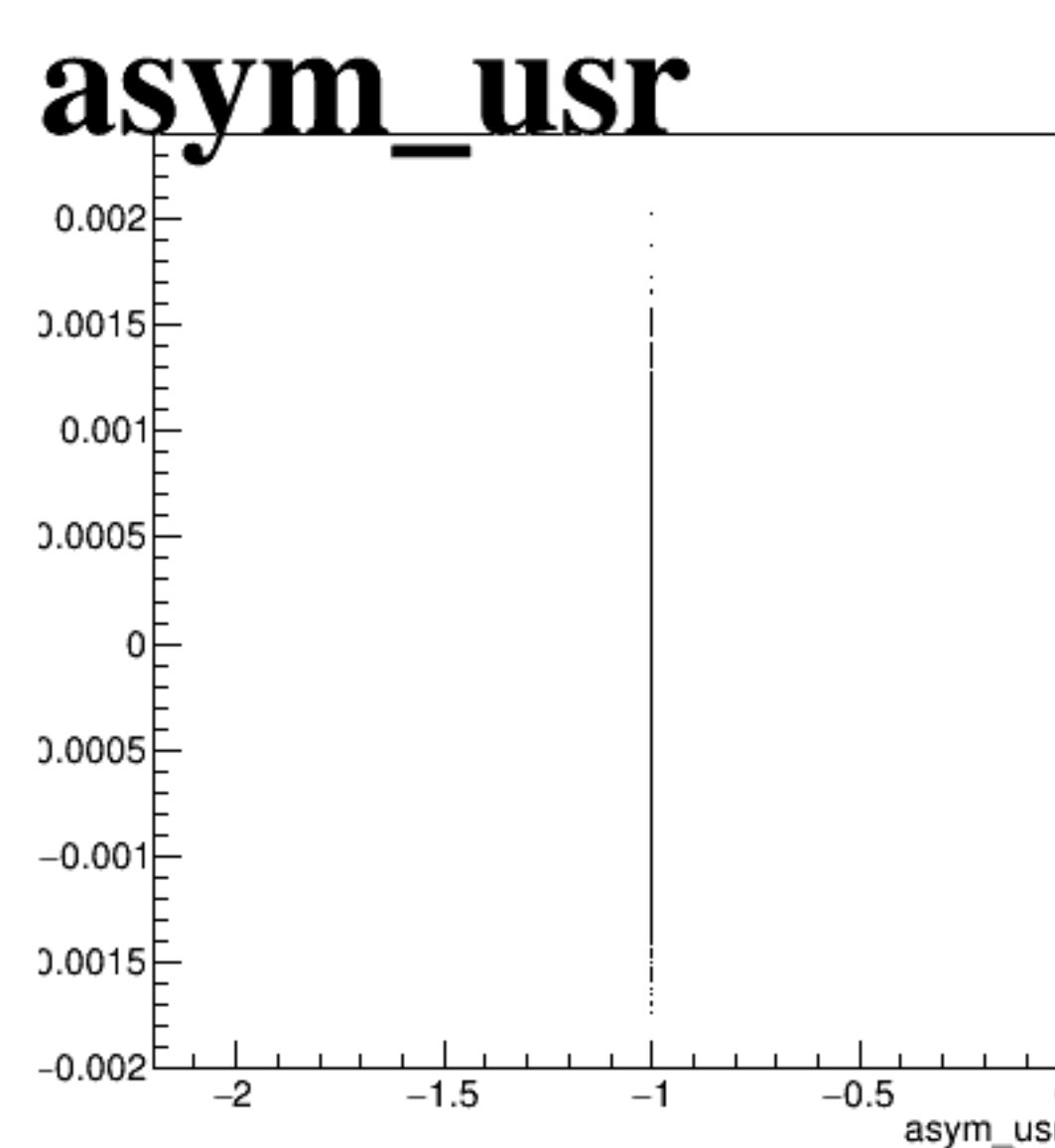
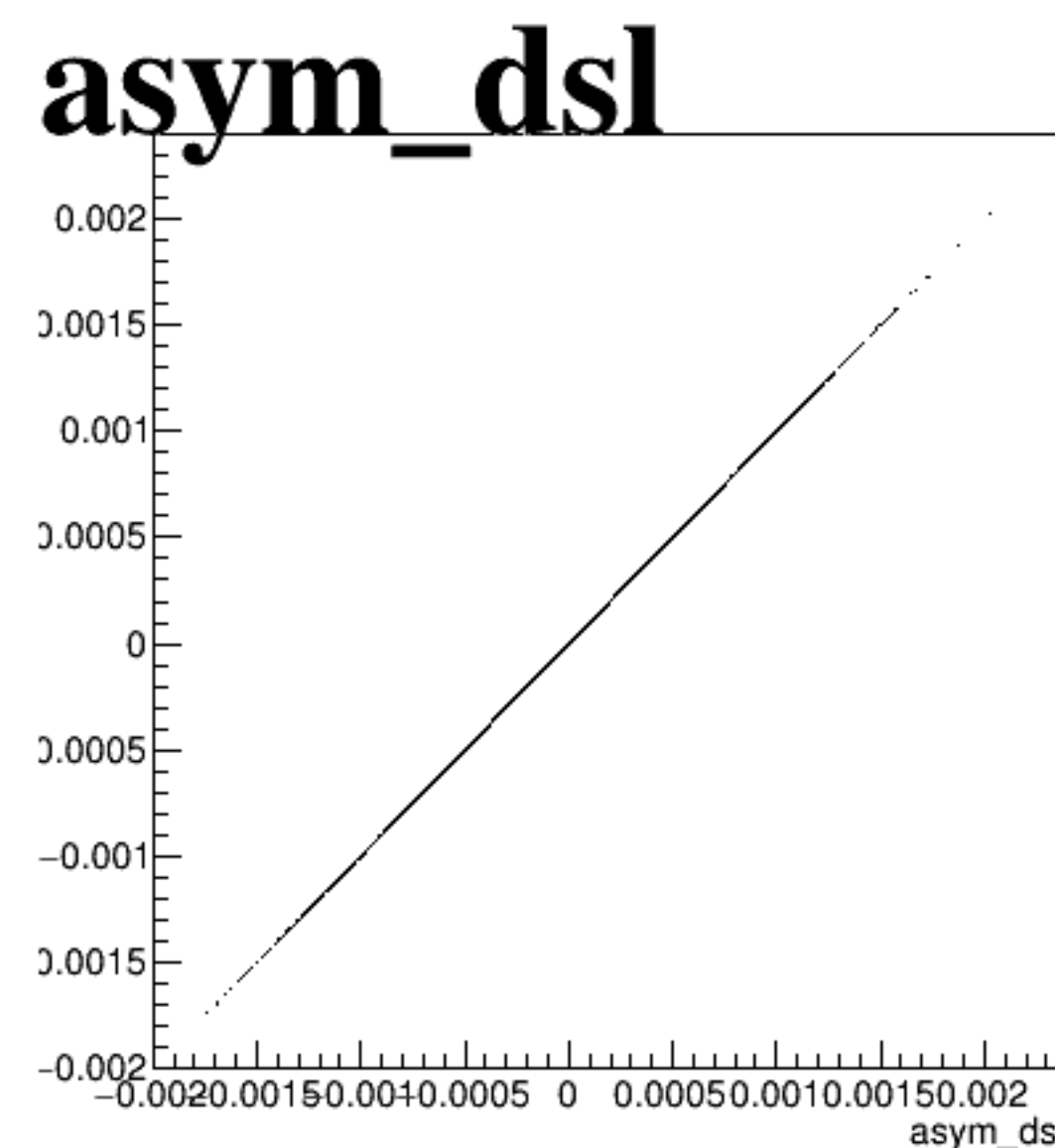
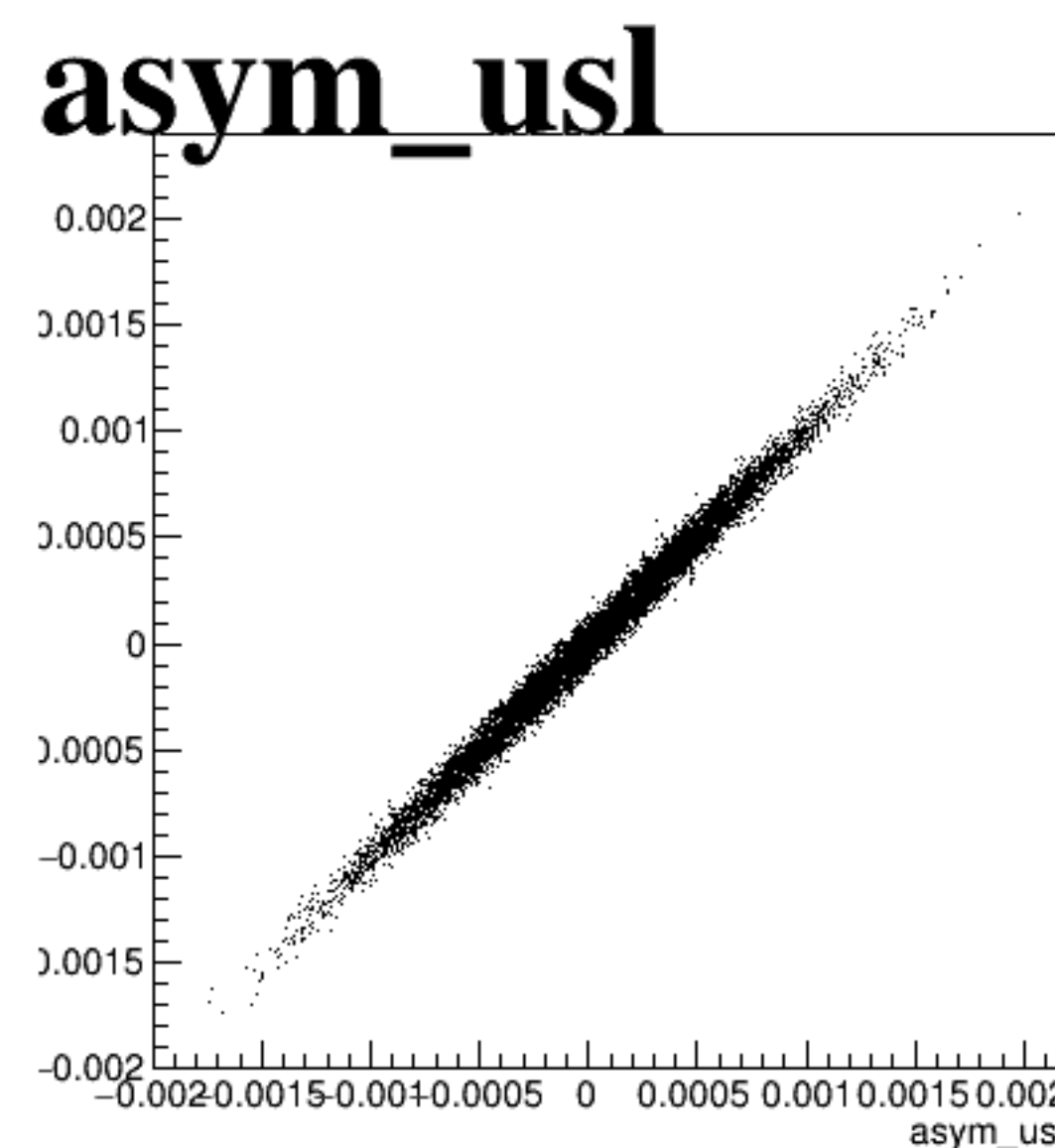
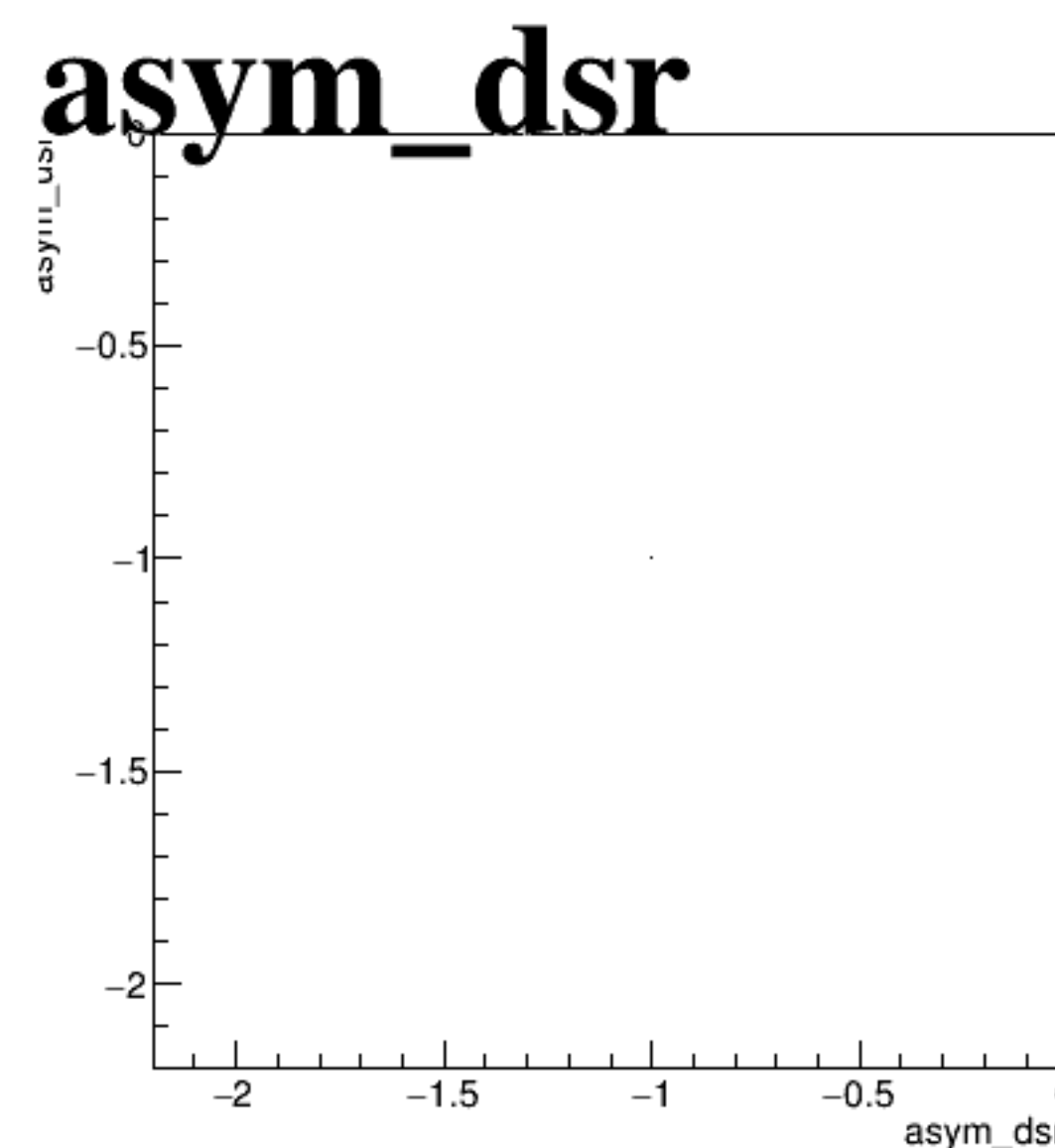
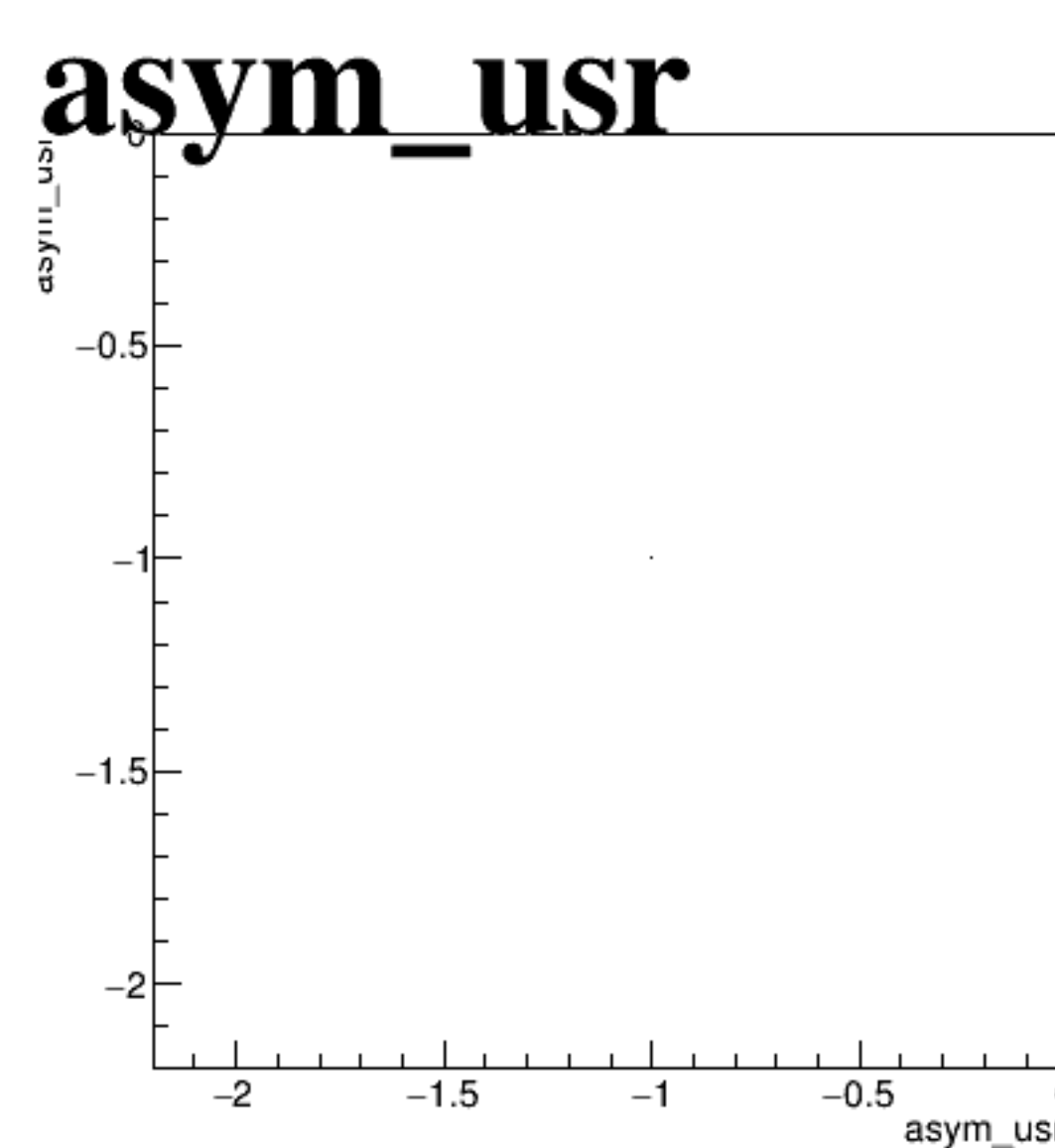
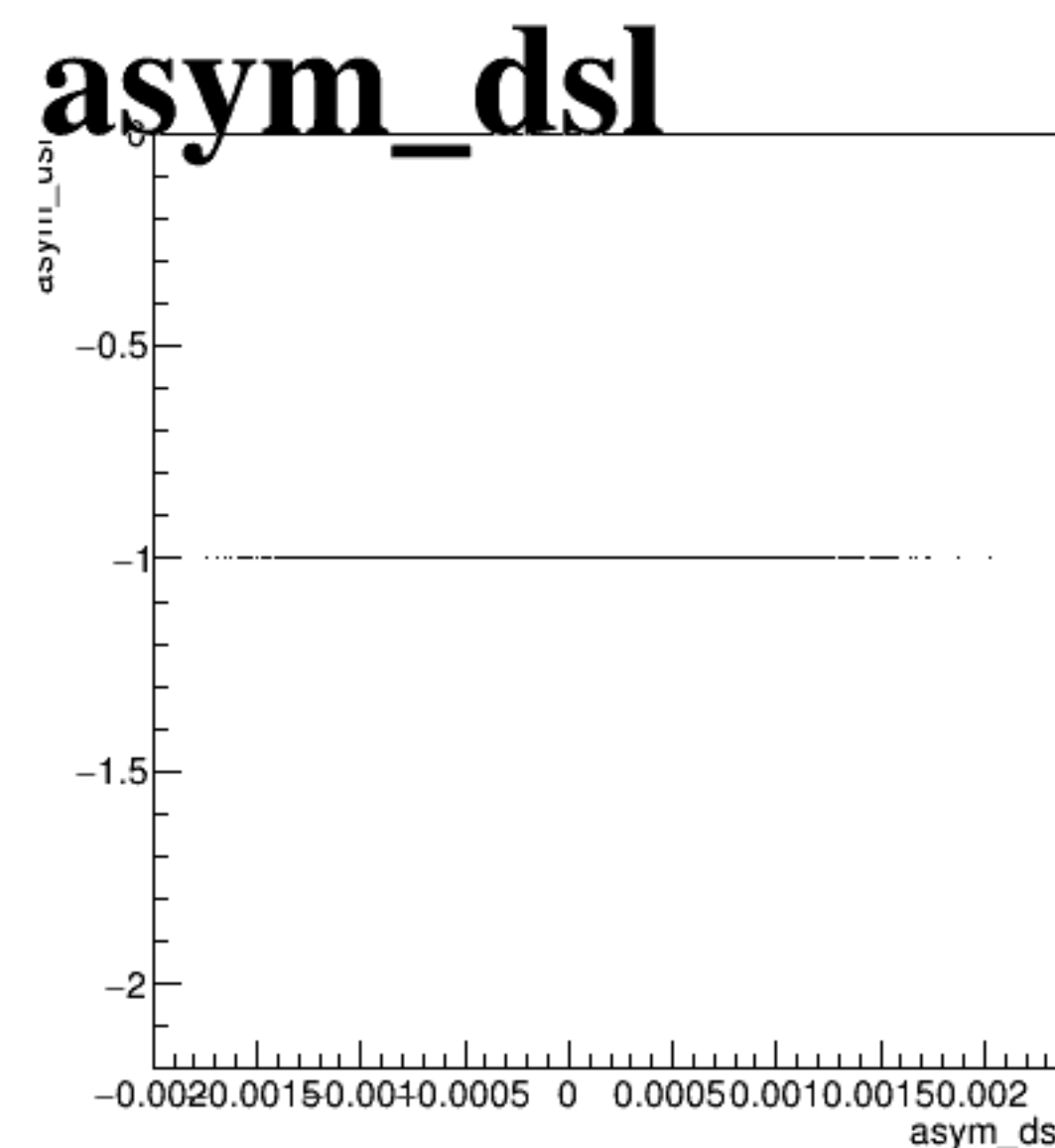
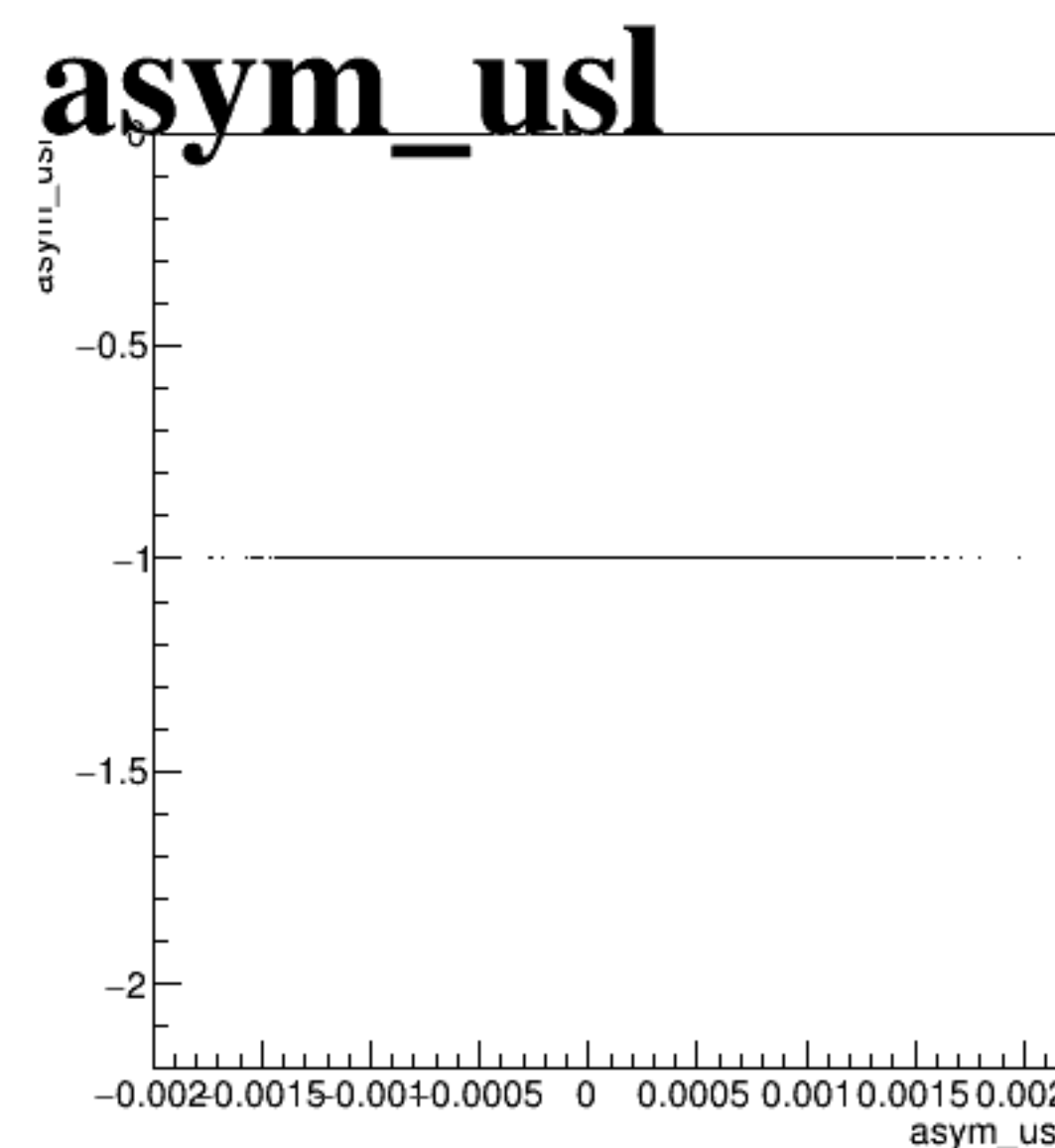
asym_dsr



asym_usl



asym_dsl

**asym_usr****asym_dsr**